# MITSUBISHI 

Mitsubishi Programmable Logic Controller

## Transition from MELSEC-A/QnA Large Type

Series to AnS/Q2AS Small Type Series Handbook


## - SAFETY PRECAUTIONS

(Be sure to read these instructions before use.)
Before using the product, read this and relevant manuals carefully and handle the product correctly with full attention to safety.

In this manual, 〇SAFETY PRECAUTIONS are classified into 2 levels: "DANGER" and "CAUTION".


Under some circumstances, failure to observe the $\$$ CAUTION level instructions may also lead to serious results.

Be sure to observe the instructions of both levels to ensure the safety.

Please keep this manual in a safe place for future reference and also pass this manual on to the end user.

## [Design Precautions]

## DANGER

- Create a safety circuit outside the PLC to ensure the whole system will operate safely even if an external power failure or a PLC failure occurs. Otherwise, incorrect output or malfunction may cause an accident.
(1) For an emergency stop circuit, protection circuit and interlock circuit that is designed for incompatible actions such as forward/reverse rotation or for damage prevention such as the upper/lower limit setting in positioning, any of them must be created outside the PLC.
(2) When the PLC detects the following error conditions, it stops the operation and turn off all the outputs.
- The overcurrent protection device or overvoltage protection device of the power supply module is activated.
- The PLC CPU detects an error such as a watchdog timer error by the self-diagnostics function.
In the case of an error of a part such as an I/O control part that cannot be detected by the PLC CPU, all the outputs may turn on. In order to make all machines operate safely in such a case, set up a fail-safe circuit or a specific mechanism outside the PLC. For a fail-safe circuit example, refer to "LOADING AND INSTALLATION" in the User's Manual of the relevant CPU.
(3) Depending on the failure of the output module's relay or transistor, the output status may remain ON or OFF incorrectly. For output signals that may lead to a serious accident, create an external monitoring circuit.


## [Design Precautions]

## DANGER

- If load current more than the rating or overcurrent due to a short circuit in the load has flowed in the output module for a long time, it may cause a fire and smoke. Provide an external safety device such as a fuse.
- Design a circuit so that the external power will be supplied after power-up of the PLC.

Activating the external power supply prior to the PLC may result in an accident due to incorrect output or malfunction.

- For the operation status of each station at a communication error in data link, refer to the respective data link manual.
Otherwise, incorrect output or malfunction may cause an accident.
- When controlling a running PLC (data modification) by connecting a peripheral device to the CPU module or a PC to an intelligent/special function module, create an interlock circuit on sequence programs so that the whole system functions safely all the time.
Also, before performing any other controls (e.g. program modification, operating status change (status control)), read the manual carefully and ensure the safety. In these controls, especially the one from an external device to a PLC in a remote location, some PLC side problem may not be resolved immediately due to failure of data communications. To prevent this, create an interlock circuit on sequence programs and establish corrective procedures for communication failure between the external device and the PLC CPU.
- When setting up the system, do not allow any empty slot on the base unit.

If any slot is left empty, be sure to use a blank cover (A1SG60) or a dummy module (A1SG62) for it. When using the extension base unit, A1S52B, A1S55B, A1S58B, A1S52B(S1), A1S55B(S1) or A1S58B(S1), attach the included dustproof cover to the module in slot 0 .
Otherwise, internal parts of the module may be flied in the short circuit test or when an overcurrent or overvoltage is accidentally applied to the external I/O section.

## $\triangle$ CAUTION

Do not install the control lines or communication cables together with the main circuit or power lines, or bring them close to each other.
Keep a distance of 100 mm or more between them.
Failure to do so may cause malfunctions due to noise.

- When an output module is used to control the lamp load, heater, solenoid valve, etc., a large current (ten times larger than the normal one) may flow at the time that the output status changes from OFF to ON. Take some preventive measures such as replacing the module with the one of a suitable current rating.


## [Installation Precautions]

## 1. CAUTION

- Use the PLC under the environment specified in the user's manual.

Otherwise, it may cause electric shocks, fires, malfunctions, product deterioration or damage.

- Insert the module fixing projection into the fixing hole in the base unit to mount the module, and tighten the module fixing screws with the specified torque.

Even correct mounting may cause malfunctions, a failure or a drop of the module if no screw is tightened.
Tightening the screw excessively may damage the screw and/or the module, resulting in a drop of the module, a short circuit or malfunctions.

Connect the extension cable to the connector of the base unit or module.
Check for incomplete connection after installing it.
Poor electrical contact may cause incorrect inputs and/or outputs.

- Insert the memory cassette and fully press it to the memory cassette connector.

Check for incomplete connection after installing it.
Poor electrical contact may cause malfunctions.

- Be sure to shut off all phases of the external power supply used by the system before mounting or removing the module.
Failure to do so may damage the module.
- Do not directly touch the conductive part or electronic components of the module.

Doing so may cause malfunctions or a failure of the module.

## [Wiring Precautions]

## DANGER

- Be sure to shut off all phases of the external power supply used by the system before wiring. Failure to do so may result in an electric shock or damage of the product.
- Before energizing and operating the system after wiring, be sure to attach the terminal cover supplied with the product.
Failure to do so may cause an electric shock.


## [Wiring Precautions]

\! CAUTION

- Ground the FG and LG terminals correctly.

Failure to do so may cause an electric shock or malfunctions.

- Wire the module correctly after confirming the rated voltage and terminal layout.

Connecting a power supply of a different voltage rating or incorrect wiring may cause a fire or failure.

- Do not connect multiple power supply modules to one module in parallel.

The power supply modules may be heated, resulting in a fire or failure.

- Press, crimp or properly solder the connector for external connection with the specified tool.

Incomplete connection may cause a short circuit, fire or malfunctions.

- Tighten terminal screws within the specified torque range.

If the screw is too loose, it may cause a short circuit, fire or malfunctions.
If too tight, it may damage the screw and/or the module, resulting in a short circuit or malfunctions.

- Carefully prevent foreign matter such as dust or wire chips from entering the module.

Failure to do so may cause a fire, failure or malfunctions.

## [Startup and Maintenance Precautions]

## DANGER

- Do not touch any terminal during power distribution.

Doing so may cause an electric shock.

- Properly connect batteries.

Do not charge, disassemble, heat or throw them into the fire and do not make them short-circuited and soldered.
Incorrect battery handling may cause personal injuries or a fire due to exothermic heat, burst and/or ignition.

- Be sure to shut off all phases of the external power supply used by the system before cleaning or retightening the terminal screws or module mounting screws.
Failure to do so may result in an electric shock.
If they are too loose, it may cause a short circuit or malfunctions.
Tightening the screw excessively may damage the screw and/or the module, resulting in a drop of the module, a short circuit or malfunctions.


## [Startup and Maintenance Precautions]

## CAUTION

- When performing online operations (especially, program modification, forced output or operating status change) by connecting a peripheral device to the running CPU module, read the manual carefully and ensure the safety. Incorrect operation will cause mechanical damage or accidents.
- Do not disassemble or modify each of modules.

Doing so may cause failure, malfunctions, personal injuries and/or a fire.

- When using a wireless communication device such as a mobile phone, keep a distance of 25 cm (9.84inch) or more from the PLC in all directions.

Failure to do so may cause malfunctions.

- Be sure to shut off all phases of the external power supply used by the system before mounting or removing the module.
Failure to do so may result in failure or malfunctions of the module.
- When replacing the fuse, use a fuse specified by the manufacturer.

Using the one for the high-rated current or an electric wire may cause a fire.

## [Startup and Maintenance Precautions]

## CAUTION

- Do not drop or apply any impact to the battery.

Doing so may damage the battery, resulting in electrolyte spillage inside the battery.
If any impact has been applied, discard the battery and never use it.

- Before handling modules, touch a grounded metal object to discharge the static electricity from the human body.
Failure to do so may cause failure or malfunctions of the module.


## [Disposal Precautions]

## 1. CAUTION

When disposing of the product, treat it as an industrial waste.

## [Transportation Precautions]

## \. CAUTION

- When transporting lithium batteries, make sure to treat them based on the transportation regulations. (Refer to the User's Manual of each CPU for details of the relevant models.)


## REVISIONS

* The handbook number is given on the bottom left of the back cover.


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## INTRODUCTION

### 1.1 Suggestions for Transition from Large-sized A/QnA Series to Small-sized AnS/Q2AS Series

### 1.1.1 Concepts for alternative models

For transition from large-sized A/QnA series to small-sized AnS/Q2AS series, the A2USHCPU-S1, Q2ASCPU-S1 or Q2ASHCPU-S1 is suggested as an alternative model.
Consider adopting the Q series when transition to the A2USHCPU-S1, Q2ASCPU-S1 or Q2ASHCPUS 1 is difficult for the following reasons: the number of extension stages used is two or more, the I/O points used is 1024 or more, the program capacity exceeds 60K steps, etc.

Alternative models suggested


When replacement is difficult


The AnNCPU indicates A1N, A2N,A2N-S1, A3N, A1NP21/R21, A2NP21/R21, A2NP21/R21-S1, A3NP21/R21, A1NP21-S3, A2NP21-S3, A2NP21-S4 and A3NP21-S3.
The AnACPU indicates A2A, A2A-S1, A3A,A2AP21/R21, A2AP21/R21-S1, A3AP21/R21, A2AP21-S3, A2AP21-S4 and A3AP21-S3.
The AnUCPU indicates A2U, A2U-S1, A3U and A4U.

At the first, figure out if each model can be used or not in reference to the basic specifications shown below. As a result of this, if a replacement is available, check the detailed specifications.
$\bigcirc$ : Usable, $\triangle$ : Alternatives are available, $\times$ : Unusable

| Selection item |  | A2USHCPU-S1 | Q2ASCPU-S1 | Q2ASHCPU-S1 | QnHCPU |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Number of extension stages | 1 stage | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ |
|  | 7 stages | $\times$ | $\times$ | $\times$ | $0^{* 1}$ |
| Number of modules mounted | 16 within | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ |
|  | More than 16 | $\times$ | $\times$ | $\times$ | $\bigcirc$ |
| I/O points | 1024 within | 0 | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ |
|  | More than 1024 | $\times$ | $\times$ | $\times$ | $\bigcirc$ |
| Program capacity | 30k steps within | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ |
|  | 60k steps within | $\times$ | $\bigcirc$ | $\bigcirc$ | $0^{* 1}$ |
|  | More than 60k steps | $\times$ | $\times$ | $\times$ | - *1 |
| I/O control method | Refresh mode | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ |
|  | Direct mode | $\Delta^{* 2}$ | $\Delta^{* 2}$ | $\Delta^{* 2}$ | $\Delta^{* 2}$ |
| Processing speed LD: $\mu \mathrm{S}$ |  | 0.09 | 0.2 | 0.075 | $\begin{gathered} 0.079 \\ \text { to } \\ 0.034 \\ \hline \end{gathered}$ |
| Timer/counter update timing | END processing | $\bigcirc$ | - | - | - |
|  | Execution of OUT Tn instruction | - | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ |
| File register (including the extension file register) | Up to 120k points | $0^{* 3 *}$ | - *5 | -*5 | O* |
|  | More than 120k points | $\times$ | ${ }^{* 5}$ | $0^{*}$ | $0^{*} 6$ |
|  | Up to 1018k points | $\times$ | ${ }^{* 5}$ | ${ }^{* 5}$ | $0^{*} 6$ |
| MELSECNET | NET (II) compatibility | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\times$ |
|  | 3-tier master station support | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\times$ |
| Number of MELSECNET modules mounted | 1 module | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\times$ |
|  | 2 modules <br> (3-tier master station) | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\times$ |
|  | NET/10 compatibility | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | O* |
| Number of computer link modules mounted *8 | 6 within | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ |
|  | More than 6 | $\times$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ |
| Bus connection to GOT |  | $0^{* 9}$ | O*10 | O*10 | $\bigcirc$ |
| Microcomputer program (except for SFC) |  | $\times$ | $\times$ | $\times$ | $\times$ |
| Dedicated instruction | CC-Link | $\bigcirc$ | $\Delta^{* 11}$ | $\Delta^{* 11}$ | $\Delta^{* 11}$ |
|  | PID/Real constant/ Character string/ Others | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ |

*1 : Differs according to the CPU type.
*2 : Applicable using partial refresh instructions and direct access instructions.
*3: Decreases according to the parameter settings.
*4 : Stored in the built-in memory (not extensible).
*5 : A memory card is required.
*6 : The standard RAM + memory card must be used.
*7: The MELSECNET/H mode is not available when any CPU other than $\mathrm{Qn}(\mathrm{H}) \mathrm{CPU}$ are used together.
*8: Up to 6 A series A1SJ71UC24S (-R2, -R4) can be mounted.
*9: Included in the number of computer link modules.
*10: Equivalent to the A series module.
*11: No program is required since the network parameters are used.

### 1.1.2 Advantages of transition from large-sized A/QnA Series to small-sized AnS/ Q2AS Series

(1) RAM memory featured as standard

The RAM memory with 112 K to 256 K byte memory capacity is built in as a standard feature.
The A2USHCPU-S1 can store file registers and comments since the RAM more than program capacity is featured as standard.
For the Q2ASCPU-S1 and Q2ASHCPU-S1, the RAM featured as standard has a capacity equivalent to the program amount.

## (2) $E^{2}$ PROM operation

Installing a memory cassette (when using the A2USHCPU-S1) or a memory card (when using the Q2ASCPU-S1 or Q2ASHCPU-S1) enables the $E^{2}$ PROM operation. (Equivalent to the conventional ROM operation)
Previously, the ROM operation required a dedicated device such as ROM writer. However, programs can be written by the PLC write operation in the same way as for the RAM, and the program is not lost even without a battery.

## (3) Compact size

The external dimensions are $130(\mathrm{H}) \times 430(\mathrm{~W}) \times 110(\mathrm{D}) \mathrm{mm}$ (when using a base with 8 slots). The mounting area becomes smaller than that of the large-sized A/QnA series.
(4) Extension connectors equipped on the right and left sides of the main base unit

An extension connector is equipped on each of the right and left sides to allow connection of the extension base unit in any position.
Bus connection to GOT is available even when an extension base is attached.
(5) DIN rail installation

The A1S base unit has screw holes, and a DIN rail installation mechanism on the rear face.

### 1.1.3 Precautions for transition from large-sized A/QnA series to small-sized AnS/ Q2AS series

(1) Be sure to confirm the functions, specifications and handling instructions before using each small-sized AnS/Q2AS series module by referring to the relevant manual.
(2) Be sure to check the operation of whole system before starting the actual operation.
(3) Screws must be tightened for the module mounting.

### 2.1 List of CPU Module Alternative Models

| Large-sized A/QnA series model to be discontinued |  | Small-sized AnS/Q2AS series alternative models |  |
| :---: | :---: | :---: | :---: |
| Product | Model | Model | Remarks (restrictions) |
| CPU module | A1NCPU <br> A1NCPUP21 <br> A1NCPUR21 | A2USHCPU-S1 <br> A2USHCPU-S1 <br> A1SJ71AP21 <br> A2USHCPU-S1 <br> A1SJ71AR21 | 1) I/O control: Refresh/Direct switching $\rightarrow$ Refresh only <br> 2) Processing speed (LD instruction): For refresh $1.0 \mu \mathrm{~s} \rightarrow 0.09 \mu \mathrm{~s}$ <br> 3) PC MIX value: $0.2 \rightarrow 2.0$ <br> 4) I/O points: 256 points $\rightarrow 1024$ points <br> 5) Program capacity: 6k steps $\rightarrow 30 \mathrm{k}$ steps <br> 6) File register points: 0 points $\rightarrow 8 \mathrm{k}$ points <br> 7) Extension stage: 1 stage $\rightarrow 1$ stage <br> 8) Applicable memory: 4KRAM/4KROM/4KEROM $\rightarrow$ built-in RAM <br> 9) Microcomputer program: Available $\rightarrow$ Not available |
|  | A2NCPU <br> A2NCPUP21 <br> A2NCPUR21 | A2USHCPU-S1 <br> A2USHCPU-S1 <br> A1SJ71AP21 <br> A2USHCPU-S1 <br> A1SJ71AR21 | 1) I/O control: Refresh/Direct switching $\rightarrow$ Refresh only <br> 2) Processing speed (LD instruction): For refresh $1.0 \mu \mathrm{~s} \rightarrow 0.09 \mu \mathrm{~s}$ <br> 3) PC MIX value: $0.2 \rightarrow 2.0$ <br> 4) I/O points: 512 points $\rightarrow 1024$ points <br> 5) Program capacity: 14k steps $\rightarrow 30 \mathrm{k}$ steps <br> 6) File register points: 4 k points $\rightarrow 8 \mathrm{k}$ points <br> 7) Extension stage: 3 stages $\rightarrow 1$ stage <br> 8) Applicable memory: Depending on the memory cassette $\rightarrow$ built-in RAM <br> 9) Microcomputer program: Available $\rightarrow$ Not available |
|  | A2NCPU-S1 <br> A2NCPUP21-S1 <br> A2NCPUR21-S1 | A2USHCPU-S1 <br> A2USHCPU-S1 <br> A1SJ71AP21 <br> A2USHCPU-S1 <br> A1SJ71AR21 | 1) I/O control: Refresh/Direct switching $\rightarrow$ Refresh only <br> 2) Processing speed (LD instruction): For refresh $1.0 \mu \mathrm{~s} \rightarrow 0.09 \mu \mathrm{~s}$ <br> 3) PC MIX value: $0.2 \rightarrow 2.0$ <br> 4) I/O points: 1024 points $\rightarrow 1024$ points <br> 5) Program capacity: 14 k steps $\rightarrow 30 \mathrm{k}$ steps <br> 6) File register points: 4 k points $\rightarrow 8 \mathrm{k}$ points <br> 7) Extension stage: 7 stages $\rightarrow 1$ stage <br> 8) Applicable memory: Depending on the memory cassette $\rightarrow$ built-in RAM <br> 9) Microcomputer program: Available $\rightarrow$ Not available |
|  | A3NCPU <br> A3NCPUP21 <br> A3NCPUR21 | A2USHCPU-S1 <br> A2USHCPU-S1 <br> A1SJ71AP21 <br> A2USHCPU-S1 <br> A1SJ71AR21 | 1) I/O control: Refresh/Direct switching $\rightarrow$ Refresh only <br> 2) Processing speed (LD instruction): For refresh $1.0 \mu \mathrm{~s} \rightarrow 0.09 \mu \mathrm{~s}$ <br> 3) PC MIX value: $0.2 \rightarrow 2.0$ <br> 4) I/O points: 2048 points $\rightarrow 1024$ points <br> 5) Program capacity: $30 \mathrm{k} \times 2$ steps $\rightarrow 30 \mathrm{k}$ steps <br> 6) File register points: 8 k points $\rightarrow 8 \mathrm{k}$ points <br> 7) Extension stage: 7 stages $\rightarrow 1$ stage <br> 8) Applicable memory: Depending on the memory cassette $\rightarrow$ built-in RAM <br> 9) Microcomputer program: Available $\rightarrow$ Not available |


| Large-sized A/QnA series model to be discontinued |  | Small-sized AnS/Q2AS series alternative models |  |
| :---: | :---: | :---: | :---: |
| Product | Model | Model | Remarks (restrictions) |
| CPU module | A2ACPU A2ACPUP21 A2ACPUR21 | A2USHCPU-S1 <br> A2USHCPU-S1 <br> A1SJ71AP21 <br> A2USHCPU-S1 <br> A1SJ71AR21 | 1) I/O control: Refresh only <br> 2) Processing speed (LD instruction): $0.2 \mu \mathrm{~s} \rightarrow 0.09 \mu \mathrm{~s}$ <br> 3) PC MIX value: $0.9 \rightarrow 2.0$ <br> 4) I/O points: 512 points $\rightarrow 1024$ points <br> 5) Program capacity: 14 k steps $\rightarrow 30 \mathrm{k}$ steps <br> 6) File register points: 8 k points $\rightarrow 8 \mathrm{k}$ points <br> 7) Extension stage: 3 stages $\rightarrow 1$ stage <br> 8) Applicable memory: Depending on the memory cassette $\rightarrow$ built-in RAM |
|  | A2ACPU-S1 <br> A2ACPUP21-S1 <br> A2ACPUR21-S1 | A2USHCPU-S1 <br> A2USHCPU-S1 <br> A1SJ71AP21 <br> A2USHCPU-S1 <br> A1SJ71AR21 | 1) I/O control: Refresh only <br> 2) Processing speed (LD instruction): $0.2 \mu \mathrm{~s} \rightarrow 0.09 \mu \mathrm{~s}$ <br> 3) PC MIX value: $0.9 \rightarrow 2.0$ <br> 4) I/O points: 1024 points $\rightarrow 1024$ points <br> 5) Program capacity: 14k steps $\rightarrow 30 \mathrm{k}$ steps <br> 6) File register points: 8 k points $\rightarrow 8 \mathrm{k}$ points <br> 7) Extension stage: 7 stages $\rightarrow 1$ stage <br> 8) Applicable memory: Depending on the memory cassette $\rightarrow$ built-in RAM |
|  | A3ACPU <br> A3ACPUP21 <br> A3ACPUR21 | A2USHCPU-S1 <br> A2USHCPU-S1 <br> A1SJ71AP21 <br> A2USHCPU-S1 <br> A1SJ71AR21 | 1) I/O control: Refresh only <br> 2) Processing speed (LD instruction): $0.15 \mu \mathrm{~s} \rightarrow 0.09 \mu \mathrm{~s}$ <br> 3) PC MIX value: $1.2 \rightarrow 2.0$ <br> 4) I/O points: 2048 points $\rightarrow 1024$ points <br> 5) Program capacity: $30 \mathrm{k} \times 2$ steps $\rightarrow 30 \mathrm{k}$ steps <br> 6) File register points: 8 k points $\rightarrow 8 \mathrm{k}$ points <br> 7) Extension stage: 7 stages $\rightarrow 1$ stage <br> 8) Applicable memory: Depending on the memory cassette $\rightarrow$ built-in RAM |
|  | A2UCPU | A2USHCPU-S1 | 1) I/O control: Refresh only <br> 2) Processing speed (LD instruction): $0.2 \mu \mathrm{~s} \rightarrow 0.09 \mu \mathrm{~s}$ <br> 3) PC MIX value: $0.9 \rightarrow 2.0$ <br> 4) I/O points: 512 points $\rightarrow 1024$ points <br> 5) Program capacity: 14 k steps $\rightarrow 30 \mathrm{k}$ steps <br> 6) File register points: $8 k$ points $\rightarrow 8 k$ points <br> 7) Extension stage: 3 stages $\rightarrow 1$ stage <br> 8) Applicable memory: Depending on the memory cassette $\rightarrow$ built-in RAM |
|  | A2UCPU-S1 | A2USHCPU-S1 | 1) I/O control: Refresh only <br> 2) Processing speed (LD instruction): $0.2 \mu \mathrm{~s} \rightarrow 0.09 \mu \mathrm{~s}$ <br> 3) PC MIX value: $0.9 \rightarrow 2.0$ <br> 4) I/O points: 1024 points $\rightarrow 1024$ points <br> 5) Program capacity: 14 k steps $\rightarrow 30 \mathrm{k}$ steps <br> 6) File register points: 8 k points $\rightarrow 8 \mathrm{k}$ points <br> 7) Extension stage: 7 stages $\rightarrow 1$ stage <br> 8) Applicable memory: Depending on the memory cassette $\rightarrow$ built-in RAM |
|  | A3UCPU | A2USHCPU-S1 | 1) I/O control: Refresh only <br> 2) Processing speed (LD instruction): $0.15 \mu \mathrm{~s} \rightarrow 0.09 \mu \mathrm{~s}$ <br> 3) PC MIX value: $1.2 \rightarrow 2.0$ <br> 4) I/O points: 2048 points $\rightarrow 1024$ points <br> 5) Program capacity: $30 \mathrm{k} \times 2$ steps $\rightarrow 30 \mathrm{k}$ steps <br> 6) File register points: 8 k points $\rightarrow 8 \mathrm{k}$ points <br> 7) Extension stage: 7 stages $\rightarrow 1$ stage <br> 8) Applicable memory: Depending on the memory cassette $\rightarrow$ built-in RAM |
|  | A4UCPU | A2USHCPU-S1 | 1) I/O control: Refresh only <br> 2) Processing speed (LD instruction): $0.15 \mu \mathrm{~s} \rightarrow 0.09 \mu \mathrm{~s}$ <br> 3) PC MIX value: $1.2 \rightarrow 2.0$ <br> 4) I/O points: 4096 points $\rightarrow 1024$ points <br> 5) Program capacity: $30 \mathrm{k} \times 4$ steps $\rightarrow 30 \mathrm{k}$ steps <br> 6) File register points: 8 k points $\rightarrow 8 \mathrm{k}$ points <br> 7) Extension stage: 7 stages $\rightarrow 1$ stage <br> 8) Applicable memory: Depending on the memory cassette $\rightarrow$ built-in RAM |


| Large-sized A/QnA series model to be discontinued |  | Small-sized AnS/Q2AS series alternative models |  |
| :---: | :---: | :---: | :---: |
| Product | Model | Model | Remarks (restrictions) |
| CPU module | A1NCPUP21-S3 | A2USHCPU-S1 <br> A1SJ71AP21-S3 | 1) I/O control: Refresh/Direct switching $\rightarrow$ Refresh only <br> 2) Processing speed (LD instruction): For refresh $1.0 \mu \mathrm{~s} \rightarrow 0.09 \mu \mathrm{~s}$ <br> 3) PC MIX value: $0.2 \rightarrow 2.0$ <br> 4) I/O points: 256 points $\rightarrow 1024$ points <br> 5) Program capacity: 6k steps $\rightarrow 30 \mathrm{k}$ steps <br> 6) File register points: 0 points $\rightarrow 8 \mathrm{k}$ points <br> 7) Extension stage: 1 stage $\rightarrow 1$ stage <br> 8) Applicable memory: 4KRAM/4KROM/4KEROM $\rightarrow$ built-in RAM <br> 9) Microcomputer program: Available $\rightarrow$ Not available |
|  | A2NCPUP21-S3 | A2USHCPU-S1 <br> A1SJ71AP21-S3 | 1) I/O control: Refresh/Direct switching $\rightarrow$ Refresh only <br> 2) Processing speed (LD instruction): For refresh $1.0 \mu \mathrm{~s} \rightarrow 0.09 \mu \mathrm{~s}$ <br> 3) PC MIX value: $0.2 \rightarrow 2.0$ <br> 4) I/O points: 512 points $\rightarrow 1024$ points <br> 5) Program capacity: 14 k steps $\rightarrow 30 \mathrm{k}$ steps <br> 6) File register points: $4 k$ points $\rightarrow 8 k$ points <br> 7) Extension stage: 3 stages $\rightarrow 1$ stage <br> 8) Applicable memory: Depending on the memory cassette $\rightarrow$ built-in RAM <br> 9) Microcomputer program: Available $\rightarrow$ Not available |
|  | A2NCPUP21-S4 | A2USHCPU-S1 <br> A1SJ71AP21-S3 | 1) I/O control: Refresh/Direct switching $\rightarrow$ Refresh only <br> 2) Processing speed (LD instruction): For refresh $1.0 \mu \mathrm{~s} \rightarrow 0.09 \mu \mathrm{~s}$ <br> 3) PC MIX value: $0.2 \rightarrow 2.0$ <br> 4) I/O points: 1024 points $\rightarrow 1024$ points <br> 5) Program capacity: $14 k$ steps $\rightarrow 30 k$ steps <br> 6) File register points: $4 k$ points $\rightarrow 8 k$ points <br> 7) Extension stage: 7 stages $\rightarrow 1$ stage <br> 8) Applicable memory: Depending on the memory cassette $\rightarrow$ built-in RAM <br> 9) Microcomputer program: Available $\rightarrow$ Not available |
|  | A3NCPUP21-S3 | A2USHCPU-S1 <br> A1SJ71AP21-S3 | 1) I/O control: Refresh/Direct switching $\rightarrow$ Refresh only <br> 2) Processing speed (LD instruction): For refresh $1.0 \mu \mathrm{~s} \rightarrow 0.09 \mu \mathrm{~s}$ <br> 3) PC MIX value: $0.2 \rightarrow 2.0$ <br> 4) I/O points: 2048 points $\rightarrow 1024$ points <br> 5) Program capacity: $30 \mathrm{k} \times 2$ steps $\rightarrow 30 \mathrm{k}$ steps <br> 6) File register points: 8 k points $\rightarrow 8 \mathrm{k}$ points <br> 7) Extension stage: 7 stages $\rightarrow 1$ stage <br> 8) Applicable memory: Depending on the memory cassette $\rightarrow$ built-in RAM <br> 9) Microcomputer program: Available $\rightarrow$ Not available |
|  | A2ACPUP21-S3 | A2USHCPU-S1 <br> A1SJ71AP21-S3 | 1) I/O control: Refresh only <br> 2) Processing speed (LD instruction): $0.2 \mu \mathrm{~s} \rightarrow 0.09 \mu \mathrm{~s}$ <br> 3) PC MIX value: $0.9 \rightarrow 2.0$ <br> 4) I/O points: 512 points $\rightarrow 1024$ points <br> 5) Program capacity: 14 k step $\rightarrow 30 \mathrm{k}$ steps <br> 6) File register points: 8 k points $\rightarrow 8 \mathrm{k}$ points <br> 7) Extension stage: 3 stages $\rightarrow 1$ stage <br> 8) Applicable memory: Depending on the memory cassette $\rightarrow$ built-in RAM |
|  | A2ACPUP21-S4 | A2USHCPU-S1 <br> A1SJ71AP21-S3 | 1) I/O control: Refresh only <br> 2) Processing speed (LD instruction): $0.2 \mu \mathrm{~s} \rightarrow 0.09 \mu \mathrm{~s}$ <br> 3) PC MIX value: $0.9 \rightarrow 2.0$ <br> 4) I/O points: 1024 points $\rightarrow 1024$ points <br> 5) Program capacity: 14 k steps $\rightarrow 30 \mathrm{k}$ steps <br> 6) File register points: 8 k points $\rightarrow 8 \mathrm{k}$ points <br> 7) Extension stage: 7 stages $\rightarrow 1$ stage <br> 8) Applicable memory: Depending on the memory cassette $\rightarrow$ built-in RAM |
|  | A3ACPUP21-S3 | A2USHCPU-S1 <br> A1SJ71AP21-S3 | 1) I/O control: Refresh only <br> 2) Processing speed (LD instruction): $0.15 \mu \mathrm{~s} \rightarrow 0.09 \mu \mathrm{~s}$ <br> 3) PC MIX value: $1.2 \rightarrow 2.0$ <br> 4) I/O points: 2048 points $\rightarrow 1024$ points <br> 5) Program capacity: $30 \mathrm{k} \times 2$ steps $\rightarrow 30 \mathrm{k}$ steps <br> 6) File register points: 8 k points $\rightarrow 8 \mathrm{k}$ points <br> 7) Extension stage: 7 stages $\rightarrow 1$ stage <br> 8) Applicable memory: Depending on the memory cassette $\rightarrow$ built-in RAM |


| Large-sized A/QnA series model to be discontinued |  | Small-sized AnS/Q2AS series alternative models |  |
| :---: | :---: | :---: | :---: |
| Product | Model | Model | Remarks (restrictions) |
| CPU module | Q2ACPU | Q2ASCPU-S1 | 1) I/O control: Refresh only <br> 2) Processing speed (LD instruction): $0.2 \mu \mathrm{~s} \rightarrow 0.2 \mu \mathrm{~s}$ <br> 3) PC MIX value: $1.3 \rightarrow 1.3$ <br> 4) I/O points: 512 points $\rightarrow 1024$ points <br> 5) Program capacity: 28 k steps $\rightarrow 60 \mathrm{k}$ steps <br> 6) File register points: 1018 k points $\times 2 \rightarrow 1018 \mathrm{k}$ points <br> 7) Extension stage: 3 stages $\rightarrow 1$ stage <br> 8) Number of memory cards: 2 cards $\rightarrow 1$ card <br> 9) Max. memory card SRAM capacity: 2 M bytes $\times 2$ cards $\rightarrow 2 \mathrm{M}$ bytes $\times 1$ card |
|  | Q2ACPU-S1 | Q2ASCPU-S1 | 1) I/O control: Refresh only <br> 2) Processing speed (LD instruction): $0.2 \mu \mathrm{~s} \rightarrow 0.2 \mu \mathrm{~s}$ <br> 3) PC MIX value: $1.3 \rightarrow 1.3$ <br> 4) I/O points: 1024 points $\rightarrow 1024$ points <br> 5) Program capacity: 60k steps $\rightarrow 60 \mathrm{k}$ steps <br> 6) File register points: 1018 k points $\times 2 \rightarrow 1018 \mathrm{k}$ points <br> 7) Extension stage: 7 stages $\rightarrow 1$ stage <br> 8) Number of memory cards: 2 cards $\rightarrow 1$ card <br> 9) Max. memory card SRAM capacity: 2 M bytes $\times 2$ cards $\rightarrow 2 \mathrm{M}$ bytes $\times 1$ card |
|  | Q3ACPU | Q2ASHCPU-S1 | 1) I/O control: Refresh only <br> 2) Processing speed (LD instruction): $0.15 \mu \mathrm{~s} \rightarrow 0.075 \mu \mathrm{~s}$ <br> 3) PC MIX value: $1.8 \rightarrow 3.8$ <br> 4) I/O points: 2048 points $\rightarrow 1024$ points <br> 5) Program capacity: $92 k$ steps $\rightarrow 60 \mathrm{k}$ steps <br> 6) File register points: 1018k points $\times 2 \rightarrow 1018 \mathrm{k}$ points <br> 7) Extension stage: 7 stages $\rightarrow 1$ stage <br> 8) Number of memory cards: 2 cards $\rightarrow 1$ card <br> 9) Max. memory card SRAM capacity: 2 M bytes $\times 2$ cards $\rightarrow 2 \mathrm{M}$ bytes $\times 1$ card |
|  | Q4ACPU | Q2ASHCPU-S1 | 1) I/O control: Refresh only <br> 2) Processing speed (LD instruction): $0.075 \mu \mathrm{~s} \rightarrow 0.075 \mu \mathrm{~s}$ <br> 3) PC MIX value: $3.8 \rightarrow 3.8$ <br> 4) I/O points: 4096 points $\rightarrow 1024$ points <br> 5) Program capacity: 124 k steps $\rightarrow 60 \mathrm{k}$ steps <br> 6) File register points: 1018 k points $\times 2 \rightarrow 1018 \mathrm{k}$ points <br> 7) Extension stage: 7 stages $\rightarrow 1$ stage <br> 8) Number of memory cards: 2 cards $\rightarrow 1$ card <br> 9) Max. memory card SRAM capacity: 2 M bytes $\times 2$ cards $\rightarrow 2 \mathrm{M}$ bytes $\times 1$ card |

### 2.2 CPU Module Specifications Comparisons

| Function | Contents | Large-sized A/QnA series |  |  |  | Small-sized AnS/Q2AS series |  | Precautions for replacement | Reference section |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | AnN CPU | AnA CPU | AnU CPU | $\begin{aligned} & \text { QnA } \\ & \text { CPU } \end{aligned}$ | A2USH CPU-S1 | $\begin{aligned} & \text { Q2ASH } \\ & \text { CPU-S1 } \end{aligned}$ |  |  |
| Control method | Repetitive operation of a stored program | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | O | - | - |
| I/O control method | Refresh mode/ Direct mode | $0{ }^{* 1}$ | $0^{*}$ | $0^{*}$ | $0^{*}$ | $0^{*}$ | $0^{*}$ | Use the direct I/O instructions to input/output in the direct mode, as the A2USHCPU-S1/ Q2AS(H)CPU-S1 supports the refresh mode only. | Section <br> 7.3.2 |
| Programming language | Language dedicated to sequence control (Relay symbol, logic symbol, MELSAP language) | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | 0 | $\bigcirc$ | O | The MELSAP language for the A/AnS series is MELSAP-II and that for the QnA/ Q2AS series is MELSAP3. | - |
| Processing speed | Sequence instructions ( $\mu \mathrm{s} /$ step) | 1.0 | 0.15 | 0.15 | 0.075 | 0.09 | 0.075 | - | - |
| Watchdog timer (WDT) | Watchdog timer (WDT) (ms) | 10 to 2000 | 200 | 200 | 10 to 2000 | 200 | 10 to 2000 | - | - |
| Memory capacity | User memory capacity (bytes) | Max. 448k (Memory cassette) | Max. 768k (Memory cassette) | Max. 1024k (Memory cassette) | $\begin{gathered} \text { Max. } \\ 2036 \mathrm{k} \times 2 \\ (\text { SRAM } \\ \text { card) } \end{gathered}$ | $\begin{aligned} & \text { 256k (built-in } \\ & \text { RAM) } \end{aligned}$ | Max. 2036k (SRAM card) | A memory cassette is required for the A series as user memory, while the user memory is included in the AnS series as standard equipment. | Section <br> 2.4.1 |
| Program capacity | Sequence program (steps) | $\begin{gathered} \text { Max. } \\ 30 \mathrm{k} \times 2 \end{gathered}$ | $\begin{gathered} \text { Max. } \\ 30 \mathrm{k} \times 2 \end{gathered}$ | $\begin{gathered} \text { Max. } \\ 30 \mathrm{k} \times 4 \end{gathered}$ | Max. 124k | Max. 30k | Max. 60k | If the program capacity is insufficient, consider replacing by the $Q$ series. | - |
|  | Microcomputer program (bytes) | Max. 58k | $\times$ | $\times$ | $\times$ | $\times$ | $\times$ | The AnA, AnU, QnA, Q2AS series and A2USHCPU-S1 do not include microcomputer program. Therefore, consider use of sequence program, etc., as the substitution. | - |
| Number of I/O points | $\begin{aligned} & \hline \begin{array}{l} \text { Number of } \mathrm{I} / \mathrm{O} \\ \text { points } \\ \text { (points) }{ }^{* 3} \\ \hline \end{array} \end{aligned}$ | 256 to 2048 | 512 to 2048 | 512 to 4096 | 512 to 4096 | 1024 | 1024 | If the I/O points are insufficient, consider replacing by the Q series. | - |
| Number of device points | Input device (X) (points) *4 | 256 to 2048 | 512 to 2048 | 8192 | 8192 | 8192 | 8192 | - | - |
|  | $\begin{aligned} & \text { Output device } \\ & (\mathrm{Y}) \text { (points) }{ }^{* 4} \\ & \hline \end{aligned}$ | 256 to 2048 | 512 to 2048 | 8192 | 8192 | 8192 | 8192 | - | - |
|  | Internal relay (M) (points) | Total 2048 | Total 8192 | Total 8192 | 8192 | Total 8192 | 8192 | - | - |
|  | $\begin{array}{\|l} \hline \text { Latch relay (L) } \\ \text { (points) } \\ \hline \end{array}$ |  |  |  | 8192 |  | 8192 | - | - |
|  | $\begin{aligned} & \text { Step relay (S) } \\ & \text { (points) } \end{aligned}$ |  |  |  | $8192^{* 5}$ |  | $8192{ }^{* 5}$ | - | - |
|  | $\begin{array}{\|l} \begin{array}{l} \text { Annunciator (F) } \\ \text { (points) } \end{array} \\ \hline \end{array}$ | 256 | 2048 | 2048 | 2048 | 2048 | 2048 | - | - |
|  | $\begin{array}{\|l\|} \hline \begin{array}{l} \text { Edge relay (V) } \\ \text { (points) } \end{array} \\ \hline \end{array}$ | $\times$ | $\times$ | $\times$ | 2048 | $\times$ | 2048 | - | - |
|  | Link relay (B) (points) | 1024 | 4096 | 8192 | 8192 | 8192 | 8192 | - | - |
|  | Timer ( T ) (points) | 256 | 2048 | 2048 | 2048 | 2048 | 2048 | - | - |
|  | $\begin{array}{\|l} \begin{array}{l} \text { Counter (C) } \\ \text { (points) } \end{array} \\ \hline \end{array}$ | 256 | 1024 | 1024 | 1024 | 1024 | 1024 | - | - |
|  | Data register <br> (D) (points) | 1024 | 6144 | 8192 | 12288 | 8192 | 12288 | - | - |
|  | Link register <br> (W) (points) | 1024 | 4096 | 8192 | 8192 | 8192 | 8192 | - | - |

O: Usable, $\Delta$ : Partially different in spec. (eg. setting method), $x$ : Not usable

| Function | Contents |  | Large-sized A/QnA series |  |  |  | Small-sized AnS/Q2AS series |  | Precautions for replacement | Reference section |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  | $\begin{aligned} & \text { AnN } \\ & \text { CPU } \end{aligned}$ | AnA CPU | AnU CPU | QnA CPU | A2USH CPU-S1 | $\begin{aligned} & \text { Q2ASH } \\ & \text { CPU-S1 } \end{aligned}$ |  |  |
| Number of device points | File register (R) (points) |  | 8192 | 8192 | 8192 | 32768 | 8192 | 32768 | - | - |
|  | Accumulator (A) (points) |  | 2 | 2 | 2 | $\times$ | 2 | $\times$ | Accumulators are converted to the special registers (SD718, SD719) upon A $\rightarrow$ QnA program conversion as they are not included in the QnA and Q2AS series. | - |
|  | Index register | (Z) (points) | 1 | 7 | 7 | 16 | 7 | 16 | - | - |
|  |  | (V) (points) | 1 | 7 | 7 | $\times$ | 7 | $\times$ | This is used as edge relay for the QnA and Q2AS series. | - |
|  | Nesting (N) (points) |  | 8 | 8 | 8 | 15 | 8 | 15 | - | - |
|  | Pointer (P) (points) |  | 256 | 256 | 256 | 4096 | 256 | 4096 | - | - |
|  | Special relay (M) (points) |  | 256 | 256 | 256 | 2048 | 256 | 2048 | - | - |
|  | Special register (D) (points) |  | 256 | 256 | 256 | 2048 | 256 | 2048 | - | - |
| Comment points | Comment points (points) ${ }^{*} 6$ |  | Max. 4032 | Max. 4032 | Max. 4032 | Max. approx. 50k | $\begin{aligned} & \text { Max. } \\ & 4032 \end{aligned}$ | $\begin{aligned} & \text { Max. } \\ & \text { approx. } \end{aligned}$ 50k | - | - |
| Selfdiagnostics | Watchdog timer (WDT), Memory error detection, CPU error detection, battery error detection |  | 0 | $\bigcirc$ | 0 | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | - | - |
| Operation mode at error occurrence | Stop/continue setting |  | 0 | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | - | - |
| Output mode switching at changing from STOP to RUN | Re-output operation status before STOP/ selection of output after operation execution |  | 0 | O | $\bigcirc$ | $\bigcirc$ | 0 | $\bigcirc$ | - | - |

*1: Direct I/O is also selectable with the I/O control method setting switch.
*2: Basically, only the refresh mode is applicable, but some instructions/devices can be input/output in the direct mode.
*3: This number means the number of applicable points for the access to actual I/O modules.
*4: This number means the number of usable points on the program.
*5: The step replays (S) of the QnA and Q2AS series are dedicated to the SFC.
*6: Comment points are the points that can be written to CPU.

### 2.3 CPU Module Functional Comparisons

### 2.3.1 Functional comparisons between the AnNCPU and the A2USHCPU-S1

O: Usable, $\triangle$ : Partially different in spec. (eg. setting method), $\times$ : Not usable

| Function |  | Contents | $\left\lvert\, \begin{gathered} \text { Large-sized A } \\ \text { series } \end{gathered}\right.$ | Small-sized AnS series | Precautions for replacement | Reference section |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | AnNCPU | A2USHCPU-S1 |  |  |
| $\begin{aligned} & \text { O} \\ & \text { 응 } \\ & 0 \end{aligned}$ | Constant scan |  | Executes the sequence program at constant time intervals regardless of the processing time of the program. | $\bigcirc$ | $\bigcirc$ | - | - |
|  | Latch (power backup) | Holds the data of devices in the event of power OFF, resetting, and a momentary power failure longer than 20 ms . | $\bigcirc$ | $\bigcirc$ | - | - |
|  | Remote RUN/ STOP | Executes the remote RUN/ STOP using external switches and peripheral devices. | $\bigcirc$ | $\bigcirc$ | - | - |
|  | PAUSE | Stops operations while holding the output status. | $\bigcirc$ | $\bigcirc$ | - | - |
|  | Interrupt processing | Executes the program that corresponds to the cause when an interrupt cause occurs. | O | $\triangle$ | For the A3NCPU, an interrupt program is required for each main program and subprogram separately. For the A2USHCPU-S1, only one main program is available. | - |
|  | Microcomputer mode | Executes various controls and operations over utility programs and user created microcomputer programs stored in the microcomputer program area by calling them from the sequence program. | $\bigcirc$ | $\times$ | Consider use of sequence program, etc., as the substitution. | - |
|  | Display priority of ERROR LED | The settings for ON/OFF of ERROR LED at the occurrence of error. | $\times$ | $\bigcirc$ | Target errors vary by model, but there is no functional difference. | - |
|  | ROM operation | Enables operation with parameters and programs stored in ROMs in order not to lose user programs due to battery exhaustion. | O | $\triangle$ | For the A2USHCPU-S1, use the $E^{2}$ PROM memory cassette. | $\begin{gathered} \text { Section } \\ 7.3 .5 \end{gathered}$ |
|  | Data protection function (System protect, Keyword registration/ Password registration) | Prohibits peripheral devices from reading/writing programs and comments in the memory cassettes, the memory card, and built-in memory, etc. of a CPU module. | $\bigcirc$ | $\bigcirc$ | - | - |
|  | The settings of output status at changing from STOP to RUN | The settings for the output status at changing from STOP to RUN (Y) between "re-output operation status before STOP" and "output after operation execution". | $\bigcirc$ | O | - | - |
|  | Clock function | A CPU includes a clock, of which data can be read and written. <br> The clock data consists of year, month, date, hour, minute, second and a day of the week. | $\bigcirc$ | $\bigcirc$ | - | - |
| $\begin{aligned} & \text { Do } \\ & 0.0 \\ & \hline 0 \end{aligned}$ | Write during RUN | Changes (writes to) the program of a CPU in the RUN status. | $\bigcirc$ | $\bigcirc$ | - | - |
|  | Status latch | Stores the data of all devices in the memory cassette or built-in memory at the occurrence of an error for monitoring by the peripheral device. | $0{ }^{* 1}$ | $\bigcirc$ | - | - |
|  | Sampling trace | Stores the data of specified devices at the specified intervals for monitoring by the peripheral device. | O*1 | $\bigcirc$ | - | - |
|  | Sampling operation (skip operation/ partial operation) | Stops the execution of a sequence program at the specified step. | $\bigcirc$ | $\bigcirc$ | - | - |
|  | Off-line switch | Skips the devices used for the OUT instruction in the operation processing of sequence program. | $\bigcirc$ | $\times$ | The A2USHCPU-S1 does not include the off-line switch function. | - |
|  | Online I/O module replacement | Enables I/O modules to be replaced while the CPU is in RUN. | O | $\times$ | The A2USHCPU-S1 does not include the online I/O module replacement function. | - |
|  | Self-diagnostics function | Executes self-diagnostics to check for errors and stop a CPU, etc. | $\bigcirc$ | $\bigcirc$ | - | - |

[^0]
### 2.3.2 Functional comparisons between the AnACPU, AnUCPU and the A2USHCPU-S1

| Function |  | Contents | Large-sized A series |  | Small-sized AnS | Precautions for replacement | Reference section |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | AnACPU | AnUCPU | A2USHCPU-S1 |  |  |
|  | Constant scan |  | Executes the sequence program at constant time intervals regardless of the processing time of the program. | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | - | - |
|  | Latch (power backup) | Holds the data of devices in the event of power OFF, resetting, and a momentary power failure longer than 20 ms . | O | O | $\bigcirc$ | - | - |
|  | Remote RUN/ STOP | Executes the remote RUN/ STOP using external switches and peripheral devices. | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | - | - |
|  | PAUSE | Stops operations while holding the output status. | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | - | - |
|  | Interrupt processing | Executes the program that corresponds to the cause when an interrupt cause occurs. | $\bigcirc$ | O | $\triangle$ | For the A3A/A3U/A4UCPU, an interrupt program is required for each main program and subprogram separately. For the A2USHCPU-S1, only one main program is available. | - |
|  | Display priority of ERROR LED | The settings for ON/OFF of ERROR LED at the occurrence of error. | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | - | - |
| $0$ | ROM operation | Enables operation with parameters and programs stored in ROMs in order not to lose user programs due to battery exhaustion. | $\bigcirc$ | O | $\Delta$ | For the A2USHCPU-S1, use the $\mathrm{E}^{2} \mathrm{PROM}$ memory cassette. | $\begin{gathered} \text { Section } \\ 7.3 .5 \end{gathered}$ |
|  | Data protection function (System protect, Keyword registration/ Password registration) | Prohibits peripheral devices from reading/writing programs and comments in the memory cassettes, the memory card, and built-in memory, etc. of a CPU module. | O | $\bigcirc$ | O | - | - |
|  | The settings of output status at changing from STOP to RUN | The settings for the output status at changing from STOP to RUN (Y) between "re-output operation status before STOP" and "output after operation execution". | $\bigcirc$ | O | O | - | - |
|  | Clock function | A CPU includes a clock, of which data can be read and written. The clock data consists of year, month, date, hour, minute, second and a day of the week. | 0 | $\bigcirc$ | 0 | - | - |
| $\begin{aligned} & \text { O} \\ & \frac{0}{0} \\ & \hline 0 \end{aligned}$ | Write during RUN | Changes (writes to) the program of a CPU in the RUN status. | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | - | - |
|  | Status latch | Stores the data of all devices in the memory cassette or built-in memory at the occurrence of an error for monitoring by the peripheral device. | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | - | - |
|  | Sampling trace | Stores the data of specified devices at the specified intervals for monitoring by the peripheral device. | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | - | - |
|  | Step operation (skip operation/ partial operation) | Stops the execution of a sequence program at the specified step. | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | - | - |
|  | Online I/O module replacement | Enables I/O modules to be replaced while the CPU is in RUN. | 0 | $\bigcirc$ | $\times$ | The A2USHCPU-S1 does not include the online I/O module replacement function. | - |
|  | Self-diagnostics function | Executes self-diagnostics to check for errors and stop a CPU, etc. | 0 | $\bigcirc$ | $\bigcirc$ | - | - |
|  | Error history | Stores errors detected by the diagnostics function into the CPU. Error details can be monitored from peripheral devices. | $\bigcirc$ | $\bigcirc$ | O | - | - |

### 2.3.3 Functional comparisons between the QnACPU and the Q2AS(H)CPU-S1

| Function |  | Contents | O: Usable, $\triangle$ : Partially different in spec. (eg. setting method), $\times$ : Not usable |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | Large-sized QnA series | Small-sized Q2AS series | Precautions for replacement | Reference section |
|  |  | QnACPU | Q2AS(H)CPU-S1 |  |  |
| $\begin{aligned} & \text { 유 } \\ & \text { 厄i } \end{aligned}$ | Constant scan |  | Executes the sequence program at constant time intervals regardless of the processing time of the program. | $\bigcirc$ | $\bigcirc$ | - | - |
|  | Latch (power backup) |  | Holds the data of devices in the event of power OFF, resetting, and a momentary power failure longer than 20 ms . | $\bigcirc$ | $\bigcirc$ | - | - |
|  | Remote RUN/ STOP | Executes the remote RUN/ STOP using external switches and peripheral devices. | $\bigcirc$ | $\bigcirc$ | - | - |
|  | PAUSE | Stops operations while holding the output status. | $\bigcirc$ | $\bigcirc$ | - | - |
|  | Interrupt processing | Executes the program that corresponds to the cause when an interrupt cause occurs. | $\bigcirc$ | $\bigcirc$ | - | - |
|  | Display priority of ERROR LED | The settings for ON/OFF of ERROR LED at the occurrence of error. | $\bigcirc$ | $\bigcirc$ | - | - |
|  | File management | Manages all of parameters, sequence programs, device comments, file registers, etc as files. | $\bigcirc$ | $\triangle$ | Some memory configurations differ between the QnACPU and the Q2AS(H)CPU-S1. (For the Q2AS(H)CPU-S1, only one memory card is installed.) | Section <br> 2.4.1 <br> Section <br> 7.3.4 |
|  | Structured program | Selects a suitable execution type for program application, and divides each program by designer, process or others. | $\bigcirc$ | $\bigcirc$ | - | - |
|  | I/O assignment | Performs the I/O assignment to any individual module regardless of its mounted position. | O | $\triangle$ | Only 1 stage of the extension base unit can be connected though there is no restriction on the I/O assignment. | - |
|  | Boot run (ROM operation) | Executes the sequence program after reading it from the memory card to the CPU built-in memory when the CPU goes to the RUN status. | $\bigcirc$ | $\triangle$ | Some memory configurations differ between the QnACPU and the Q2AS(H)CPU-S1. (For the Q2AS(H)CPU-S1, only one memory card is installed.) | Section <br> 2.4.1 <br> Section 7.3.5 |
|  | Data protection (System protect, Keyword registration/ Password registration) | Prohibits peripheral devices from reading/writing programs and comments in the memory cassettes, the memory card, and built-in memory, etc. of a CPU module. | O | O | - | - |
|  | Initial device value | Sets the initial value of device memory, file registers, and special function modules, etc. when the CPU has become the RUN status. | $\bigcirc$ | $\triangle$ | Some memory configurations differ between the QnACPU and the Q2AS(H)CPU-S1. (For the Q2AS(H)CPU-S1, only one memory card is installed.) | Section <br> 2.4.1 <br> Section <br> 7.3.5 |
|  | The settings of output status at changing from STOP to RUN | The settings for the output status at changing from STOP to RUN (Y) between "re-output operation status before STOP" and "output after operation execution". | O | O | - | - |
|  | Number of general data processing | Sets the number of general data processing executed in one END processing. | $\bigcirc$ | $\bigcirc$ | - | - |
|  | Clock function | A CPU incorporates a clock, which can be read/written. The clock data consists of year, month, day, hour, minute, second and a day of the week. | $\bigcirc$ | $\bigcirc$ | - | - |

O: Usable, $\Delta$ : Partially different in spec. (eg. setting method), $\times$ : Not usable

| Function |  | Contents | Large-sized QnA series | Small-sized Q2AS series | Precautions for replacement | Reference section |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | QnACPU | Q2AS(H)CPU-S1 |  |  |
|  | Write during RUN |  | Changes (writes to) the program of a CPU in the RUN status. | $\bigcirc$ | $\bigcirc$ | - | - |
|  | Status latch | Stores the data of all devices in the memory card at the occurrence of an error for monitoring by the peripheral device. | O | $\bigcirc$ | - | - |
|  | Sampling trace | Stores the data of specified devices at the specified intervals for monitoring by the peripheral device. | O* ${ }^{*}$ | O* | - | - |
|  | Program trace | Collects the execution status of specified programs and steps, and stores them in a file. | $O^{* 1 * 2}$ | $O^{* 1 * 2}$ | - | - |
|  | Simulation function | Detaches I/O modules or special modules from the CPU module and test-operates the program upon the step operation. | $0^{* 2}$ | $0^{*}$ | - | - |
|  | Step operation (skip operation partial operation) | Stops the execution of a sequence program at the specified step. | $\bigcirc$ | $\bigcirc$ | - | - |
|  | Execution time measurement (Program list monitor, scan time measurement) | Measures the operation time for each program. | O | O | - | - |
|  | Module access interval reading | Monitors the access interval of special function modules or peripheral devices. | $\bigcirc$ | $\bigcirc$ | - | - |
|  | Online I/O module replacement | Enables I/O modules to be replaced while the CPU is in RUN. | O | $\times$ | The Q2AS(H)CPU-S1 does not include the online I/O module replacement function. | - |
|  | Self-diagnostics function | Executes self-diagnostics to check for errors and stop a CPU, etc. | $\bigcirc$ | $\bigcirc$ | - | - |
|  | Error history | Stores errors, which are detected with the diagnostics function, in a CPU or memory card. The stored history can be monitored with peripheral devices. | $\bigcirc$ | $\bigcirc$ | - | - |

*1: The SRAM card is required.
*2: GPPQ is required. This is not applicable to GX Developer.

### 2.4 Precautions for CPU Module Replacement

### 2.4.1 Memory for CPU module

The memory configuration is shown in (1). Examine the following points depending on the memory capacity before replacement and applications.
-Memory to store
-To use or not use a memory card
(1) Memory configuration and data that can be stored

1) Large-sized $A$ series and small-sized AnS series

Large-sized A series


Small-sized AnS series (A2USHCPU-S1)

2) Large-sized QnA series and small-sized Q2AS series

Large-sized QnA series


| Memory <br> card A <br> (ROM) Program <br> Parameter <br> Comment <br> Initial device value <br> File register |
| :--- | :--- |
| (Drive 2) |


|  |  |
| :--- | :--- |
| Memory <br> card B <br> (RAM) Program <br> Parameter <br> Comment <br> Initial device value <br> File register <br> (Drive 3) Local device <br> Error history  |  |
| Memory <br> card B <br> (ROM) Program <br> Parameter <br> Comment <br> Initial device value <br> File register <br> (Drive 4)  |  |



## (2) Capacity of each memory

The following table shows the memory of CPU modules, in which the user program, etc. is stored, together with its capacity.

| Item |  | Model |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | Large-sized A series | Small-sized AnS series | Large-sized QnA series | Small-sized Q2AS series |
| Memory cassette | RAM | Max. 1024k bytes | - | - | - |
|  | EPROM | Max. 256k bytes | - | - | - |
|  | $E^{2}$ PROM | Max. 256k bytes | Max. 64k bytes | - | - |
| Built-in RAM |  | - | 256k bytes | Max. 496k bytes (Program memory) | Max. 240k bytes (Program memory) |
| Memory card ${ }^{* 1}$ | SRAM card | - | - | Max. 2M bytes | Max. 2M bytes |
|  | $E^{2} \mathrm{PROM}$ card | - | - | Max. 512k bytes | Max. 512k bytes |
|  | Flash card | - | - | Max. 1M bytes ${ }^{*} 2$ | Max. 1M bytes ${ }^{*} 2$ |

[^1]
### 3.1 List of I/O Module Alternative Models

| A series model to be discontinued |  | AnS series alternative model |  |
| :---: | :---: | :---: | :---: |
| Product | Model | Model | Remarks (restrictions) |
| Input module | AX10 | A1SX10 | 1) External wiring change: Required <br> Screw size: M3 $\rightarrow$ M3.5 <br> 2) Change in number of modules: Not required <br> 3) Program changes <br> Change in number of occupied I/O points: Not required <br> 4) Specification changes <br> Change in rated input voltage: Not required <br> Change in rated input current: Required <br> Change in ON voltage/ON current: Required <br> Change in OFF voltage/OFF current: Required <br> Change in input resistance: Required <br> 5) Change in functions: Not required |
|  | AX11 | A1SX10 | 1) External wiring change: Required <br> Screw size: M3 $\rightarrow$ M3.5 <br> 2) Change in number of modules: Required (2 modules necessary) <br> 3) Program changes <br> Change in number of occupied I/O points: Not required (32=16×2) <br> 4) Specification changes <br> Change in rated input voltage: Not required <br> Change in rated input current: Required <br> Change in ON voltage/ON current: Required <br> Change in OFF voltage/OFF current: Required <br> Change in input resistance: Required <br> 5) Change in functions: Not required |
|  | AX11EU | A1SX10EU | 1) External wiring change: Required <br> 2) Change in number of modules: Required (2 modules necessary) <br> 3) Program changes <br> Change in number of occupied I/O points: Not required ( $32=16 \times 2$ ) <br> 4) Specification changes <br> Change in rated input voltage: Not required <br> Change in rated input current: Required <br> Change in ON voltage/ON current: Required <br> Change in OFF voltage/OFF current: Required <br> Change in input resistance: Required <br> 5) Change in functions: Not required |
|  | AX20 | A1SX20 | 1) External wiring change: Required <br> Screw size: M3 $\rightarrow$ M3.5 <br> 2) Change in number of modules: Not required <br> 3) Program changes <br> Change in number of occupied I/O points: Not required <br> 4) Specification changes <br> Change in rated input voltage: Not required <br> Change in rated input current: Required <br> Change in ON voltage/ON current: Required <br> Change in OFF voltage/OFF current: Required <br> Change in input resistance: Required <br> 5) Change in functions: Not required |


| A series model to be discontinued |  | AnS series alternative model |  |
| :---: | :---: | :---: | :---: |
| Product | Model | Model | Remarks (restrictions) |
| Input module | AX21 | A1SX20 | 1) External wiring change: Required <br> Screw size: M3 $\rightarrow$ M3.5 <br> 2) Change in number of modules: Required (2 modules necessary) <br> 3) Program changes <br> Change in number of occupied I/O points: Not required (32=16×2) <br> 4) Specification changes <br> Change in rated input voltage: Required <br> Change in rated input current: Required <br> Change in ON voltage/ON current: Required <br> Change in OFF voltage/OFF current: Required <br> Change in input resistance: Required <br> 5) Change in functions: Not required |
|  | AX21EU | A1SX20EU | 1) External wiring change: Required <br> 2) Change in number of modules: Required (2 modules necessary) <br> 3) Program changes <br> Change in number of occupied I/O points: Not required (32=16×2) <br> 4) Specification changes <br> Change in rated input voltage: Not required <br> Change in rated input current: Required <br> Change in ON voltage/ON current: Required <br> Change in OFF voltage/OFF current: Required <br> Change in input resistance: Required <br> 5) Change in functions: Not required |
|  | AX31 | A1SX30 | 1) External wiring change: Required <br> Screw size: M3 $\rightarrow$ M3.5 <br> 2) Change in number of modules: Required (2 modules necessary) <br> 3) Program changes <br> Change in number of occupied I/O points: Not required ( $32=16 \times 2$ ) <br> 4) Specification changes <br> Change in rated input voltage: Required <br> Change in rated input current: Required <br> Change in ON voltage/ON current: Required <br> Change in OFF voltage/OFF current: Required <br> Change in input resistance: Required <br> 5) Change in functions: Not required |
|  | AX31-S1 | A1SX41 | 1) External wiring change: Required (Connector terminal block must be converted.) <br> Screw size: M3 $\rightarrow$ M3.5 <br> 2) Change in number of modules: Not required <br> 3) Program changes <br> Change in number of occupied I/O points: Not required <br> 4) Specification changes <br> Change in rated input voltage: Not required <br> Change in rated input current: Required <br> Change in ON voltage/ON current: Required <br> Change in OFF voltage/OFF current: Required <br> Change in input resistance: Required <br> 5) Change in functions: Not required |
|  | AX40 | A1SX40 | 1) External wiring change: Required <br> Screw size: M3 $\rightarrow$ M3.5 <br> 2) Change in number of modules: Not required <br> 3) Program changes <br> Change in number of occupied I/O points: Not required <br> 4) Specification changes <br> Change in rated input voltage: Not required <br> Change in rated input current: Required <br> Change in ON voltage/ON current: Required <br> Change in OFF voltage/OFF current: Required <br> Change in input resistance: Required <br> 5) Change in functions: Not required |


| A series model to be discontinued |  | AnS series alternative model |  |
| :---: | :---: | :---: | :---: |
| Product | Model | Model | Remarks (restrictions) |
|  | AX40-UL | A1SX40 | 1) External wiring change: Required <br> 2) Change in number of modules: Not required <br> 3) Program changes <br> Change in number of occupied I/O points: Not required <br> 4) Specification changes <br> Change in rated input voltage: Not required <br> Change in rated input current: Required <br> Change in ON voltage/ON current: Required <br> Change in OFF voltage/OFF current: Required <br> Change in input resistance: Required <br> 5) Change in functions: Not required |
|  | AX41 | A1SX41 | 1) External wiring change: Required (Connector terminal block must be converted.) <br> 2) Change in number of modules: Not required <br> 3) Program changes <br> Change in number of occupied I/O points: Not required <br> 4) Specification changes <br> Change in rated input voltage: Not required <br> Change in rated input current: Required <br> Change in ON voltage/ON current: Required <br> Change in OFF voltage/OFF current: Required <br> Change in input resistance: Required <br> 5) Change in functions: Not required |
| Input module | AX41-S1 | A1SX41-S1 | 1) External wiring change: Required (Connector terminal block must be converted.) <br> 2) Change in number of modules: Not required <br> 3) Program changes <br> Change in number of occupied I/O points: Not required <br> 4) Specification changes <br> Change in rated input voltage: Required (12VDC cannot be used.) <br> Change in rated input current: Required <br> Change in ON voltage/ON current: Required <br> Change in OFF voltage/OFF current: Required <br> Change in input resistance: Required <br> 5) Change in functions: Not required |
|  | AX41-UL | A1SX41 | 1) External wiring change: Required (Connector terminal block must be converted.) <br> 2) Change in number of modules: Not required <br> 3) Program changes <br> Change in number of occupied I/O points: Not required <br> 4) Specification changes <br> Change in rated input voltage: Not required <br> Change in rated input current: Required <br> Change in ON voltage/ON current: Required <br> Change in OFF voltage/OFF current: Required <br> Change in input resistance: Required <br> 5) Change in functions: Not required |
|  | AX42 | A1SX42 | 1) External wiring change: Not required <br> 2) Change in number of modules: Not required <br> 3) Program changes <br> Change in number of occupied I/O points: Not required <br> 4) Specification changes <br> Change in rated input voltage: Not required <br> Change in rated input current: Required <br> Change in ON voltage/ON current: Required <br> Change in OFF voltage/OFF current: Required <br> Change in input resistance: Required <br> 5) Change in functions: Not required |
|  | AX42-S1 | A1SX42-S1 | 1) External wiring change: Not required <br> 2) Change in number of modules: Not required <br> 3) Program changes <br> Change in number of occupied I/O points: Not required <br> 4) Specification changes <br> Change in rated input voltage: Required (12VDC cannot be used.) <br> Change in rated input current: Required <br> Change in ON voltage/ON current: Required <br> Change in OFF voltage/OFF current: Required <br> Change in input resistance: Required <br> 5) Change in functions: Not required |


| A series model to be discontinued |  | AnS series alternative model |  |
| :---: | :---: | :---: | :---: |
| Product | Model | Model | Remarks (restrictions) |
| Input module | AX50-S1 | None | Alternating with the A1SX40 is recommended. <br> 1) External wiring change: Required <br> Connect a $3.3 \mathrm{k} \Omega$ (1W or more) resistor to the external signal wire serially. <br> 2) Change in number of modules: Not required <br> 3) Program changes <br> Change in number of occupied I/O points: Not required <br> 4) Specification changes <br> Change in rated input voltage: Required <br> Change in rated input current: Required <br> Change in ON voltage/ON current: Required <br> Change in OFF voltage/OFF current: Required <br> Change in input resistance: Required <br> 5) Change in functions: Not required |
|  | AX60-S1 | None | Alternating with the A1SX40 is recommended. <br> 1) External wiring change: Required <br> Connect a $15 \mathrm{k} \Omega$ (3W or more) resistor to the external signal wire serially. <br> 2) Change in number of modules: Not required <br> 3) Program changes <br> Change in number of occupied I/O points: Not required <br> 4) Specification changes <br> Change in rated input voltage: Required <br> Change in rated input current: Required <br> Change in ON voltage/ON current: Required <br> Change in OFF voltage/OFF current: Required <br> Change in input resistance: Required <br> 5) Change in functions: Not required |
|  | AX70 | A1SX71 | 1) External wiring change: Required (Connector terminal block must be converted.) <br> 2) Change in number of modules: Not required <br> 3) Program changes <br> Change in number of occupied I/O points: Required <br> 4) Specification changes <br> Change in rated input voltage: Not required <br> Change in rated input current: Required <br> Change in ON voltage/ON current: Required <br> Change in OFF voltage/OFF current: Required <br> Change in input resistance: Required <br> 5) Change in functions: Not required |
|  | AX71 | A1SX71 | 1) External wiring change: Required (Connector terminal block must be converted.) <br> 2) Change in number of modules: Not required <br> 3) Program changes <br> Change in number of occupied I/O points: Not required <br> 4) Specification changes <br> Change in rated input voltage: Not required <br> Change in rated input current: Required <br> Change in ON voltage/ON current: Required <br> Change in OFF voltage/OFF current: Required <br> Change in input resistance: Required <br> 5) Change in functions: Not required |
|  | AX80 | A1SX80 | 1) External wiring change: Required <br> Screw size: M3 $\rightarrow$ M3.5 <br> 2) Change in number of modules: Not required <br> 3) Program changes <br> Change in number of occupied I/O points: Not required <br> 4) Specification changes <br> Change in rated input voltage: Not required <br> Change in rated input current: Required <br> Change in ON voltage/ON current: Required <br> Change in OFF voltage/OFF current: Required <br> Change in input resistance: Required <br> 5) Change in functions: Not required |


| A series model to be discontinued |  | AnS series alternative model |  |
| :---: | :---: | :---: | :---: |
| Product | Model | Model | Remarks (restrictions) |
| Input module | AX80E | A1SX80-S1 | 1) External wiring change: Required <br> Screw size: M3 $\rightarrow$ M3.5 <br> 2) Change in number of modules: Not required <br> 3) Program changes <br> Change in number of occupied I/O points: Not required <br> 4) Specification changes <br> Change in rated input voltage: Required (12VDC cannot be used.) <br> Change in rated input current: Required <br> Change in ON voltage/ON current: Required <br> Change in OFF voltage/OFF current: Required <br> Change in input resistance: Required <br> 5) Change in functions: Not required |
|  | AX81 | A1SX81 | 1) External wiring change: Required (Connector terminal block must be converted.) <br> 2) Change in number of modules: Not required <br> 3) Program changes <br> Change in number of occupied I/O points: Not required <br> 4) Specification changes <br> Change in rated input voltage: Not required <br> Change in rated input current: Required <br> Change in ON voltage/ON current: Required <br> Change in OFF voltage/OFF current: Required <br> Change in input resistance: Required <br> 5) Change in functions: Not required |
|  | AX81B | None | Alternating with the A1SX81 is recommended. <br> 1) External wiring change: Required (Connector terminal block must be converted.) <br> 2) Change in number of modules: Not required <br> 3) Program changes <br> Change in number of occupied I/O points: Required <br> 4) Specification changes <br> Change in rated input voltage: Not required <br> Change in rated input current: Required <br> Change in ON voltage/ON current: Required <br> Change in OFF voltage/OFF current: Required <br> Change in input resistance: Required <br> 5) Functions: The wire breakage detection function is not provided. |
|  | AX81-S1 | A1SX81 | 1) External wiring change: Required (Connector terminal block must be converted.) <br> 2) Change in number of modules: Not required <br> 3) Program changes <br> Change in number of occupied I/O points: Not required <br> 4) Specification changes <br> Change in rated input voltage: Not required <br> Change in rated input current: Required <br> Change in ON voltage/ON current: Required <br> Change in OFF voltage/OFF current: Required <br> Change in input resistance: Required <br> 5) Change in functions: Not required |
|  | AX81-S2 | None | Alternating with the A1SX81 is recommended. <br> 1) External wiring change: Required (Connector terminal block must be converted.) <br> Connect a $3.3 \mathrm{k} \Omega$ ( 1 W or more) or $5.6 \mathrm{k} \Omega$ ( 2 W or more) resistor serially to the external signal wire at 48VDC or 60VDC, respectively. <br> 2) Change in number of modules: Not required <br> 3) Program changes <br> Change in number of occupied I/O points: Not required <br> 4) Specification changes <br> Change in rated input voltage: Required <br> Change in rated input current: Required <br> Change in ON voltage/ON current: Required <br> Change in OFF voltage/OFF current: Required <br> Change in input resistance: Required <br> 5) Change in functions: Not required |


| A series mode | discontinued |  | AnS series alternative model |
| :---: | :---: | :---: | :---: |
| Product | Model | Model | Remarks (restrictions) |
| Input module | AX81-S3 | A1SX80-S1 | 1) External wiring change: Required <br> Screw size: M3 $\rightarrow$ M3.5 <br> 2) Change in number of modules: Required (2 modules necessary) <br> 3) Program changes <br> Change in number of occupied I/O points: Required <br> 4) Specification changes <br> Change in rated input voltage: Required (12VDC cannot be used.) <br> Change in rated input current: Required <br> Change in ON voltage/ON current: Required <br> Change in OFF voltage/OFF current: Required <br> Change in input resistance: Required <br> 5) Change in functions: Not required |
|  | AX82 | A1SX82-S1 | 1) External wiring change: Required ( $D$ sub $\rightarrow F C N$ connector) <br> 2) Change in number of modules: Not required <br> 3) Program changes <br> Change in number of occupied I/O points: Not required <br> 4) Specification changes <br> Change in rated input voltage: Required (12VDC cannot be used.) <br> Change in rated input current: Required <br> Change in ON voltage/ON current: Required <br> Change in OFF voltage/OFF current: Required <br> Change in input resistance: Required <br> 5) Change in functions: Not required |


| A series model to be discontinued |  | AnS series alternative model |  |
| :---: | :---: | :---: | :---: |
| Product | Model | Model | Remarks (restrictions) |
| Output module | AY10 | A1SY10 | 1) External wiring change: Required <br> Screw size: M3 $\rightarrow$ M3.5 <br> 2) Change in number of modules: Not required <br> 3) Program changes <br> Change in number of occupied I/O points: Not required <br> 4) Specification changes <br> Change in rated output voltage: Not required <br> Change in rated output current: Not required (Note that a contact life is half.) <br> 5) Change in functions: Not required |
|  | AY10A | A1SY18A | 1) External wiring change: Required <br> Screw size: M3 $\rightarrow$ M3.5 <br> 2) Change in number of modules: Required (2 modules necessary) <br> 3) Program changes <br> Change in number of occupied I/O points: Required <br> 4) Specification changes <br> Change in rated output voltage: Not required <br> Change in rated output current: Not required <br> 5) Change in functions: Not required |
|  | AY11 | A1SY10 | 1) External wiring change: Required <br> Screw size: M3 $\rightarrow$ M3.5 <br> 2) Change in number of modules: Not required <br> 3) Program changes <br> Change in number of occupied I/O points: Not required <br> 4) Specification changes <br> Change in rated output voltage: Not required <br> Change in rated output current: Not required (Note that a contact life is half.) <br> 5) Change in functions: Required (No varistor, relay not replaceable) |
|  | AY11A | A1SY18A | 1) External wiring change: Required <br> Screw size: M3 $\rightarrow$ M3.5 <br> 2) Change in number of modules: Required (2 modules necessary) <br> 3) Program changes <br> Change in number of occupied I/O points: Required <br> 4) Specification changes <br> Change in rated output voltage: Not required <br> Change in rated output current: Not required <br> 5) Change in functions: Required (No varistor) |
|  | AY11AEU | A1SY18AEU | 1) External wiring change: Required <br> 2) Change in number of modules: Required (2 modules necessary) <br> 3) Program changes <br> Change in number of occupied I/O points: Required <br> 4) Specification changes <br> Change in rated output voltage: Not required <br> Change in rated output current: Not required <br> 5) Change in functions: Required (No varistor) |
|  | AY11E | A1SY10 | 1) External wiring change: Required <br> Screw size: M3 $\rightarrow$ M3.5 <br> 2) Change in number of modules: Not required <br> 3) Program changes <br> Change in number of occupied I/O points: Not required <br> 4) Specification changes <br> Change in rated output voltage: Not required <br> Change in rated output current: Not required (Note that a contact life is half.) <br> 5) Change in functions: Required (No fuse, no varistor) |
|  | AY11EEU | A1SY10EU | 1) External wiring change: Required <br> Screw size: M3 $\rightarrow$ M3.5 <br> 2) Change in number of modules: Not required <br> 3) Program changes <br> Change in number of occupied I/O points: Not required <br> 4) Specification changes <br> Change in rated output voltage: Not required <br> Change in rated output current: Not required (Note that a contact life is half.) <br> 5) Change in functions: Required (No fuse, no varistor) |


| A series model to be discontinued |  | AnS series alternative model |  |
| :---: | :---: | :---: | :---: |
| Product | Model | Model | Remarks (restrictions) |
| Output module | AY11-UL | A1SY10 | 1) External wiring change: Required <br> 2) Change in number of modules: Not required <br> 3) Program changes <br> Change in number of occupied I/O points: Not required <br> 4) Specification changes <br> Change in rated output voltage: Not required <br> Change in rated output current: Not required (Note that a contact life is half.) <br> 5) Change in functions: Required (No varistor) |
|  | AY13 | A1SY10 | 1) External wiring change: Required <br> Screw size: M3 $\rightarrow$ M3.5 <br> 2) Change in number of modules: Required (2 modules necessary) <br> 3) Program changes <br> Change in number of occupied I/O points: Not required ( $32=16 \times 2$ ) <br> 4) Specification changes <br> Change in rated output voltage: Not required <br> Change in rated output current: Not required (Note that a contact life is half.) <br> 5) Change in functions: Not required |
|  | AY13E | A1SY10 | 1) External wiring change: Required <br> Screw size: M3 $\rightarrow$ M3.5 <br> 2) Change in number of modules: Required (2 modules necessary) <br> 3) Program changes <br> Change in number of occupied I/O points: Not required ( $32=16 \times 2$ ) <br> 4) Specification changes <br> Change in rated output voltage: Not required <br> Change in rated output current: Not required (Note that a contact life is half.) <br> 5) Change in functions: Required (No fuse) |
|  | AY13EU | A1SY10EU | 1) External wiring change: Required <br> 2) Change in number of modules: Required (2 modules necessary) <br> 3) Program changes <br> Change in number of occupied I/O points: Not required ( $32=16 \times 2$ ) <br> 4) Specification changes <br> Change in rated output voltage: Not required <br> Change in rated output current: Not required (Note that a contact life is half.) <br> 5) Change in functions: Not required |
|  | AY15EU | A1SY14EU | 1) External wiring change: Required <br> 2) Change in number of modules: Required (2 modules necessary) <br> 3) Program changes <br> Change in number of occupied I/O points: Not required ( $32=16 \times 2$ ) <br> 4) Specification changes <br> Change in rated output voltage: Not required <br> Change in rated output current: Not required (Note that a contact life is half.) <br> 5) Change in functions: Not required |
|  | AY22 | A1SY22 | 1) External wiring change: Required <br> Screw size: M3 $\rightarrow$ M3.5 <br> 2) Change in number of modules: Not required <br> 3) Program changes <br> Change in number of occupied I/O points: Not required <br> 4) Specification changes <br> Change in rated output voltage: Not required <br> Change in rated output current: Required (Output 2A $\rightarrow 0.6 A$ ) <br> 5) Change in functions: Required (No fuse, no varistor) |
|  | AY23 | A1SY22 | 1) External wiring change: Required <br> Screw size: M3 $\rightarrow$ M3.5 <br> 2) Change in number of modules: Required (2 modules necessary) <br> 3) Program changes <br> Change in number of occupied I/O points: Not required (32=16×2) <br> 4) Specification changes <br> Change in rated output voltage: Not required <br> Change in rated output current: Not required <br> 5) Change in functions: Required (No fast-blow fuse) |


| A series model to be discontinued |  | AnS series alternative model |  |
| :---: | :---: | :---: | :---: |
| Product | Model | Model | Remarks (restrictions) |
| Output module | AY40 | A1SY40 | 1) External wiring change: Required <br> Screw size: M3 $\rightarrow$ M3.5 <br> 2) Change in number of modules: Not required <br> 3) Program changes <br> Change in number of occupied I/O points: Not required <br> 4) Specification changes <br> Change in rated output voltage: Not required <br> Change in rated output current: Not required <br> 5) Change in functions: Not required |
|  | AY40A | A1SY68A | 1) External wiring change: Required <br> Screw size: M3 $\rightarrow$ M3.5 <br> 2) Change in number of modules: Required (2 modules necessary) <br> 3) Program changes <br> Change in number of occupied I/O points: Required <br> 4) Specification changes <br> Change in rated output voltage: Not required <br> Change in rated output current: Not required <br> Response: Slow <br> 5) Change in functions: Not required |
|  | AY41 | A1SY41 | 1) External wiring change: Required (Connector terminal block must be converted.) <br> 2) Change in number of modules: Not required <br> 3) Program changes <br> Change in number of occupied I/O points: Not required <br> 4) Specification changes <br> Change in rated output voltage: Not required <br> Change in rated output current: Not required <br> 5) Change in functions: Not required |
|  | AY41-UL | A1SY41 | 1) External wiring change: Required (Connector terminal block must be converted.) <br> 2) Change in number of modules: Not required <br> 3) Program changes <br> Change in number of occupied I/O points: Not required <br> 4) Specification changes <br> Change in rated output voltage: Not required <br> Change in rated output current: Not required <br> 5) Change in functions: Not required |
|  | AY42 | A1SY42P | 1) External wiring change: Not required <br> 2) Change in number of modules: Not required <br> 3) Program changes <br> Change in number of occupied I/O points: Not required <br> 4) Specification changes <br> Change in rated output voltage: Not required <br> Change in rated output current: Not required <br> 5) Change in functions: Not required |
|  | AY42-S1 | A1SY42P | 1) External wiring change: Not required <br> 2) Change in number of modules: Not required <br> 3) Program changes <br> Change in number of occupied I/O points: Not required <br> 4) Specification changes <br> Change in rated output voltage: Not required <br> Change in rated output current: Not required <br> Response time: Required ( 0.3 ms or less $\rightarrow 1 \mathrm{~ms}$ or less) <br> 5) Change in functions: Not required |
|  | AY42-S3 | A1SY42P | 1) External wiring change: Not required <br> 2) Change in number of modules: Not required <br> 3) Program changes <br> Change in number of occupied I/O points: Not required <br> 4) Specification changes <br> Change in rated output voltage: Not required <br> Change in rated output current: Not required <br> 5) Change in functions: Required (The short protection function is equivalent to that of a fuse.) |

I/O MODULE REPLACEMENT

| A series model to be discontinued |  | AnS series alternative model |  |
| :---: | :---: | :---: | :---: |
| Product | Model | Model | Remarks (restrictions) |
| Output module | AY42-S4 | A1SY42P | 1) External wiring change: Required (External supply power is required.) <br> 2) Change in number of modules: Not required <br> 3) Program changes <br> Change in number of occupied I/O points: Not required <br> 4) Specification changes <br> Change in rated output voltage: Not required <br> Change in rated output current: Not required <br> 5) Change in functions: Not required |
|  | AY50 | A1SY50 | 1) External wiring change: Required <br> Screw size: M3 $\rightarrow$ M3.5 <br> 2) Change in number of modules: Not required <br> 3) Program changes <br> Change in number of occupied I/O points: Not required <br> 4) Specification changes <br> Change in rated output voltage: Not required <br> Change in rated output current: Not required <br> 5) Change in functions: Required (Fuse not replaceable) |
|  | AY51 | A1SY50 | 1) External wiring change: Required <br> Screw size: M3 $\rightarrow$ M3.5 <br> 2) Change in number of modules: Required (2 modules necessary) <br> 3) Program changes <br> Change in number of occupied I/O points: Not required (32=16×2) <br> 4) Specification changes <br> Change in rated output voltage: Not required <br> Change in rated output current: Not required <br> 5) Change in functions: Not required |
|  | AY51-S1 | A1SY50 | 1) External wiring change: Required <br> Screw size: M3 $\rightarrow$ M3.5 <br> 2) Change in number of modules: Required (2 modules necessary) <br> 3) Program changes <br> Change in number of occupied I/O points: Not required ( $32=16 \times 2$ ) <br> 4) Specification changes <br> Change in rated output voltage: Not required <br> Change in rated output current: Not required <br> 5) Change in functions: Required (Fuse not replaceable) |
|  | AY51-UL | A1SY50 | 1) External wiring change: Required <br> 2) Change in number of modules: Required (2 modules necessary) <br> 3) Program changes <br> Change in number of occupied I/O points: Not required ( $32=16 \times 2$ ) <br> 4) Specification changes <br> Change in rated output voltage: Not required <br> Change in rated output current: Not required <br> 5) Change in functions: Not required |
|  | AY60 | A1SY60 | 1) External wiring change: Required <br> Screw size: M3 $\rightarrow$ M3.5 <br> 2) Change in number of modules: Not required <br> 3) Program changes <br> Change in number of occupied I/O points: Required <br> 4) Specification changes <br> Change in rated input voltage: Required (48VDC cannot be used) <br> Change in rated output current: Not required <br> 5) Change in functions: Not required |
|  | AY60E | A1SY60E | 1) External wiring change: Required <br> Screw size: M3 $\rightarrow$ M3.5 <br> 2) Change in number of modules: Not required <br> 3) Program changes <br> Change in number of occupied I/O points: Required <br> 4) Specification changes <br> Change in rated input voltage: Required (48VDC cannot be used) <br> Change in rated output current: Not required <br> 5) Change in functions: Not required |


| A series model to be discontinued |  | AnS series alternative model |  |
| :---: | :---: | :---: | :---: |
| Product | Model | Model | Remarks (restrictions) |
| Output module | AY60S | A1SY60 | 1) External wiring change: Required <br> Screw size: M3 $\rightarrow$ M3.5 <br> 2) Change in number of modules: Not required <br> 3) Program changes <br> Change in number of occupied I/O points: Required <br> 4) Specification changes <br> Change in rated input voltage: Required (48VDC not applicable) <br> Change in rated output current: Not required <br> 5) Change in functions: Not required |
|  | AY70 | A1SY71 | 1) External wiring change: Required (Connector terminal block must be converted.) <br> 2) Change in number of modules: Not required <br> 3) Program changes <br> Change in number of occupied I/O points: Required <br> 4) Specification changes <br> Change in rated output voltage: Not required <br> Change in rated output current: Not required <br> 5) Change in functions: Not required |
|  | AY71 | A1SY71 | 1) External wiring change: Required (Connector terminal block must be converted.) <br> 2) Change in number of modules: Not required <br> 3) Program changes <br> Change in number of occupied I/O points: Not required <br> 4) Specification changes <br> Change in rated output voltage: Not required <br> Change in rated output current: Not required <br> 5) Change in functions: Not required |
|  | AY72 | A1SY71 | 1) External wiring change: Not required <br> 2) Change in number of modules: Required (2 modules necessary) <br> 3) Program changes <br> Change in number of occupied I/O points: Not required (64=32×2) <br> 4) Specification changes <br> Change in rated output voltage: Not required <br> Change in rated output current: Not required <br> 5) Change in functions: Not required |
|  | AY80 | A1SY80 | 1) External wiring change: Required <br> Screw size: M3 $\rightarrow$ M3.5 <br> 2) Change in number of modules: Not required <br> 3) Program changes <br> Change in number of occupied I/O points: Not required <br> 4) Specification changes <br> Change in rated output voltage: Not required <br> Change in rated output current: Not required <br> 5) Change in functions: Required (Fuse not replaceable) |
|  | AY81 | A1SY81 | 1) External wiring change: Required (Connector terminal block must be converted.) <br> 2) Change in number of modules: Not required <br> 3) Program changes <br> Change in number of occupied I/O points: Not required <br> 4) Specification changes <br> Change in rated output voltage: Not required <br> Change in rated output current: Required (Output $0.5 \mathrm{~A} \rightarrow 0.1 \mathrm{~A}$ ) <br> 5) Change in functions: Not required |
|  | AY82-EP | A1SY82 | 1) External wiring change: Required (D sub $\rightarrow$ FCN connector) <br> 2) Change in number of modules: Not required <br> 3) Program changes <br> Change in number of occupied I/O points: Not required <br> 4) Specification changes <br> Change in rated output voltage: Not required <br> Change in rated output current: Not required <br> 5) Change in functions: Not required |


| A series model to be discontinued |  | AnS series alternative model |  |
| :---: | :---: | :---: | :---: |
| Product | Model | Model | Remarks (restrictions) |
| I/O module | AH42 | A1SH42 | 1) External wiring change: Not required <br> 2) Change in number of modules: Not required <br> 3) Program changes <br> Change in rated input voltage: Required (32 points occupied) <br> 4) Specification changes <br> Change in rated input voltage: Required (12VDC cannot be used.) <br> Change in rated input current: Required <br> Change in ON voltage/ON current: Required <br> Change in OFF voltage/OFF current: Required <br> Change in input resistance: Required <br> 5) Change in functions: Not required |
| Dynamic scan I/O module | A42XY | $\begin{aligned} & \text { A1S42X } \\ & \text { A1S42Y } \end{aligned}$ | 1) External wiring change: Required <br> 2) Change in number of modules: Required (Input and output modules are separately required.) <br> 3) Program changes <br> Change in rated input voltage: Required (32 points occupied) <br> 4) Specification changes <br> Change in rated input voltage: Required (12VDC cannot be used.) <br> Change in rated input current: Required <br> Change in ON voltage/ON current: Required <br> Change in OFF voltage/OFF current: Required <br> Change in input resistance: Required <br> 5) Change in functions: Not required |
|  | Al61 | A1SI61 | 1) External wiring change: Required <br> Screw size: M3 $\rightarrow$ M3.5 <br> 2) Change in number of modules: Not required <br> 3) Program changes <br> Change in number of occupied I/O points: Not required <br> 4) Specification changes <br> Change in rated input voltage: Not required <br> Change in rated input current: Required <br> Change in ON voltage/ON current: Required <br> Change in OFF voltage/OFF current: Required <br> Change in input resistance: Required <br> 5) Change in functions: Required (The interrupt processing is set in 4-point units.) |
| Interrupt module | Al61-S1 | A1SI61 | 1) External wiring change: Required <br> Screw size: M3 $\rightarrow$ M3.5 <br> 2) Change in number of modules: Not required <br> 3) Program changes <br> Change in rated input voltage: Required (16 points occupied) <br> 4) Specification changes <br> Change in rated input voltage: Not required <br> Change in rated input current: Required <br> Change in ON voltage/ON current: Required <br> Change in OFF voltage/OFF current: Required <br> Change in input resistance: Required <br> 5) Change in functions: Required (The interrupt processing is set in 4-point units.) <br> 6) Others: The response time is different. |

### 3.2 Specifications Comparisons between I/O Modules

### 3.2.1 Specifications comparisons between input modules

(1) Specifications comparisons between the AX10 and the A1SX10

| Specification |  | AX10 | A1SX10 | $\left\lvert\, \begin{gathered} \text { Compati- } \\ \text { bility } \end{gathered}\right.$ | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Number of input points |  | 16 points | 16 points | $\bigcirc$ |  |
| Isolation method |  | Photocoupler | Photocoupler | $\bigcirc$ |  |
| Rated input voltage |  | 100-120VAC $50 / 60 \mathrm{~Hz}$ | 100-120VAC $50 / 60 \mathrm{~Hz}$ | $\bigcirc$ |  |
| Input voltage distortion |  | Within 5\% | Within 5\% | $\bigcirc$ |  |
| Rated input current |  | $\begin{gathered} 10 \mathrm{~mA} \\ (100 \mathrm{VAC}, 60 \mathrm{~Hz}) \end{gathered}$ | Approx. 6mA (100VAC, 60 Hz ) | $\Delta$ | Rated input current is smaller. ${ }^{* 1}$ |
| Inrush current |  | Max. 300 mA within 0.3 ms (At 132VAC) | Max. 200mA within 1 ms (At 132VAC) | $\bigcirc$ |  |
| Operating voltage range |  | $\begin{gathered} 85 \text { to } 132 \mathrm{VAC} \\ (50 / 60 \mathrm{~Hz} \pm 5 \%) \end{gathered}$ | $\begin{gathered} 85 \text { to } 132 \mathrm{VAC} \\ (50 / 60 \mathrm{~Hz} \pm 5 \%) \\ \hline \end{gathered}$ | $\bigcirc$ |  |
| Maximum simultaneous input points |  | 100\% (16 points) simultaneously <br> ON | Refer to the derating chart. *2 | $\Delta$ | Use within the range shown in the derating chart. |
| ON voltage/ON current |  | 80VAC or more/6mA or more | 80 VAC or more/5mA or more | $\bigcirc$ |  |
| OFF voltage/OFF current |  | 40VAC or less/4mA or less | 30 VAC or less/1.4mA or less | $\triangle$ | OFF current is smaller. ${ }^{*}$ |
| Input resistance |  | Approx. $10 \mathrm{k} \Omega(60 \mathrm{~Hz})$ <br> Approx. $12 \mathrm{k} \Omega(50 \mathrm{~Hz})$ | Approx. $18 \mathrm{k} \Omega(60 \mathrm{~Hz})$ <br> Approx. $21 \mathrm{k} \Omega(50 \mathrm{~Hz})$ | $\triangle$ | Input resistance is greater. ${ }^{* 1}$ |
| Response time | $\mathrm{OFF} \rightarrow \mathrm{ON}$ | 15 ms or less | $\begin{gathered} 20 \mathrm{~ms} \text { or less } \\ (100 \mathrm{VAC}, 60 \mathrm{~Hz}) \\ \hline \end{gathered}$ | $\triangle$ | The response times differ. |
|  | $\mathrm{ON} \rightarrow$ OFF | 25 ms or less | $\begin{gathered} 35 \mathrm{~ms} \text { or less } \\ (100 \mathrm{VAC}, 60 \mathrm{~Hz}) \end{gathered}$ | $\Delta$ |  |
| Common terminal arrangement |  | 16 points/common (common terminal: TB9,TB18) | 16 points/common (common terminal: TB9,TB18) | $\bigcirc$ |  |
| Operation indicator |  | ON indication (LED) | ON indication (LED) | $\bigcirc$ |  |
| External connection |  | 20-point terminal block connector (M3×6 screws) | 20-point terminal block connector (M3.5×7 screws) | $\times$ | Wiring must be changed. |
| Applicable wire size |  | 0.75 to $2 \mathrm{~mm}^{2}$ | 0.75 to $1.25 \mathrm{~mm}^{2}$ | $\times$ |  |
| Applicable solderless terminal |  | R1.25-3, R2-3, RAV1.25-3, RAV2-3 | R1.25-3.5, R2-3.5, RAV1.25-3.5, RAV2-3.5 | $\times$ |  |
| Current consumption |  | $\begin{gathered} 0.055 \mathrm{~A} \\ \text { (TYP. all points ON) } \end{gathered}$ | 0.050 A (TYP. all points ON) | $\bigcirc$ |  |
| External dimensions |  | $250(\mathrm{H}) \times 37.5(\mathrm{~W}) \times 121(\mathrm{D}) \mathrm{mm}$ | $130(\mathrm{H}) \times 34.5(\mathrm{~W}) \times 93.6(\mathrm{D}) \mathrm{mm}$ | $\triangle$ | The dimensions are different. |
| Weight |  | 0.39 kg | 0.21 kg | $\bigcirc$ |  |

*1 Check the specifications of sensor or switch to connect to the A1SX10.
*2 The figure on the right shows derating.


## (2) Specifications comparisons between the AX11 and the A1SX10

| Specification |  | AX11 | A1SX10 | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Number of input points |  | 32 points | 16 points | $\times$ | when seventeen or more points are used, use two of the A1SX10 modules. |
| Isolation method |  | Photocoupler | Photocoupler | $\bigcirc$ |  |
| Rated input voltage |  | 100-120VAC $50 / 60 \mathrm{~Hz}$ | 100-120VAC $50 / 60 \mathrm{~Hz}$ | $\bigcirc$ |  |
| Input voltage distortion |  | Within 5\% | Within 5\% | $\bigcirc$ |  |
| Rated input current |  | $\begin{gathered} 10 \mathrm{~mA} \\ (100 \mathrm{VAC}, 60 \mathrm{~Hz}) \end{gathered}$ | $\begin{gathered} \text { Approx. } 6 \mathrm{~mA} \\ (100 \mathrm{VAC}, 60 \mathrm{~Hz}) \end{gathered}$ | $\triangle$ | Rated input current is smaller. ${ }^{* 1}$ |
| Inrush current |  | Max. 300 mA within 0.3 ms (At 132VAC) | Max. 200mA within 1 ms (At 132VAC) | $\bigcirc$ |  |
| Operating voltage range |  | $\begin{gathered} \hline 85 \text { to } 132 \mathrm{VAC} \\ (50 / 60 \mathrm{~Hz} \pm 5 \%) \\ \hline \end{gathered}$ | $\begin{gathered} 85 \text { to } 132 \mathrm{VAC} \\ (50 / 60 \mathrm{~Hz} \pm 5 \%) \end{gathered}$ | $\bigcirc$ |  |
| Maximum simultaneous input points |  | 60\% (20 points) simultaneously ON | Refer to the derating chart. *2 | $\bigcirc$ |  |
| ON voltage/ON current |  | 80VAC or more/6mA or more | 80VAC or more/5mA or more | $\bigcirc$ |  |
| OFF voltage/OFF current |  | 40 VAC or less/4mA or less | 30 VAC or less/1.4mA or less | $\triangle$ | OFF current is smaller.* ${ }^{*}$ |
| Input resistance |  | Approx. 10k $\Omega(60 \mathrm{~Hz})$ <br> Approx. $12 \mathrm{k} \Omega(50 \mathrm{~Hz})$ | Approx. $18 \mathrm{k} \Omega(60 \mathrm{~Hz})$ <br> Approx. $21 \mathrm{k} \Omega(50 \mathrm{~Hz})$ | $\triangle$ | Input resistance is greater. ${ }^{* 1}$ |
| Response time | $\mathrm{OFF} \rightarrow \mathrm{ON}$ | 15 ms or less | $\begin{gathered} 20 \mathrm{~ms} \text { or less } \\ (100 \mathrm{VAC}, 60 \mathrm{~Hz}) \end{gathered}$ | $\triangle$ | The response times differ. |
|  | $\mathrm{ON} \rightarrow$ OFF | 25 ms or less | $\begin{gathered} 35 \mathrm{~ms} \text { or less } \\ (100 \mathrm{VAC}, 60 \mathrm{~Hz}) \end{gathered}$ | $\Delta$ |  |
| Common terminal arrangement |  | 32 points/common (common terminal: TB9,TB18,TB27,TB36) | 16 points/common (common terminal: TB9,TB18) | $\bigcirc$ |  |
| Operation indicator |  | ON indication (LED) | ON indication (LED) | $\bigcirc$ |  |
| External connection |  | 38-point terminal block connector (M3×6 screws) | 20-point terminal block connector (M3.5×7 screws) | $\times$ | Wiring must be changed. |
| Applicable wire size |  | 0.75 to $2 \mathrm{~mm}^{2}$ | 0.75 to $1.25 \mathrm{~mm}^{2}$ | $\times$ |  |
| Applicable solderless terminal |  | R1.25-3, R2-3, RAV1.25-3, RAV2-3 | R1.25-3.5, R2-3.5, RAV1.25-3.5, RAV2-3.5 | $\times$ |  |
| Current consumption |  | 0.11A <br> (TYP. all points ON) | 0.05A <br> (TYP. all points ON) | $\bigcirc$ |  |
| External dimensions |  | $250(\mathrm{H}) \times 37.5(\mathrm{~W}) \times 131(\mathrm{D}) \mathrm{mm}$ | $130(\mathrm{H}) \times 34.5(\mathrm{~W}) \times 93.6(\mathrm{D}) \mathrm{mm}$ | $\Delta$ | The dimensions are different. |
| Weight |  | 0.49 kg | 0.21 kg | $\bigcirc$ |  |

*1 Check the specifications of sensor or switch to connect to the A1SX10.
*2 The figure on the right shows derating.

(3) Specifications comparisons between the AX11EU and the A1SX10EU

| Specification |  | AX11EU | A1SX10EU | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Number of input points |  | 32 points | 16 points | $\times$ | when seventeen or more points are used, use two of the A1SX10EU modules. |
| Isolation method |  | Photocoupler | Photocoupler | $\bigcirc$ |  |
| Rated input voltage |  | 100-120VAC $50 / 60 \mathrm{~Hz}$ | 100-120VAC 50/60Hz | $\bigcirc$ |  |
| Input voltage distortion |  | Within 5\% | Within 5\% | $\bigcirc$ |  |
| Rated input current |  | Approx. 12mA (120VAC, 60 Hz ) | $\begin{gathered} \text { Approx. } 7 \mathrm{~mA} \\ (120 \mathrm{VAC}, 60 \mathrm{~Hz}) \end{gathered}$ | $\triangle$ | Rated input current is smaller. ${ }^{* 1}$ |
| Inrush current |  | Max. 300mA within 1 ms (At 132VAC) | Max. 200mA within 1 ms (At 132VAC) | $\bigcirc$ |  |
| Operating voltage range |  | $\begin{gathered} 85 \text { to } 132 \mathrm{VAC} \\ (50 / 60 \mathrm{~Hz} \pm 5 \%) \end{gathered}$ | $\begin{gathered} 85 \text { to 132VAC } \\ (50 / 60 \mathrm{~Hz} \pm 5 \%) \end{gathered}$ | $\bigcirc$ |  |
| Maximum simultaneous input points |  | 60\% (20 points) simultaneously ON | 100\% simultaneously ON | $\bigcirc$ |  |
| ON voltage/ON current |  | 79VAC or more/6mA or more | 80VAC or more/5mA or more | $\bigcirc$ |  |
| OFF voltage/OFF current |  | 40VAC or less/4mA or less | 30 VAC or less/1.4mA or less | $\triangle$ | OFF current is smaller. ${ }^{*}$ |
| Input resistance |  | Approx. $10 \mathrm{k} \Omega(60 \mathrm{~Hz})$ <br> Approx. $12 \mathrm{k} \Omega(50 \mathrm{~Hz})$ | Approx. $18 \mathrm{k} \Omega(60 \mathrm{~Hz})$ <br> Approx. $21 \mathrm{k} \Omega(50 \mathrm{~Hz})$ | $\triangle$ | Input resistance is greater. ${ }^{*}$ |
| Response time | $\mathrm{OFF} \rightarrow \mathrm{ON}$ | $\begin{gathered} 15 \mathrm{~ms} \text { or less } \\ (100 \mathrm{VAC}, 60 \mathrm{~Hz}) \end{gathered}$ | $\begin{aligned} & 20 \mathrm{~ms} \text { or less } \\ & (100 \mathrm{VAC}, 60 \mathrm{~Hz}) \end{aligned}$ | $\triangle$ | The response times differ. |
|  | $\mathrm{ON} \rightarrow \mathrm{OFF}$ | $\begin{gathered} 25 \mathrm{~ms} \text { or less } \\ (100 \mathrm{VAC}, 60 \mathrm{~Hz}) \end{gathered}$ | $\begin{gathered} 35 \mathrm{~ms} \text { or less } \\ (100 \mathrm{VAC}, 60 \mathrm{~Hz}) \\ \hline \end{gathered}$ | $\triangle$ |  |
| Common terminal arrangement |  | 32 points/common (common terminal: TB9,TB18,TB27,TB36) | 16 points/common (common terminal: TB9,TB18) | $\bigcirc$ |  |
| Operation indicator |  | ON indication (LED) | ON indication (LED) | $\bigcirc$ |  |
| External connection |  | 38-point terminal block connector (M3.5×6 screws) | 20-point terminal block connector (M3.5×7 screws) | $\times$ | Wiring must be changed. |
| Applicable wire size |  | 0.75 to $2 \mathrm{~mm}^{2}$ <br> (AWG14 to AWG19) | 0.75 to $1.25 \mathrm{~mm}^{2}$ <br> (AWG16 to AWG19) | $\times$ |  |
| Applicable solderless terminal |  | RAV1.25-3.5,RAV2-3.5 | RAV1.25-3.5 | $\times$ |  |
| Current consumption |  | $0.15 \mathrm{~A}$ <br> (TYP. all points ON) | 0.05 A (TYP. all points ON) | $\bigcirc$ |  |
| Dielectric withstand voltage (Across external circuit and internal circuit) |  | 1780VAC rms/3cycles (altitude 2000m) | 1780VAC rms/3cycles (altitude 2000m) | $\bigcirc$ |  |
| Insulation resistance |  | $10 \mathrm{M} \Omega$ or more by insulation resistance tester | $10 \mathrm{M} \Omega$ or more by insulation resistance tester | $\bigcirc$ |  |
| External dimensions |  | $250(\mathrm{H}) \times 37.5(\mathrm{~W}) \times 131(\mathrm{D}) \mathrm{mm}$ | $130(\mathrm{H}) \times 34.5(\mathrm{~W}) \times 93.6(\mathrm{D}) \mathrm{mm}$ | $\triangle$ | The dimensions are different. |
| Weight |  | 0.50 kg | 0.21 kg | $\bigcirc$ |  |

[^2]
## (4) Specifications comparisons between the AX20 and the A1SX20

| Specification |  | AX20 | A1SX20 | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Number of input points |  | 16 points | 16 points | $\bigcirc$ |  |
| Isolation method |  | Photocoupler | Photocoupler | $\bigcirc$ |  |
| Rated input voltage |  | 200-240VAC $50 / 60 \mathrm{~Hz}$ | 200-240VAC $50 / 60 \mathrm{~Hz}$ | $\bigcirc$ |  |
| Input voltage distortion |  | Within 5\% | Within 5\% | $\bigcirc$ |  |
| Rated input current |  | $\begin{gathered} 10 \mathrm{~mA} \\ (200 \mathrm{VAC}, 60 \mathrm{~Hz}) \end{gathered}$ | Approx. 9mA (200VAC, 60Hz) | $\triangle$ | Rated input current is smaller. ${ }^{*}$ |
| Inrush current |  | Max. 600 mA within 0.12 ms <br> (At 264VAC) | Max. 500 mA within 1 ms (At 264VAC) | $\bigcirc$ |  |
| Operating voltage range |  | $\begin{aligned} & 170 \text { to } 264 \mathrm{VAC} \\ & (50 / 60 \mathrm{~Hz} \pm 5 \%) \end{aligned}$ | $\begin{aligned} & 170 \text { to } 264 \mathrm{VAC} \\ & (50 / 60 \mathrm{~Hz} \pm 5 \%) \end{aligned}$ | $\bigcirc$ |  |
| Maximum simultaneous input points |  | 100\% (16 points) simultaneously ON | Refer to the derating chart. *2 | $\triangle$ | Use within the range shown in the derating chart. |
| ON voltage/ON current |  | 160VAC or more/5.5mA or more | 80 VAC or more/4mA or more | $\bigcirc$ |  |
| OFF voltage/OFF current |  | 70VAC or less/3.5mA or less | 30 VAC or less/1mA or less | $\triangle$ | OFF current is smaller. ${ }^{*}$ |
| Input resistance |  | Approx. $22 \mathrm{k} \Omega(60 \mathrm{~Hz})$ <br> Approx. $24 \mathrm{k} \Omega(50 \mathrm{~Hz})$ | Approx. 22k $\Omega(60 \mathrm{~Hz})$ <br> Approx. 27k $\Omega(50 \mathrm{~Hz})$ | $\bigcirc$ |  |
| Response time | $\mathrm{OFF} \rightarrow \mathrm{ON}$ | 15 ms or less | 30 ms or less (200VAC, 60Hz) | $\triangle$ | The response times differ. |
|  | $\mathrm{ON} \rightarrow \mathrm{OFF}$ | 25 ms or less | 55 ms or less (200VAC, 60Hz) | $\triangle$ |  |
| Common terminal arrangement |  | 16 points/common (common terminal: TB9,TB18) | 16 points/common (common terminal: TB9,TB18) | $\bigcirc$ |  |
| Operation indicator |  | ON indication (LED) | ON indication (LED) | $\bigcirc$ |  |
| External connection |  | 20-point terminal block connector (M3×6 screws) | 20-point terminal block connector (M3.5×7 screws) | $\times$ | Wiring must be changed. |
| Applicable wire size |  | 0.75 to $2 \mathrm{~mm}^{2}$ | 0.75 to $1.25 \mathrm{~mm}^{2}$ | $\times$ |  |
| Applicable solderless terminal |  | R1.25-3, R2-3, RAV1.25-3, RAV2-3 | R1.25-3.5, R2-3.5, RAV1.25-3.5, RAV2-3.5 | $\times$ |  |
| Current consumption |  | 0.055 A (TYP. all points ON) | 0.050 A (TYP. all points ON) | $\bigcirc$ |  |
| External dimensions |  | $250(\mathrm{H}) \times 37.5(\mathrm{~W}) \times 121(\mathrm{D}) \mathrm{mm}$ | $130(\mathrm{H}) \times 34.5(\mathrm{~W}) \times 93.6(\mathrm{D}) \mathrm{mm}$ | $\triangle$ | The dimensions are different. |
| Weight |  | 0.38 kg | 0.23 kg | $\bigcirc$ |  |

*1 Check the specifications of sensor or switch to connect to the A1SX20.
*2 The figure on the right shows derating.

(5) Specifications comparisons between the AX21 and the A1SX20

| Specification |  | $\bigcirc$ : Compatible, $\triangle$ : Partial change required, $\times$ : Incompatible |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | AX21 | A1SX20 | $\begin{array}{\|c\|} \text { Compati- } \\ \text { bility } \end{array}$ | Precautions for replacement |
| Number of input points |  | 32 points | 16 points | $\times$ | When seventeen or more points are used, use two of the A1SX20 modules. |
| Isolation method |  | Photocoupler | Photocoupler | $\bigcirc$ |  |
| Rated input voltage |  | 200-240VAC $50 / 60 \mathrm{~Hz}$ | 200-240VAC $50 / 60 \mathrm{~Hz}$ | $\bigcirc$ |  |
| Input voltage distortion |  | Within 5\% | Within 5\% | $\bigcirc$ |  |
| Rated input current |  | 10 mA (220VAC, 60Hz) | $\begin{gathered} \text { Approx. } 9 \mathrm{~mA} \\ (200 \mathrm{VAC}, 60 \mathrm{~Hz}) \\ \hline \end{gathered}$ | $\triangle$ | Rated input current is smaller. ${ }^{*}$ |
| Inrush current |  | Max. 600 mA within 0.12 ms (At 264VAC) | Max. 500mA within 1 ms (At 264VAC) | $\bigcirc$ |  |
| Operating voltage range |  | 170 to 264VAC (50/60Hz $\pm 5 \%$ ) | 170 to 264VAC $(50 / 60 \mathrm{~Hz} \pm 5 \%)$ | $\bigcirc$ |  |
| Maximum simultaneous input points |  | 60\% (20 points) simultaneously ON | Refer to the derating chart. *2 | $\Delta$ | Use within the range shown in the derating chart. |
| ON voltage/ON current |  | 160VAC or more/5.5mA or more | 80VAC or more/4mA or more | $\bigcirc$ |  |
| OFF voltage/OFF current |  | 70VAC or less/3.5mA or less | 30 VAC or less/1mA or less | $\triangle$ | OFF current is smaller.*1 |
| Input resistance |  | Approx. 22k $\Omega(60 \mathrm{~Hz})$ <br> Approx. $24 \mathrm{k} \Omega(50 \mathrm{~Hz})$ | Approx. $22 \mathrm{k} \Omega(60 \mathrm{~Hz})$ <br> Approx. $27 \mathrm{k} \Omega(50 \mathrm{~Hz})$ | $\bigcirc$ |  |
| Response time | $\mathrm{OFF} \rightarrow \mathrm{ON}$ | 15 ms or less | 30 ms or less (200VAC, 60 Hz ) | $\triangle$ | The response times differ. |
|  | ON $\rightarrow$ OFF | 25 ms or less | $\begin{gathered} 55 \mathrm{~ms} \text { or less } \\ (200 \mathrm{VAC}, 60 \mathrm{~Hz}) \end{gathered}$ | $\triangle$ |  |
| Common terminal arrangement |  | 32 points/common (common terminal: TB9,TB18,TB27,TB36) | 16 points/common (common terminal: TB9,TB18) | $\bigcirc$ |  |
| Operation indicator |  | ON indication (LED) | ON indication (LED) | $\bigcirc$ |  |
| External connection |  | 38-point terminal block connector (M3×6 screws) | 20-point terminal block connector (M3.5×7 screws) | $\times$ | Wiring must be changed. |
| Applicable wire size |  | 0.75 to $2 \mathrm{~mm}^{2}$ | 0.75 to $1.25 \mathrm{~mm}^{2}$ | $\times$ |  |
| Applicable solderless terminal |  | R1.25-3, R2-3, RAV1.25-3, RAV2-3 | R1.25-3.5, R2-3.5, RAV1.25-3.5, RAV2-3.5 | $\times$ |  |
| Current consumption |  | $0.11 \mathrm{~A}$ <br> (TYP. all points ON) | $0.05 \mathrm{~A}$ <br> (TYP. all points ON) | $\bigcirc$ |  |
| External dimensions |  | $250(\mathrm{H}) \times 37.5(\mathrm{~W}) \times 131(\mathrm{D}) \mathrm{mm}$ | $130(\mathrm{H}) \times 34.5(\mathrm{~W}) \times 93.6(\mathrm{D}) \mathrm{mm}$ | $\Delta$ | The dimensions are different. |
| Weight |  | 0.50 kg | 0.23 kg | $\bigcirc$ |  |

*1 Check the specifications of sensor or switch to connect to the A1SX20.
*2 The figure on the right shows derating.

## (6) Specifications comparisons between the AX21EU and the A1SX20EU

| Specification |  | AX21EU | A1SX20EU | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Number of input points |  | 32 points | 16 points | $\times$ | When seventeen or more points are used, use two of the A1SX20EU modules. |
| Isolation method |  | Photocoupler | Photocoupler | $\bigcirc$ |  |
| Rated input voltage |  | 200-240VAC $50 / 60 \mathrm{~Hz}$ | 200-240VAC $50 / 60 \mathrm{~Hz}$ | $\bigcirc$ |  |
| Input voltage distortion |  | Within 5\% | Within 5\% | $\bigcirc$ |  |
| Rated input current |  | Approx. 12mA (240VAC, 60Hz) | Approx. 11mA (240VAC, 60 Hz ) | $\triangle$ | Rated input current is smaller. ${ }^{*}$ |
| Inrush current |  | Max. 600 mA within 0.5 ms (At 264VAC) | Max. 500 mA within 1 ms (At 264VAC) | $\bigcirc$ |  |
| Operating voltage range |  | 170 to 264VAC $(50 / 60 \mathrm{~Hz} \pm 5 \%)$ | 170 to 264VAC (50/60Hz $\pm 5 \%)$ | $\bigcirc$ |  |
| Maximum simultaneous input points |  | 60\% (20 points) simultaneously ON | Refer to the derating chart. *2 | $\bigcirc$ |  |
| ON voltage/ON current |  | 160VAC or more/5.5mA or more | 80VAC or more/4mA or more | $\bigcirc$ |  |
| OFF voltage/OFF current |  | 70VAC or less/3.5mA or less | 30 VAC or less/1mA or less | $\triangle$ | OFF current is smaller. ${ }^{* 1}$ |
| Input resistance |  | Approx. $22 \mathrm{k} \Omega(60 \mathrm{~Hz})$ <br> Approx. $24 \mathrm{k} \Omega$ ( 50 Hz ) | Approx. $22 \mathrm{k} \Omega(60 \mathrm{~Hz})$ <br> Approx. 27k $\Omega(50 \mathrm{~Hz})$ | $\bigcirc$ |  |
| Response time | $\mathrm{OFF} \rightarrow \mathrm{ON}$ | $\begin{gathered} 15 \mathrm{~ms} \text { or less } \\ (200 \mathrm{VAC}, 60 \mathrm{~Hz}) \end{gathered}$ | $\begin{gathered} 30 \mathrm{~ms} \text { or less } \\ (200 \mathrm{VAC}, 60 \mathrm{~Hz}) \\ \hline \end{gathered}$ | $\triangle$ | The response times differ. |
|  | ON $\rightarrow$ OFF | 25 ms or less (200VAC, 60Hz) | 55ms or less (200VAC, 60Hz) | $\triangle$ |  |
| Common terminal arrangement |  | 32 points/common (common terminal: TB9,TB18,TB27,TB36) | 16 points/common (common terminal: TB9,TB18) | $\bigcirc$ |  |
| Operation indicator |  | ON indication (LED) | ON indication (LED) | $\bigcirc$ |  |
| External connection |  | 38-point terminal block connector (M3.5×6 screws) | 20-point terminal block connector (M3.5×7 screws) | $\times$ | Wiring must be changed. |
| Applicable wire size |  | 0.75 to $2 \mathrm{~mm}^{2}$ <br> (AWG14 to AWG19) | 0.75 to $1.25 \mathrm{~mm}^{2}$ <br> (AWG16 to AWG19) | $\times$ |  |
| Applicable solderless terminal |  | RAV1.25-3.5,RAV2-3.5 | RAV1.25-3.5 | $\times$ |  |
| Current consumption |  | $0.15 \mathrm{~A}$ <br> (TYP. all points ON) | 0.05 A (TYP. all points ON) | $\bigcirc$ |  |
| External dimensions |  | $250(\mathrm{H}) \times 37.5(\mathrm{~W}) \times 131(\mathrm{D}) \mathrm{mm}$ | $130(\mathrm{H}) \times 34.5(\mathrm{~W}) \times 93.6(\mathrm{D}) \mathrm{mm}$ | $\triangle$ | The dimensions are different. |
| Weight |  | 0.50 kg | 0.23 kg | $\bigcirc$ |  |

[^3]
(7) Specifications comparisons between the AX31 and the A1SX30

| Specification |  | AX31 | A1SX30 | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Number of input points |  | 32 points | 16 points | $\times$ | When seventeen or more points are used, use two of the A1SX30 modules. |
| Isolation method |  | Photocoupler | Photocoupler | $\bigcirc$ |  |
| Rated input voltage |  | $\begin{gathered} 12 / 24 \mathrm{VDC} \\ 12 / 24 \mathrm{VAC}(50 / 60 \mathrm{~Hz}) \end{gathered}$ | $\begin{gathered} 12 / 24 \mathrm{VDC} \\ 12 / 24 \mathrm{VAC}(50 / 60 \mathrm{~Hz}) \end{gathered}$ | $\bigcirc$ |  |
| Rated input current |  | 8.5 mA $(24 \mathrm{VDC} / \mathrm{AC})$ 4.0 mA $(12 \mathrm{VDC} / \mathrm{AC})$ | 8.5 mA $(24 \mathrm{VDC} / \mathrm{AC})$ 4.0 mA $(12 \mathrm{VDC} / \mathrm{AC})$ | $\bigcirc$ |  |
| Operating voltage range |  | ```10.2 to 26.4VDC (ripple ratio within 5%), 10.2 to 26.4VAC (50/60Hz }\pm5%``` | $\begin{aligned} & \text { DC10.2 to } 26.4 \mathrm{~V} \\ & \text { (ripple ratio within } 5 \% \text { ), } \\ & 10.2 \text { to } 26.4 \mathrm{VAC} \\ & (50 / 60 \mathrm{~Hz} \pm 5 \%) \\ & \hline \end{aligned}$ | $\bigcirc$ |  |
| Maximum simultaneous input points |  | 100\% simultaneously ON | Refer to the derating chart. *1 | $\triangle$ | Use within the range shown in the derating chart. |
| ON voltage/ON current |  | 7VDC/AC or more/2mA or more | 7VDC/AC or more/2mA or more | $\bigcirc$ |  |
| OFF voltage/OFF current |  | $2.5 \mathrm{VDC} / \mathrm{AC}$ or less/0.7mA or less | 2.7VDC/AC or less/0.7mA or less | $\bigcirc$ |  |
| Input resistance |  | Approx. $2.7 \mathrm{k} \Omega$ | Approx. $2.7 \mathrm{k} \Omega$ | $\bigcirc$ |  |
| Response time | $\mathrm{OFF} \rightarrow \mathrm{ON}$ | 20 ms or less $(12 / 24 \mathrm{VDC})$, 25 ms or less $(12 / 24 \mathrm{VAC}, 60 \mathrm{~Hz})$ | 20 ms or less $(12 / 24 \mathrm{VDC})$, 25 ms or less $(12 / 24 \mathrm{VAC}, 60 \mathrm{~Hz})$ | $\bigcirc$ |  |
|  | $\mathrm{ON} \rightarrow$ OFF | 20 ms or less $(12 / 24 \mathrm{VDC})$, 20 ms or less $(12 / 24 \mathrm{VAC}, 60 \mathrm{~Hz})$ | 20 ms or less (12/24VDC), 20 ms or less (12/24VAC, 60 Hz ) | $\bigcirc$ |  |
| Common terminal arrangement |  | 32 points/common (common terminal: TB9,TB18,TB27,TB36) | 16 points/common (common terminal: TB9,TB18) | $\bigcirc$ |  |
| Operation indicator |  | ON indication (LED) | ON indication (LED) | $\bigcirc$ |  |
| External connection |  | 38-point terminal block connector (M3×6 screws) | 20-point terminal block connector (M3.5×7 screws) | $\times$ | Wiring must be changed. |
| Applicable wire size |  | 0.75 to $2 \mathrm{~mm}^{2}$ | 0.75 to $1.25 \mathrm{~mm}^{2}$ | $\times$ |  |
| Applicable solderless terminal |  | R1.25-3,R2-3, RAV1.25-3,RAV2-3 | R1.25-3.5,R2-3.5, RAV1.25-3.5,RAV2-3.5 | $\times$ |  |
| Current consumption |  | $0.11 \mathrm{~A}$ <br> (TYP. all points ON) | 0.05A <br> (TYP. all points ON) | $\bigcirc$ |  |
| External dimensions |  | $250(\mathrm{H}) \times 37.5(\mathrm{~W}) \times 131(\mathrm{D}) \mathrm{mm}$ | $130(\mathrm{H}) \times 34.5(\mathrm{~W}) \times 93.6(\mathrm{D}) \mathrm{mm}$ | $\triangle$ | The dimensions are different. |
| Weight |  | 0.49 kg | 0.20 kg | $\bigcirc$ |  |

*1 The figure on the right shows derating.

(8) Specifications comparisons between the AX31-S1 and the A1SX41

| Specification |  | AX31-S1 | A1SX41 | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Number of input points |  | 32 points | 32 points | $\bigcirc$ |  |
| Isolation method |  | Photocoupler | Photocoupler | $\bigcirc$ |  |
| Rated input voltage |  | 24VDC | 12/24VDC | $\bigcirc$ |  |
| Rated input current |  | 8.5 mA | Approx. 3mA/Approx. 7 mA | $\triangle$ | Rated input current is smaller. ${ }^{* 1}$ |
| Operating voltage range |  | 19.2 to 26.4 VDC (ripple ratio within 5\%) | 10.2 to 26.4 VDC (ripple ratio within 5\%) | $\bigcirc$ |  |
| Maximum simultaneous input points |  | 100\% simultaneously ON | Refer to the derating chart. *2 | $\triangle$ | Use within the range shown in the derating chart. |
| ON voltage/ON current |  | 16 VDC or more/5mA or more | 8 VDC or more/2mA or more | $\bigcirc$ |  |
| OFF voltage/OFF current |  | 8VDC or less/2mA or less | 4VDC or less/1 mA or less | $\Delta$ | OFF current is smaller.*1 |
| Input resistance |  | Approx. $2.7 \mathrm{k} \Omega$ | Approx. $3.3 \mathrm{k} \Omega$ | $\triangle$ | Input resistance is greater. ${ }^{*}$ |
| Response time | OFF $\rightarrow$ ON | 10 ms or less | 10 ms or less (24VDC) | $\bigcirc$ |  |
|  | ON $\rightarrow$ OFF | 10 ms or less | 10 ms or less (24VDC) | $\bigcirc$ |  |
| Common terminal arrangement |  | 32 points/common (common terminal: TB9,TB18,TB27,TB36) | 32 points/common (common terminal: B1, B2) | $\bigcirc$ |  |
| Operation indicator |  | ON indication (LED) | ON indication (LED) | $\bigcirc$ |  |
| External connection |  | 38-point terminal block connector (M3×6 screws) | 40-pin connector (included) | $\times$ | Wiring must be changed. ${ }^{* 3}$ |
| Applicable wire size |  | 0.75 to $2 \mathrm{~mm}^{2}$ | $0.3 \mathrm{~mm}^{2}$ | $\times$ |  |
| Applicable solderless terminal |  | R1.25-3,R2-3, RAV1.25-3,RAV2-3 | - | $\times$ |  |
| Current consumption |  | 0.11 A (TYP. all points ON) | 0.08 A (TYP. all points ON) | $\bigcirc$ |  |
| External dimensions |  | $250(\mathrm{H}) \times 37.5(\mathrm{~W}) \times 131(\mathrm{D}) \mathrm{mm}$ | $130(\mathrm{H}) \times 34.5(\mathrm{~W}) \times 93.6(\mathrm{D}) \mathrm{mm}$ | $\triangle$ |  |
| Weight |  | 0.49 kg | 0.21 kg | $\bigcirc$ |  |

*1 Check the specifications of sensor or switch to connect to the A1SX41.
*2 The figure on the right shows derating.
*3 By using connectors/terminal block converter modules (A6TBXY36, etc.), conversion to the terminal block is possible.

(9) Specifications comparisons between the AX40 and the A1SX40

| Specification |  | AX40 | A1SX40 | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Number of input points |  | 16 points | 16 points | $\bigcirc$ |  |
| Isolation method |  | Photocoupler | Photocoupler | $\bigcirc$ |  |
| Rated input voltage |  | 12/24VDC | 12/24VDC | $\bigcirc$ |  |
| Rated input current |  | Approx. 4 mA /Approx. 10 mA | Approx. 3mA/Approx. 7 mA | $\triangle$ | Rated input current is smaller. ${ }^{* 1}$ |
| Operating voltage range |  | $\begin{gathered} 10.2 \text { to } 26.4 \mathrm{VDC} \\ \text { (ripple ratio within 5\%) } \end{gathered}$ | $\begin{aligned} & 10.2 \text { to } 26.4 \mathrm{VDC} \\ & \text { (ripple ratio within } 5 \% \text { ) } \end{aligned}$ | $\bigcirc$ |  |
| Maximum simultaneous input points |  | 100\% (8 points/common) simultaneously ON | 100\% simultaneously ON | $\bigcirc$ |  |
| ON voltage/ON current |  | 9.5 VDC or more/3mA or more | 8VDC or more/2mA or more | $\bigcirc$ |  |
| OFF voltage/OFF current |  | 6 VDC or less/1.5mA or less | 4VDC or less/1mA or less | $\triangle$ | OFF current is smaller. ${ }^{*}$ |
| Input resistance |  | Approx. $2.4 \mathrm{k} \Omega$ | Approx. $3.3 \mathrm{k} \Omega$ | $\triangle$ | Input resistance is greater. ${ }^{* 1}$ |
| Response time | $\mathrm{OFF} \rightarrow \mathrm{ON}$ | 10 ms or less | 10 ms or less (24VDC) | $\bigcirc$ |  |
|  | $\mathrm{ON} \rightarrow$ OFF | 10 ms or less | 10 ms or less (24VDC) | $\bigcirc$ |  |
| Common terminal arrangement |  | 8 points/common (common terminal: TB9,TB18) | 16 points/common (common terminal: TB9,TB18) | $\triangle$ | As 2 commons are reduced to 1 , wiring a different voltage for each common is not possible. |
| Operation indicator |  | ON indication (LED) | ON indication (LED) | $\bigcirc$ |  |
| External connection |  | 20-point terminal block connector (M3×6 screws) | 20-point terminal block connector (M3.5×7 screws) | $\times$ | Wiring must be changed. |
| Applicable wire size |  | 0.75 to $2 \mathrm{~mm}^{2}$ | 0.75 to $1.25 \mathrm{~mm}^{2}$ | $\times$ |  |
| Applicable solderless terminal |  | $\begin{gathered} \text { R1.25-3,R2-3, } \\ \text { RAV1.25-3,RAV2-3 } \end{gathered}$ | R1.25-3.5,R2-3.5, RAV1.25-3.5,RAV2-3.5 | $\times$ |  |
| Current consumption |  | 0.055 A (TYP. all points ON) | 0.050 A (TYP. all points ON) | $\bigcirc$ |  |
| External dimensions |  | $250(\mathrm{H}) \times 37.5(\mathrm{~W}) \times 121(\mathrm{D}) \mathrm{mm}$ | $130(\mathrm{H}) \times 34.5(\mathrm{~W}) \times 93.6(\mathrm{D}) \mathrm{mm}$ | $\triangle$ | The dimensions are different. |
| Weight |  | 0.36 kg | 0.20 kg | $\bigcirc$ |  |

*1 Check the specifications of sensor or switch to connect to the A1SX40.
(10) Specifications comparisons between the AX40-UL and the A1SX40

| Specification |  | AX40-UL | A1SX40 | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Number of input points |  | 16 points | 16 points | $\bigcirc$ |  |
| Isolation method |  | Photocoupler | Photocoupler | $\bigcirc$ |  |
| Rated input voltage |  | 12/24VDC | 12/24VDC | $\bigcirc$ |  |
| Rated input current |  | Approx. $4 \mathrm{~mA} /$ Approx. 10 mA | Approx. 3mA/Approx. 7 mA | $\triangle$ | Rated input current is smaller. ${ }^{* 1}$ |
| Operating voltage range |  | 10.2 to 26.4 VDC (ripple ratio within 5\%) | 10.2 to 26.4 VDC (ripple ratio within 5\%) | 0 |  |
| Maximum simultaneous input points |  | 100\% (8 points/common) simultaneously ON | 100\% simultaneously ON | $\bigcirc$ |  |
| ON voltage/ON current |  | 9.5VDC or more/3mA or more | 8 VDC or more/2mA or more | $\bigcirc$ |  |
| OFF voltage/OFF current |  | 6 VDC or less $/ 1.5 \mathrm{~mA}$ or less | 4VDC or less/1mA or less | $\triangle$ | OFF current is smaller. ${ }^{*}$ |
| Input resistance |  | Approx. $2.4 \mathrm{k} \Omega$ | Approx. $3.3 \mathrm{k} \Omega$ | $\triangle$ | Input resistance is greater. ${ }^{* 1}$ |
| Response time | OFF $\rightarrow$ ON | 10 ms or less | 10 ms or less (24VDC) | 0 |  |
|  | ON $\rightarrow$ OFF | 10 ms or less | 10 ms or less (24VDC) | 0 |  |
| Common terminal arrangement |  | 8 points/common (common terminal: TB9,TB18) | 16 points/common (common terminal: TB9,TB18) | $\Delta$ | As 2 commons are reduced to 1 , wiring a different voltage for each common is not possible. |
| Operation indicator |  | ON indication (LED) | ON indication (LED) | 0 |  |
| External connection |  | 20-point terminal block connector (M3.5×7 screws) | 20-point terminal block connector (M3. $5 \times 7$ screws) | $\times$ |  |
| Applicable wire size |  | 0.75 to $2 \mathrm{~mm}^{2}$ | 0.75 to $1.25 \mathrm{~mm}^{2}$ | $\times$ | Wiring must be changed. |
| Applicable solderless terminal |  | R1.25-3.5,R2-3.5, RAV1.25-3.5,RAV2-3.5 | R1.25-3.5,R2-3.5, RAV1.25-3.5,RAV2-3.5 | $\times$ |  |
| Current consumption |  | $\begin{gathered} \hline 0.055 \mathrm{~A} \\ \text { (TYP. all points ON) } \\ \hline \end{gathered}$ | $\begin{gathered} \hline 0.050 \mathrm{~A} \\ \text { (TYP. all points ON) } \\ \hline \end{gathered}$ | $\bigcirc$ |  |
| External dimensions |  | $250(\mathrm{H}) \times 37.5(\mathrm{~W}) \times 121$ (D) mm | 130(H) $\times 34.5(\mathrm{~W}) \times 93.6$ (D) mm | $\triangle$ | The dimensions are different. |
| Weight |  | 0.36 kg | 0.20 kg | $\bigcirc$ |  |

*1 Check the specifications of sensor or switch to connect to the A1SX40.
(11) Specifications comparisons between the AX41 and the A1SX41

| Specification |  | AX41 | A1SX41 | $\left\lvert\, \begin{gathered} \text { Compati } \\ \text { bility } \end{gathered}\right.$ | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Number of input points |  | 32 points | 32 points | $\bigcirc$ |  |
| Isolation method |  | Photocoupler | Photocoupler | $\bigcirc$ |  |
| Rated input voltage |  | 12/24VDC | 12/24VDC | $\bigcirc$ |  |
| Rated input current |  | Approx. 4 mA /Approx. 10 mA | Approx. 3mA/Approx. 7mA | $\triangle$ | Rated input current is smaller. ${ }^{*}$ |
| Operating voltage range |  | $\begin{gathered} 10.2 \text { to } 26.4 \mathrm{VDC} \\ \text { (ripple ratio within 5\%) } \end{gathered}$ | 10.2 to 26.4 VDC (ripple ratio within 5\%) | $\bigcirc$ |  |
| Maximum simultaneous input points |  | 60\% (5 points/common) simultaneously ON | Refer to the derating chart. *2 | $\bigcirc$ |  |
| ON voltage/ON current |  | 9.5 VDC or more/3mA or more | 8 VDC or more/2mA or more | $\bigcirc$ |  |
| OFF voltage/OFF current |  | 6 VDC or less/1.5mA or less | 4VDC or less/1mA or less | $\triangle$ | OFF current is smaller. ${ }^{* 1}$ |
| Input resistance |  | Approx. $2.4 \mathrm{k} \Omega$ | Approx. $3.3 \mathrm{k} \Omega$ | $\triangle$ | Input resistance is greater. ${ }^{* 1}$ |
| Response time | $\mathrm{OFF} \rightarrow \mathrm{ON}$ | 10 ms or less | 10 ms or less (24VDC) | $\bigcirc$ |  |
|  | $\mathrm{ON} \rightarrow$ OFF | 10 ms or less | 10 ms or less (24VDC) | $\bigcirc$ |  |
| Common terminal arrangement |  | 8 points/common (common terminal: TB9,TB18,TB27,TB36) | 32 points/common (common terminal: B1, B2) | $\triangle$ | As 4 commons are reduced to 1 , wiring a different voltage for each common is not possible. |
| Operation indicator |  | ON indication (LED) | ON indication (LED) | $\bigcirc$ |  |
| External connection |  | 38-point terminal block connector (M3×6 screws) | 40-pin connector (included) | $\times$ |  |
| Applicable wire size |  | 0.75 to $2 \mathrm{~mm}^{2}$ | $0.3 \mathrm{~mm}^{2}$ | $\times$ | Wiring must be changed. ${ }^{*}$ |
| Applicable solderless terminal |  | $\begin{gathered} \text { R1.25-3,R2-3, } \\ \text { RAV1.25-3,RAV2-3 } \end{gathered}$ | - | $\times$ |  |
| Current consumption |  | 0.11 A (TYP. all points ON) | 0.08 A (TYP. all points ON) | $\bigcirc$ |  |
| External dimensions |  | $250(\mathrm{H}) \times 37.5(\mathrm{~W}) \times 131(\mathrm{D}) \mathrm{mm}$ | $130(\mathrm{H}) \times 34.5(\mathrm{~W}) \times 93.6(\mathrm{D}) \mathrm{mm}$ | $\triangle$ |  |
| Weight |  | 0.44 kg | 0.21 kg | $\bigcirc$ |  |

*1 Check the specifications of sensor or switch to connect to the A1SX41.
*2 The figure on the right shows derating.
*3 By using connectors/terminal block converter modules (A6TBXY36, etc.), conversion to the terminal block is possible.

(12) Specifications comparisons between the AX41-S1 and the A1SX41-S1

| Specification |  | AX41-S1 | A1SX41-S1 | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Number of input points |  | 32 points | 32 points | $\bigcirc$ |  |
| Isolation method |  | Photocoupler | Photocoupler | $\bigcirc$ |  |
| Rated input voltage |  | 12/24VDC | 24VDC | $\triangle$ | 12 VDC cannot be used. |
| Rated input current |  | Approx. $4 \mathrm{~mA} /$ Approx. 10 mA | Approx. 7mA | $\triangle$ | Rated input current is smaller. ${ }^{* 1}$ |
| Operating voltage range |  | 10.2 to 26.4 VDC (ripple ratio within 5\%) | 19.2 to 26.4 VDC (ripple ratio within 5\%) | $\triangle$ | 12VDC cannot be used. |
| Maximum simultaneous input points |  | 60\% (5 points/common) simultaneously ON | Refer to the derating chart. *2 | $\bigcirc$ |  |
| ON voltage/ON current |  | 9.5VDC or more/3mA or more | 17 VDC or more/4.5mA or more | $\Delta$ | 12VDC cannot be used. |
| OFF voltage/OFF current |  | 6 VDC or less/1.5mA or less | 3.5 VDC or less $/ 0.8 \mathrm{~mA}$ or less | $\Delta$ | 12 VDC cannot be used. |
| Input resistance |  | Approx. $2.4 \mathrm{k} \Omega$ | Approx. $3.3 \mathrm{k} \Omega$ | $\triangle$ | Input resistance is greater. ${ }^{* 1}$ |
| Response time | OFF $\rightarrow$ ON | 0.1 ms or less | $\begin{gathered} 0.3 \mathrm{~ms} \text { or less } \\ (24 \mathrm{VDC}) \end{gathered}$ | $\triangle$ | The response times differ. |
|  | ON $\rightarrow$ OFF | 0.2 ms or less | 0.3 ms or less (24VDC) | $\triangle$ |  |
| Common terminal arrangement |  | 8 points/common (common terminal: TB9,TB18,TB27,TB36) | 32points/common (common terminal: B1, B2) | $\triangle$ | As 4 commons are reduced to 1 , wiring a different voltage for each common is not possible. |
| Operation indicator |  | ON indication (LED) | ON indication (LED) | $\bigcirc$ |  |
| External connection |  | 38-point terminal block connector (M3×6 screws) | 40-pin connector (included) | $\times$ | Wiring must be changed. ${ }^{* 3}$ |
| Applicable wire size |  | 0.75 to $2 \mathrm{~mm}^{2}$ | $0.3 \mathrm{~mm}^{2}$ | $\times$ |  |
| Applicable solderless terminal |  | $\begin{gathered} \text { R1.25-3,R2-3, } \\ \text { RAV1.25-3,RAV2-3 } \end{gathered}$ | - | $\times$ |  |
| Current consumption |  |  | 0.12 A (TYP. all points ON) | $\Delta$ | Current capacity must be reviewed. |
| External dimensions |  | $250(\mathrm{H}) \times 37.5(\mathrm{~W}) \times 131$ (D) mm | $130(\mathrm{H}) \times 34.5(\mathrm{~W}) \times 93.6$ (D) mm | $\triangle$ |  |
| Weight |  | 0.44 kg | 0.21 kg | $\bigcirc$ |  |

*1 Check the specifications of sensor or switch to connect to the A1SX41S1.
*2 The figure on the right shows derating.
*3 By using connectors/terminal block converter modules (A6TBXY36, etc.), conversion to the terminal block is possible.

(13) Specifications comparisons between the AX41-UL and the A1SX41
$\bigcirc$ : Compatible, $\Delta$ : Partial change required, $\times$ : Incompatible

| Specification |  | AX41-UL | A1SX41 | $\left\lvert\, \begin{gathered} \text { Compati-\| } \\ \text { bility } \end{gathered}\right.$ | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Number of input points |  | 32 points | 32 points | $\bigcirc$ |  |
| Isolation method |  | Photocoupler | Photocoupler | $\bigcirc$ |  |
| Rated input voltage |  | 12/24VDC | 12/24VDC | $\bigcirc$ |  |
| Rated input current |  | Approx. 4 mA /Approx. 10 mA | Approx. 3mA/Approx. 7mA | $\triangle$ | Rated input current is smaller. ${ }^{* 1}$ |
| Operating voltage range |  | $\begin{gathered} 10.2 \text { to } 26.4 \mathrm{VDC} \\ \text { (ripple ratio within 5\%) } \end{gathered}$ | $\begin{gathered} 10.2 \text { to } 26.4 \mathrm{VDC} \\ \text { (ripple ratio within 5\%) } \end{gathered}$ | $\bigcirc$ |  |
| Maximum simultaneous input points |  | 60\% (5 points/common) simultaneously ON | Refer to the derating chart. *2 | $\bigcirc$ |  |
| ON voltage/ON current |  | 9.5 VDC or more/3mA or more | 8VDC or more/2mA or more | $\bigcirc$ |  |
| OFF voltage/OFF current |  | 6 VDC or less/1.5mA or less | 4VDC or less/1mA or less | $\triangle$ | OFF current is smaller. ${ }^{*}$ |
| Input resistance |  | Approx. $2.4 \mathrm{k} \Omega$ | Approx. $3.3 \mathrm{k} \Omega$ | $\triangle$ | Input resistance is greater. ${ }^{* 1}$ |
| Response time | $\mathrm{OFF} \rightarrow \mathrm{ON}$ | 10 ms or less | 10 ms or less (24VDC) | $\bigcirc$ |  |
|  | $\mathrm{ON} \rightarrow$ OFF | 10 ms or less | 10 ms or less (24VDC) | $\bigcirc$ |  |
| Common terminal arrangement |  | 8 points/common (common terminal: TB9,TB18,TB27,TB36) | 32 points/common (common terminal: B1, B2) | $\triangle$ | As 4 commons are reduced to 1 , wiring a different voltage for each common is not possible. |
| Operation indicator |  | ON indication (LED) | ON indication (LED) | $\bigcirc$ |  |
| External connection |  | 38-point terminal block connector (M3.5×6 screws) | 40-pin connector (included) | $\times$ |  |
| Applicable wire size |  | 0.75 to $2 \mathrm{~mm}^{2}$ | $0.3 \mathrm{~mm}^{2}$ | $\times$ | Wiring must be changed. ${ }^{* 3}$ |
| Applicable solderless terminal |  | R1.25-3.5,R2-3.5, RAV1.25-3.5,RAV2-3.5 | - | $\times$ |  |
| Current consumption |  | $0.11 \mathrm{~A}$ <br> (TYP. all points ON) | 0.08 A (TYP. all points ON) | $\bigcirc$ |  |
| External dimensions |  | $250(\mathrm{H}) \times 37.5(\mathrm{~W}) \times 131(\mathrm{D}) \mathrm{mm}$ | $130(\mathrm{H}) \times 34.5(\mathrm{~W}) \times 93.6(\mathrm{D}) \mathrm{mm}$ | $\triangle$ |  |
| Weight |  | 0.44 kg | 0.21 kg | $\bigcirc$ |  |

*1 Check the specifications of sensor or switch to connect to the A1SX41.
*2 The figure on the right shows derating.
*3 By using connectors/terminal block converter modules (A6TBXY36, etc.), conversion to the terminal block is possible.

(14) Specifications comparisons between the AX42 and the A1SX42

| Specification |  | AX42 | A1SX42 | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Number of input points |  | 64 points | 64 points | $\bigcirc$ |  |
| Isolation method |  | Photocoupler | Photocoupler | $\bigcirc$ |  |
| Rated input voltage |  | 12/24VDC | 12/24VDC | $\bigcirc$ |  |
| Rated input current |  | Approx. 3mA/Approx. 7 mA | Approx. 2mA/Approx. 5 mA | $\triangle$ | Rated input current is smaller. *1 |
| Operating voltage range |  | $\begin{aligned} & 10.2 \text { to } 26.4 \mathrm{VDC} \\ & \text { (ripple ratio within } 5 \% \text { ) } \end{aligned}$ | $\begin{gathered} 10.2 \text { to } 26.4 \mathrm{VDC} \\ \text { (ripple ratio within 5\%) } \end{gathered}$ | $\bigcirc$ |  |
| Maximum simultaneous input points |  | 60\% (20 points/common) simultaneously ON | Refer to the derating chart. *2 | $\triangle$ | Use within the range shown in the derating chart. |
| ON voltage/ON current |  | 9.5 VDC or more/3mA or more | 8 VDC or more/2mA or more | $\bigcirc$ |  |
| OFF voltage/OFF current |  | 6 VDC or less/1.5mA or less | 4 VDC or less/0.6mA or less | $\triangle$ | OFF current is smaller. ${ }^{* 1}$ |
| Input resistance |  | Approx. $3.4 \mathrm{k} \Omega$ | Approx. $5 \mathrm{k} \Omega$ | $\triangle$ | Input resistance is greater. ${ }^{*}$ |
| Response time | $\mathrm{OFF} \rightarrow \mathrm{ON}$ | 10 ms or less | 10 ms or less (24VDC) | $\bigcirc$ |  |
|  | $\mathrm{ON} \rightarrow \mathrm{OFF}$ | 10 ms or less | 10 ms or less (24VDC) | $\bigcirc$ |  |
| Common terminal arrangement |  | 32 points/common (common terminal: 1B1,1B2,2B1,2B2) | 32 points/common (common terminal: 1B1,1B2,2B1,2B2) | $\bigcirc$ |  |
| Operation indicator |  | ON indication (LED) <br> 32 point switch-over using a switch | ON indication (LED) <br> 32 point switch-over using a switch | $\bigcirc$ |  |
| External connection |  | 40-pin connector (with solder) $\times 2$ | 40-pin connector (with solder) $\times 2$ | $\bigcirc$ |  |
| Applicable wire size |  | $0.3 \mathrm{~mm}^{2}$ | $0.3 \mathrm{~mm}^{2}$ | $\bigcirc$ |  |
| Current consumption |  | $0.12 \mathrm{~A}$ <br> (TYP. all points ON) | 0.09A <br> (TYP. all points ON) | $\bigcirc$ |  |
| External dimensions |  | $250(\mathrm{H}) \times 37.5(\mathrm{~W}) \times 106(\mathrm{D}) \mathrm{mm}$ | $130(\mathrm{H}) \times 34.5(\mathrm{~W}) \times 93.6(\mathrm{D}) \mathrm{mm}$ | $\triangle$ |  |
| Weight |  | 0.51 kg | 0.28 kg | $\bigcirc$ |  |

*1 Check the specifications of sensor or switch to connect to the A1SX42.
*2 The figure on the right shows derating.

(15) Specifications comparisons between the AX42-S1 and the A1SX42-S1

| Specification |  | AX42-S1 | A1SX42-S1 | $\left\lvert\, \begin{gathered} \text { Compati- } \\ \text { bility } \end{gathered}\right.$ | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Number of input points |  | 64 points | 64 points | $\bigcirc$ |  |
| Isolation method |  | Photocoupler | Photocoupler | $\bigcirc$ |  |
| Rated input voltage |  | 12/24VDC | 24VDC | $\triangle$ | 12VDC cannot be used. |
| Rated input current |  | Approx. 3mA/Approx. 7mA | Approx. 5mA | $\triangle$ | Rated input current is smaller. ${ }^{*}$ |
| Operating voltage range |  | $\begin{gathered} 10.2 \text { to } 26.4 \mathrm{VDC} \\ \text { (ripple ratio within 5\%) } \end{gathered}$ | $19.2 \text { to } 26.4 \mathrm{VDC}$ <br> (ripple ratio within 5\%) | $\triangle$ | 12VDC cannot be used. |
| Maximum simultaneous input points |  | 60\% (20 points/common) simultaneously ON | Refer to the derating chart. *2 | $\triangle$ | Use within the range shown in the derating chart. |
| ON voltage/ON current |  | 9.5 VDC or more/3mA or more | 18.5VDC or more/3.5mA or more | $\triangle$ | 12VDC cannot be used. |
| OFF voltage/OFF current |  | 6 VDC or less/1.5mA or less | 3 VDC or less/0.45mA or less | $\triangle$ | 12VDC cannot be used. |
| Input resistance |  | Approx. $3.4 \mathrm{k} \Omega$ | Approx. $4.7 \mathrm{k} \Omega$ | $\Delta$ | Input resistance is greater. ${ }^{*}$ |
| Response time | $\mathrm{OFF} \rightarrow \mathrm{ON}$ | 0.5 ms or less | 0.3 ms or less (24VDC) | $\bigcirc$ |  |
|  | $\mathrm{ON} \rightarrow \mathrm{OFF}$ | 0.5 ms or less | 0.3 ms or less (24VDC) | $\bigcirc$ |  |
| Common terminal arrangement |  | 32 points/common (common terminal: 1B1,1B2,2B1,2B2) | 32 points/common (common terminal: 1B1,1B2,2B1,2B2) | $\bigcirc$ |  |
| Operation indicator |  | ON indication (LED) <br> 32 point switch-over using a switch | ON indication (LED) 32 point switch-over using a switch | $\bigcirc$ |  |
| External connection |  | 40-pin connector (with solder) $\times 2$ | 40-pin connector <br> $\times 2$ (included) | $\bigcirc$ |  |
| Applicable wire size |  | $0.3 \mathrm{~mm}^{2}$ | $0.3 \mathrm{~mm}^{2}$ | $\bigcirc$ |  |
| Accessory |  | External wiring connectors $\times 2$ | External wiring connectors $\times 2$ | $\bigcirc$ |  |
| Current consumption |  | $0.12 \mathrm{~A}$ <br> (TYP. all points ON) | $0.16 \mathrm{~A}$ <br> (TYP. all points ON) | $\triangle$ | Current capacity must be reviewed. |
| External dimensions |  | $250(\mathrm{H}) \times 37.5(\mathrm{~W}) \times 106(\mathrm{D}) \mathrm{mm}$ | $130(\mathrm{H}) \times 34.5(\mathrm{~W}) \times 93.6(\mathrm{D}) \mathrm{mm}$ | $\triangle$ |  |
| Weight |  | 0.51 kg | 0.28 kg | $\bigcirc$ |  |

*1 Check the specifications of sensor or switch to connect to the A1SX42-S1.
*2 The figure on the right shows derating.

(16) Specifications comparisons between the AX50-S1 and the A1SX40

|  | O: Compatible, $\Delta:$ Partial change required, $\times$ : Incompatible |  |
| :--- | :---: | :---: | :---: | :---: |

*1 Connect a resistor of $3.3 \mathrm{k} \Omega$ ( 1 W or more) serially to the external signal line that connects external devices to the A1SX40.
(17) Specifications comparisons between the AX60-S1 and the A1SX40

| Specification |  | $\bigcirc$ : Compatible, $\triangle$ : Partial change required, $\times$ : Incompatible |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | AX60-S1 | A1SX40 | $\begin{array}{\|c\|} \text { Compati- } \\ \text { bility } \end{array}$ | Precautions for replacement |
| Number of input points |  | 16 points | 16 points | $\bigcirc$ |  |
| Isolation method |  | Photocoupler | Photocoupler | $\bigcirc$ |  |
| Rated input voltage |  | 100/110/125VDC | 12/24VDC | $\times$ | Voltages exceeding 26.4VDC cannot be applied. ${ }^{*}$ |
| Rated input current |  | 2 mA | Approx. 3mA/Approx. 7 mA | $\bigcirc$ |  |
| Maximum inrush current |  | $\begin{aligned} & 65 \mathrm{~mA}(121 \mathrm{VDC}) \\ & 75 \mathrm{~mA}(140 \mathrm{VDC}) \end{aligned}$ | - | $\bigcirc$ |  |
| Operating voltage range |  | 85 to 140VDC <br> (ripple ratio within 5\%) | 10.2 to 26.4 VDC <br> (ripple ratio within 5\%) | $\times$ | Voltages exceeding 26.4VDC cannot be applied. ${ }^{* 1}$ |
| Maximum simultaneous input points |  | 60\% (5 points/common) simultaneously ON | 100\% simultaneously ON | $\bigcirc$ |  |
| ON voltage/ON current |  | 80 VDC or more/1.4mA or more | 8 VDC or more/2mA or more | $\times$ | Voltages exceeding 26.4VDC cannot be applied. ${ }^{* 1}$ |
| OFF voltage/OFF current |  | 20 VDC or less/0.5mA or less | 4VDC or less/1mA or less | $\times$ | Voltages exceeding 26.4VDC cannot be applied. ${ }^{*}$ |
| Input resistance |  | Approx. $50 \mathrm{k} \Omega$ | Approx. $3.3 \mathrm{k} \Omega$ | $\times$ | Input resistance is smaller. ${ }^{* 1}$ |
| Response time | $\mathrm{OFF} \rightarrow \mathrm{ON}$ | 10 ms or less | 10 ms or less (24VDC) | $\bigcirc$ |  |
|  | $\mathrm{ON} \rightarrow$ OFF | 20 ms or less | 10 ms or less (24VDC) | $\bigcirc$ |  |
| Common terminal arrangement |  | 8 points/common | 16 points/common (common terminal: TB9,TB18) | $\Delta$ | As 2 commons are reduced to 1 , wiring a different voltage for each common is not possible. |
| Operation indicator |  | ON indication (LED) | ON indication (LED) | $\bigcirc$ |  |
| External connection |  | 20-point terminal block connector (M3×6 screws) | 20-point terminal block connector (M3.5×7 screws) | $\times$ | Wiring must be changed. |
| Applicable wire size |  | 0.75 to $2 \mathrm{~mm}^{2}$ | 0.75 to $1.25 \mathrm{~mm}^{2}$ | $\times$ |  |
| Applicable solderless terminal |  | $\begin{gathered} \text { R1.25-3,R2-3, } \\ \text { RAV1.25-3,RAV2-3 } \end{gathered}$ | R1.25-3.5,R2-3.5, RAV1.25-3.5,RAV2-3.5 | $\times$ |  |
| Current consumption |  | 0.055 A (TYP. all points ON) | 0.050 A (TYP. all points ON) | $\bigcirc$ |  |
| External dimensions |  | $250(\mathrm{H}) \times 37.5(\mathrm{~W}) \times 121(\mathrm{D}) \mathrm{mm}$ | $130(\mathrm{H}) \times 34.5(\mathrm{~W}) \times 93.6(\mathrm{D}) \mathrm{mm}$ | $\triangle$ | The dimensions are different. |
| Weight |  | 0.40 kg | 0.20 kg | $\bigcirc$ |  |

*1 Connect a resistor of $15 \mathrm{k} \Omega$ (3W or more) serially to the external signal line that connects external devices to the A1SX40.
(18) Specifications comparisons between the AX70 and the A1SX71

| Specification |  | AX70 | A1SX71 | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Number of input points |  | 16 points | 32 points | $\times$ | Set sixteen points in the I/O assignment of Parameter. |
| Isolation method |  | Photocoupler | Photocoupler | $\bigcirc$ |  |
| Rated input voltage |  | 5V/12V/24VDC | 5V/12V/24VDC | $\bigcirc$ |  |
| Rated input current |  | $3.5 \mathrm{~mA} / 2 \mathrm{~mA} / 4.5 \mathrm{~mA}$ (TYP.), <br> $5.5 \mathrm{~mA} / 3 \mathrm{~mA} / 6 \mathrm{~mA}(\mathrm{MAX}$. | 5VDC Approx. 1.2mA <br> 12VDC Approx. 3.3mA <br> 24VDC Approx. 7 mA | $\triangle$ | Rated input current is smaller. ${ }^{* 2}$ |
| Operating voltage range |  | 4.5 to 5.5 VDC (SW ON), 10.2 to 26.4 VDC (SW OFF) | 4.5 to 26.4 VDC (ripple ratio within 5\%) | 0 |  |
| Maximum simultaneous input points |  | 100\% (8points/common) simultaneously ON | Refer to the derating chart. *1 | $\triangle$ | Use within the range shown in the derating chart. |
| ON voltage/ON current |  | 3.5VDC or more/1.0mA or more (SW ON), <br> 5VDC or more/1.0mA or more (SW OFF) | 3.5 VDC or more/1mA or more | 0 |  |
| OFF voltage/OFF current |  | 1.1 VDC or less $/ 0.2 \mathrm{~mA}$ or less (SW ON), <br> 2 VDC or less $/ 0.2 \mathrm{~mA}$ or less (SW OFF) | 1 VDC or less/0.1mA or less | $\triangle$ | OFF current is smaller. ${ }^{*}$ |
| Input resistance |  | Approx. 1.4k $\Omega$ (SW ON), Approx. $5.5 \mathrm{k} \Omega$ (SW OFF) | Approx. $3.5 \mathrm{k} \Omega$ | $\Delta$ | Input resistance is greater. ${ }^{*}$ |
| Response time | OFF $\rightarrow$ ON | 1.5 ms or less | 1.5 ms or less | $\bigcirc$ |  |
|  | ON $\rightarrow$ OFF | 3 ms or less | 3 ms or less | $\bigcirc$ |  |
| Common terminal arrangement |  | 8 points/common (common terminal: TB9, TB18) | 32 points/common (common terminal: B1, B2) | $\triangle$ | As 2 commons are reduced to 1 , wiring a different voltage for each common is not possible. |
| Operation indicator |  | ON indication (LED) | ON indication (LED) | $\bigcirc$ |  |
| External connection |  | 20-point terminal block connector (M3×6 screws) | 40-pin connector (included) | $\times$ |  |
| Applicable wire size |  | 0.75 to $2 \mathrm{~mm}^{2}$ | $0.3 \mathrm{~mm}^{2}$ | $\times$ | Wiring must be changed. |
| Applicable solderless terminal |  | $\begin{gathered} \text { R1.25-3,R2-3, } \\ \text { RAV1.25-3,RAV2-3 } \end{gathered}$ | - | $\times$ |  |
| Current consumption |  | $\begin{gathered} 0.055 \mathrm{~A} \\ \text { (TYP. all points ON) } \end{gathered}$ | $\begin{gathered} 0.075 \mathrm{~A} \\ \text { (TYP. all points ON) } \end{gathered}$ | $\triangle$ | Current capacity must be reviewed. |
| External dimensions |  | 250 (H) $\times 37.5$ (W) $\times 121$ (D) mm | 130(H) $\times 34.5(\mathrm{~W}) \times 93.6$ (D) mm | $\triangle$ |  |
| Weight |  | 0.36 kg | 0.19 kg | $\bigcirc$ |  |

*1 The figure on the right shows derating.
*2 Check the specifications of sensor or switch to connect to the A1SX71.

(19) Specifications comparisons between the AX71 and the A1SX71

| Specification |  | AX71 | A1SX71 | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Number of input points |  | 32 points | 32 points | $\bigcirc$ |  |
| Isolation method |  | Photocoupler | Photocoupler | $\bigcirc$ |  |
| Rated input voltage |  | 5V/12V/24VDC | 5V/12V/24VDC | $\bigcirc$ |  |
| Rated input current |  | $3.5 \mathrm{~mA} / 2 \mathrm{~mA} / 4.5 \mathrm{~mA}$ (TYP.), <br> $5.5 \mathrm{~mA} / 3 \mathrm{~mA} / 6 \mathrm{~mA}(\mathrm{MAX}$.) | 5VDC Approx. 1.2mA 12VDC Approx. 3.3mA 24VDC Approx. 7mA | $\Delta$ | Rated input current is smaller. ${ }^{*}{ }^{*}$ |
| Operating voltage range |  | 4.5 to 5.5 VDC (SW ON), 10.2 to 26.4 VDC (SW OFF) | 4.5 to 26.4 VDC (ripple ratio within 5\%) | 0 |  |
| Maximum simultaneous input points |  | 100\% (8points/common) simultaneously ON | Refer to the derating chart. *2 | $\Delta$ | Use within the range shown in the derating chart. |
| ON voltage/ON current |  | 3.5VDC or more/1.0mA or more (SW ON), <br> 5VDC or more/1.0mA or more (SW OFF) | 3.5 VDC or more/1mA or more | 0 |  |
| OFF voltage/OFF current |  | $\begin{gathered} 1.1 \mathrm{VDC} \text { or less } / 0.2 \mathrm{~mA} \text { or less } \\ (\mathrm{SW} \text { ON), } \\ 2 \mathrm{VDC} \text { or less/0.2mA or less } \\ \text { (SW OFF) } \\ \hline \end{gathered}$ | 1 VDC or less/ $/ 0.1 \mathrm{~mA}$ or less | $\Delta$ | OFF current is smaller. ${ }^{* 2}$ |
| Input resistance |  | Approx. $1.4 \mathrm{k} \Omega$ (SW ON), Approx. $5.5 \mathrm{k} \Omega$ (SW OFF) | Approx. $3.5 \mathrm{k} \Omega$ | $\Delta$ | Reduced at SW OFF.*2 |
| Response time | OFF $\rightarrow$ ON | 1.5 ms or less | 1.5 ms or less | $\bigcirc$ |  |
|  | ON $\rightarrow$ OFF | 3 ms or less | 3 ms or less | $\bigcirc$ |  |
| Common terminal arrangement |  | 8 points/common (common terminal: TB9,TB18,TB27,TB36) | 32 points/common (common terminal: B1, B2) | $\Delta$ | As 4 commons are reduced to 1 , wiring a different voltage for each common is not possible. |
| Operation indicator |  | ON indication (LED) | ON indication (LED) | 0 |  |
| External connection |  | 38-point terminal block connector (M3×6 screws) | 40-pin connector (included) | $\times$ |  |
| Applicable wire size |  | 0.75 to $2 \mathrm{~mm}^{2}$ | $0.3 \mathrm{~mm}^{2}$ | $\times$ | Wiring must be changed. |
| Applicable solderless terminal |  | R1.25-3,R2-3, RAV1.25-3,RAV2-3 | - | $\times$ |  |
| Current consumption |  | 0.110 A (TYP. all points ON) | 0.075 A (TYP. all points ON) | 0 |  |
| External dimensions |  | 250 (H) $\times 37.5(\mathrm{~W}) \times 131$ (D) mm | 130(H) $\times 34.5(\mathrm{~W}) \times 93.6$ (D) mm | $\triangle$ |  |
| Weight |  | 0.45 kg | 0.19 kg | $\bigcirc$ |  |

*1 Check the specifications of sensor or switch to connect to the A1SX71.
*2 The figure on the right shows derating.

(20) Specifications comparisons between the AX80 and the A1SX80

| Specification |  | AX80 | A1SX80 | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Number of input points |  | 16 points | 16 points | $\bigcirc$ |  |
| Isolation method |  | Photocoupler | Photocoupler | $\bigcirc$ |  |
| Rated input voltage |  | 12/24VDC | 12/24VDC | $\bigcirc$ |  |
| Rated input current |  | $4 \mathrm{~mA} / 10 \mathrm{~mA}$ | Approx. 3mA/Approx. 7mA | $\triangle$ | Rated input current is smaller. ${ }^{*}$ |
| Operating voltage range |  | $10.2 \text { to } 26.4 \mathrm{VDC}$ <br> (ripple ratio within 5\%) | 10.2 to 26.4 VDC (ripple ratio within 5\%) | $\bigcirc$ |  |
| Maximum simultaneous input points |  | 100\% (8points/common) simultaneously ON | 100\% simultaneously ON <br> (At 26.4VDC) | $\bigcirc$ |  |
| ON voltage/ON current |  | 9.5 VDC or more/3mA or more | 8VDC or more/2mA or more | $\bigcirc$ |  |
| OFF voltage/OFF current |  | 6 VDC or less/1.5mA or less | 4VDC or less/1mA or less | $\triangle$ | OFF current is smaller. ${ }^{*}$ |
| Input resistance |  | Approx. $2.4 \mathrm{k} \Omega$ | Approx. $3.3 \mathrm{k} \Omega$ | $\triangle$ | Input resistance is greater. ${ }^{* 1}$ |
| Response time | $\mathrm{OFF} \rightarrow \mathrm{ON}$ | 10 ms or less | 10 ms or less (24VDC) | $\bigcirc$ |  |
|  | $\mathrm{ON} \rightarrow$ OFF | 10 ms or less | 10 ms or less (24VDC) | $\bigcirc$ |  |
| Common terminal arrangement |  | 8 points/common (common terminal: TB9,TB18) | 16 points/common (common terminal: TB9,TB18) | $\triangle$ | As 2 commons are reduced to 1 , wiring a different voltage for each common is not possible. |
| Operation indicator |  | ON indication (LED) | ON indication (LED) | $\bigcirc$ |  |
| External connection |  | 20-point terminal block connector (M3×6 screws) | 20-point terminal block connector (M3.5×7 screws) | $\times$ | Wiring must be changed. |
| Applicable wire size |  | 0.75 to $2 \mathrm{~mm}^{2}$ | 0.75 to $1.25 \mathrm{~mm}^{2}$ | $\times$ |  |
| Applicable solderless terminal |  | $\begin{gathered} \text { R1.25-3,R2-3, } \\ \text { RAV1.25-3,RAV2-3 } \end{gathered}$ | R1.25-3.5,R2-3.5, RAV1.25-3.5,RAV2-3.5 | $\times$ |  |
| Current consumption |  | 0.055 A (TYP. all points ON) | 0.050 A (TYP. all points ON) | $\bigcirc$ |  |
| External dimensions |  | $250(\mathrm{H}) \times 37.5(\mathrm{~W}) \times 121(\mathrm{D}) \mathrm{mm}$ | $130(\mathrm{H}) \times 34.5(\mathrm{~W}) \times 93.6(\mathrm{D}) \mathrm{mm}$ | $\triangle$ | The dimensions are different. |
| Weight |  | 0.36 kg | 0.20 kg | $\bigcirc$ |  |

*1 Check the specifications of sensor or switch to connect to the A1SX80.
(21) Specifications comparisons between the AX80E and the A1SX80-S1

| Specification |  | O : Compatible, $\Delta$ : Partial change required, $\times$ : Incompatible |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | AX80E | A1SX80-S1 | $\begin{array}{\|c\|} \text { Compati- } \\ \text { bility } \end{array}$ | Precautions for replacement |
| Number of input points |  | 16 points | 16 points | $\bigcirc$ |  |
| Isolation method |  | Photocoupler | Photocoupler | $\bigcirc$ |  |
| Rated input voltage |  | 12/24VDC | 24VDC | $\Delta$ | 12VDC cannot be used. |
| Rated input current |  | $4 \mathrm{~mA} / 10 \mathrm{~mA}$ | Approx. 7 mA | $\Delta$ | Rated input current is smaller. ${ }^{*}$ |
| Operating voltage range |  | 10.2 to 26.4 VDC (ripple ratio within 5\%) | 19.2 to 26.4 VDC <br> (ripple ratio within 5\%) | $\triangle$ | 12VDC cannot be used. |
| Maximum simultaneous input points |  | 100\% (8points/common) simultaneously ON | $85 \%$ simultaneously ON <br> (At 26.4VDC) | $\triangle$ | Use within the specification range. |
| ON voltage/ON current |  | 9.5VDC or more/2.6mA or more | 17VDC or more/5mA or more | $\triangle$ | 12VDC cannot be used. |
| OFF voltage/OFF current |  | 6 VDC or less/1.0mA or less | 5 VDC or less/1.7mA or less | $\Delta$ | 12VDC cannot be used. |
| Input resistance |  | Approx. $2.4 \mathrm{k} \Omega$ | Approx. $3.3 \mathrm{k} \Omega$ | $\Delta$ | Input resistance is greater. ${ }^{* 1}$ |
| Response time | OFF $\rightarrow$ ON | $\begin{aligned} & 5.5 \mathrm{~ms} \\ & \text { (TYP.) } \end{aligned}$ | $\begin{gathered} 0.4 \mathrm{~ms} \\ (24 \mathrm{VDC}) \end{gathered}$ | $\bigcirc$ |  |
|  | ON $\rightarrow$ OFF | 6.0 ms (TYP.) | $\begin{gathered} \hline 0.5 \mathrm{~ms} \\ (24 \mathrm{VDC}) \end{gathered}$ | $\bigcirc$ |  |
| Response time high-speed mode (upper 8 points only) | OFF $\rightarrow$ ON | 0.5 ms or less | $\begin{gathered} 0.4 \mathrm{~ms} \\ (24 \mathrm{VDC}) \end{gathered}$ | $\bigcirc$ |  |
|  | ON $\rightarrow$ OFF | 1.0 ms or less | $\begin{gathered} 0.5 \mathrm{~ms} \\ (24 \mathrm{VDC}) \end{gathered}$ | $\bigcirc$ |  |
| Common terminal arrangement |  | 8 points/common (common terminal: TB9,TB18) | 16 points/common (common terminal: TB9,TB18) | $\Delta$ | As 2 commons are reduced to 1 ,wiring a different voltage for each common is not possible. |
| Operation indicator |  | ON indication (LED) | ON indication (LED) | $\bigcirc$ |  |
| External connection |  | 20-point terminal block connector (M3×6 screws) | 20-point terminal block connector (M3. $5 \times 7$ screws) | $\times$ | Wiring must be changed. |
| Applicable wire size |  | 0.75 to $2 \mathrm{~mm}^{2}$ | 0.75 to $1.25 \mathrm{~mm}^{2}$ | $\times$ |  |
| Applicable solderless terminal |  | R1.25-3,R2-3, RAV1.25-3,RAV2-3 | R1.25-3.5,R2-3.5, RAV1.25-3.5,RAV2-3.5 | $\times$ |  |
| Current consumption |  | 0.055 A (TYP. all points ON) | $\begin{gathered} 0.050 \mathrm{~A} \\ \text { (TYP. all points ON) } \end{gathered}$ | $\bigcirc$ |  |
| External dimensions |  | 250 (H) $\times 37.5(\mathrm{~W}) \times 121$ (D) mm | 130(H) $\times 34.5(\mathrm{~W}) \times 93.6$ (D) mm | $\Delta$ | The dimensions are different. |
| Weight |  | 0.36 kg | 0.2 kg | 0 |  |

*1 Check the specifications of sensor or switch to connect to the A1SX80-S1.
(22) Specifications comparisons between the AX81 and the A1SX81

| Specification |  | AX81 | A1SX81 | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Number of input points |  | 32 points | 32 points | $\bigcirc$ |  |
| Isolation method |  | Photocoupler | Photocoupler | $\bigcirc$ |  |
| Rated input voltage |  | 12/24VDC | 12/24VDC | $\bigcirc$ |  |
| Rated input current |  | $4 \mathrm{~mA} / 10 \mathrm{~mA}$ | Approx. 3mA/Approx. 7 mA | $\triangle$ | Rated input current is smaller. ${ }^{* 1}$ |
| Operating voltage range |  | $\begin{gathered} 10.2 \text { to } 26.4 \mathrm{VDC} \\ \text { (ripple ratio within 5\%) } \end{gathered}$ | $\begin{aligned} & 10.2 \text { to } 26.4 \mathrm{VDC} \\ & \text { (ripple ratio within } 5 \% \text { ) } \end{aligned}$ | $\bigcirc$ |  |
| Maximum simultaneous input points |  | 60\% (5points/common) simultaneously ON | Refer to the derating chart. *2 | $\bigcirc$ |  |
| ON voltage/ON current |  | 9.5 VDC or more/3mA or more | 8VDC or more/2mA or more | $\bigcirc$ |  |
| OFF voltage/OFF current |  | 6 VDC or less/1.5mA or less | 4VDC or less/1mA or less | $\triangle$ | OFF current is smaller. ${ }^{*}$ |
| Input resistance |  | Approx. $2.4 \mathrm{k} \Omega$ | Approx. $3.3 \mathrm{k} \Omega$ | $\triangle$ | Input resistance is greater. ${ }^{* 1}$ |
| Response time | $\mathrm{OFF} \rightarrow \mathrm{ON}$ | 10 ms or less | 10 ms or less (24VDC) | $\bigcirc$ |  |
|  | $\mathrm{ON} \rightarrow$ OFF | 10 ms or less | 10 ms or less (24VDC) | $\bigcirc$ |  |
| Common terminal arrangement |  | 8 points/common (common terminal: TB9,TB18,TB27,TB36) | 32 points/common (common terminal: 17, 18, 36) | $\triangle$ | As 4 commons are reduced to 1 , wiring a different voltage for each common is not possible. |
| Operation indicator |  | ON indication (LED) | ON indication (LED) | $\bigcirc$ |  |
| External connection |  | 38-point terminal block connector (M3×6 screws) | 37-pin D sub-connector (included) | $\times$ |  |
| Applicable wire size |  | 0.75 to $2 \mathrm{~mm}^{2}$ | $0.3 \mathrm{~mm}^{2}$ | $\times$ | Wiring must be changed. ${ }^{* 3}$ |
| Applicable solderless terminal |  | $\begin{gathered} \text { R1.25-3,R2-3, } \\ \text { RAV1.25-3,RAV2-3 } \end{gathered}$ | - | $\times$ |  |
| Current consumption |  | $0.11 \mathrm{~A}$ <br> (TYP. all points ON) | 0.08 A (TYP. all points ON) | $\bigcirc$ |  |
| External dimensions |  | $250(\mathrm{H}) \times 37.5(\mathrm{~W}) \times 131(\mathrm{D}) \mathrm{mm}$ | $130(\mathrm{H}) \times 34.5(\mathrm{~W}) \times 93.6(\mathrm{D}) \mathrm{mm}$ | $\triangle$ |  |
| Weight |  | 0.45 kg | 0.24 kg | $\bigcirc$ |  |

*1 Check the specifications of sensor or switch to connect to the A1SX81.
*2 The figure on the right shows derating.
*3 By using connectors/terminal block converter modules (A6TBX36-E, etc.), conversion to the terminal block is possible.

(23) Specifications comparisons between the AX81B and the A1SX81

|  | O: Compatible, $\Delta:$ Partial change required, $\times$ : Incompatible |  |
| :--- | :---: | :---: | :---: | :---: |

*1 The figure on the right shows derating.
*2 By using connectors/terminal block converter modules(A6TBX36-E, etc.), conversion to the terminal block is possible.

(24) Specifications comparisons between the AX81-S1 and the A1SX81

| Specification |  | AX81-S1 | A1SX81 | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Number of input points |  | 32 points | 32 points | $\bigcirc$ |  |
| Isolation method |  | Photocoupler | Photocoupler | $\bigcirc$ |  |
| Rated input voltage |  | 12/24VDC | 12/24VDC | $\bigcirc$ |  |
| Rated input current |  | $2.5 \mathrm{~mA} / 5 \mathrm{~mA}$ | Approx. 3mA/Approx. 7mA | $\triangle$ | Rated input current is smaller. ${ }^{* 1}$ |
| Operating voltage range |  | $\begin{gathered} 10.2 \text { to } 26.4 \mathrm{VDC} \\ \text { (ripple ratio within 5\%) } \end{gathered}$ | $\begin{gathered} 10.2 \text { to } 26.4 \mathrm{VDC} \\ \text { (ripple ratio within 5\%) } \end{gathered}$ | $\bigcirc$ |  |
| Maximum simultaneous input points |  | 60\% (5points/common) simultaneously ON | Refer to the derating chart. *2 | $\bigcirc$ |  |
| ON voltage/ON current |  | 5.6 VDC or more/1.1mA or more | 8 VDC or more/2mA or more | $\triangle$ | The ON voltage has been increased.*1 |
| OFF voltage/OFF current |  | 2.4VDC or less/0.39mA or less | 4VDC or less/1mA or less | $\bigcirc$ |  |
| Input resistance |  | Approx. $4.8 \mathrm{k} \Omega$ | Approx. $3.3 \mathrm{k} \Omega$ | $\triangle$ | Input resistance is smaller. ${ }^{*}$ |
| Response time | $\mathrm{OFF} \rightarrow \mathrm{ON}$ | 10 ms or less | 10 ms or less (24VDC) | $\bigcirc$ |  |
|  | $\mathrm{ON} \rightarrow$ OFF | 10 ms or less | 10 ms or less (24VDC) | $\bigcirc$ |  |
| Common terminal arrangement |  | 8 points/common (common terminal: TB9,TB18,TB27,TB36) | 32 points/common (common terminal: 17, 18, 36) | $\Delta$ | As 4 commons are reduced to 1 , wiring a different voltage for each common is not possible. |
| Operation indicator |  | ON indication (LED) | ON indication (LED) | $\bigcirc$ |  |
| External connection |  | 38-point terminal block connector (M3×6 screws) | 37-pin D sub-connector (included) | $\times$ |  |
| Applicable wire size |  | 0.75 to $2 \mathrm{~mm}^{2}$ | $0.3 \mathrm{~mm}^{2}$ | $\times$ | Wiring must be changed. ${ }^{* 3}$ |
| Applicable solderless terminal |  | $\begin{gathered} \text { R1.25-3,R2-3, } \\ \text { RAV1.25-3,RAV2-3 } \end{gathered}$ | - | $\times$ |  |
| Current consumption |  | 0.105 A (TYP. all points ON) | $\begin{gathered} 0.080 \mathrm{~A} \\ \text { (TYP. all points ON) } \end{gathered}$ | $\bigcirc$ |  |
| External dimensions |  | $250(\mathrm{H}) \times 37.5(\mathrm{~W}) \times 131(\mathrm{D}) \mathrm{mm}$ | $130(\mathrm{H}) \times 34.5(\mathrm{~W}) \times 93.6(\mathrm{D}) \mathrm{mm}$ | $\triangle$ |  |
| Weight |  | 0.45 kg | 0.24 kg | $\bigcirc$ |  |

*1 Check the specifications of sensor or switch to connect to the A1SX81.
*2 The figure on the right shows derating.
*3 By using connectors/terminal block converter modules (A6TBX36-E, etc.), conversion to the terminal block is possible.

(25) Specifications comparisons between the AX81-S2 and the A1SX81

| Specification |  | AX81-S2 | A1SX81 | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Number of input points |  | 32 points | 32 points | $\bigcirc$ |  |
| Isolation method |  | Photocoupler | Photocoupler | $\bigcirc$ |  |
| Rated input voltage |  | 48/60VDC | 12/24VDC | $\times$ | Voltages exceeding 26.4VDC cannot be applied. ${ }^{* 1}$ |
| Rated input current |  | $3 \mathrm{~mA} / 4 \mathrm{~mA}$ | Approx. 3mA/Approx. 7 mA | $\bigcirc$ |  |
| Operating voltage range |  | 41 to 66VDC (ripple ratio within 5\%) | 10.2 to 26.4 VDC <br> (ripple ratio within 5\%) | $\times$ | Voltages exceeding 26.4VDC cannot be applied. ${ }^{* 1}$ |
| Maximum simultaneous input points |  | 60\% (5points/common) simultaneously ON | Refer to the derating chart. *2 | $\bigcirc$ |  |
| ON voltage/ON current |  | 31 VDC or more/1.7mA or more | 8VDC or more/2mA or more | $\times$ | Voltages exceeding 26.4VDC cannot be applied. ${ }^{*}$ |
| OFF voltage/OFF current |  | 10 VDC or less/0.5mA or less | 4VDC or less/1mA or less | $\times$ | Voltages exceeding 26.4VDC cannot be applied. ${ }^{*}$ |
| Input resistance |  | Approx. $18 \mathrm{k} \Omega$ | Approx. $3.3 \mathrm{k} \Omega$ | $\times$ | Input resistance is smaller. |
| Response time | $\mathrm{OFF} \rightarrow \mathrm{ON}$ | 20 ms or less (60VDC) | 10 ms or less (24VDC) | $\bigcirc$ |  |
|  | $\mathrm{ON} \rightarrow$ OFF | 20 ms or less (60VDC) | 10 ms or less (24VDC) | $\bigcirc$ |  |
| Common terminal arrangement |  | 8 points/common (common terminal: TB9,TB18,TB27,TB36) | 32 points/common (common terminal: 17, 18, 36) | $\Delta$ | As 4 commons are reduced to 1 , wiring a different voltage for each common is not possible. |
| Operation indicator |  | ON indication (LED) | ON indication (LED) | $\bigcirc$ |  |
| External connection |  | 38-point terminal block connector (M3×6 screws) | 37-pin D sub-connector (included) | $\times$ |  |
| Applicable wire size |  | 0.75 to $2 \mathrm{~mm}^{2}$ | $0.3 \mathrm{~mm}^{2}$ | $\times$ | Wiring must be changed. ${ }^{* 3}$ |
| Applicable solderless terminal |  | $\begin{gathered} \text { R1.25-3,R2-3, } \\ \text { RAV1.25-3,RAV2-3 } \end{gathered}$ | - | $\times$ |  |
| Current consumption |  | $0.110 \mathrm{~A}$ <br> (TYP. all points ON) | 0.080A <br> (TYP. all points ON) | $\bigcirc$ |  |
| External dimensions |  | $250(\mathrm{H}) \times 37.5(\mathrm{~W}) \times 131(\mathrm{D}) \mathrm{mm}$ | 130(H) $\times 34.5(\mathrm{~W}) \times 93.6(\mathrm{D}) \mathrm{mm}$ | $\triangle$ |  |
| Weight |  | 0.45 kg | 0.24 kg | $\bigcirc$ |  |

*1 For use of 48VDC, connect a resistor of $3.3 \mathrm{k} \Omega$ (1W or more) in series with the external signal line connected between the A1SX81 and an external device.
For use of 60 VDC , connect a resistor of $5.6 \mathrm{k} \Omega$ ( 2 W or more) in series with the external signal line connected between the A1SX81 and an external device.
*2 The figure on the right shows derating.
*3 By using connectors/terminal block converter modules (A6TBX36-E, etc.), conversion to the terminal block is possible.

(26) Specifications comparisons between the AX81-S3 and the A1SX80-S1

| Specification |  | AX81-S3 | A1SX80-S1 | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Number of input points |  | 32 points | 16 points | $\times$ | When seventeen or more points are used, use two of the A1SX80-S1 modules. |
| Isolation method |  | Photocoupler | Photocoupler | $\bigcirc$ |  |
| Rated input voltage |  | 12/24VDC | 24VDC | $\triangle$ | 12VDC cannot be used. |
| Rated input current |  | $4 \mathrm{~mA} / 10 \mathrm{~mA}$ | Approx. 7 mA | $\triangle$ | Rated input current is smaller. ${ }^{*}$ |
| Operating voltage range |  | $\begin{gathered} 10.2 \text { to } 26.4 \mathrm{VDC} \\ \text { (ripple ratio within 5\%) } \end{gathered}$ | $19.2 \text { to } 26.4 \mathrm{VDC}$ <br> (ripple ratio within 5\%) | $\triangle$ | 12VDC cannot be used. |
| Maximum simultaneous input points |  | 60\% (5points/common) simultaneously ON | 85\% simultaneously ON (26.4VDC) | $\triangle$ | Use within the specification range. |
| ON voltage/ON current |  | 9.5 VDC or more/3mA or more | 17VDC or more/5mA or more | $\triangle$ | 12VDC cannot be used. |
| OFF voltage/OFF current |  | 6 VDC or less/1.5mA or less | 5 VDC or less/1.7mA or less | $\triangle$ | 12VDC cannot be used. |
| Input resistance |  | Approx. $2.4 \mathrm{k} \Omega$ | Approx. $3.3 \mathrm{k} \Omega$ | $\triangle$ | Input resistance is greater. ${ }^{* 1}$ |
| Response time | $\mathrm{OFF} \rightarrow \mathrm{ON}$ | 0.1 ms or less | $\begin{gathered} 0.4 \mathrm{~ms} \\ (24 \mathrm{VDC}) \end{gathered}$ | $\triangle$ | The response times differ. |
|  | $\mathrm{ON} \rightarrow \mathrm{OFF}$ | 0.2 ms or less | $\begin{gathered} 0.5 \mathrm{~ms} \\ (24 \mathrm{VDC}) \end{gathered}$ | $\triangle$ |  |
| Common terminal arrangement |  | 8 points/common (common terminal: TB9,TB18,TB27,TB36) | 16 points/common (common terminal: TB9,TB18) | $\triangle$ | As 2 commons are reduced to 1 , wiring a different voltage for each common is not possible. |
| Operation indicator |  | ON indication (LED) | ON indication (LED) | $\bigcirc$ |  |
| External connection |  | 38-point terminal block connector (M3×6 screws) | 20-point terminal block connector (M3.5×7 screws) | $\times$ | Wiring must be changed. |
| Applicable wire size |  | 0.75 to $2 \mathrm{~mm}^{2}$ | 0.75 to $1.25 \mathrm{~mm}^{2}$ | $\times$ |  |
| Applicable solderless terminal |  | $\begin{gathered} \text { R1.25-3,R2-3, } \\ \text { RAV1.25-3,RAV2-3 } \end{gathered}$ | R1.25-3.5,R2-3.5, RAV1.25-3.5,RAV2-3.5 | $\times$ |  |
| Current consumption |  | $0.110 \mathrm{~A}$ <br> (TYP. all points ON) | 0.050 A (TYP. all points ON) | $\bigcirc$ |  |
| External dimensions |  | $250(\mathrm{H}) \times 37.5(\mathrm{~W}) \times 121(\mathrm{D}) \mathrm{mm}$ | $130(\mathrm{H}) \times 34.5(\mathrm{~W}) \times 93.6(\mathrm{D}) \mathrm{mm}$ | $\triangle$ | The dimensions are different. |
| Weight |  | 0.45 kg | 0.2 kg | $\bigcirc$ |  |

*1 Check the specifications of sensor or switch to connect to the A1SX80-S1.
(27) Specifications comparisons between the AX82 and the A1SX82-S1

| Specification |  | AX82 | A1SX82-S1 | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Number of input points |  | 64 points | 64 points | $\bigcirc$ |  |
| Isolation method |  | Photocoupler | Photocoupler | $\bigcirc$ |  |
| Rated input voltage |  | 12/24VDC | 24VDC | $\triangle$ | 12VDC cannot be used. |
| Rated input current |  | Approx. 3mA/Approx. 7mA | Approx. 5mA | $\triangle$ | Rated input current is smaller. ${ }^{* 1}$ |
| Operating voltage range |  | $\begin{gathered} 10.2 \text { to } 26.4 \mathrm{VDC} \\ \text { (ripple ratio within 5\%) } \end{gathered}$ | 19.2 to 26.4 VDC (ripple ratio within 5\%) | $\triangle$ | 12VDC cannot be used. |
| Maximum simultaneous input points |  | 40 points <br> (When located next to the power supply module : 26 points) | 50\% (16points/common) simultaneously ON (24VDC) | $\triangle$ | Use within the specification range. |
| ON voltage/ON current |  | 9.5 VDC or more/2.6mA or more | 18.5VDC or more/3.5mA or more | $\triangle$ | 12VDC cannot be used. |
| OFF voltage/OFF current |  | 6VDC or less/1.0mA or less | 3 VDC or less/0.45mA or less | $\triangle$ | 12VDC cannot be used. |
| Input resistance |  | Approx. $3.4 \mathrm{k} \Omega$ | Approx. $4.7 \mathrm{k} \Omega$ | $\triangle$ | Input impedance is larger. ${ }^{* 1}$ |
| Response time | $\mathrm{OFF} \rightarrow \mathrm{ON}$ | 10 ms or less | $\begin{gathered} 0.3 \mathrm{~ms} \\ (24 \mathrm{VDC}) \end{gathered}$ | $\bigcirc$ |  |
|  | $\mathrm{ON} \rightarrow \mathrm{OFF}$ | 10 ms or less | $\begin{gathered} 0.3 \mathrm{~ms} \\ (24 \mathrm{VDC}) \end{gathered}$ | $\bigcirc$ |  |
| Common terminal arrangement |  | 32 points/common (common terminal: $1-17,1-18,1-36,2-17,2-18,2-36)$ | 32 points/common (common terminal: 1B1,1B2,2B1,2B2) | $\bigcirc$ |  |
| Operation indicator |  | ON indication (LED) <br> 32 point switch-over using a switch | ON indication (LED) <br> 32 point switch-over using a switch | $\bigcirc$ |  |
| External connection |  | 37-pin D sub-connector (with solder) $\times 2$ | 40-pin connector <br> $\times 2$ (included) | $\times$ | Connector must be changed. |
| Applicable wire size |  | $0.3 \mathrm{~mm}^{2}$ | $0.3 \mathrm{~mm}^{2}$ | $\bigcirc$ |  |
| Accessory |  | External wiring D sub connectors $\times 2$ | External wiring connectors $\times 2$ | $\triangle$ | The shapes of the accessory connectors are different. |
| Current consumption |  | $0.12 \mathrm{~A}$ <br> (TYP. all points ON) | $0.16 \mathrm{~A}$ <br> (TYP. all points ON) | $\bigcirc$ |  |
| External dimensions |  | $250(\mathrm{H}) \times 37.5(\mathrm{~W}) \times 106$ (D) mm | $130(\mathrm{H}) \times 34.5(\mathrm{~W}) \times 93.6(\mathrm{D}) \mathrm{mm}$ | $\triangle$ |  |
| Weight |  | 0.60 kg | 0.28 kg | $\bigcirc$ |  |

*1 Check the specifications of sensor or switch to connect to the A1SX82-S1.

### 3.2.2 Specifications comparisons between output modules

(1) Specifications comparisons between the AY10 and the A1SY10
$\bigcirc$ : Compatible, $\Delta$ : Partial change required, $\times$ : Incompatible

| Specification | AY10 | A1SY10 | Compati- <br> bility |
| :--- | :---: | :---: | :---: |
| Number of output <br> points | Precautions for <br> replacement |  |  |
| Isolation method | 16 points | Photocoupler | O |

## (2) Specifications comparisons between the AY10A and the A1SY18A


(3) Specifications comparisons between the AY11 and the A1SY10

| Specification |  | AY11 | A1SY10 | $\left\lvert\, \begin{gathered} \text { Compati- } \\ \text { bility } \end{gathered}\right.$ | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Number of output points |  | 16 points | 16 points | $\bigcirc$ |  |
| Isolation method |  | Photocoupler | Photocoupler | $\bigcirc$ |  |
| Rated switching voltage/current |  | 24VDC 2A (resistive load)/point 240VAC 2A (COS $\phi=1$ )/point 8A/common | 24VDC 2A (resistive load)/point 240VAC 2A ( $\operatorname{COS} \phi=1$ )/point 8A/common | $\bigcirc$ |  |
| Minimum switching load |  | 5VDC 1mA | 5VDC 1mA | $\bigcirc$ |  |
| Maximum switching voltage |  | 264VAC 125VDC | 264VAC 125VDC | $\bigcirc$ |  |
| Leakage current at OFF |  | $\begin{gathered} 0.1 \mathrm{~mA} \\ (200 \mathrm{VAC}, 60 \mathrm{~Hz}) \end{gathered}$ | - | $\bigcirc$ |  |
| Response time | $\mathrm{OFF} \rightarrow \mathrm{ON}$ | 10 ms or less | 10 ms or less | $\bigcirc$ |  |
|  | ON $\rightarrow$ OFF | 12 ms or less | 12 ms or less | $\bigcirc$ |  |
| Mechanical life |  | 20 million times or more | 20 million times or more | $\bigcirc$ |  |
| Electrical life |  | Rated switching voltage/current load 200 thousand times or more | Rated switching voltage/current load 100 thousand times or more | $\triangle$ |  |
|  |  | 200VAC 1.5A, 240VAC 1A $(\operatorname{COS} \phi=0.7) 200$ thousand times or more <br> 200VAC 0.75A, 240VAC 0.5A $(\operatorname{COS} \phi=0.35) 200$ thousand times or more <br> 24VDC 1A, 100VDC 0.1A (L/R=7ms) 200 thousand times or more | 200VAC 1.5A, 240VAC 1A $(\operatorname{COS} \phi=0.7) 100$ thousand times or more $200 \mathrm{VAC} 1 \mathrm{~A}, 240 \mathrm{VAC} 0.5 \mathrm{~A}$ $(\operatorname{COS} \phi=0.35) 100$ thousand times or more $24 \mathrm{VDC} 1 \mathrm{~A}, 100 \mathrm{VDC} 0.1 \mathrm{~A}$ $(\mathrm{~L} / \mathrm{R}=7 \mathrm{~ms}) 100$ thousand times or more | $\triangle$ | Reduce the exchange intervals of the modules as Mechanical/Electrical Life is cut to about half. |
| Maximum switching frequency |  | 3600 times/hour | 3600 times/hour | $\bigcirc$ |  |
| Surge suppressor |  | Varistor (387 to 473V) | None | $\times$ | The varistor is not built in. ${ }^{*}$ |
| Relay socket |  | Yes | None | $\times$ | Replace the module itself when its relay has a failure. |
| Common terminal arrangement |  | 8 points/common (common terminal: TB9,TB18) | 8 points/common (common terminal: TB9,TB18) | $\bigcirc$ |  |
| Operation indicator |  | ON indication (LED) | ON indication (LED) | $\bigcirc$ |  |
| External supply power | Voltage | $\begin{gathered} 24 \mathrm{VDC} \pm 10 \% \\ \text { Ripple voltage } 4 \mathrm{Vp}-\mathrm{p} \text { or less } \end{gathered}$ | $\begin{gathered} 24 \mathrm{VDC} \pm 10 \% \\ \text { Ripple voltage } 4 \mathrm{Vp}-\mathrm{p} \text { or less } \end{gathered}$ | $\bigcirc$ |  |
|  | Current | 150 mA (24VDC TYP. all points ON) | 90 mA (24VDC TYP. all points ON) | $\bigcirc$ |  |
| External connection |  | 20-point terminal block connector (M3×6 screws) | 20-point terminal block connector (M3.5×7 screws) | $\times$ |  |
| Applicable wire size |  | 0.75 to $2 \mathrm{~mm}^{2}$ | 0.75 to $1.25 \mathrm{~mm}^{2}$ | $\times$ | Wiring must be changed. |
| Applicable solderless terminal |  | $\begin{gathered} \text { R1.25-3,R2-3, } \\ \text { RAV1.25-3,RAV2-3 } \end{gathered}$ | $\begin{gathered} \text { R1.25-3.5,R2-3.5, } \\ \text { RAV1.25-3.5,RAV2-3.5 } \end{gathered}$ | $\times$ |  |
| Current consumption |  | $0.115 \mathrm{~A}$ <br> (TYP. all points ON) | $0.120 \mathrm{~A}$ <br> (TYP. all points ON) | $\triangle$ | Current capacity must be reviewed. |
| External dimensions |  | $250(\mathrm{H}) \times 37.5(\mathrm{~W}) \times 121(\mathrm{D}) \mathrm{mm}$ | $130(\mathrm{H}) \times 34.5(\mathrm{~W}) \times 93.6(\mathrm{D}) \mathrm{mm}$ | $\triangle$ | The dimensions are different. |
| Weight |  | 0.50 kg | 0.25 kg | $\bigcirc$ |  |

*1 Connect a varistor to reduce external noise.

I/O MODULE REPLACEMENT
(4) Specifications comparisons between the AY11A and the A1SY18A

| Specification |  | AY11A | A1SY18A | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Number of output points |  | 16 points | 8 points (16 points occupied) | $\times$ | When nine or more points are used, use two of the A1SY18A modules. |
| Isolation method |  | Photocoupler | Photocoupler | 0 |  |
| Rated switching voltage/current |  | 24VDC 2A (resistive load) /point 240VAC $2 \mathrm{~A}(\operatorname{COS} \phi=1$ )/point 16A/all points | 24VDC 2A (resistive load)/point 240VAC 2A (COS $\phi=1$ )/point 8A/module | $\bigcirc$ |  |
| Minimum switching load |  | 5VDC 1mA | 5 VDC 1 mA | $\bigcirc$ |  |
| Maximum switching voltage |  | 264VAC 125VDC | 264VAC 125VDC | $\bigcirc$ |  |
| Leakage current at OFF |  | 0.1mA (200VAC, 60 Hz ) | - | 0 |  |
| Response time | OFF $\rightarrow$ ON | 10 ms or less | 10 ms or less | $\bigcirc$ |  |
|  | ON $\rightarrow$ OFF | 12 ms or less | 12 ms or less | $\bigcirc$ |  |
| Mechanical life |  | 20 million times or more | 20 million times or more | $\bigcirc$ |  |
| Electrical life |  | Rated switching voltage/current load <br> 200 thousand times or more | Rated switching voltage/current load <br> 200 thousand times or more | O |  |
|  |  | 200VAC 1.5A, 240VAC 1A ( $\operatorname{COS} \phi=0.7$ ) 200 thousand times or more <br> 200VAC 0.75A, 240VAC 0.5A <br> $(\operatorname{COS} \phi=0.35) 200$ thousand times or more <br> 24VDC 1A, 100VDC 0.1A <br> ( $\mathrm{L} / \mathrm{R}=7 \mathrm{~ms}$ ) 200 thousand times or more | 200VAC 1.5A, 240VAC 1A $(\operatorname{COS} \phi=0.7) 200$ thousand times or more <br> 200VAC 0.75A, 240VAC 0.5A <br> (COS $\phi=0.35) 200$ thousand times or more <br> 24VDC 1A, 100VDC 0.1A <br> ( $\mathrm{L} / \mathrm{R}=7 \mathrm{~ms}$ ) 200 thousand times or more | $\bigcirc$ |  |
| Maximum switching frequency |  | 3600 times/hour | 3600 times/hour | $\bigcirc$ |  |
| Surge suppressor |  | Varistor (387 to 473V) | None | $\times$ | The varistor is not built in. ${ }^{* 1}$ |
| Common terminal arrangement |  | Not provided (all points independent) | Not provided (all points independent) | $\bigcirc$ |  |
| Operation indicator |  | ON indication (LED) | ON indication (LED) | $\bigcirc$ |  |
| External supply power | Voltage | $24 \mathrm{VDC} \pm 10 \%$ Ripple voltage 4 Vp -p or less | $24 \mathrm{VDC} \pm 10 \%$ Ripple voltage 4 Vp -p or less | $\bigcirc$ |  |
|  | Current | $\begin{gathered} 150 \mathrm{~mA} \\ \text { (24VDC TYP. all points ON) } \end{gathered}$ | 75 mA (24VDC TYP. all points ON) | $\bigcirc$ |  |
| External connection |  | 38-point terminal block connector (M3×6 screws) | 20-point terminal block connector (M3. $5 \times 7$ screws) | $\times$ |  |
| Applicable wire size |  | 0.75 to $2 \mathrm{~mm}^{2}$ | 0.75 to $1.25 \mathrm{~mm}^{2}$ | $\times$ | Wiring must be changed. |
| Applicable solderless terminal |  | R1.25-3,R2-3, RAV1.25-3,RAV2-3 | R1.25-3.5,R2-3.5, RAV1.25-3.5,RAV2-3.5 | $\times$ |  |
| Current consumption |  | 0.115 A (TYP. all points ON) | 0.240 A (TYP. all points ON) | $\Delta$ | Current capacity must be reviewed. |
| External dimensions |  | 250 (H) $\times 37.5(\mathrm{~W}) \times 131$ (D) mm | $130(\mathrm{H}) \times 34.5(\mathrm{~W}) \times 93.6$ (D) mm | $\triangle$ | The dimensions are different. |
| Weight |  | 0.47 kg | 0.25kg | $\triangle$ | When calculating the weight, note that the weight becomes heavier if the two or more A1SY18A modules are used. |

*1 Connect a varistor to reduce external noise.
(5) Specifications comparisons between the AY11AEU and the A1SY18AEU

| Specification |  | AY11AEU | A1SY18AEU | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Number of output points |  | 16 points | 8 points (16 points occupied) | $\times$ | When nine or more points are used, use two of the A1SY18AEU modules. |
| Isolation method |  | Photocoupler | Photocoupler | $\bigcirc$ |  |
| Rated switching voltage/current |  | 24VDC 2A (resistive load)/point 24VAC 2A (COS $\phi=1$ )/point 16A/all points | 24VDC 2A (resistive load)/point 240VAC 2A (COS $\phi=1$ )/point | $\bigcirc$ |  |
| Minimum switching load |  | 5VDC 1mA | 5VDC 1mA | $\bigcirc$ |  |
| Maximum switching voltage |  | 49.9VAC 74.9VDC | 264VAC 125VDC | $\bigcirc$ |  |
| Leakage current at OFF |  | 0.1 mA (49.9VAC, 60 Hz ) | - | $\bigcirc$ |  |
| Response time | $\mathrm{OFF} \rightarrow \mathrm{ON}$ | 10 ms or less | 10 ms or less | $\bigcirc$ |  |
|  | ON $\rightarrow$ OFF | 12 ms or less | 12 ms or less | $\bigcirc$ |  |
| Mechanical life |  | 20 million times or more | 20 million times or more | $\bigcirc$ |  |
| Electrical life |  | Rated switching voltage/current load 200 thousand times or more | Rated switching voltage/current load 200 thousand times or more | $\bigcirc$ |  |
|  |  | 24VAC 1.5A <br> $(\operatorname{COS} \phi=0.7) 200$ thousand times or more 24VAC 0.75A <br> $(\operatorname{COS} \phi=0.35) 200$ thousand times or more <br> 24VDC 1A, 48VDC 0.1A (L/R=7ms) 200 thousand times or more | 200VAC 1.5A, 240VAC 1A $(\operatorname{COS} \phi=0.7) 200$ thousand times or more $200 \mathrm{VAC} 0.75 \mathrm{~A}, 240 \mathrm{VAC} 0.5 \mathrm{~A}$ $(\operatorname{COS} \phi=0.35) 200$ thousand times or more $24 \mathrm{VDC} 1 \mathrm{~A}, 100 \mathrm{VDC} 0.1 \mathrm{~A}$ $(\mathrm{~L} / \mathrm{R}=7 \mathrm{~ms}) 200$ thousand times or more | $\bigcirc$ |  |
| Maximum switching frequency |  | 3600 times/hour | 3600 times/hour | $\bigcirc$ |  |
| Surge suppressor |  | Varistor (387 to 473V) | None | $\times$ | The varistor is not built in. ${ }^{* 1}$ |
| Common terminal arrangement |  | Not provided (all points independent) | Not provided (all points independent) | $\bigcirc$ |  |
| Operation indicator |  | ON indication (LED) | ON indication (LED) | $\bigcirc$ |  |
| External supply power | Voltage | $\begin{gathered} 24 \mathrm{VDC} \pm 10 \% \\ \text { Ripple voltage } 4 \mathrm{Vp}-\mathrm{p} \text { or less } \end{gathered}$ | $\begin{gathered} 24 \mathrm{VDC} \pm 10 \% \\ \text { Ripple voltage } 4 \mathrm{Vp}-\mathrm{p} \text { or less } \end{gathered}$ | $\bigcirc$ |  |
|  | Current | 150mA (24VDC TYP. all points ON) | 75mA (24VDC TYP. all points ON) | $\bigcirc$ |  |
| External connection |  | 38-point terminal block connector (M3×6 screws) | 20-point terminal block connector (M3.5×7 screws) | $\times$ |  |
| Applicable wire size |  | 0.75 to $2 \mathrm{~mm}^{2}$ | 0.75 to $1.25 \mathrm{~mm}^{2}$ | $\times$ | Wiring must be changed. |
| Applicable solderless terminal |  | $\begin{gathered} \text { R1.25-3,R2-3, } \\ \text { RAV1.25-3,RAV2-3 } \end{gathered}$ | RAV1.25-3.5 | $\times$ |  |
| Current consumption |  | 0.115A <br> (TYP. all points ON) | 0.240A <br> (TYP. all points ON) | $\Delta$ | Current capacity must be reviewed. |
| External dimensions |  | $250(\mathrm{H}) \times 37.5(\mathrm{~W}) \times 131(\mathrm{D}) \mathrm{mm}$ | $130(\mathrm{H}) \times 34.5(\mathrm{~W}) \times 93.6(\mathrm{D}) \mathrm{mm}$ | $\triangle$ | The dimensions are different. |
| Weight |  | 0.47 kg | 0.25 kg | $\triangle$ | When calculating the weight, note that the weight becomes heavier if the two or more A1SY18AEU modules are used. |

*1 Connect a varistor to reduce external noise.

## (6) Specifications comparisons between the AY11E and the A1SY10

|  |  |  |  |
| :--- | :---: | :---: | :---: |
| Specification | AY11E | A1SY10 | Compati- <br> bility |
| Number of output <br> points | replacement |  |  |

[^4](7) Specifications comparisons between the AY11EEU and the A1SY10EU

| Specification |  | AY11EEU | A1SY10EU | $\begin{gathered} \text { Compati- } \\ \text { bility } \end{gathered}$ | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Number of output points |  | 16 points | 16 points | $\bigcirc$ |  |
| Isolation method |  | Photocoupler | Photocoupler | $\bigcirc$ |  |
| Rated switching voltage/current |  | 24VDC 2A (resistive load)/point 24VAC 2A (COS $\phi=1$ )/point 8A/common | 24VDC 2A (resistive load)/point 240 VAC 2 A ( $\operatorname{COS} \phi=1$ )/point 8A/common | $\bigcirc$ |  |
| Minimum switching load |  | $5 \mathrm{VDC} \mathrm{1mA}$ | 5VDC 1mA | $\bigcirc$ |  |
| Maximum switching voltage |  | 49.9VAC 74.9VDC | 132VAC 125VDC | 0 |  |
| Leakage current at OFF |  | 0.1 mA ( $49.9 \mathrm{VAC}, 60 \mathrm{~Hz}$ ) | - | $\bigcirc$ |  |
| Response time | OFF $\rightarrow$ ON | 10 ms or less | 10 ms or less | $\bigcirc$ |  |
|  | ON $\rightarrow$ OFF | 12 ms or less | 12 ms or less | $\bigcirc$ |  |
| Mechanical life |  | 20 million times or more | 20 million times or more | 0 |  |
| Electrical life |  | Rated switching voltage/current load <br> 200 thousand times or more | Rated switching voltage/current load 100 thousand times or more | $\Delta$ |  |
|  |  | 24VAC 1.5A <br> $(\operatorname{COS} \phi=0.7) 200$ thousand times or more 24VAC 0.75A <br> $(\operatorname{COS} \phi=0.35) 200$ thousand times or more <br> 24VDC 1A, 48VDC 0.1A (L/R=7ms) 200 thousand times or more | 100VAC 2A, 120VAC 2A <br> (COS $\phi=0.7$ ) 200 thousand times or more <br> 100VAC 2A, 120VAC 2A <br> $(\operatorname{COS} \phi=0.35) 100$ thousand times or more <br> 24VDC 1.5A, 100VDC 0.1A <br> (L/R=7ms) 100 thousand times or more | $\Delta$ | Reduce the exchange intervals of the modules as Mechanical/Electrical Life is cut to about half. |
| Maximum switching frequency |  | 3600 times/hour | 3600 times/hour | 0 |  |
| Surge suppressor |  | Varistor (387 to 473V) | None | $\times$ | The varistor is not built in. ${ }^{*}$ |
| Common terminal arrangement |  | 8 points/common (common terminal: TB9,TB18) | 8 points/common (common terminal: TB9,TB18) | $\bigcirc$ |  |
| Operation indicator |  | ON indication (LED) | ON indication (LED) | 0 |  |
| Fuse |  | 8A MF51NM8 or FGMA250V8A | None | $\times$ | The fuse is not built in. ${ }^{*}$ |
| Fuse blow indicator |  | None | None | $\bigcirc$ |  |
| External supply power | Voltage | $24 \mathrm{VDC} \pm 10 \%$ Ripple voltage 4 Vp -p or less | $24 \mathrm{VDC} \pm 10 \%$ Ripple voltage 4 Vp -p or less | $\bigcirc$ |  |
|  | Current | 150 mA (24VDC TYP. all points ON) | 90 mA (24VDC TYP. all points ON) | 0 |  |
| External connection |  | 20-point terminal block connector (M3×6 screws) | 20-point terminal block connector (M3. $5 \times 7$ screws) | $\times$ |  |
| Applicable wire size |  | 0.75 to $2 \mathrm{~mm}^{2}$ | 0.75 to $1.25 \mathrm{~mm}^{2}$ | $\times$ | Wiring must be changed. |
| Applicable solderless terminal |  | R1.25-3,R2-3, RAV1.25-3,RAV2-3 | RAV1.25-3.5 | $\times$ |  |
| Current consumption |  | 0.115A <br> (TYP. all points ON) | $\begin{gathered} 0.120 \mathrm{~A} \\ \text { (TYP. all points ON) } \end{gathered}$ | $\Delta$ | Current capacity must be reviewed. |
| External dimensions |  | $250(\mathrm{H}) \times 37.5(\mathrm{~W}) \times 121$ (D) mm | $130(\mathrm{H}) \times 34.5(\mathrm{~W}) \times 93.6$ (D) mm | $\triangle$ | The dimensions are different. |
| Weight |  | 0.47 kg | 0.25 kg | $\bigcirc$ |  |

*1 Connect a varistor to reduce external noise.
*2 Mount a fuse on every external terminal to prevent external devices and modules from burning out upon load short circuit.
(8) Specifications comparisons between the AY11-UL and the A1SY10

| Specification |  | AY11-UL | A1SY10 | $\left\|\begin{array}{c} \text { Compati- } \\ \text { bility } \end{array}\right\|$ | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Number of output points |  | 16 points | 16 points | $\bigcirc$ |  |
| Isolation method |  | Photocoupler | Photocoupler | $\bigcirc$ |  |
| Rated switching voltage/current |  | 24VDC 2A (resistive load)/point 240VAC 2A (COS $\phi=1$ )/point 8A/common | 24VDC 2A (resistive load)/point 240VAC 2A (COS $\phi=1$ )/point 8A/common | $\bigcirc$ |  |
| Minimum switching load |  | 5VDC 1mA | 5VDC 1mA | $\bigcirc$ |  |
| Maximum switching voltage |  | 264VAC 125VDC | 264VAC 125VDC | $\bigcirc$ |  |
| Leakage current at OFF |  | 0.1 mA (200VAC, 60 Hz ) | - | $\bigcirc$ |  |
| Response time | $\mathrm{OFF} \rightarrow \mathrm{ON}$ | 10 ms or less | 10 ms or less | $\bigcirc$ |  |
|  | ON $\rightarrow$ OFF | 12 ms or less | 12 ms or less | $\bigcirc$ |  |
| Mechanical life |  | 20 million times or more | 20 million times or more | $\bigcirc$ |  |
| Electrical life |  | Rated switching voltage/current load 200 thousand times or more | Rated switching voltage/current load 100 thousand times or more | $\triangle$ |  |
|  |  | 200VAC 1.5A, 240VAC 1A (COS $\phi=0.7$ ) 200 thousand times or more <br> 200VAC 0.75A, 240VAC 0.5A (COS $\phi=0.35$ ) 200 thousand times or more <br> 24VDC 1A, 100VDC 0.1A <br> (L/R=7ms) 200 thousand times or more | 200VAC 1.5A, 240VAC 1A $(\operatorname{COS} \phi=0.7) 100$ thousand times or more $200 \mathrm{VAC} 1 \mathrm{~A}, 240 \mathrm{VAC} 0.5 \mathrm{~A}$ $(\operatorname{COS} \phi=0.35) 100$ thousand times or more $24 \mathrm{VDC} 1 \mathrm{~A}, 100 \mathrm{VDC} 0.1 \mathrm{~A}$ $(\mathrm{~L} / \mathrm{R}=7 \mathrm{~ms}) 100$ thousand times or more | $\triangle$ | Reduce the exchange intervals of the modules as Mechanical/Electrical Life is cut to about half. |
| Maximum switching frequency |  | 3600 times/hour | 3600 times/hour | $\bigcirc$ |  |
| Surge suppressor |  | Varistor (387 to 473V) | None | $\times$ | The varistor is not built in. ${ }^{* 1}$ |
| Relay socket |  | Yes | None | $\times$ | Replace the module itself when its relay has a failure. |
| Common terminal arrangement |  | 8 points/common (common terminal: TB9,TB18) | 8 points/common (common terminal: TB9,TB18) | $\bigcirc$ |  |
| Operation indicator |  | ON indication (LED) | ON indication (LED) | $\bigcirc$ |  |
| External supply power | Voltage | $24 \mathrm{VDC} \pm 10 \%$ Ripple voltage $4 \mathrm{Vp}-\mathrm{p}$ or less | $\begin{gathered} 24 \mathrm{VDC} \pm 10 \% \\ \text { Ripple voltage } 4 \mathrm{Vp}-\mathrm{p} \text { or less } \end{gathered}$ | $\bigcirc$ |  |
|  | Current | 150 mA (24VDC TYP. all points ON) | 90 mA (24VDC TYP. all points ON) | $\bigcirc$ |  |
| External connection |  | 20-point terminal block connector (M3.5×6 screws) | 20-point terminal block connector (M3.5×7 screws) | $\times$ |  |
| Applicable wire size |  | 0.75 to $2 \mathrm{~mm}^{2}$ | 0.75 to $1.25 \mathrm{~mm}^{2}$ | $\times$ | Wiring must be changed. |
| Applicable solderless terminal |  | R1.25-3.5,R2-3.5, RAV1.25-3.5,RAV2-3.5 | R1.25-3.5,R2-3.5, RAV1.25-3.5,RAV2-3.5 | $\bigcirc$ |  |
| Current consumption |  | 0.12A <br> (TYP. all points ON) | $0.12 \mathrm{~A}$ <br> (TYP. all points ON) | $\bigcirc$ |  |
| External dimensions |  | $250(\mathrm{H}) \times 37.5(\mathrm{~W}) \times 121(\mathrm{D}) \mathrm{mm}$ | $130(\mathrm{H}) \times 34.5(\mathrm{~W}) \times 93.6(\mathrm{D}) \mathrm{mm}$ | $\triangle$ | The dimensions are different. |
| Weight |  | 0.50 kg | 0.25 kg | $\bigcirc$ |  |

*1 Connect a varistor to reduce external noise.
(9) Specifications comparisons between the AY13 and the A1SY10

| Specification |  | AY13 | A1SY10 | Compati-\| bility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Number of output points |  | 32 points | 16 points | $\times$ | When seventeen or more points are used, use two of the A1SY10 modules. |
| Isolation method |  | Photocoupler | Photocoupler | $\bigcirc$ |  |
| Rated switching voltage/current |  | 24VDC 2A (resistive load)/point 240VAC 2A (COS $\phi=1$ )/point 5A/common | 24VDC 2A (resistive load)/point 240VAC 2A (COS $\phi=1$ )/point 8A/common | $\bigcirc$ |  |
| Minimum switching load |  | 5VDC 1mA | 5VDC 1mA | $\bigcirc$ |  |
| Maximum switching voltage |  | 264VAC 125VDC | 264VAC 125VDC | $\bigcirc$ |  |
| Response time | $\mathrm{OFF} \rightarrow \mathrm{ON}$ | 10 ms or less | 10 ms or less | $\bigcirc$ |  |
|  | ON $\rightarrow$ OFF | 12 ms or less | 12 ms or less | $\bigcirc$ |  |
| Mechanical life |  | 20 million times or more | 20 million times or more | $\bigcirc$ |  |
| Electrical life |  | Rated switching voltage/current load 200 thousand times or more | Rated switching voltage/current load 100 thousand times or more | $\triangle$ |  |
|  |  | 200VAC 1.5A, 240VAC 1A $(\operatorname{COS} \phi=0.7) 200$ thousand times or more $200 \mathrm{VAC} 0.75 \mathrm{~A}, 240 \mathrm{VAC} 0.5 \mathrm{~A}$ $(\operatorname{COS} \phi=0.35) 200$ thousand times or more $24 \mathrm{VDC} 1 \mathrm{~A}, 100 \mathrm{VDC} 0.1 \mathrm{~A}$ $(\mathrm{~L} / \mathrm{R}=7 \mathrm{~ms}) 200$ thousand times or more | 200VAC 1.5A, 240VAC 1A $(\operatorname{COS} \phi=0.7) 100$ thousand times or more $200 \mathrm{VAC} 1 \mathrm{~A}, 240 \mathrm{VAC} 0.5 \mathrm{~A}$ $(\operatorname{COS} \phi=0.35) 100$ thousand times or more $24 \mathrm{VDC} 1 \mathrm{~A}, 100 \mathrm{VDC} 0.1 \mathrm{~A}$ $(\mathrm{~L} / \mathrm{R}=7 \mathrm{~ms}) 100$ thousand times or more | $\triangle$ | Reduce the exchange intervals of the modules as Mechanical/Electrical Life is cut to about half. |
| Maximum switching frequency |  | 3600 times/hour | 3600 times/hour | $\bigcirc$ |  |
| Common terminal arrangement |  | 8 points/common (common terminal: TB9,TB18,TB27,TB36) | 8 points/common (common terminal: TB9,TB18) | $\bigcirc$ |  |
| Operation indicator |  | ON indication (LED) | ON indication (LED) | $\bigcirc$ |  |
| External supply power | Voltage | $24 \mathrm{VDC} \pm 10 \%$ Ripple voltage $4 \mathrm{Vp}-\mathrm{p}$ or less | $24 \mathrm{VDC} \pm 10 \%$ Ripple voltage $4 \mathrm{Vp}-\mathrm{p}$ or less | $\bigcirc$ |  |
|  | Current | 290mA (24VDC TYP. all points ON) | 90mA (24VDC TYP. all points ON) | $\bigcirc$ |  |
| External connection |  | 38-point terminal block connector (M3×6 screws) | 20-point terminal block connector (M3.5×7 screws) | $\times$ |  |
| Applicable wire size |  | 0.75 to $2 \mathrm{~mm}^{2}$ | 0.75 to $1.25 \mathrm{~mm}^{2}$ | $\times$ | Wiring must be changed. |
| Applicable solderless terminal |  | $\begin{gathered} \text { R1.25-3,R2-3, } \\ \text { RAV1.25-3,RAV2-3 } \end{gathered}$ | $\begin{gathered} \text { R1.25-3.5,R2-3.5, } \\ \text { RAV1.25-3.5,RAV2-3.5 } \end{gathered}$ | $\times$ |  |
| Current consumption |  | 0.23 A (TYP. all points ON) | 0.12 A (TYP. all points ON) | $\triangle$ | Review current capacity hen using the two A1SY10 modules. |
| External dimensions |  | $250(\mathrm{H}) \times 37.5(\mathrm{~W}) \times 131(\mathrm{D}) \mathrm{mm}$ | $130(\mathrm{H}) \times 34.5(\mathrm{~W}) \times 93.6(\mathrm{D}) \mathrm{mm}$ | $\triangle$ | The dimensions are different. |
| Weight |  | 0.59 kg | 0.25 kg | $\bigcirc$ |  |

(10) Specifications comparisons between the AY13E and the A1SY10

\begin{tabular}{|c|c|c|c|c|c|}
\hline \multicolumn{2}{|l|}{Specification} \& AY13E \& A1SY10 \& \[
\left\lvert\, \begin{gathered}
\text { Compati- } \\
\text { bility }
\end{gathered}\right.
\] \& Precautions for replacement \\
\hline \multicolumn{2}{|l|}{Number of output points} \& 32 points \& 16 points \& \(\times\) \& When seventeen or more points are used, use two of the A1SY10 modules. \\
\hline \multicolumn{2}{|l|}{Isolation method} \& Photocoupler \& Photocoupler \& \(\bigcirc\) \& \\
\hline \multicolumn{2}{|l|}{Rated switching voltage/ current} \& 24VDC 2A (resistive load)/point 240VAC 2A (COS \(\phi=1\) )/point 5A/common \& 24VDC 2A (resistive load)/point 240VAC 2A (COS \(\phi=1\) )/point 8A/common \& \(\bigcirc\) \& \\
\hline \multicolumn{2}{|l|}{Minimum switching load} \& 5VDC 1mA \& 5VDC 1mA \& \(\bigcirc\) \& \\
\hline \multicolumn{2}{|l|}{Maximum switching voltage} \& 250VAC 125VDC \& 264VAC 125VDC \& \(\bigcirc\) \& \\
\hline \multirow[t]{2}{*}{Response time} \& \(\mathrm{OFF} \rightarrow \mathrm{ON}\) \& 10 ms or less \& 10 ms or less \& \(\bigcirc\) \& \\
\hline \& ON \(\rightarrow\) OFF \& 12 ms or less \& 12 ms or less \& \(\bigcirc\) \& \\
\hline \multicolumn{2}{|l|}{Mechanical life} \& 20 million times or more \& 20 million times or more \& \(\bigcirc\) \& \\
\hline \multicolumn{2}{|l|}{Electrical life} \& \begin{tabular}{c} 
Rated switching voltage/current \\
load \\
200 thousand times or more \\
\hline \(200 \mathrm{VAC} 1.5 \mathrm{~A}, 240 \mathrm{VAC} 1 \mathrm{~A}\) \\
\((\mathrm{COS} \phi=0.7) 200\) thousand times \\
or more \\
\(200 \mathrm{VAC} 0.75 \mathrm{~A}, 240 \mathrm{VAC} 0.5 \mathrm{~A}\) \\
(COS \(\phi=0.35) 200\) thousand \\
times or more \\
\(24 \mathrm{VDC} 1 \mathrm{~A}, 100 \mathrm{VDC} 0.1 \mathrm{~A}\) \\
\((\mathrm{~L} / \mathrm{R}=7 \mathrm{~ms}) 200\) thousand times or \\
more
\end{tabular} \& Rated switching voltage/current
load
100 thousand times or more
\(200 \mathrm{VAC} 1.5 \mathrm{~A}, 240 \mathrm{VAC} 1 \mathrm{~A}\)
\((\operatorname{COS} \phi=0.7) 100\) thousand times
or more
\(200 \mathrm{VAC} 1 \mathrm{~A}, 240 \mathrm{VAC} 0.5 \mathrm{~A}\)
\((\mathrm{COS} \phi=0.35) 100\) thousand
times or more
\(24 \mathrm{VDC} 1 \mathrm{~A}, 100 \mathrm{VDC} 0.1 \mathrm{~A}\)
\((\mathrm{~L} / \mathrm{R}=7 \mathrm{~ms}) 100\) thousand times or
more \& \(\triangle\)

$\triangle$ \& Reduce the exchange intervals of the modules as Mechanical/Electrical Life is cut to about half. <br>
\hline \multicolumn{2}{|l|}{Maximum switching frequency} \& 3600 times/hour \& 3600 times/hour \& $\bigcirc$ \& <br>
\hline \multicolumn{2}{|l|}{Surge suppressor} \& None \& None \& $\bigcirc$ \& <br>
\hline \multicolumn{2}{|l|}{Common terminal arrangement} \& 8 points/common (common terminal: TB9,TB18,TB27,TB36) \& 8 points/common (common terminal: TB9,TB18) \& $\bigcirc$ \& <br>
\hline \multicolumn{2}{|l|}{Operation indicator} \& ON indication (LED) \& ON indication (LED) \& $\bigcirc$ \& <br>

\hline \multicolumn{2}{|l|}{Fuse} \& $$
\begin{gathered}
\text { 8A MF51NM8 } \\
\text { or } \\
\text { FGMA250V8A }
\end{gathered}
$$ \& None \& $\times$ \& The fuse is not built in. ${ }^{* 1}$ <br>

\hline \multicolumn{2}{|l|}{Fuse blow indicator} \& None \& None \& $\bigcirc$ \& <br>
\hline \multirow[t]{2}{*}{External supply power} \& Voltage \& $24 \mathrm{VDC} \pm 10 \%$

Ripple voltage $4 \mathrm{Vp}-\mathrm{p}$ or less \& $$
\begin{gathered}
24 \mathrm{VDC} \pm 10 \% \\
\text { Ripple voltage } 4 \mathrm{Vp}-\mathrm{p} \text { or less } \\
\hline
\end{gathered}
$$ \& $\bigcirc$ \& <br>

\hline \& Current \& 290mA (24VDC TYP. all points ON) \& 90mA (24VDC TYP. all points ON) \& $\bigcirc$ \& <br>
\hline \multicolumn{2}{|l|}{External connection} \& 38-point terminal block connector (M3×6 screws) \& 20-point terminal block connector (M3.5×7 screws) \& $\times$ \& <br>
\hline \multicolumn{2}{|l|}{Applicable wire size} \& 0.75 to $2 \mathrm{~mm}^{2}$ \& 0.75 to $1.25 \mathrm{~mm}^{2}$ \& $\times$ \& Wiring must be changed. <br>
\hline \multicolumn{2}{|l|}{Applicable solderless terminal} \& R1.25-3,R2-3, RAV1.25-3,RAV2-3 \& R1.25-3.5,R2-3.5, RAV1.25-3.5,RAV2-3.5 \& $\times$ \& <br>

\hline \multicolumn{2}{|l|}{Current consumption} \& | $0.23 \mathrm{~A}$ |
| :--- |
| (TYP. all points ON) | \& | $0.12 \mathrm{~A}$ |
| :--- |
| (TYP. all points ON) | \& $\Delta$ \& Review current capacity when using the two A1SY10 modules. <br>

\hline \multicolumn{2}{|l|}{External dimensions} \& $250(\mathrm{H}) \times 37.5(\mathrm{~W}) \times 131(\mathrm{D}) \mathrm{mm}$ \& $130(\mathrm{H}) \times 34.5(\mathrm{~W}) \times 93.6(\mathrm{D}) \mathrm{mm}$ \& $\triangle$ \& The dimensions are different. <br>
\hline \multicolumn{2}{|l|}{Weight} \& 0.60 kg \& 0.25 kg \& $\bigcirc$ \& <br>
\hline
\end{tabular}

*1 Mount a fuse on every external terminal to prevent external devices and modules from burning out upon load short circuit.
(11) Specifications comparisons between the AY13EU and the A1SY10EU

| Specification |  | AY13EU | A1SY10EU | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Number of output points |  | 32 points | 16 points | $\times$ | When seventeen or more points are used, use two of the A1SY10EU modules. |
| Isolation method |  | Photocoupler | Photocoupler | $\bigcirc$ |  |
| Rated switching voltage/current |  | 24VDC 2A (resistive load)/point 24VAC 2A (COS $\phi=1$ )/point 5A/common | 24VDC 2A (resistive load)/point 240VAC 2A (COS $\phi=1$ )/point 8A/common | $\bigcirc$ |  |
| Minimum switching load |  | 5VDC 1mA | 5VDC 1mA | $\bigcirc$ |  |
| Maximum switching voltage |  | 49.9VAC 74.9VDC | 132VAC 125VDC | $\bigcirc$ |  |
| Response time | $\mathrm{OFF} \rightarrow \mathrm{ON}$ | 10 ms or less | 10 ms or less | $\bigcirc$ |  |
|  | $\mathrm{ON} \rightarrow \mathrm{OFF}$ | 12 ms or less | 12 ms or less | $\bigcirc$ |  |
| Mechanical life |  | 20 million times or more | 20 million times or more | $\bigcirc$ |  |
| Electrical life |  | Rated switching voltage/current load <br> 200 thousand times or more | Rated switching voltage/current load 200 thousand times or more | $\bigcirc$ |  |
|  |  | 24VAC 1.5A <br> $(\operatorname{COS} \phi=0.7) 200$ thousand times or more 24VAC 0.75A <br> $(\operatorname{COS} \phi=0.35) 200$ thousand times or more <br> 24VDC 1A, 48VDC 0.1A ( $\mathrm{L} / \mathrm{R}=7 \mathrm{~ms}$ ) 200 thousand times or more | 100VAC 2A, 120VAC 2A $(\operatorname{COS} \phi=0.7) 200$ thousand times or more <br> 100VAC 2A, 120VAC 2A $(\operatorname{COS} \phi=0.35) 100$ thousand times or more <br> 24VDC 1.5A, 100VDC 0.1A (L/R=7ms) 100 thousand times or more | $\triangle$ | Reduce the exchange intervals of the modules as Mechanical/ Electrical Life is cut to about half. |
| Maximum switching frequency |  | 3600 times/hour | 3600 times/hour | $\bigcirc$ |  |
| Common terminal arrangement |  | 8 points/common (common terminal: TB9,TB18,TB27,TB36) | 8 points/common (common terminal: TB9,TB18) | $\bigcirc$ |  |
| Operation indicator |  | ON indication (LED) | ON indication (LED) | $\bigcirc$ |  |
| External supply power | Voltage | $\begin{gathered} 24 \mathrm{VDC} \pm 10 \% \\ \text { Ripple voltage } 4 \mathrm{Vp}-\mathrm{p} \text { or less } \end{gathered}$ | $24 \mathrm{VDC} \pm 10 \%$ Ripple voltage $4 \mathrm{Vp}-\mathrm{p}$ or less | $\bigcirc$ |  |
|  | Current | 290mA (24VDC TYP. all points ON) | 90 mA (24VDC TYP. all points ON) | $\bigcirc$ |  |
| External connection |  | 38-point terminal block connector (M3×6 screws) | 20-point terminal block connector (M3.5×7 screws) | $\times$ |  |
| Applicable wire size |  | 0.75 to $2 \mathrm{~mm}^{2}$ | 0.75 to $1.25 \mathrm{~mm}^{2}$ | $\times$ | Wiring must be changed. |
| Applicable solderless terminal |  | $\begin{gathered} \text { R1.25-3,R2-3, } \\ \text { RAV1.25-3,RAV2-3 } \end{gathered}$ | RAV1.25-3.5 | $\times$ |  |
| Current consumption |  | 0.23 A (TYP. all points ON) | 0.12 A (TYP. all points ON) | $\Delta$ | Review current capacity when using the two A1SY10EU modules. |
| External dimensions |  | $250(\mathrm{H}) \times 37.5(\mathrm{~W}) \times 131(\mathrm{D}) \mathrm{mm}$ | $130(\mathrm{H}) \times 34.5(\mathrm{~W}) \times 93.6(\mathrm{D}) \mathrm{mm}$ | $\triangle$ | The dimensions are different. |
| Weight |  | 0.59 kg | 0.25 kg | $\bigcirc$ |  |

I/O MODULE REPLACEMENT
(12) Specifications comparisons between the AY15EU and the A1SY14EU

| Specification |  | AY15EU | A1SY14EU | $\left\lvert\, \begin{gathered} \text { Compati- } \\ \text { bility } \end{gathered}\right.$ | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Number of output points |  | 24 points (32 points occupied) | 12 points (16 points occupied) | $\times$ | When thirteen or more points are used, use two of the A1SY14EU modules. |
| Isolation method |  | Photocoupler | Photocoupler | $\bigcirc$ |  |
| Rated switching voltage/current |  | 24VDC 2A (resistive load)/point 240VAC 2A (COS $\phi=1$ )/point 8A/common | 24VDC 2A (resistive load)/point 240VAC 2A (COS $\phi=1$ )/point 8A/common | $\bigcirc$ |  |
| Minimum switching load |  | 5VDC 10mA | 5VDC 10mA | $\bigcirc$ |  |
| Maximum switching voltage |  | 264VAC 125VDC | 264VAC 125VDC | $\bigcirc$ |  |
| Response time | $\mathrm{OFF} \rightarrow \mathrm{ON}$ | 10 ms or less | 10 ms or less | $\bigcirc$ |  |
|  | $\mathrm{ON} \rightarrow$ OFF | 12 ms or less | 12 ms or less | $\bigcirc$ |  |
| Mechanical life |  | 20 million times or more | 20 million times or more | $\bigcirc$ |  |
| Electrical life |  | Rated switching voltage/current load 100 thousand times or more | Rated switching voltage/current load 200 thousand times or more | $\bigcirc$ |  |
|  |  | 200VAC 2A, 240VAC 1.8A $(\operatorname{COS} \phi=0.7) 200$ thousand times or more <br> 200VAC 1.1A, 240VAC 0.9A (COS $\phi=0.35$ ) 200 thousand times or more <br> 24VDC 1.1A, 100VDC 0.1A (L/R=7ms) 200 thousand times or more | 200VAC 2A, 240VAC 1.8A $(\operatorname{COS} \phi=0.7) 200$ thousand times or more <br> 200VAC 1.1A, 240VAC 0.9A (COS $\phi=0.35$ ) 200 thousand times or more <br> 24VDC 1.1A, 100VDC 0.1A (L/R=7ms) 200 thousand times or more | $\bigcirc$ |  |
| Maximum switching frequency |  | 3600 times/hour | 3600 times/hour | $\bigcirc$ |  |
| Surge suppressor |  | None | None | $\bigcirc$ |  |
| Common terminal arrangement |  | 8 points/common (common terminal: TB9,TB20,TB31) | 4 points/common (common terminal: TB5,TB10,TB15) | $\bigcirc$ |  |
| Operation indicator |  | ON indication (LED) | ON indication (LED) | $\bigcirc$ |  |
| External supply power | Voltage | $24 \mathrm{VDC} \pm 10 \%$ <br> Ripple voltage 4Vp-p or less (Must be SELV power supply) | $\begin{gathered} 24 \mathrm{VDC} \pm 10 \% \\ \text { Ripple voltage } 4 \mathrm{Vp}-\mathrm{p} \text { or less } \end{gathered}$ | $\bigcirc$ |  |
|  | Current | 220mA (24VDC TYP. all points ON) <br> (Must be SELV power supply) | 100mA (24VDC TYP. all points ON) <br> (Must be SELV power supply) | $\bigcirc$ |  |
| External connection |  | 38-point terminal block connector (M3.5×6 screws) | 20-point terminal block connector (M3.5×7 screws) | $\times$ |  |
| Applicable wire size |  | $0.75 \text { to } 2 \mathrm{~mm}^{2}$ <br> (AWG14 to AWG19) | 0.75 to $1.25 \mathrm{~mm}^{2}$ <br> (AWG16 to AWG19) | $\times$ | Wiring must be changed. |
| Applicable solderless terminal |  | RAV1.25-3.5,RAV2-3.5 | RAV1.25-3.5 | $\times$ |  |
| Dielectric withstand voltage |  | (AC external batch relay drive power supply. 5 V internal circuit) 2830 VAC rms/3cycles (altitude 2000m) <br> (Relay-drive power supply, 5V internal circuit) 500VAC rms/3cycles (altitude 2000m) | (AC external batch relay drive power supply. 5V internal circuit) 2830VAC rms/3cycles (altitude 2000m) <br> (Relay-drive power supply, 5V internal circuit) 500VAC rms/3cycles (altitude 2000m) | $\bigcirc$ |  |
| Insulation resistance |  | $10 \mathrm{M} \Omega$ or more by insulation resistance tester | $10 \mathrm{M} \Omega$ or more by insulation resistance tester | $\bigcirc$ |  |

$\bigcirc$ : Compatible, $\Delta$ : Partial change required, $\times$ : Incompatible

| Specification | AY15EU | A1SY14EU | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: |
| Current consumption | 0.15 A (TYP. all points ON) | $\begin{gathered} 0.12 \mathrm{~A} \\ \text { (TYP. all points ON) } \end{gathered}$ | $\triangle$ | Review current capacity when using two of the A1SY14EU modules. |
| External dimensions | $250(\mathrm{H}) \times 37.5(\mathrm{~W}) \times 131(\mathrm{D}) \mathrm{mm}$ | $130(\mathrm{H}) \times 34.5(\mathrm{~W}) \times 93.6(\mathrm{D}) \mathrm{mm}$ | $\triangle$ | The dimensions are different. |
| Weight | 0.50 kg | 0.25 kg | $\bigcirc$ |  |

(13) Specifications comparisons between the AY22 and the A1SY22

| Specification |  |  | $\bigcirc$ : Compatible, $\triangle$ : Partial change required, $\times$ : Incompatible |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | AY22 | A1SY22 | $\left\lvert\, \begin{gathered} \text { Compati- } \\ \text { bility } \end{gathered}\right.$ | Precautions for replacement |
| Number of output points |  | 16 points | 16 points | $\bigcirc$ |  |
| Isolation method |  | Photocoupler | Photocoupler | $\bigcirc$ |  |
| Rated load voltage |  | 100-240VAC $50 / 60 \mathrm{~Hz} \pm 5 \%$ | $100-240 \mathrm{VAC} 50 / 60 \mathrm{~Hz} \pm 3 \mathrm{~Hz}$ | $\bigcirc$ |  |
| Maximum load voltage |  | 264VAC | 264VAC | $\bigcirc$ |  |
| Maximum load current |  | 2A/point, 3.3A/common | 0.6A/point, 2.4A/common | $\times$ | Carefully select load for use since the maximum load current per point is lowered. |
| Minimum load voltage current |  | 24VAC 100mA 100VAC 10mA 240VAC 20mA | 24VAC 100mA 100VAC 10mA 240VAC 20 mA | $\bigcirc$ |  |
| Maximum inrush current |  | 40A 10 ms or less 15A 100ms or less | 20A 10 ms or less 8A 100ms or less | $\Delta$ | The inrush current value differs.Use caution on selecting the load to use. |
| Leakage current at OFF |  | 1.5 mA (At $120 \mathrm{VAC}, 60 \mathrm{~Hz}$ ) 3 mA (At $240 \mathrm{VAC}, 60 \mathrm{~Hz}$ ) | 1.5 mA or less (At $120 \mathrm{VAC}, 60 \mathrm{~Hz}$ ) 3 mA or less (At $240 \mathrm{VAC}, 60 \mathrm{~Hz}$ ) | $\bigcirc$ |  |
| Maximum voltage drop at ON |  | 1.5VAC or less ( 1 to 2 A ) <br> 1.8 VAC or less ( 0.2 to 1 A ) <br> 5 VAC or less (0.2A or less) | 1.5VAC or less ( 0.1 to 0.6 A ) <br> 1.8 VAC or less ( 50 to 100 mA ) <br> 2 VAC or less ( 10 to 50 mA ) | $\bigcirc$ |  |
| Response time | OFF $\rightarrow$ ON | 1 ms or less | 1 ms or less | $\bigcirc$ |  |
|  | $\mathrm{ON} \rightarrow$ OFF | 0.5 cycles +1 ms or less | 0.5 cycles +1 ms or less | $\bigcirc$ |  |
| Surge suppressor |  | CR absorber $(0.022 \mu \mathrm{~F}+47 \Omega)$ <br> Varistor (387 to 473V) | CR absorber ( $0.01 \mu \mathrm{~F}+47 \Omega$ ) | $\Delta$ | The varistor is not built in. *1 |
| Common terminal arrangement |  | 8 points/common (common terminal: TB9,TB18) | 8 points/common (common terminal: TB9,TB18) | $\bigcirc$ |  |
| Operation indicator |  | ON indication (LED) | ON indication (LED) | $\bigcirc$ |  |
| Fuse |  | 7A fast blow fuse (1 fuse/common) HP-70k | 5A fuse (1 fuse/common) Not replaceable | $\triangle$ | If a fast blow fuse is necessary, connect it outside. |
| Fuse blow indicator |  | Yes (LED is turned ON when a fuse is blown. Signal is output to a PLC CPU.) | Yes (LED is turned ON when a fuse is blown. Signal is output to a PLC CPU.) | $\triangle$ | Error LED is also turned ON when the external supply power is OFF. |
| External supply power | Voltage | - | 100-240VAC (85 to 264VAC) | $\times$ | External supply power is required. |
|  | Current | - | 2 mA <br> (TYP. 200VAC/common) | $\times$ |  |
| External connection |  | 20-point terminal block connector (M3×6 screws) | 20-point terminal block connector (M3.5×7 screws) | $\times$ | Wiring must be changed. |
| Applicable wire size |  | 0.75 to $2 \mathrm{~mm}^{2}$ | 0.75 to $1.25 \mathrm{~mm}^{2}$ | $\times$ |  |
| Applicable solderless terminal |  | $\begin{gathered} \text { R1.25-3,R2-3, } \\ \text { RAV1.25-3,RAV2-3 } \end{gathered}$ | R1.25-3.5,R2-3.5, RAV1.25-3.5,RAV2-3.5 | $\times$ |  |
| Current consumption |  | 0.305 A (TYP. all points ON) | 0.270 A (TYP. all points ON) | $\bigcirc$ |  |
| External dimensions |  | $250(\mathrm{H}) \times 37.5(\mathrm{~W}) \times 121(\mathrm{D}) \mathrm{mm}$ | $130(\mathrm{H}) \times 34.5(\mathrm{~W}) \times 93.6(\mathrm{D}) \mathrm{mm}$ | $\triangle$ | The dimensions are different. |
| Weight |  | 0.71 kg | 0.24 kg | $\bigcirc$ |  |

*1 Connect a varistor to reduce external noise.
(14) Specifications comparisons between the AY23 and the A1SY22

| Specification |  | AY23 | A1SY22 | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Number of output points |  | 32 points | 16 points | $\times$ | When seventeen or more points are used, use two of the A1SY22 modules. |
| Isolation method |  | Photocoupler | Photocoupler | 0 |  |
| Rated load voltage |  | 100-240VAC 40 to 70 Hz | $100-240$ VAC $50 / 60 \mathrm{~Hz} \pm 3 \mathrm{~Hz}$ | $\bigcirc$ |  |
| Maximum load voltage |  | 264VAC | 264VAC | $\bigcirc$ |  |
| Maximum load current |  | 0.6A/point, 2.4A/common (When placing next to the power supply module: 1.05A/common) | 0.6A/point, 2.4A/common | 0 |  |
| Minimum load voltage current |  | 24 VAC 100 mA 100VAC 10 mA 240VAC 10mA | 24 VAC 100 mA 100 VAC 10 mA 240 VAC 20 mA | $\Delta$ | Carefully select load for use since the minimum load current is increased. |
| Maximum inrush current |  | 20A 10 ms or less 8A 100ms or less | 20A 10ms or less 8A 100ms or less | 0 |  |
| Leakage current at OFF |  | 1.5 mA (At 120 VAC 60 Hz ) 3 mA (At 240 VAC 60 Hz ) | 1.5 mA or less (At 120 VAC 60 Hz ) 3 mA or less (At 240VAC 60Hz) | 0 |  |
| Maximum voltage drop at ON |  | 1.5 VAC or less ( 100 to 600 mA ) 1.8 VAC or less ( 50 to 100 mA ) 2 VAC or less ( 10 to 50 mA ) | 1.5VAC or less ( 0.1 to 0.6 A ) 1.8 VAC or less ( 50 to 100 mA ) 2 VAC or less ( 10 to 50 mA ) | 0 |  |
| Response time | OFF $\rightarrow$ ON | 1 ms or less | 1 ms or less | $\bigcirc$ |  |
|  | ON $\rightarrow$ OFF | 0.5 cycles +1 ms or less | 0.5 cycles +1 ms or less | $\bigcirc$ |  |
| Surge suppressor |  | CR absorber( $0.022 \mu \mathrm{~F}+47 \Omega$ ) | CR absorber ( $0.01 \mu \mathrm{~F}+47 \mathrm{\Omega}$ ) | $\bigcirc$ |  |
| Common terminal arrangement |  | 8 points/common (common terminal: TB9,TB18,TB27,TB36) | 8 points/common (common terminal: TB9,TB18) | 0 |  |
| Operation indicator |  | ON indication (LED) | ON indication (LED) | $\bigcirc$ |  |
| Fuse |  | 3.2A fast blow fuse (1 fuse/common) HP-32 | 5A fuse (1 fuse/common) Not replaceable | $\triangle$ | Connect the fast blow fuse to the external if necessary. |
| Fuse blow indicator |  | Yes (LED is turned ON when a fuse is blown. Signal is output to a PLC CPU.) | Yes (LED is turned ON when a fuse is blown. Signal is output to a PLC CPU.) | $\triangle$ | Fuse blow error also occurs when the external supply power is OFF. |
| External supply power | Voltage | - | 100-240VAC (85 to 264VAC) | $\times$ | External supply power is required. |
|  | Current | - | 2mA (TYP. 200VAC/common) | $\times$ |  |
| External connection |  | 38-point terminal block connector (M3×6 screws) | 20-point terminal block connector (M3. $5 \times 7$ screws) | $\times$ | Wiring must be changed. |
| Applicable wire size |  | 0.75 to $2 \mathrm{~mm}^{2}$ | 0.75 to $1.25 \mathrm{~mm}^{2}$ | $\times$ |  |
| Applicable solderless terminal |  | R1.25-3,R2-3, RAV1.25-3,RAV2-3 | R1.25-3.5,R2-3.5, RAV1.25-3.5,RAV2-3.5 | $\times$ |  |
| Current consumption |  | 0.59 A (TYP. all points ON) | 0.27A (TYP. all points ON) | 0 |  |
| External dimensions |  | 250 (H) $\times 37.5(\mathrm{~W}) \times 131$ (D) mm | 130(H) $\times 34.5(\mathrm{~W}) \times 93.6$ (D) mm | $\Delta$ | The dimensions are different. |
| Weight |  | 0.55 kg | 0.24 kg | $\bigcirc$ |  |

(15) Specifications comparisons between the AY40 and the A1SY40

| Specification |  | $\bigcirc$ : Compatible, $\triangle$ : Partial change required, $\times$ : Incompatible |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | AY40 | A1SY40 | Compatibility | Precautions for replacement |
| Number of output points |  | 16 points | 16 points | $\bigcirc$ |  |
| Isolation method |  | Photocoupler | Photocoupler | $\bigcirc$ |  |
| Rated load voltage |  | 12/24VDC | 12/24VDC | $\bigcirc$ |  |
| Operating load voltage range |  | 10.2 to 40VDC | 10.2 to 30VDC | $\Delta$ | Voltages exceeding 30VDC can not be applied. |
| Maximum load current |  | $0.1 \mathrm{~A} /$ point, $0.8 \mathrm{~A} /$ common | 0.1A/point, $0.8 \mathrm{~A} /$ common | $\bigcirc$ |  |
| Maximum inrush current |  | 0.4A | 0.4 A 10 ms or less | $\bigcirc$ |  |
| Leakage current at OFF |  | 0.1 mA or less | 0.1 mA or less | $\bigcirc$ |  |
| Maximum voltage drop at ON |  | $\begin{gathered} \text { 2.5VDC ( } 0.1 \mathrm{~A}) \\ 1.75 \mathrm{VDC}(5 \mathrm{~mA}) \\ 1.7 \mathrm{VDC}(1 \mathrm{~mA}) \end{gathered}$ | $\begin{aligned} & 1.0 \mathrm{VDC}(\text { (TYP.) } 0.1 \mathrm{~A} \\ & 2.5 \mathrm{VDC}(\mathrm{MAX} .) 0.1 \mathrm{~A} \end{aligned}$ | $\bigcirc$ |  |
| Response time | $\mathrm{OFF} \rightarrow \mathrm{ON}$ | 2 ms or less | 2 ms or less | $\bigcirc$ |  |
|  | $\mathrm{ON} \rightarrow$ OFF | 2 ms or less (resistive load) | 2 ms or less (resistive load) | $\bigcirc$ |  |
| Surge suppressor |  | Clamp diode | Zener diode | $\bigcirc$ |  |
| Common terminal arrangement |  | 8 points/common (common terminal: TB10,TB20) | 8 points/common (common terminal: TB10,TB20) | $\bigcirc$ |  |
| Operation indicator |  | ON indication (LED) | ON indication (LED) | $\bigcirc$ |  |
| External supply power | Voltage | 12/24VDC (10.2 to 40VDC) | 12/24VDC (10.2 to 30VDC) | $\triangle$ | Voltages exceeding 30VDC cannot be applied. |
|  | Current | 8mA (TYP. 24VDC/common) | 8mA (TYP. 24VDC/common) | $\bigcirc$ |  |
| External connection |  | 20-point terminal block connector (M3×6 screws) | 20-point terminal block connector (M3.5×7 screws) | $\times$ | Wiring must be changed. |
| Applicable wire size |  | 0.75 to $2 \mathrm{~mm}^{2}$ | 0.75 to $1.25 \mathrm{~mm}^{2}$ | $\times$ |  |
| Applicable solderless terminal |  | R1.25-3,R2-3, RAV1.25-3,RAV2-3 | R1.25-3.5,R2-3.5, RAV1.25-3.5,RAV2-3.5 | $\times$ |  |
| Current consumption |  | 0.115 A (TYP. all points ON) | 0.270 A (TYP. all points ON) | $\triangle$ | Current capacity must be reviewed. |
| External dimensions |  | $250(\mathrm{H}) \times 37.5(\mathrm{~W}) \times 121(\mathrm{D}) \mathrm{mm}$ | $130(\mathrm{H}) \times 34.5(\mathrm{~W}) \times 93.6(\mathrm{D}) \mathrm{mm}$ | $\triangle$ | The dimensions are different. |
| Weight |  | 0.36 kg | 0.19 kg | $\bigcirc$ |  |

(16) Specifications comparisons between the AY40A and the A1SY68A

| Specification |  | $\bigcirc$ : Compatible, $\triangle$ : Partial change required, $\times$ : Incompatible |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | AY40A | A1SY68A | Compatibility | Precautions for replacement |
| Number of output points |  | 16 points | 8 points (16 points occupied) | $\times$ | When nine or more points are used, use two of the A1SY68A modules. |
| Isolation method |  | Photocoupler | Photocoupler | $\bigcirc$ |  |
| Rated load voltage |  | 12/24VDC | 5/12/24/48VDC | $\bigcirc$ |  |
| Operating load voltage range |  | 10.2 to 30VDC (Max. applied voltage) | 4.5 to 52.8 VDC | $\bigcirc$ |  |
| Maximum load current |  | 0.3A/point | 2A/point | $\bigcirc$ |  |
| Maximum inrush current |  | 1A 100ms or less | 8A 10ms or less | $\bigcirc$ |  |
| Leakage current at OFF |  | 0.1 mA or less | 0.1 mA or less | $\bigcirc$ |  |
| Maximum voltage drop at ON |  | $1.5 \mathrm{VDC}(50 \mathrm{~mA}$ to 0.3 A$)$ <br> 1.0 VDC ( 50 mA or less) | 0.4VDC (MAX.) 2A | $\bigcirc$ |  |
| Response time | OFF $\rightarrow$ ON | 2 ms or less | 3 ms or less | $\triangle$ | The response times differ. |
|  | ON $\rightarrow$ OFF | 2 ms or less (resistive load) | 10 ms or less (resistive load) | $\triangle$ |  |
| Surge suppressor |  | Surge suppression diode | Zener diode | $\bigcirc$ |  |
| Common terminal arrangement |  | Not provided (all points independent) | Not provided (all points independent) | $\bigcirc$ |  |
| Operation indicator |  | ON indication (LED) | ON indication (LED) | $\bigcirc$ |  |
| External connection |  | 38-point terminal block connector (M3×6 screws) | 20-point terminal block connector (M3.5×7 screws) | $\times$ | Wiring must be changed. |
| Applicable wire size |  | 0.75 to $2 \mathrm{~mm}^{2}$ | 0.75 to $1.25 \mathrm{~mm}^{2}$ | $\times$ |  |
| Applicable solderless terminal |  | $\begin{gathered} \text { R1.25-3,R2-3, } \\ \text { RAV1.25-3,RAV2-3 } \end{gathered}$ | R1.25-3.5,R2-3.5, RAV1.25-3.5,RAV2-3.5 | $\times$ |  |
| Current consumption |  | $0.19 \mathrm{~A}$ <br> (TYP. all points ON) | 0.11A <br> (TYP. all points ON) | $\triangle$ | Review current capacity when using the two A1SY68 modules. |
| External dimensions |  | $250(\mathrm{H}) \times 37.5(\mathrm{~W}) \times 131(\mathrm{D}) \mathrm{mm}$ | $130(\mathrm{H}) \times 34.5(\mathrm{~W}) \times 93.6(\mathrm{D}) \mathrm{mm}$ | $\triangle$ | The dimensions are different. |
| Weight |  | 0.42 kg | 0.20kg | $\bigcirc$ |  |

(17) Specifications comparisons between the AY41 and the A1SY41

| Specification |  | AY41 | A1SY41 | Compati-\| | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Number of output points |  | 32 points | 32 points | $\bigcirc$ |  |
| Isolation method |  | Photocoupler | Photocoupler | $\bigcirc$ |  |
| Rated load voltage |  | 12/24VDC | 12/24VDC | $\bigcirc$ |  |
| Operating load voltage range |  | 10.2 to 40VDC | 10.2 to 30VDC | $\triangle$ | Voltages exceeding 30VDC cannot be applied. |
| Maximum load current |  | 0.1A/point, 1.6A/common | $0.1 \mathrm{~A} /$ point, 2A/common | $\bigcirc$ |  |
| Maximum inrush current |  | 0.4A | 0.4 A 10 ms or less | $\bigcirc$ |  |
| Leakage current at OFF |  | 0.1 mA or less | 0.1 mA or less | $\bigcirc$ |  |
| Maximum voltage drop at ON |  | $\begin{gathered} \text { 2.5VDC ( } 0.1 \mathrm{~A} \text { ) } \\ 1.75 \mathrm{VDC}(5 \mathrm{~mA}) \\ 1.7 \mathrm{VDC}(1 \mathrm{~mA}) \end{gathered}$ | $\begin{aligned} & \text { 1.0VDC (TYP.) } 0.1 \mathrm{~A} \\ & 2.5 \mathrm{VDC} \text { (MAX.) } 0.1 \mathrm{~A} \end{aligned}$ | $\bigcirc$ |  |
| Response time | $\mathrm{OFF} \rightarrow \mathrm{ON}$ | 2 ms or less | 2 ms or less | $\bigcirc$ |  |
|  | $\mathrm{ON} \rightarrow$ OFF | 2 ms or less (resistive load) | 2ms or less (resistive load) | $\bigcirc$ |  |
| Surge suppressor |  | Clamp diode | Zener diode | $\bigcirc$ |  |
| Common terminal arrangement |  | 16 points/common (common terminal: TB18,TB36) | 32 points/common (common terminal: A1,A2) | $\triangle$ | As 2 commons are reduced to 1 , wiring a different voltage for each common is not possible. |
| Operation indicator |  | ON indication (LED) | ON indication (LED) | $\bigcirc$ |  |
| External supply power | Voltage | 12/24VDC (10.2 to 40VDC) | 12/24VDC (10.2 to 30VDC) | $\triangle$ | Voltages exceeding 30VDC cannot be applied. |
|  | Current | 20mA (TYP. 24VDC/common) | 8mA <br> (TYP. 24VDC/common) | $\bigcirc$ |  |
| External connection |  | 38-point terminal block connector (M3×6 screws) | 40-pin connector (accessory) | $\times$ |  |
| Applicable wire size |  | 0.75 to $2 \mathrm{~mm}^{2}$ | $0.3 \mathrm{~mm}^{2}$ | $\times$ | Wiring must be changed.*1 |
| Applicable solderless terminal |  | R1.25-3,R2-3, RAV1.25-3,RAV2-3 | - | $\times$ |  |
| Current consumption |  | 0.23A <br> (TYP. all points ON) | 0.50 A (TYP. all points ON) | $\triangle$ | Current capacity must be reviewed. |
| External dimensions |  | $250(\mathrm{H}) \times 37.5(\mathrm{~W}) \times 131(\mathrm{D}) \mathrm{mm}$ | $130(\mathrm{H}) \times 34.5(\mathrm{~W}) \times 93.6(\mathrm{D}) \mathrm{mm}$ | $\triangle$ |  |
| Weight |  | 0.44 kg | 0.21 kg | $\bigcirc$ |  |

*1 By using connectors/terminal block converter modules(A6TBXY36, etc.), conversion to the terminal block is possible.
(18) Specifications comparisons between the AY41-UL and the A1SY41

| Specification |  | AY41-UL | A1SY41 | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Number of output points |  | 32 points | 32 points | $\bigcirc$ |  |
| Isolation method |  | Photocoupler | Photocoupler | $\bigcirc$ |  |
| Rated load voltage |  | 12/24VDC | 12/24VDC | $\bigcirc$ |  |
| Operating load voltage range |  | 10.2 to 40VDC | 10.2 to 30VDC | $\triangle$ | Voltages exceeding 30VDC cannot be applied. |
| Maximum load current |  | 0.1A/point, 1.6A/common | 0.1A/point, 2A/common | $\bigcirc$ |  |
| Maximum inrush current |  | 0.4A | 0.4 A 10 ms or less | $\bigcirc$ |  |
| Leakage current at OFF |  | 0.1 mA or less | 0.1 mA or less | $\bigcirc$ |  |
| Maximum voltage drop at ON |  | $\begin{gathered} \text { 2.5VDC ( } 0.1 \mathrm{~A}) \\ 1.75 \mathrm{VDC}(5 \mathrm{~mA}) \\ 1.7 \mathrm{VDC}(1 \mathrm{~mA}) \end{gathered}$ | $\begin{aligned} & \text { 1.0VDC (TYP.) } 0.1 \mathrm{~A} \\ & 2.5 \mathrm{VDC} \text { (MAX.) } 0.1 \mathrm{~A} \end{aligned}$ | $\bigcirc$ |  |
| Response time | OFF $\rightarrow$ ON | 2 ms or less | 2 ms or less | $\bigcirc$ |  |
|  | $\mathrm{ON} \rightarrow$ OFF | 2 ms or less (resistive load) | 2 ms or less (resistive load) | $\bigcirc$ |  |
| Surge suppressor |  | Clamp diode | Zener diode | $\bigcirc$ |  |
| Common terminal arrangement |  | 16 points/common (common terminal: TB18,TB36) | 32 points/common (common terminal: A1,A2) | $\Delta$ | As 2 commons are reduced to 1 , wiring a different voltage for each common is not possible. |
| Operation indicator |  | ON indication (LED) | ON indication (LED) | $\bigcirc$ |  |
| External supply power | Voltage | 12/24VDC (10.2 to 40VDC) | 12/24VDC (10.2 to 30VDC) | $\triangle$ | Voltages exceeding 30VDC cannot be applied. |
|  | Current | 20 mA (TYP. 24VDC/common) | 8mA (TYP. 24VDC/common) | $\bigcirc$ |  |
| External connection |  | 38-point terminal block connector (M3.5×6 screws) | 40-pin connector (accessory) | $\times$ |  |
| Applicable wire size |  | 0.75 to $2 \mathrm{~mm}^{2}$ | $0.3 \mathrm{~mm}^{2}$ | $\times$ | Wiring must be changed.*1 |
| Applicable solderless terminal |  | R1.25-3.5, R2-3, RAV1.25-3.5,RAV2-3.5 | - | $\times$ |  |
| Current consumption |  | 0.23 A (TYP. all points ON) | 0.50 A (TYP. all points ON) | $\triangle$ | Current capacity must be reviewed. |
| External dimensions |  | $250(\mathrm{H}) \times 37.5(\mathrm{~W}) \times 131(\mathrm{D}) \mathrm{mm}$ | $130(\mathrm{H}) \times 34.5(\mathrm{~W}) \times 93.6(\mathrm{D}) \mathrm{mm}$ | $\triangle$ |  |
| Weight |  | 0.44 kg | 0.21 kg | $\bigcirc$ |  |

*1 By using connectors/terminal block converter modules(A6TBXY36, etc.), conversion to the terminal block is possible.
(19) Specifications comparisons between the AY42 and the A1SY42P

| Specification |  | $\bigcirc$ : Compatible, $\Delta$ : Partial change required, $\times$ : Incompatible |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | AY42 | A1SY42P | $\begin{gathered} \text { Compati- } \\ \text { bility } \end{gathered}$ | Precautions for replacement |
| Number of output points |  | 64 points | 64 points | $\bigcirc$ |  |
| Isolation method |  | Photocoupler | Photocoupler | $\bigcirc$ |  |
| Rated load voltage |  | 12/24VDC | 12/24VDC | $\bigcirc$ |  |
| Operating load voltage range |  | 10.2 to 40VDC | 10.2 to 30VDC | $\Delta$ | Voltages exceeding 30VDC cannot be applied. |
| Maximum load current |  | 0.1A/point, 2A/common (When placing next to the power supply module: $1.6 \mathrm{~A} / \mathrm{common}$ ) | 0.1A/point, 2A/common | $\bigcirc$ |  |
| Maximum inrush current |  | 0.4A | 0.7A 10ms or less | $\bigcirc$ |  |
| Leakage current at OFF |  | 0.1 mA or less | 0.1 mA or less | $\bigcirc$ |  |
| Maximum voltage drop at ON |  | $\begin{gathered} \hline 2.5 \mathrm{VDC}(0.1 \mathrm{~A}) \\ 1.75 \mathrm{VDC}(5 \mathrm{~mA}) \\ 1.7 \mathrm{VDC}(1 \mathrm{~mA}) \end{gathered}$ | $\begin{aligned} & 0.1 \mathrm{VDC}(\text { (TYP.) } 0.1 \mathrm{~A} \\ & 0.2 \mathrm{VDC} \text { (MAX.) } 0.1 \mathrm{~A} \end{aligned}$ | $\bigcirc$ |  |
| Response time | $\mathrm{OFF} \rightarrow \mathrm{ON}$ | 2 ms or less | 1 ms or less | $\bigcirc$ |  |
|  | $\mathrm{ON} \rightarrow$ OFF | $\begin{gathered} 2 \mathrm{~ms} \text { or less } \\ \text { (resistive load) } \end{gathered}$ | 1ms or less (rated load, resistance load) | $\bigcirc$ |  |
| Surge suppressor |  | Clamp diode | Zener diode | $\bigcirc$ |  |
| Common terminal arrangement |  | 32 points/common (common terminal: 1A1,1A2,2A1,2A2) | 32 points/common (common terminal: 1A1,1A2,2A1,2A2) | $\bigcirc$ |  |
| Operation indicator |  | ON indication (LED) <br> 32 point switch-over using a switch | ON indication (LED) <br> 32 point switch-over using a switch | $\bigcirc$ |  |
| External supply power | Voltage | 12/24VDC (10.2 to 40VDC) | 12/24VDC (10.2 to 30VDC) | $\Delta$ | Voltages exceeding 30VDC cannot be applied. |
|  | Current | 40mA (TYP. 24VDC/common) | 14mA (At 24VDC/common) | $\bigcirc$ |  |
| External connection |  | 40-pin connector (with solder) $\times 2$ | 40-pin connector (accessory) $\times 2$ | $\bigcirc$ |  |
| Applicable wire size |  | $0.3 \mathrm{~mm}^{2}$ | $0.3 \mathrm{~mm}^{2}$ | $\bigcirc$ |  |
| Accessory |  | External wiring connectors $\times 2$ | External wiring connectors $\times 2$ | $\bigcirc$ |  |
| Current consumption |  | 0.34A <br> (TYP. all points ON) | $0.17 \mathrm{~A}$ <br> (TYP. all points ON) | $\bigcirc$ |  |
| External dimensions |  | $250(\mathrm{H}) \times 37.5(\mathrm{~W}) \times 106$ (D) mm | 130(H) $\times 34.5(\mathrm{~W}) \times 93.6(\mathrm{D}) \mathrm{mm}$ | $\triangle$ |  |
| Weight |  | 0.50 kg | 0.17 kg | $\bigcirc$ |  |

(20) Specifications comparisons between the AY42-S1 and the A1SY42P

| Specification |  | AY42-S1 | A1SY42P | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Number of output points |  | 64 points | 64 points | $\bigcirc$ |  |
| Isolation method |  | Photocoupler | Photocoupler | $\bigcirc$ |  |
| Rated load voltage |  | 12/24VDC | 12/24VDC | $\bigcirc$ |  |
| Operating load voltage range |  | 10.2 to 40VDC | 10.2 to 30VDC | $\Delta$ | Voltages exceeding 30VDC cannot be applied. |
| Maximum load current |  | 0.1A/point, 2A/common (When placing next to the power supply module: $1.6 \mathrm{~A} / \mathrm{common}$ ) | 0.1A/point, 2A/common | $\bigcirc$ |  |
| Maximum inrush current |  | 0.4A | 0.7A 10ms or less | $\bigcirc$ |  |
| Leakage current at OFF |  | 0.1 mA or less | 0.1 mA or less | $\bigcirc$ |  |
| Maximum voltage drop at ON |  | $\begin{gathered} 2.5 \mathrm{VDC}(0.1 \mathrm{~A}) \\ 1.75 \mathrm{VDC}(5 \mathrm{~mA}) \\ 1.7 \mathrm{VDC}(1 \mathrm{~mA}) \end{gathered}$ | $\begin{aligned} & 0.1 \mathrm{VDC}(\text { (TYP.) } 0.1 \mathrm{~A} \\ & 0.2 \mathrm{VDC} \text { (MAX.) } 0.1 \mathrm{~A} \end{aligned}$ | $\bigcirc$ |  |
| Response time | $\mathrm{OFF} \rightarrow \mathrm{ON}$ | 0.1 ms or less | 1 ms or less | $\triangle$ | The response times differ. |
|  | $\mathrm{ON} \rightarrow$ OFF | 0.3 ms or less (resistive load) | 1 ms or less (rated load, resistance load) | $\triangle$ |  |
| Surge suppressor |  | Clamp diode | Zener diode | $\bigcirc$ |  |
| Common terminal arrangement |  | 32 points/common (common terminal: 1A1,1A2,2A1,2A2) | 32 points/common (common terminal: 1A1,1A2,2A1,2A2) | $\bigcirc$ |  |
| Operation indicator |  | ON indication (LED) <br> 32 point switch-over using a switch | ON indication (LED) <br> 32 point switch-over using a switch | $\bigcirc$ |  |
| External supply power | Voltage | 12/24VDC (10.2 to 40VDC) | 12/24VDC (10.2 to 30VDC) | $\triangle$ | Voltages exceeding 30VDC cannot be applied. |
|  | Current | 40mA (TYP. 24VDC/common) | 14mA (At 24VDC/common) | $\bigcirc$ |  |
| External connection |  | 40-pin connector (with solder) $\times 2$ | 40-pin connector (accessory) $\times 2$ | $\bigcirc$ |  |
| Applicable wire size |  | $0.3 \mathrm{~mm}^{2}$ | $0.3 \mathrm{~mm}^{2}$ | $\bigcirc$ |  |
| Accessory |  | External wiring connectors $\times 2$ | External wiring connectors $\times 2$ | $\bigcirc$ |  |
| Current consumption |  | 0.29 A (TYP. all points ON) | 0.17 A <br> (TYP. all points ON) | $\bigcirc$ |  |
| External dimensions |  | $250(\mathrm{H}) \times 37.5(\mathrm{~W}) \times 106(\mathrm{D}) \mathrm{mm}$ | $130(\mathrm{H}) \times 34.5(\mathrm{~W}) \times 93.6(\mathrm{D}) \mathrm{mm}$ | $\triangle$ |  |
| Weight |  | 0.50 kg | 0.17 kg | $\bigcirc$ |  |

(21) Specifications comparisons between the AY42-S3 and the A1SY42P

| Specification |  | AY42-S3 | A1SY42P | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Number of output points |  | 64 points | 64 points | $\bigcirc$ |  |
| Isolation method |  | Photocoupler | Photocoupler | $\bigcirc$ |  |
| Rated load voltage |  | 12/24VDC | 12/24VDC | $\bigcirc$ |  |
| Operating load voltage range |  | 10.2 to 40VDC | 10.2 to 30VDC | $\triangle$ | Voltages exceeding 30VDC cannot be applied. |
| Maximum load current |  | 0.1A/point, 2A/common (When placing next to the power supply module: 1.6A/common) | 0.1A/point, 2A/common | $\bigcirc$ |  |
| Maximum inrush current |  | 0.4A/point 3.5A/fuse | 0.7A 10 ms or less | $\bigcirc$ |  |
| Leakage current at OFF |  | 0.1 mA or less | 0.1 mA or less | $\bigcirc$ |  |
| Maximum voltage drop at ON |  | $\begin{gathered} 2.5 \mathrm{VDC}(0.1 \mathrm{~A}) \\ 1.75 \mathrm{VDC}(5 \mathrm{~mA}) \\ 1.7 \mathrm{VDC}(1 \mathrm{~mA}) \end{gathered}$ | $\begin{aligned} & \text { 0.1VDC (TYP.) 0.1A } \\ & 0.2 \mathrm{VDC} \text { (MAX.) } 0.1 \mathrm{~A} \end{aligned}$ | $\bigcirc$ |  |
| Response time | $\mathrm{OFF} \rightarrow \mathrm{ON}$ | 2 ms or less | 1 ms or less | $\bigcirc$ |  |
|  | $\mathrm{ON} \rightarrow$ OFF | 2 ms or less (resistive load) | 1ms or less (rated load, resistance load) | $\bigcirc$ |  |
| Surge suppressor |  | Clamp diode | Zener diode | $\bigcirc$ |  |
| Common terminal arrangement |  | 32 points/common (common terminal: 1A1,1A2,2A1,2A2) | 32 points/common (common terminal: 1A1,1A2,2A1,2A2) | $\bigcirc$ |  |
| Operation indicator |  | ON indication (LED) <br> 32 point switch-over using a switch | ON indication (LED) <br> 32 point switch-over using a switch | $\bigcirc$ |  |
| Fuse |  | 1.6A normal fuse (2 fuses/common) | Not equipped | $\triangle$ | The fuse-equivalent short circuit protection function is incorporated. |
| Fuse blow indicator |  | Yes | - | $\times$ | Fuse blown is not displayed since the A1SY42P does not have fuses. |
| External supply power | Voltage | 12/24VDC (10.2 to 40VDC) | 12/24VDC (10.2 to 30VDC) | $\triangle$ | Voltages exceeding 30VDC cannot be applied. |
|  | Current | 40 mA (TYP. 24VDC/common) | 14 mA (At 24VDC/common) | $\bigcirc$ |  |
| External connection |  | 40-pin connector (with solder) $\times 2$ | 40-pin connector (included) $\times 2$ | $\bigcirc$ |  |
| Applicable wire size |  | $0.3 \mathrm{~mm}^{2}$ | $0.3 \mathrm{~mm}^{2}$ | $\bigcirc$ |  |
| Accessory |  | External wiring connectors $\times 2$ | External wiring connectors $\times 2$ | $\bigcirc$ |  |
| Current consumption |  | $0.29 \mathrm{~A}$ <br> (TYP. all points ON) | $0.17 \mathrm{~A}$ <br> (TYP. all points ON) | $\bigcirc$ |  |
| External dimensions |  | $250(\mathrm{H}) \times 37.5(\mathrm{~W}) \times 106(\mathrm{D}) \mathrm{mm}$ | $130(\mathrm{H}) \times 34.5(\mathrm{~W}) \times 93.6(\mathrm{D}) \mathrm{mm}$ | $\triangle$ |  |
| Weight |  | 0.50 kg | 0.17 kg | $\bigcirc$ |  |

(22) Specifications comparisons between the AY42-S4 and the A1SY42P

| Specification |  | AY42-S4 | A1SY42P | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Number of output points |  | 64 points | 64 points | $\bigcirc$ |  |
| Isolation method |  | Photocoupler | Photocoupler | $\bigcirc$ |  |
| Rated load voltage |  | 12/24VDC | 12/24VDC | $\bigcirc$ |  |
| Operating load voltage range |  | 10.2 to 30VDC | 10.2 to 30VDC | $\bigcirc$ |  |
| Maximum load current |  | 0.1A/point, 1.92A/common | 0.1A/point, 2A/common | $\bigcirc$ |  |
| Maximum inrush current |  | 0.4 A 10 ms or less | 0.7A 10 ms or less | $\bigcirc$ |  |
| Leakage current at OFF |  | 0.1 mA or less | 0.1 mA or less | $\bigcirc$ |  |
| Maximum voltage drop at ON |  | 2.5VDC (MAX.) 0.1A 1.0VDC (TYP.) 0.1A | $\begin{aligned} & 0.1 \mathrm{VDC}(\text { (TYP.) 0.1A } \\ & 0.2 \mathrm{VDC} \text { (MAX.) } 0.1 \mathrm{~A} \end{aligned}$ | $\bigcirc$ |  |
| Response time | $\mathrm{OFF} \rightarrow \mathrm{ON}$ | 2 ms or less | 1 ms or less | $\bigcirc$ |  |
|  | $\mathrm{ON} \rightarrow$ OFF | $\begin{aligned} & 2 \mathrm{~ms} \text { or less } \\ & \text { (resistive load) } \end{aligned}$ | 1 ms or less (rated load, resistance load) | $\bigcirc$ |  |
| Surge suppressor |  | Photocoupler build-in zener diode | Zener diode | $\bigcirc$ |  |
| Common terminal arrangement |  | 32 points/common (common terminal: 1A1,1A2,2A1,2A2) | 32 points/common (common terminal: 1A1,1A2,2A1,2A2) | $\bigcirc$ |  |
| Operation indicator |  | ON indication (LED) <br> 32 point switch-over using a switch | ON indication (LED) 32 point switch-over using a switch | $\bigcirc$ |  |
| External supply power | Voltage | - | 12/24VDC (10.2 to 30VDC) | $\times$ | External supply power is required. |
|  | Current | - | 14 mA (At 24VDC/common) | $\times$ |  |
| External connection |  | 40-pin connector (with solder) $\times 2$ | 40-pin connector $\times 2$ (included) | $\bigcirc$ |  |
| Applicable wire size |  | $0.3 \mathrm{~mm}^{2}$ | $0.3 \mathrm{~mm}^{2}$ | $\bigcirc$ |  |
| Accessory |  | External wiring connectors $\times 2$ | External wiring connectors $\times 2$ | $\bigcirc$ |  |
| Current consumption |  | $0.50 \mathrm{~A}$ <br> (TYP. 60\% or less simultaneously ON) | $0.17 \mathrm{~A}$ <br> (TYP. all points ON) | $\bigcirc$ |  |
| External dimensions |  | $250(\mathrm{H}) \times 37.5(\mathrm{~W}) \times 106(\mathrm{D}) \mathrm{mm}$ | $130(\mathrm{H}) \times 34.5(\mathrm{~W}) \times 93.6(\mathrm{D}) \mathrm{mm}$ | $\triangle$ |  |
| Weight |  | 0.44 kg | 0.17 kg | $\bigcirc$ |  |

(23) Specifications comparisons between the AY50 and the A1SY50

| Specification |  | $\bigcirc$ : Compatible, $\Delta$ : Partial change required, $\times$ : Incompatible |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | AY50 | A1SY50 | $\begin{array}{\|c\|} \text { Compati- } \\ \text { bility } \end{array}$ | Precautions for replacement |
| Number of output points |  | 16 points | 16 points | $\bigcirc$ |  |
| Isolation method |  | Photocoupler | Photocoupler | $\bigcirc$ |  |
| Rated load voltage |  | 12/24VDC | 12/24VDC | $\bigcirc$ |  |
| Operating load voltage range |  | 10.2 to 30VDC | 10.2 to 30VDC | $\bigcirc$ |  |
| Maximum load current |  | 0.5A/point, 2A/common | 0.5A/point, 2A/common | $\bigcirc$ |  |
| Maximum inrush current |  | 7 A 10 ms or less 3.5 A 100 ms or less | 4A 10ms or less | $\Delta$ | The inrush current value differs.Use caution on selecting the load to use. |
| Leakage current at OFF |  | 0.1 mA or less | 0.1 mA or less | $\bigcirc$ |  |
| Maximum voltage drop at ON |  | $\begin{aligned} & 0.9 \mathrm{VDC} \text { (TYP.) 0.5A } \\ & 1.5 \mathrm{VDC} \text { (MAX.) } 0.5 \mathrm{~A} \end{aligned}$ | $\begin{aligned} & 0.9 \mathrm{VDC} \text { (TYP.) 0.5A } \\ & 1.5 \mathrm{VDC} \text { (MAX.) } 0.5 \mathrm{~A} \end{aligned}$ | $\bigcirc$ |  |
| Response time | $\mathrm{OFF} \rightarrow \mathrm{ON}$ | 2 ms or less | 2 ms or less | $\bigcirc$ |  |
|  | $\mathrm{ON} \rightarrow$ OFF | 2 ms or less (resistive load) | 2 ms or less (resistive load) | $\bigcirc$ |  |
| Surge suppressor |  | Varistor (52 to 62V) | Zener diode | $\bigcirc$ |  |
| Common terminal arrangement |  | 8 points/common (common terminal: TB10,TB20) | 8 points/common (common terminal: TB10,TB20) | $\bigcirc$ |  |
| Operation indicator |  | ON indication (LED) | ON indication (LED) | $\bigcirc$ |  |
| Fuse |  | 2A fast blow fuse(1 fuse/common) | 3.2A (not replaceable) (fuse blow capacity: 50A) | $\Delta$ | Connect the fast blow fuse to the external if necessary. |
| Fuse blow indicator |  | Yes (LED is turned ON when a fuse is blown. Signal is output to a PLC CPU.) | Yes (When a fuse is blown, LED indicates and signal is output to CPU.) | $\Delta$ | Fuse blow error also occurs when the external supply power is OFF. |
| External supply power | Voltage | 12/24VDC (10.2 to 30VDC) | 12/24VDC (10.2 to 30VDC) | $\bigcirc$ |  |
|  | Current | 65mA (TYP. 24VDC/common) | 60mA (TYP. 24VDC/common) | $\bigcirc$ |  |
| External connection |  | 20-point terminal block connector (M3×6 screws) | 20-point terminal block connector (M3.5×7 screws) | $\times$ | Wiring must be changed. |
| Applicable wire size |  | 0.75 to $2 \mathrm{~mm}^{2}$ | 0.75 to $1.25 \mathrm{~mm}^{2}$ | $\times$ |  |
| Applicable solderless terminal |  | R1.25-3,R2-3, RAV1.25-3,RAV2-3 | R1.25-3.5,R2-3.5, RAV1.25-3.5,RAV2-3.5 | $\times$ |  |
| Current consumption |  | 0.115A <br> (TYP. all points ON) | $0.120 \mathrm{~A}$ <br> (TYP. all points ON) | $\triangle$ | Current capacity must be reviewed. |
| External dimensions |  | $250(\mathrm{H}) \times 37.5(\mathrm{~W}) \times 121(\mathrm{D}) \mathrm{mm}$ | $130(\mathrm{H}) \times 34.5(\mathrm{~W}) \times 93.6(\mathrm{D}) \mathrm{mm}$ | $\triangle$ | The dimensions are different. |
| Weight |  | 0.42 kg | 0.20 kg | $\bigcirc$ |  |

(24) Specifications comparisons between the AY51 and the A1SY50

| Specification |  | AY51 | A1SY50 | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Number of output points |  | 32 points | 16 points | $\times$ | When seventeen or more points are used, use two of the A1SY50 modules. |
| Isolation method |  | Photocoupler | Photocoupler | $\bigcirc$ |  |
| Rated load voltage |  | 12/24VDC | 12/24VDC | $\bigcirc$ |  |
| Operating load voltage range |  | 10.2 to 30VDC | 10.2 to 30VDC | $\bigcirc$ |  |
| Maximum load current |  | 0.5A/point, 4A/common (When placing next to the power supply module: $3.3 \mathrm{~A} /$ common) | 0.5A/point, 2A/common | $\bigcirc$ |  |
| Maximum inrush current |  | 4A 10ms or less | 4A 10ms or less | $\bigcirc$ |  |
| Leakage current at OFF |  | 0.1 mA or less | 0.1 mA or less | $\bigcirc$ |  |
| Maximum voltage drop at ON |  | $\begin{aligned} & \text { 0.9VDC (TYP.) 0.5A } \\ & 1.5 \mathrm{VDC} \text { (MAX.) } 0.5 \mathrm{~A} \end{aligned}$ | $\begin{aligned} & \text { 0.9VDC (TYP.) 0.5A } \\ & 1.5 \mathrm{VDC} \text { (MAX.) } 0.5 \mathrm{~A} \end{aligned}$ | $\bigcirc$ |  |
| Response time | $\mathrm{OFF} \rightarrow \mathrm{ON}$ | 2 ms or less | 2 ms or less | $\bigcirc$ |  |
|  | $\mathrm{ON} \rightarrow$ OFF | 2 ms or less (resistive load) | 2 ms or less (resistive load) | $\bigcirc$ |  |
| Surge suppressor |  | Varistor (52 to 62V) | Zener diode | $\bigcirc$ |  |
| Common terminal arrangement |  | 16 points/common (common terminal: TB18,TB36) | 8 points/common (common terminal: TB10,TB20) | $\bigcirc$ |  |
| Operation indicator |  | ON indication (LED) | ON indication (LED) | $\bigcirc$ |  |
| Fuse |  | None | 3.2A (not replaceable) (fuse blow capacity: 50A) | $\bigcirc$ |  |
| Fuse blow indicator |  | - | Yes (When the fuse is blown, LED indicates and signal is output to CPU.) | $\triangle$ | Fuse blow error also occurs when the external supply power is OFF. |
| External supply power | Voltage | 12/24VDC (10.2 to 30VDC) | 12/24VDC (10.2 to 30VDC) | $\bigcirc$ |  |
|  | Current | 50mA (TYP. 24VDC/common) | 60mA (TYP. 24VDC/common) | $\bigcirc$ |  |
| External connection |  | 38-point terminal block connector (M3×6 screws) | 20-point terminal block connector (M3.5×7 screws) | $\times$ |  |
| Applicable wire size |  | 0.75 to $2 \mathrm{~mm}^{2}$ | 0.75 to $1.25 \mathrm{~mm}^{2}$ | $\times$ | Wiring must be changed. |
| Applicable solderless terminal |  | $\begin{gathered} \text { R1.25-3,R2-3, } \\ \text { RAV1.25-3,RAV2-3 } \end{gathered}$ | R1.25-3.5,R2-3.5, RAV1.25-3.5,RAV2-3.5 | $\times$ |  |
| Current consumption |  | 0.230 A (TYP. all points ON) | 0.120 A (TYP. all points ON) | $\triangle$ | Review current capacity when using the two A1SY50 modules. |
| External dimensions |  | $250(\mathrm{H}) \times 37.5(\mathrm{~W}) \times 131(\mathrm{D}) \mathrm{mm}$ | $130(\mathrm{H}) \times 34.5(\mathrm{~W}) \times 93.6(\mathrm{D}) \mathrm{mm}$ | $\triangle$ | The dimensions are different. |
| Weight |  | 0.53 kg | 0.20 kg | $\bigcirc$ |  |

(25) Specifications comparisons between the AY51-S1 and the A1SY50

| Specification |  | AY51-S1 | A1SY50 | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Number of output points |  | 32 points | 16 points | $\times$ | When seventeen or more points are used, use two of the A1SY50 modules. |
| Isolation method |  | Photocoupler | Photocoupler | $\bigcirc$ |  |
| Rated load voltage |  | 12/24VDC | 12/24VDC | $\bigcirc$ |  |
| Operating load voltage range |  | 10.2 to 30VDC | 10.2 to 30VDC | $\bigcirc$ |  |
| Maximum load current |  | 0.3A/point, 2A/common <br> (1A/fuse common) | 0.5A/point, 2A/common | $\bigcirc$ |  |
| Maximum inrush current |  | 3 A 10 ms or less | 4A 10ms or less | $\bigcirc$ |  |
| Leakage current at OFF |  | 0.1 mA or less | 0.1 mA or less | $\bigcirc$ |  |
| Maximum voltage drop at ON |  | $\begin{gathered} \text { 1VDC (TYP.) } 0.3 \mathrm{~A} \\ \text { 1.5VDC (MAX.) } 0.3 \mathrm{~A} \end{gathered}$ | $\begin{aligned} & 0.9 \mathrm{VDC} \text { (TYP.) 0.5A } \\ & \text { 1.5VDC (MAX.) } 0.5 \mathrm{~A} \end{aligned}$ | $\bigcirc$ |  |
| Response time | $\mathrm{OFF} \rightarrow \mathrm{ON}$ | 2 ms or less | 2 ms or less | $\bigcirc$ |  |
|  | $\mathrm{ON} \rightarrow \mathrm{OFF}$ | $\begin{aligned} & 2 \mathrm{~ms} \text { or less } \\ & \text { (resistive load) } \end{aligned}$ | 2 ms or less (resistive load) | $\bigcirc$ |  |
| Surge suppressor |  | Transistor built-in zener diode | Zener diode | $\bigcirc$ |  |
| Common terminal arrangement |  | 16 points/common (common terminal: TB18,TB36) 8 points/fuse common | 8 points/common (common terminal: TB10,TB20) | $\bigcirc$ |  |
| Operation indicator |  | ON indication (LED) | ON indication (LED) | $\bigcirc$ |  |
| Fuse |  | 1A fast blow fuse (2 fuses/common in 8 point units) MP-10 | 3.2A (not replaceable) (fuse blow capacity: 50A) | $\triangle$ | Connect the fast blow fuse to the external if necessary. |
| Fuse blow indicator |  | Yes (LED is turned ON when a fuse is blown. Signal is output to a PLC CPU.) | Yes (When a fuse is blown, LED indicates and signal is output to CPU.) | $\triangle$ | Fuse blow error also occurs when the external supply power is OFF. |
| External supply power | Voltage | 12/24VDC (10.2 to 30VDC) | 12/24VDC (10.2 to 30VDC) | $\bigcirc$ |  |
|  | Current | 100mA (TYP. 24VDC/common) | 60mA (TYP. 24VDC/common) | $\bigcirc$ |  |
| External connection |  | 38-point terminal block connector (M3×6 screws) | 20-point terminal block connector (M3.5×7 screws) | $\times$ |  |
| Applicable wire size |  | 0.75 to $2 \mathrm{~mm}^{2}$ | 0.75 to $1.25 \mathrm{~mm}^{2}$ | $\times$ | Wiring must be changed. |
| Applicable solderless terminal |  | $\begin{gathered} \text { R1.25-3,R2-3, } \\ \text { RAV1.25-3,RAV2-3 } \end{gathered}$ | $\begin{gathered} \text { R1.25-3.5,R2-3.5, } \\ \text { RAV1.25-3.5,RAV2-3.5 } \end{gathered}$ | $\times$ |  |
| Current consumption |  | 0.310 A (TYP. all points ON) | 0.120 A (TYP. all points ON) | $\bigcirc$ |  |
| External dimensions |  | $250(\mathrm{H}) \times 37.5(\mathrm{~W}) \times 131(\mathrm{D}) \mathrm{mm}$ | $130(\mathrm{H}) \times 34.5(\mathrm{~W}) \times 93.6(\mathrm{D}) \mathrm{mm}$ | $\Delta$ | The dimensions are different. |
| Weight |  | 0.55 kg | 0.20 kg | $\bigcirc$ |  |

(26) Specifications comparisons between the AY51-UL and the A1SY50

| Specification |  | AY51-UL | A1SY50 | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Number of output points |  | 32 points | 16 points | $\times$ | When seventeen or more points are used, use two of the A1SY50 modules. |
| Isolation method |  | Photocoupler | Photocoupler | $\bigcirc$ |  |
| Rated load voltage |  | 12/24VDC | 12/24VDC | $\bigcirc$ |  |
| Operating load voltage range |  | 10.2 to 30VDC | 10.2 to 30VDC | $\bigcirc$ |  |
| Maximum load current |  | 0.5A/point, 4A/common (When placing next to the power supply module: 3.3A/common) | 0.5A/point, 2A/common | $\bigcirc$ |  |
| Maximum inrush current |  | 0.4 A 10 ms or less | 4A 10ms or less | $\bigcirc$ |  |
| Leakage current at OFF |  | 0.1 mA or less | 0.1 mA or less | $\bigcirc$ |  |
| Maximum voltage drop at ON |  | $\begin{aligned} & \hline 0.9 \mathrm{VDC} \text { (TYP.) } 0.5 \mathrm{~A} \\ & 1.5 \mathrm{VDC} \text { (MAX.) } 0.5 \mathrm{~A} \\ & \hline \end{aligned}$ | $\begin{aligned} & \hline 0.9 \mathrm{VDC} \text { (TYP.) } 0.5 \mathrm{~A} \\ & 1.5 \mathrm{VDC} \text { (MAX.) } 0.5 \mathrm{~A} \\ & \hline \end{aligned}$ | $\bigcirc$ |  |
| Response time | $\mathrm{OFF} \rightarrow \mathrm{ON}$ | 2 ms or less | 2 ms or less | $\bigcirc$ |  |
|  | $\mathrm{ON} \rightarrow$ OFF | 2 ms or less (resistive load) | 2 ms or less (resistive load) | $\bigcirc$ |  |
| Surge suppressor |  | Varistor (52 to 62V) | Zener diode | $\bigcirc$ |  |
| Common terminal arrangement |  | 16 points/common (common terminal: TB18,TB36) | 8 points/common (common terminal: TB10,TB20) | $\bigcirc$ |  |
| Operation indicator |  | ON indication (LED) | ON indication (LED) | $\bigcirc$ |  |
| Fuse |  | None | 3.2A (not replaceable) (fuse blow capacity: 50A) | $\bigcirc$ |  |
| Fuse blow indicator |  | None | Yes (When a fuse is blown, LED indicates and signal is output to CPU.) | $\bigcirc$ | Fuse blow error also occurs when the external supply power is OFF. |
| External supply power | Voltage | 12/24VDC (10.2 to 30VDC) | 12/24VDC (10.2 to 30VDC) | $\bigcirc$ |  |
|  | Current | 50mA (TYP. 24VDC/common) | 60mA (TYP. $24 \mathrm{VDC} /$ common) | $\bigcirc$ |  |
| External connection |  | 38-point terminal block connector (M3.5×6 screws) | 20-point terminal block connector (M3.5×7 screws) | $\times$ |  |
| Applicable wire size |  | 0.75 to $2 \mathrm{~mm}^{2}$ | 0.75 to $1.25 \mathrm{~mm}^{2}$ | $\times$ | Wiring must be changed. |
| Applicable solderless terminal |  | R1.25-3.5,R2-3.5, RAV1.25-3.5,RAV2-3.5 | R1.25-3.5,R2-3.5, RAV1.25-3.5,RAV2-3.5 | $\bigcirc$ |  |
| Current consumption |  | 0.230 A (TYP. all points ON) | 0.120 A (TYP. all points ON) | $\Delta$ | Review current capacity hen using the two A1SY50 modules. |
| External dimensions |  | $250(\mathrm{H}) \times 37.5(\mathrm{~W}) \times 131(\mathrm{D}) \mathrm{mm}$ | $130(\mathrm{H}) \times 34.5(\mathrm{~W}) \times 93.6(\mathrm{D}) \mathrm{mm}$ | $\triangle$ | The dimensions are different. |
| Weight |  | 0.53 kg | 0.20 kg | $\bigcirc$ |  |

(27) Specifications comparisons between the AY60 and the A1SY60

| Specification |  | AY60 | A1SY60 | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Number of output points |  | 16 points | 16 points | $\bigcirc$ |  |
| Isolation method |  | Photocoupler | Photocoupler | $\bigcirc$ |  |
| Rated load voltage |  | 24VDC (12/48V) | 24VDC | $\triangle$ | Voltages exceeding 26.4VDC cannot be applied. |
| Operating load voltage range |  | 21.6 to 26.4 VDC (10.2 to 56VDC) | 21.6 to 26.4VDC | $\triangle$ | Voltages exceeding 26.4VDC cannot be applied. |
| Maximum load current |  | 2A/point, 5A/common <br> (3A/fuse) <br> (When placing next to the power supply module: 3A/common) | 2A/point, $4 \mathrm{~A} /$ common $\left(25^{\circ} \mathrm{C}\right)$, <br> $1.8 \mathrm{~A} /$ point, $3.6 \mathrm{~A} /$ common $\left(45^{\circ} \mathrm{C}\right)$, <br> $1.6 \mathrm{~A} /$ point, $3.2 \mathrm{~A} /$ common $\left(55^{\circ} \mathrm{C}\right)$ | $\triangle$ | Since the maximum load current per common is different, pay attention to the current used in the entire module. |
| Maximum inrush current |  | 4A 100ms or less, 8A 10ms or less | 8A 10ms or less | $\bigcirc$ |  |
| Leakage current at OFF |  | 0.1 mA or less | 0.1 mA or less | $\bigcirc$ |  |
| Maximum voltage drop at ON |  | 1.5 VDC (2A) | $\begin{aligned} & \text { 0.9VDC (TYP.) 2A, } \\ & \text { 1.5VDC (MAX.) 2A } \end{aligned}$ | $\bigcirc$ |  |
| Response time | $\mathrm{OFF} \rightarrow \mathrm{ON}$ | 2 ms or less | 2 ms or less | $\bigcirc$ |  |
|  | $\mathrm{ON} \rightarrow$ OFF | 2 ms or less (resistive load) | 2 ms or less (resistive load) | $\bigcirc$ |  |
| Surge suppressor |  | Varistor (108 to 132V) | Zener diode | $\bigcirc$ |  |
| Common terminal arrangement |  | 8 points/common (common terminal: TB10,TB20) | 8 points/common (common terminal: TB10,TB20) | $\bigcirc$ |  |
| Operation indicator |  | ON indication (LED) | ON indication (LED) | $\bigcirc$ |  |
| Fuse |  | 3.2A fast blow fuse (2 fuse/common) MP-32 | 5A fuse (1 fuse/common) Not replaceable | $\triangle$ | Connect the fast blow fuse to the external if necessary. |
| Fuse blow indicator |  | Yes (LED is turned ON when a fuse is blown. Signal is output to a PLC CPU.) | Yes (LED is turned ON when a fuse is blown. Signal is output to a PLC CPU.) | $\triangle$ | Fuse blow error also occurs when the external supply power is OFF. |
| External supply power | Voltage | 24VDC (21.6 to 26.4VDC) | 24VDC (21.6 to 26.4VDC) | $\bigcirc$ |  |
|  | Current | 65mA (TYP. 24VDC/common) | 15 mA (TYP. 24VDC/common) | $\bigcirc$ |  |
| External connection |  | 20-point terminal block connector (M3×6 screws) | 20-point terminal block connector (M3.5×7 screws) | $\times$ |  |
| Applicable wire size |  | 0.75 to $2 \mathrm{~mm}^{2}$ | 0.75 to $1.25 \mathrm{~mm}^{2}$ | $\times$ | Wiring must be changed. |
| Applicable solderless terminal |  | R1.25-3,R2-3, RAV1.25-3,RAV2-3 | R1.25-3.5,R2-3.5, RAV1.25-3.5,RAV2-3.5 | $\times$ |  |
| Current consumption |  | $0.115 \mathrm{~A}$ <br> (TYP. all points ON) | $0.120 \mathrm{~A}$ <br> (TYP. all points ON) | $\triangle$ | Current capacity must be reviewed. |
| External dimensions |  | $250(\mathrm{H}) \times 37.5(\mathrm{~W}) \times 121(\mathrm{D}) \mathrm{mm}$ | $130(\mathrm{H}) \times 34.5(\mathrm{~W}) \times 93.6(\mathrm{D}) \mathrm{mm}$ | $\triangle$ | The dimensions are different. |
| Weight |  | 0.64 kg | 0.20 kg | $\bigcirc$ |  |

(28) Specifications comparisons between the AY60E and the A1SY60E

| Specification |  | $\bigcirc$ : Compatible, $\Delta$ : Partial change required, $\times$ : Incompatible |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | AY60E | A1SY60E | $\left\lvert\, \begin{gathered} \text { Compati- } \\ \text { bility } \end{gathered}\right.$ | Precautions for replacement |
| Number of points | utput | 16 points | 16 points | $\bigcirc$ |  |
| Isolation m | thod | Photocoupler | Photocoupler | $\bigcirc$ |  |
| Rated load voltage |  | 24VDC (12/48V) | 5/12/24VDC | $\triangle$ | Voltages exceeding 26.4VDC cannot be applied. |
| Operating load voltage range |  | 21.6 to 26.4 VDC (10.2 to 56VDC) | 4.5 to 26.4VDC | $\triangle$ | Voltages exceeding 26.4VDC cannot be applied. |
| Maximum load current |  | 12/24VDC 2A/point, 48VDC 0.8A/point, 5A/common <br> (When placing next to the power supply module: 3A/common) | 2A/point (condition: $\tau=\mathrm{L} / \mathrm{R} \leqq 2.5 \mathrm{~ms}$ ) 4A/common | $\triangle$ | Since the maximum load current per common is different, pay attention to the current used in the entire module. |
| Maximum inrush current |  | 4A 100ms or less 8A 10ms or less | 8 A 10 ms or less | $\bigcirc$ |  |
| Leakage current at OFF |  | 0.1 mA or less | 0.1 mA or less | $\bigcirc$ |  |
| Maximum voltage drop at ON |  | 1.5VDC (2A) | $\begin{aligned} & 0.2 \mathrm{VDC} \text { (MAX.) 1A } \\ & 0.4 \mathrm{VDC} \text { (MAX.) 2A } \end{aligned}$ | $\bigcirc$ |  |
| Response time | $\mathrm{OFF} \rightarrow \mathrm{ON}$ | 2 ms or less | 3 ms or less | $\triangle$ | The response times differ. |
|  | $\mathrm{ON} \rightarrow \mathrm{OFF}$ | 2 ms or less (resistive load) | 10 ms or less (resistive load) | $\triangle$ |  |
| Surge suppressor |  | Surge suppression diode | Zener diode | $\bigcirc$ |  |
| Common terminal arrangement |  | 8 points/common (common terminal: TB10,TB20) | 8 points/common (common terminal: TB10,TB20) | $\bigcirc$ |  |
| Operation indicator |  | ON indication (LED) | ON indication (LED) | $\bigcirc$ |  |
| Fuse |  | 5A fast blow fuse (2 fuses/common) | 7A fuse (1 fuse/common) Not replaceable | $\triangle$ | Connect the fast blow fuse to the external if necessary. |
| Fuse blow indicator |  | Yes (LED is turned ON when a fuse is blown. The signal is output to a PLC CPU.) | Yes (LED is turned ON when a fuse is blown. The signal is output to a PLC CPU.) | $\triangle$ | Fuse blow error also occurs when the external supply power is OFF. |
| External supply power | Voltage | 24VDC (21.6 to 26.4VDC) | 12/24VDC (10.2 to 30VDC) | $\bigcirc$ |  |
|  | Current | 65 mA <br> (TYP. 24VDC/common) | 10 mA <br> (TYP. 24VDC/common) | $\bigcirc$ |  |
| External connection |  | 20-point terminal block connector (M3×6 screws) | 20-point terminal block connector (M3.5×7 screws) | $\times$ | Wiring must be changed. |
| Applicable wire size |  | 0.75 to $2 \mathrm{~mm}^{2}$ | 0.75 to $1.25 \mathrm{~mm}^{2}$ | $\times$ |  |
| Applicable solderless terminal |  | R1.25-3,R2-3, RAV1.25-3,RAV2-3 | R1.25-3.5,R2-3.5, RAV1.25-3.5,RAV2-3.5 | $\times$ |  |
| Current consumption |  | $0.115 \mathrm{~A}$ <br> (TYP. all points ON) | 0.200 A (TYP. all points ON) | $\triangle$ | Current capacity must be reviewed. |
| External dimensions |  | $250(\mathrm{H}) \times 37.5(\mathrm{~W}) \times 121(\mathrm{D}) \mathrm{mm}$ | $130(\mathrm{H}) \times 34.5(\mathrm{~W}) \times 93.6(\mathrm{D}) \mathrm{mm}$ | $\triangle$ | The dimensions are different. |
| Weight |  | 0.63 kg | 0.20 kg | $\bigcirc$ |  |

(29) Specifications comparisons between the AY60S and the A1SY60

| Specification |  | AY60S | A1SY60 | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Number of output points |  | 16 points | 16 points | $\bigcirc$ |  |
| Isolation method |  | Photocoupler | Photocoupler | $\bigcirc$ |  |
| Rated load voltage |  | 24/48VDC (12V) | 24VDC | $\triangle$ | Voltages exceeding 26.4VDC cannot be applied. |
| Operating load voltage range |  | $\begin{aligned} & 21.6 \text { to } 52.8 \mathrm{VDC} \\ & \text { (10.2 to } 52.8 \mathrm{VDC} \text { ) } \end{aligned}$ | 21.6 to 26.4VDC | $\triangle$ | Voltages exceeding 26.4VDC cannot be applied. |
| Maximum load current |  | 2A/point, 6.4A/common <br> (5A/ fuse) <br> (When placing next to the power supply module: 5A/common) | 2A/point, $4 \mathrm{~A} /$ common $\left(25^{\circ} \mathrm{C}\right)$, <br> $1.8 \mathrm{~A} /$ point, $3.6 \mathrm{~A} / \mathrm{common}\left(45^{\circ} \mathrm{C}\right)$, <br> $1.6 \mathrm{~A} /$ point, $3.2 \mathrm{~A} /$ common $\left(55^{\circ} \mathrm{C}\right)$ | $\triangle$ | Since the maximum load current per common is different, pay attention to the current used in the entire module. |
| Maximum inrush current |  | 4A 100ms or less, 8A 10ms or less | 8 A 10 ms or less | $\bigcirc$ |  |
| Leakage current at OFF |  | 0.1 mA or less | 0.1 mA or less | $\bigcirc$ |  |
| Maximum voltage drop at ON |  | 1VDC (2A) | $\begin{aligned} & \hline 0.9 \mathrm{VDC} \text { (TYP.) 2A, } \\ & \text { 1.5VDC (MAX.) 2A } \\ & \hline \end{aligned}$ | $\bigcirc$ |  |
| Response time | $\mathrm{OFF} \rightarrow \mathrm{ON}$ | 1 ms or less | 2 ms or less | $\triangle$ | The response times differ. |
|  | $\mathrm{ON} \rightarrow \mathrm{OFF}$ | 3 ms or less (resistive load) | 2 ms or less (resistive load) | $\bigcirc$ |  |
| Surge suppressor |  | Varistor (90 to 110V) | Zener diode | $\bigcirc$ |  |
| Common terminal arrangement |  | 8 points/common (common terminal: TB10,TB20) | 8 points/common (common terminal: TB10,TB20) | $\bigcirc$ |  |
| Operation indicator |  | ON indication (LED) | ON indication (LED) | $\bigcirc$ |  |
| Fuse |  | 5A fast blow fuse (2 fuse/common) MP-50 | 5A fuse (1 fuse/common) Not replaceable | $\triangle$ | Connect the fast blow fuse to the external if necessary. |
| Fuse blow indicator |  | Yes (LED is turned ON when a fuse is blown. The signal is output to a PLC CPU.) | Yes (LED is turned ON when a fuse is blown. The signal is output to a PLC CPU.) | $\triangle$ | Fuse blow error also occurs when the external supply power is OFF. |
| External supply power | Voltage | 24/48VDC (21.6 to 52.8VDC) | 24 VDC (21.6 to 26.4VDC) | $\Delta$ | Voltages exceeding 26.4VDC cannot be applied. |
|  | Current | 3mA (TYP. 24VDC/common) | 15mA (TYP. 24VDC/common) | $\triangle$ | Current capacity must be reviewed. |
| External connection |  | 20-point terminal block connector (M3×6 screws) | 20-point terminal block connector (M3.5×7 screws) | $\times$ |  |
| Applicable wire size |  | 0.75 to $2 \mathrm{~mm}^{2}$ | 0.75 to $1.25 \mathrm{~mm}^{2}$ | $\times$ | Wiring must be changed. |
| Applicable solderless terminal |  | $\begin{gathered} \text { R1.25-3,R2-3, } \\ \text { RAV1.25-3,RAV2-3 } \end{gathered}$ | R1.25-3.5,R2-3.5, RAV1.25-3.5,RAV2-3.5 | $\times$ |  |
| Current consumption |  | 0.75 A (TYP. all points ON) | $0.12 \mathrm{~A}$ <br> (TYP. all points ON) | $\triangle$ | Current capacity must be reviewed. |
| External dimensions |  | $250(\mathrm{H}) \times 37.5(\mathrm{~W}) \times 121(\mathrm{D}) \mathrm{mm}$ | $130(\mathrm{H}) \times 34.5(\mathrm{~W}) \times 93.6(\mathrm{D}) \mathrm{mm}$ | $\Delta$ | The dimensions are different. |
| Weight |  | 0.66kg | 0.20kg | $\bigcirc$ |  |

(30) Specifications comparisons between the AY70 and the A1SY71

| Specification |  |  | $\bigcirc$ : Compatible, $\Delta$ : Partial change required, $\times$ : Incompatible |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | AY70 | A1SY71 | Compatibility | Precautions for replacement |
| Number of output points |  | 16 points | 32 points | $\times$ | Set sixteen points in the I/O assignment of Parameter. |
| Isolation method |  | Photocoupler | Photocoupler | $\bigcirc$ |  |
| Rated load voltage |  | 5/12VDC | 5/12VDC | $\bigcirc$ |  |
| Operating load voltage range |  | 4.5 to 15VDC | 4.5 to 15VDC | $\bigcirc$ |  |
| Maximum load current |  | $16 \mathrm{~mA} /$ point, $128 \mathrm{~mA} /$ common | 16mA/point, $256 \mathrm{~mA} /$ common | $\bigcirc$ |  |
| Maximum inrush current |  | $50 \mathrm{~mA} \mathrm{10ms}$ | 40 mA 10 ms or less | $\Delta$ | The inrush current value differs.Use caution on selecting the load to use. |
| Output voltage at OFF |  | $\begin{gathered} \mathrm{V}_{\mathrm{OH}}: 3.5 \mathrm{VDC} \\ \left(\mathrm{Vcc}=5 \mathrm{VDC}, \mathrm{I}_{\mathrm{OH}}=0.4 \mathrm{~mA}\right) \end{gathered}$ | $\begin{gathered} \mathrm{V}_{\mathrm{OH}}: 3.5 \mathrm{VDC} \\ \left(\mathrm{Vcc}=5 \mathrm{VDC}, \mathrm{I}_{\mathrm{OH}}=0.4 \mathrm{~mA}\right) \end{gathered}$ | $\bigcirc$ |  |
| Maximum voltage drop at ON |  | $\mathrm{V}_{\mathrm{OL}}: 0.2 \mathrm{VDC}\left(\mathrm{I}_{\mathrm{OL}}=16 \mathrm{~mA}\right)$ | $\mathrm{V}_{\mathrm{OL}}: 0.3 \mathrm{VDC}$ | $\Delta$ | Check the input specifications of external equipment to be connected since the maximum voltage drop is bigger when turning ON. |
| Response time | OFF $\rightarrow$ ON | 1 ms or less | 1 ms or less | $\bigcirc$ |  |
|  | $\mathrm{ON} \rightarrow \mathrm{OFF}$ | 1 ms or less | $\begin{gathered} 1 \mathrm{~ms} \text { or less } \\ \text { (resistive load) } \end{gathered}$ | $\bigcirc$ |  |
| Common terminal arrangement |  | 8 points/common (common terminal: TB10,TB20) | 32 points/common (common terminal: A1,A2) | $\triangle$ | As 2 commons are reduced to 1 , wiring a different voltage for each common is not possible. |
| Operation indicator |  | ON indication (LED) | ON indication (LED) | $\bigcirc$ |  |
| Fuse |  | None | 1.6A (not replaceable) (fuse blow capacity: 50A) | $\bigcirc$ |  |
| Fuse blow indicator |  | - | Yes (LED is turned ON when a fuse is blown. The signal is output to a PLC CPU) | $\bigcirc$ | Fuse blow error also occurs when the external supply power is OFF. |
| External supply power | Voltage | 5/12VDC (4.5 to 15VDC) | 5/12VDC (4.5 to 15VDC) | $\bigcirc$ |  |
|  | Current | 55 mA $($ TYP.12VDC/common) | 150mA (12VDC/common) (MAX. all points ON) | $\Delta$ | Current capacity must be reviewed. |
| External connection |  | 20-point terminal block connector (M3×6 screws) | 40-pin connector (included) | $\times$ | Wiring must be |
| Applicable wire size |  | 0.75 to $2 \mathrm{~mm}^{2}$ | $0.3 \mathrm{~mm}^{2}$ | $\times$ | change |
| Current consumption |  | $0.10 \mathrm{~A}$ <br> (TYP. all points ON) | $0.40 \mathrm{~A}$ <br> (TYP. all points ON) | $\triangle$ | Current capacity must be reviewed. |
| External dimensions |  | $250(\mathrm{H}) \times 37.5(\mathrm{~W}) \times 121(\mathrm{D}) \mathrm{mm}$ | $130(\mathrm{H}) \times 34.5(\mathrm{~W}) \times 93.6(\mathrm{D}) \mathrm{mm}$ | $\triangle$ |  |
| Weight |  | 0.36 kg | 0.19 kg | $\bigcirc$ |  |

(31) Specifications comparisons between the AY71 and the A1SY71

| Specification |  | AY71 | A1SY71 | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Number of output points |  | 32 points | 32 points | $\bigcirc$ |  |
| Isolation method |  | Photocoupler | Photocoupler | $\bigcirc$ |  |
| Rated load voltage |  | 5/12VDC | 5/12VDC | $\bigcirc$ |  |
| Operating load voltage range |  | 4.5 to 15VDC | 4.5 to 15VDC | $\bigcirc$ |  |
| Maximum load current |  | 16mA/point, $256 \mathrm{~mA} /$ common (Sink loading) | $16 \mathrm{~mA} /$ point, $256 \mathrm{~mA} /$ common | $\bigcirc$ |  |
| Maximum inrush current |  | 50 mA 10 ms | 40 mA 10 ms or less | $\triangle$ | The inrush current value differs.Use caution on selecting the load to use. |
| Output voltage at OFF |  | $\begin{gathered} \mathrm{V}_{\mathrm{OH}}: 3.5 \mathrm{VDC} \\ \left(\mathrm{Vcc}=5 \mathrm{VDC}, \mathrm{I}_{\mathrm{OH}}=0.4 \mathrm{~mA}\right) \end{gathered}$ | $\begin{gathered} \mathrm{V}_{\mathrm{OH}}: 3.5 \mathrm{VDC} \\ \left(\mathrm{Vcc}=5 \mathrm{VDC}, \mathrm{I}_{\mathrm{OH}}=0.4 \mathrm{~mA}\right) \end{gathered}$ | $\bigcirc$ |  |
| Maximum voltage drop at ON |  | $\mathrm{V}_{\mathrm{OL}}: 0.2 \mathrm{VDC}\left(\mathrm{l}_{\mathrm{OL}}=16 \mathrm{~mA}\right)$ | $\mathrm{V}_{\mathrm{OL}}: 0.3 \mathrm{VDC}$ | $\triangle$ | Check the input specifications of external equipment to be connected since the maximum voltage drop is bigger when turning ON . |
| Response time | $\mathrm{OFF} \rightarrow \mathrm{ON}$ | 1 ms or less | 1 ms or less | $\bigcirc$ |  |
|  | $\mathrm{ON} \rightarrow$ OFF | 1 ms or less | $\begin{gathered} 1 \mathrm{~ms} \text { or less } \\ \text { (resistive load) } \end{gathered}$ | $\bigcirc$ |  |
| Common terminal arrangement |  | 16 points/common (common terminal: TB18,TB36) | 32 points/common (common terminal: A1,A2) | $\triangle$ | As 2 commons are reduced to 1 , wiring a different voltage for each common is not possible. |
| Operation indicator |  | ON indication (LED) | ON indication (LED) | $\bigcirc$ |  |
| Fuse |  | None | 1.6A (not replaceable) (fuse blow capacity: 50A) | $\bigcirc$ |  |
| Fuse blow indicator |  | None | Yes (LED is turned ON when a fuse is blown. The signal is output to a PLC CPU.) | $\triangle$ | Since the fuse blown error detection is executed, the parameter or sequence program must be reviewed. |
| External supply power | Voltage | 5/12VDC (4.5 to 15VDC) | 5/12VDC (4.5 to 15VDC) | $\bigcirc$ |  |
|  | Current | 100mA (TYP.12VDC/common) | 150mA (12VDC/common) (MAX. all points ON) | $\triangle$ | Current capacity must be reviewed. |
| External connection |  | 38-point terminal block connector (M3×6 screws) | 40-pin connector (included) | $\times$ | Wiring must be |
| Applicable wire size |  | 0.75 to $2 \mathrm{~mm}^{2}$ | $0.3 \mathrm{~mm}^{2}$ | $\times$ | changed. |
| Current consumption |  | $0.20 \mathrm{~A}$ <br> (TYP. all points ON) | 0.40 A <br> (TYP. all points ON) | $\triangle$ | Current capacity must be reviewed. |
| External dimensions |  | $250(\mathrm{H}) \times 37.5(\mathrm{~W}) \times 131(\mathrm{D}) \mathrm{mm}$ | $130(\mathrm{H}) \times 34.5(\mathrm{~W}) \times 93.6(\mathrm{D}) \mathrm{mm}$ | $\triangle$ |  |
| Weight |  | 0.44 kg | 0.19 kg | $\bigcirc$ |  |

(32) Specifications comparisons between the AY72 and the A1SY71

| Specification |  | AY72 | A1SY71 | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Number of output points |  | 64 points | 32 points | $\times$ | When thirty-two or more points are used, use two of the A1SY71 modules. |
| Isolation method |  | Photocoupler | Photocoupler | $\bigcirc$ |  |
| Rated load voltage |  | 5/12VDC | 5/12VDC | $\bigcirc$ |  |
| Operating load voltage range |  | 4.5 to 15VDC | 4.5 to 15VDC | $\bigcirc$ |  |
| Maximum load current |  | 16mA/point, $512 \mathrm{~mA} /$ common (Sink loading) | $16 \mathrm{~mA} /$ point, $256 \mathrm{~mA} /$ common | $\bigcirc$ |  |
| Maximum inrush current |  | $50 \mathrm{~mA} \mathrm{10ms}$ | $40 \mathrm{~mA} \mathrm{10ms} \mathrm{or} \mathrm{less}$ | $\triangle$ | The inrush current value differs.Use caution on selecting the load to use. |
| Output voltage at OFF |  | $\begin{gathered} \mathrm{V}_{\mathrm{OH}}: 3.5 \mathrm{VDC} \\ \left(\mathrm{Vcc}=5 \mathrm{VDC}, \mathrm{I}_{\mathrm{OH}}=0.4 \mathrm{~mA}\right) \end{gathered}$ | $\begin{gathered} \mathrm{V}_{\mathrm{OH}}: 3.5 \mathrm{VDC} \\ \left(\mathrm{Vcc}=5 \mathrm{VDC}, \mathrm{I}_{\mathrm{OH}}=0.4 \mathrm{~mA}\right) \end{gathered}$ | $\bigcirc$ |  |
| Maximum voltage drop at ON |  | $\mathrm{V}_{\mathrm{OL}}: 0.2 \mathrm{VDC}\left(\mathrm{l}_{\mathrm{OL}}=16 \mathrm{~mA}\right)$ | $\mathrm{V}_{\mathrm{OL}}: 0.3 \mathrm{VDC}$ | $\triangle$ | Check the input specifications of external equipment to be connected since the maximum voltage drop is bigger when turning ON. |
| Response time | $\mathrm{OFF} \rightarrow \mathrm{ON}$ | 1 ms or less | 1 ms or less | $\bigcirc$ |  |
|  | $\mathrm{ON} \rightarrow \mathrm{OFF}$ | 1 ms or less | 1 ms or less (resistive load) | $\bigcirc$ |  |
| Common terminal arrangement |  | 32 points/common (common terminal: 1A1,1A2,2A1,2A2) | 32 points/common (common terminal: A1,A2) | $\bigcirc$ |  |
| Operation indicator |  | ON indication (LED) | ON indication (LED) | $\bigcirc$ |  |
| Fuse |  | None | 1.6A (not replaceable) (fuse blow capacity: 50A) | $\bigcirc$ |  |
| Fuse blow indicator |  | None | Yes (LED is turned ON when a fuse is blown. The signal is output to a PLC CPU.) | $\triangle$ | Since the fuse blow error detection is executed, the parameter or sequence program must be reviewed. |
| External supply power | Voltage | 5/12VDC (4.5 to 15VDC) | 5/12VDC (4.5 to 15VDC) | $\bigcirc$ |  |
|  | Current | 300mA (TYP.12VDC 1-common ON) | 150 mA (12VDC/common) <br> (MAX. all points ON) | $\bigcirc$ |  |
| External connection |  | 40-pin connector (with solder) $\times 2$ | 40-pin connector (included) | $\bigcirc$ |  |
| Applicable wire size |  | $0.3 \mathrm{~mm}^{2}$ | $0.3 \mathrm{~mm}^{2}$ | $\bigcirc$ |  |
| Accessory |  | External wiring connectors $\times 2$ | External wiring connectors $\times 1$ | $\bigcirc$ |  |
| Current consumption |  | 0.30A <br> (TYP. all points ON) | $0.40 \mathrm{~A}$ <br> (TYP. all points ON) | $\triangle$ | Current capacity must be reviewed. |
| External dimensions |  | $250(\mathrm{H}) \times 37.5(\mathrm{~W}) \times 106$ (D) mm | $130(\mathrm{H}) \times 34.5(\mathrm{~W}) \times 93.6(\mathrm{D}) \mathrm{mm}$ | $\triangle$ |  |
| Weight |  | 0.47 kg | 0.19 kg | $\bigcirc$ |  |

(33) Specifications comparisons between the AY80 and the A1SY80

| Specification |  | $\bigcirc$ : Compatible, $\triangle$ : Partial change required, $\times$ : Incompatible |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | AY80 | A1SY80 | Compatibility | Precautions for replacement |
| Number of output points |  | 16 points | 16 points | $\bigcirc$ |  |
| Isolation method |  | Photocoupler | Photocoupler | $\bigcirc$ |  |
| Rated load voltage |  | 12/24VDC | 12/24VDC | $\bigcirc$ |  |
| Operating load voltage range |  | 10.2 to 30VDC | 10.2 to 30VDC | $\bigcirc$ |  |
| Maximum load current |  | 0.5A/point, 2A/common | 0.8A/point, 3.2A/common | $\bigcirc$ |  |
| Maximum inrush current |  | 7A 10ms or less 3.5 A 100 ms or less | 8 A 10 ms or less | $\bigcirc$ |  |
| Leakage current at OFF |  | 0.1 mA or less | 0.1 mA or less | $\bigcirc$ |  |
| Maximum voltage drop at ON |  | 1.5VDC (MAX.) 0.5A | 1.5VDC (MAX.) 0.8A | $\bigcirc$ |  |
| Response time | $\mathrm{OFF} \rightarrow \mathrm{ON}$ | 2 ms or less | 2 ms or less | $\bigcirc$ |  |
|  | $\mathrm{ON} \rightarrow$ OFF | $\begin{gathered} 2 \mathrm{~ms} \text { or less } \\ \text { (resistive load) } \end{gathered}$ | 2 ms or less (resistive load) | $\bigcirc$ |  |
| Surge suppressor |  | Varistor (52 to 62V) | Zener diode | $\bigcirc$ |  |
| Common terminal arrangement |  | 8 points/common (common terminal: TB9,TB19) | 8 points/common (common terminal: TB9,TB19) | $\bigcirc$ |  |
| Operation indicator |  | ON indication (LED) | ON indication (LED) | $\bigcirc$ |  |
| Fuse |  | 2A fast blow fuse (1 fuse/common) MP-20 | 5A fuse (1 fuse/common) Not replaceable (fuse blow capacity: 50A) | $\Delta$ | Connect the fast blow fuse to the external if necessary. |
| Fuse blow indicator |  | Yes (LED is turned ON when a fuse is blown. The signal is output to a PLC CPU.) | Yes (LED is turned ON when a fuse is blown. The signal is output to a PLC CPU.) | $\triangle$ | Fuse blow error also occurs when the external supply power is OFF. |
| External supply power | Voltage | 12/24VDC (10.2 to 30VDC) | 12/24VDC (10.2 to 30VDC) | $\bigcirc$ |  |
|  | Current | 60mA (TYP. 24VDC/common) | 20 mA (TYP. 24VDC/common) | $\bigcirc$ |  |
| External connection |  | 20-point terminal block connector (M3×6 screws) | 20-point terminal block connector (M3.5×7 screws) | $\times$ |  |
| Applicable wire size |  | 0.75 to $2 \mathrm{~mm}^{2}$ | 0.75 to $1.25 \mathrm{~mm}^{2}$ | $\times$ | Wiring must be changed. |
| Applicable solderless terminal |  | $\begin{gathered} \text { R1.25-3,R2-3, } \\ \text { RAV1.25-3,RAV2-3 } \end{gathered}$ | R1.25-3.5,R2-3.5, RAV1.25-3.5,RAV2-3.5 | $\times$ |  |
| Current consumption |  | $0.115 \mathrm{~A}$ <br> (TYP. all points ON) | $0.120 \mathrm{~A}$ <br> (TYP. all points ON) | $\triangle$ | Current capacity must be reviewed. |
| External dimensions |  | $250(\mathrm{H}) \times 37.5(\mathrm{~W}) \times 121(\mathrm{D}) \mathrm{mm}$ | $130(\mathrm{H}) \times 34.5(\mathrm{~W}) \times 93.6(\mathrm{D}) \mathrm{mm}$ | $\triangle$ | The dimensions are different. |
| Weight |  | 0.42 kg | 0.20 kg | $\bigcirc$ |  |

(34) Specifications comparisons between the AY81 and the A1SY81

| Specification |  | AY81 | A1SY81 | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Number of output points |  | 32 points | 32 points | $\bigcirc$ |  |
| Isolation method |  | Photocoupler | Photocoupler | $\bigcirc$ |  |
| Rated load voltage |  | 12/24VDC | 12/24VDC | $\bigcirc$ |  |
| Operating load voltage range |  | 10.2 to 30VDC | 10.2 to 30VDC | $\bigcirc$ |  |
| Maximum load current |  | 0.5A/point, 4A/common (When placing next to the power supply module: 3A/common) | 0.1A/point, 2A/common | $\triangle$ | Carefully select load for use since the maximum load current per point is lowered. ${ }^{* 1}$ |
| Maximum inrush current |  | 4A 10ms or less | 0.4 A 10 ms or less | $\triangle$ | The inrush current value differs.Use caution on selecting the load to use. |
| Leakage current at OFF |  | 0.1 mA or less | 0.1 mA or less | $\bigcirc$ |  |
| Maximum voltage drop at ON |  | 1.5VDC (MAX.) 0.5A | $\begin{aligned} & \text { 1.0VDC (TYP.) } 0.1 \mathrm{~A} \\ & 2.5 \mathrm{VDC} \text { (MAX.) } 0.1 \mathrm{~A} \end{aligned}$ | $\triangle$ | Because different values for maximum voltage drop are given when turning ON, care should be taken to select loads to be used. |
| Response time | $\mathrm{OFF} \rightarrow \mathrm{ON}$ | 2 ms or less | 2 ms or less | $\bigcirc$ |  |
|  | $\mathrm{ON} \rightarrow \mathrm{OFF}$ | $\begin{gathered} 2 \mathrm{~ms} \text { or less } \\ \text { (resistive load) } \end{gathered}$ | 2 ms or less (resistive load) | $\bigcirc$ |  |
| Surge suppressor |  | Varistor (52 to 62V) | Zener diode | $\bigcirc$ |  |
| Common terminal arrangement |  | 16 points/common (common terminal: TB17,TB35) | 32 points/common (common terminal: 17,18,36) | $\Delta$ | As 2 commons are reduced to 1 , wiring a different voltage for each common is not possible. |
| Operation indicator |  | ON indication (LED) | ON indication (LED) | $\bigcirc$ |  |
| External supply power | Voltage | 12/24VDC (10.2 to 30VDC) | 12/24VDC (10.2 to 30VDC) | $\bigcirc$ |  |
|  | Current | 50 mA (TYP. $24 \mathrm{VDC} /$ common) | $8 \mathrm{~mA}(24 \mathrm{VDC} /$ common) | $\bigcirc$ |  |
| External connection |  | 38-point terminal block connector (M3× 6 screws) | 37-pin D sub connector (included) | $\times$ | Wiring must be |
| Applicable wire size |  | 0.75 to $2 \mathrm{~mm}^{2}$ | $0.3 \mathrm{~mm}^{2}$ | $\times$ |  |
| Current consumption |  | $0.23 \mathrm{~A}$ <br> (TYP. all points ON) | $0.50 \mathrm{~A}$ <br> (TYP. all points ON) | $\Delta$ | Current capacity must be reviewed. |
| External dimensions |  | $250(\mathrm{H}) \times 37.5(\mathrm{~W}) \times 131$ (D)mm | 130(H)×34.5(W) $\times 93.6(\mathrm{D}) \mathrm{mm}$ | $\triangle$ |  |
| Weight |  | 0.53 kg | 0.23 kg | $\bigcirc$ |  |

*1 Replacement with the interface terminal module (FA-TH16YTH11S) allows 1.0A/point, 8A/common outputs.
*2 By using connectors/terminal block converter modules (A6TBY36-E, etc.), conversion to the terminal block is possible.
(35) Specifications comparisons between the AY82-EP and the A1SY82

| Specification |  | AY82-EP | A1SY82 | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Number of output points |  | 64 points | 64 points | $\bigcirc$ |  |
| Isolation method |  | Photocoupler | Photocoupler | $\bigcirc$ |  |
| Rated load voltage |  | 12/24VDC | 12/24VDC | $\bigcirc$ |  |
| Operating load voltage range |  | 10.2 to 26.4VDC | 10.2 to 30VDC | $\bigcirc$ |  |
| Maximum load current |  | $\begin{gathered} 0.1 \mathrm{~A} / \text { point } \\ 0.04 \mathrm{~A} / \text { point }\left(60 \% \mathrm{ON}, 55^{\circ} \mathrm{C}\right) \end{gathered}$ | 0.1A/point, 2A/common | $\bigcirc$ |  |
| Maximum inrush current |  | No limit (Short protect) | 0.4A 10ms or less | $\Delta$ | The inrush current value differs.Use caution on selecting the load to use. |
| Leakage current at OFF |  | 0.1 mA or less | 0.1 mA or less | $\bigcirc$ |  |
| Maximum voltage drop at ON |  | $\begin{gathered} \text { 3.5VDC (0.1A) } \\ \text { 2.5VDC (0.1A TYP.) } \end{gathered}$ | $\begin{aligned} & \hline \text { 1.0VDC (TYP.) } 0.1 \mathrm{~A} \\ & 2.5 \mathrm{VDC} \text { (MAX.) } 0.1 \mathrm{~A} \end{aligned}$ | $\bigcirc$ |  |
| Response time | $\mathrm{OFF} \rightarrow \mathrm{ON}$ | 0.5 ms or less | 2 ms or less | $\triangle$ | The response times differ. |
|  | $\mathrm{ON} \rightarrow \mathrm{OFF}$ | 1.5 ms or less | 2 ms or less (resistive load) | $\triangle$ |  |
| Surge suppressor |  | Surge suppression diode | Zener diode | $\bigcirc$ |  |
| Common terminal arrangement |  | 32 points/common (common terminal: $1-17,1-18,1-36,2-17,2-18,2-36)$ | 32 points/common (common terminal: 1B1,1B2,2B1,2B2) | $\bigcirc$ |  |
| Operation indicator |  | ON indication (LED) | ON indication (LED) <br> 32-point switch-over using a switch | $\bigcirc$ |  |
| Protection |  | Yes <br> (Overheat protection function and short-circuit protection function) <br> - Overheat protection function is detected in 1 common units. When Overheat protection function occurs at a 1 point of 1 common, output of all points for the corresponded common terminal is turned OFF. | None | $\times$ | No protection function |
| Protection detection display |  | None <br> (No signal output to a PLC CPU) | None | $\times$ | No protection function |
| Protection function reset |  | Automatic reset (reset by canceling overheat protection function) | None | $\times$ | No protection function |
| External power supply | Voltage | 12/24VDC (10.2 to 30VDC) | 12/24VDC (10.2 to 30VDC) | $\bigcirc$ |  |
|  | Current | 50mA (TYP. 24VDC/common) | 8 mA (24VDC/common) | $\bigcirc$ |  |
| External connection |  | 37-pin connector (with solder) $\times 2$ | 40-pin connector | $\times$ | Wiring must be changed. |
| Applicable wire size |  | $0.3 \mathrm{~mm}^{2}$ | $0.3 \mathrm{~mm}^{2}$ | $\bigcirc$ |  |
| Accessory |  | External wiring connectors $\times 2$ | External wiring connectors $\times 1$ | $\bigcirc$ |  |
| Current consumption |  | $0.29 \mathrm{~A}$ <br> (TYP. all points ON) | $0.93 \mathrm{~A}$ <br> (TYP. all points ON) | $\triangle$ | Current capacity must be reviewed. |
| External dimensions |  | $250(\mathrm{H}) \times 37.5(\mathrm{~W}) \times 106(\mathrm{D}) \mathrm{mm}$ | $130(\mathrm{H}) \times 34.5(\mathrm{~W}) \times 93.6(\mathrm{D}) \mathrm{mm}$ | $\triangle$ |  |
| Weight |  | 0.58 kg | 0.27 kg | $\bigcirc$ |  |

### 3.3 Specifications Comparisons between I/O Modules

(1) Specifications comparisons between the AH42 and the A1SH42

| Specification |  |  | AH42 | A1SH42 | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | Number of input points |  | 32 points | 32 points | $\bigcirc$ |  |
|  | Isolation method |  | Photocoupler | Photocoupler | $\bigcirc$ |  |
|  | Input type |  | Sink type | Sink type | $\bigcirc$ |  |
|  | Rated input voltage |  | 12/24VDC | 12/24VDC | $\bigcirc$ |  |
|  | Rated input current |  | Approx. 3mA/Approx. 7mA | Approx. 2mA/Approx. 5mA | $\triangle$ | Rated input current is smaller. ${ }^{* 1}$ |
|  | Operating voltage range |  | $\begin{gathered} 10.2 \text { to } 26.4 \mathrm{VDC} \\ \text { (ripple ratio within 5\%) } \end{gathered}$ | 10.2 to 26.4 VDC (ripple ratio within 5\%) | $\bigcirc$ |  |
|  | Maximum simultaneous input points |  | 60\% simultaneously ON | Refer to the derating chart. *2 | $\bigcirc$ |  |
|  | ON voltage/ON current |  | 9.5 VDC or more/3mA or more | 8 VDC or more/2mA or more | $\bigcirc$ |  |
|  | OFF voltage/OFF current |  | 6 VDC or less/1.5mA or less | 4 VDC or less/0.6mA or less | $\triangle$ | OFF current is smaller. ${ }^{*}$ |
|  | Input resistance |  | Approx. $3.3 \mathrm{k} \Omega$ | Approx. $5 \mathrm{k} \Omega$ | $\triangle$ | Input resistance is greater. ${ }^{*}$ |
|  | Response time | $\mathrm{OFF} \rightarrow \mathrm{ON}$ | 10 ms or less (24VDC) | 10 ms or less (24VDC) | $\bigcirc$ |  |
|  |  | ON $\rightarrow$ OFF | 10 ms or less (24VDC) | 10 ms or less (24VDC) | $\bigcirc$ |  |
|  | Common terminal arrangement |  | 32 points/common (common terminal: 1B1,1B2) | 32 points/common (common terminal: 1B1,1B2) | $\bigcirc$ |  |
|  | Number of output points |  | 32 points | 32 points | $\bigcirc$ |  |
|  | Isolation method |  | Photocoupler | Photocoupler | $\bigcirc$ |  |
|  | Output type |  | Sink type | Sink type | $\bigcirc$ |  |
|  | Rated load voltage |  | 12/24VDC | 12/24VDC | $\bigcirc$ |  |
|  | Operating load voltage range |  | 10.2 to 40VDC | 10.2 to 30VDC | $\triangle$ | Voltages exceeding 30VDC cannot be applied. |
|  |  |  | 0.1A/point, 1A/common | 0.1A/point, 1.6A/common | $\bigcirc$ |  |
|  | Maximum inrush current |  | 0.4 A 10 ms or less | 0.4 A 10 ms or less | $\bigcirc$ |  |
|  | Leakage current at OFF |  | 0.1 mA or less | 0.1 mA or less | $\bigcirc$ |  |
|  | Maximum voltage drop at ON |  | $\begin{gathered} \hline 2.5 \mathrm{VDC}(0.1 \mathrm{~A}) \\ 1.75 \mathrm{VDC}(5 \mathrm{~mA}) \\ 1.7 \mathrm{VDC}(1 \mathrm{~mA}) \end{gathered}$ | $\begin{aligned} & 1.0 \mathrm{VDC} \text { (TYP.) } 0.1 \mathrm{~A} \\ & 2.5 \mathrm{VDC} \text { (MAX.) } 0.1 \mathrm{~A} \end{aligned}$ | $\bigcirc$ |  |
|  | Response time | $\mathrm{OFF} \rightarrow \mathrm{ON}$ | 2 ms or less | 2 ms or less | $\bigcirc$ |  |
|  |  | ON $\rightarrow$ OFF | 2 ms or less (resistive load) | 2 ms or less (resistive load) | $\bigcirc$ |  |
|  | Surge suppressor |  | Clamp diode | Zener diode | $\bigcirc$ |  |
|  | Common terminal arrangement |  | 32 points/common (common terminal: 2A1,2A2) | 32 points/common (common terminal: 2A1,2A2) | $\bigcirc$ |  |
|  | External supply | Voltage | $\begin{gathered} 12 / 24 \mathrm{VDC} \\ (10.2 \text { to } 40 \mathrm{VDC}) \end{gathered}$ | $\begin{gathered} 12 / 24 \mathrm{VDC} \\ (10.2 \text { to } 30 \mathrm{VDC}) \end{gathered}$ | $\Delta$ | Voltages exceeding 30VDC cannot be applied. |
|  | power | Current | 40mA (24VDC TYP.) | 8 mA (At 24VDC)/common (MAX. all points ON) | $\bigcirc$ |  |
| Operation indicator |  |  | ON indication (LED) <br> 32-point switch-over using a switch | ON indication (LED) <br> 32-point switch-over using a switch | $\bigcirc$ |  |
| External connection |  |  | 40 -pin connector $\times 2$ | 40 -pin connector (included) $\times 2$ | $\bigcirc$ |  |


| Specification | AH42 | A1SH42 | Compati- <br> bility | Precautions for <br> replacement |
| :--- | :---: | :---: | :---: | :---: |
| Applicable wire size | $0.3 \mathrm{~mm}^{2}$ | $0.3 \mathrm{~mm}^{2}$ | O |  |
| Accessory | 40-pin connector $\times 2$ <br> (with solder) | 40 -pin connector $\times 2$ <br> (with solder) | O |  |
| Occupied points | 64 points (output 64 points) | 32 points (I/O assignment: input// <br> output composite) | $\times$ | Output number <br> (Y口)differs. ${ }^{*}$ |
| Current consumption | 0.25 A <br> (TYP. all points ON) | 0.50 A <br> (TYP. all points ON) | $\Delta$ | Current capacity must be <br> reviewed. |
| External dimensions | $250(\mathrm{H}) \times 37.5(\mathrm{~W}) \times 121(\mathrm{D}) \mathrm{mm}$ | $130(\mathrm{H}) \times 34.5(\mathrm{~W}) \times 93.6(\mathrm{D}) \mathrm{mm}$ | $\Delta$ |  |
| Weight | 0.70 kg | 0.27 kg | O |  |

*1 Check the specifications of sensor or switch to connect to the A1SH42.
*2 The figure on the right shows derating.
*3 Modify the output number used in the program.
(For the A1SH42, the same number of X 0 to $\mathrm{X1F}$ and Y 0 to Y 1 F )

(2) Specifications comparisons between the A42XY and the A1S42X/A1S42Y
(a) Specifications comparisons between the A42XY (input part) and the A1S42X
$\bigcirc$ : Compatible, $\Delta$ : Partial change required, $\times$ : Incompatible

| Specification | A42XY (input specification) | A1S42X | Compati- <br> bility |
| :--- | :---: | :---: | :---: |
| Number of input points | P4 points | Precautions for <br> replacement |  |
| Isolation method | Photocoupler | Phitch setting) |  |

*1 Check the specifications of sensor or switch to connect to the A1S42X.
(b) Specifications comparisons between the A42XY (output part) and the A1S42Y
$\bigcirc$ : Compatible, $\triangle$ : Partial change required, $\times$ : Incompatible

| Specification |  | A42XY (output specification) | A1S42Y | $\begin{array}{\|c\|} \hline \text { Compati- } \\ \text { bility } \end{array}$ | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Number of output points |  | 64 points | 16/32/48/64 points (switch setting) | $\bigcirc$ |  |
| Isolation method |  | Photocoupler | Photocoupler | $\bigcirc$ |  |
| Output type |  | Dynamic scan of 8 outputs $\times 8$ | Dynamic scan of 8 outputs $\times 8$ | $\bigcirc$ |  |
| Rated load voltage |  | $\begin{gathered} 12 / 24 \mathrm{VDC} \\ \text { (10.2 to } 26.4 \mathrm{VDC} \\ \text { ripple ratio within 5\%) } \end{gathered}$ | $\begin{gathered} 12 / 24 \mathrm{VDC} \\ \text { (10.2 to } 26.4 \mathrm{VDC} \\ \text { ripple ratio within 5\%) } \end{gathered}$ | $\bigcirc$ |  |
| Maximum output current |  | $50 \mathrm{~mA} /$ point (built in limiting resistor $(1 \mathrm{k} \Omega)$ not used) | $0.1 \mathrm{~A} /$ point | $\bigcirc$ |  |
| Maximum voltage drop at ON |  | 1.5 V on the source side (built in limiting resistor not used) 1 V on the sink side | 1.1VDC on the source side 1.5 VDC on the sink side | $\Delta$ | Voltage dorp is greater. |
| Maximum simultaneous output points |  | 60\% simultaneously ON (built in limiting resistor ( $1 \mathrm{k} \Omega$ ) not used) | 100\% simultaneously ON | $\bigcirc$ |  |
| Dynamic scan synchronization |  | 16 ms or less | 13.3 ms | $\bigcirc$ |  |
|  |  | 16 ms or less | 13.3 ms | $\bigcirc$ |  |
| Operation indicator |  | ON indication (LED) 8-point switch-over using a rotary switch | ON indication (LED) <br> 32-point switch-over using a switch | $\bigcirc$ |  |
| External supply power | Voltage | $\begin{gathered} \hline 12 / 24 \mathrm{VDC} \\ \text { (10.2 to } 26.4 \mathrm{VDC} \\ \text { ripple ratio within 5\%) } \end{gathered}$ | $\begin{gathered} \hline 12 / 24 \mathrm{VDC} \\ \text { (10.2 to } 26.4 \mathrm{VDC} \\ \text { ripple ratio within 5\%) } \end{gathered}$ | $\bigcirc$ |  |
|  | Current | $180 \mathrm{~mA} \mathrm{TYP}$. | 80mA (At 24VDC)/common | $\bigcirc$ |  |
| External connection |  | Output: 32-pin connector | 24-pin connector | $\times$ | Wiring must be changed. |
| Applicable wire size |  | $0.3 \mathrm{~mm}^{2}$ | $0.3 \mathrm{~mm}^{2}$ | $\bigcirc$ |  |
| Occupied points |  | 64 points (output 64 points) | 64 points (I/O assignment: output) | $\Delta$ | The number of occupied points is 128 points (64points $\times 2=128$ points) when using both modules of the A1S42X and A1S42Y. |
| Current consumption |  | $\begin{aligned} & 0.11 \mathrm{~A} \\ & \text { (TYP.) } \end{aligned}$ | 0.10 A (TYP. all points ON) | $\Delta$ | Review current capacity when using with the A1S42X. |
| External dimensions |  | $250(\mathrm{H}) \times 37.5(\mathrm{~W}) \times 119(\mathrm{D}) \mathrm{mm}$ | $130(\mathrm{H}) \times 34.5(\mathrm{~W}) \times 93.6(\mathrm{D}) \mathrm{mm}$ | $\triangle$ | The dimensions are different. |
| Weight |  | 0.60 kg | 0.19 kg | $\bigcirc$ |  |

### 3.4 Specifications Comparisons between Interrupt Modules

(1) Specifications comparisons between the AI61 and the A1SI61

| Specification |  | Al61 | A1SI61 | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Number of interrupt input points |  | 16 points | 16 points | $\bigcirc$ |  |
| Isolation method |  | Photocoupler | Photocoupler | $\bigcirc$ |  |
| Rated input voltage |  | 12/24VDC | 12/24VDC | $\bigcirc$ |  |
| Rated input current |  | $\begin{gathered} \hline 6 \mathrm{~mA} \\ (12 \mathrm{VDC}) \\ 14 \mathrm{~mA} \\ (24 \mathrm{VDC}) \end{gathered}$ | $\begin{gathered} \hline 4 \mathrm{~mA} \\ (12 \mathrm{VDC}) \\ 8 \mathrm{~mA} \\ (24 \mathrm{VDC}) \end{gathered}$ | $\triangle$ | Rated input current is smaller. ${ }^{* 1}$ |
| Operating voltage range |  | 10.2 to 26.4VDC | 10.2 to 26.4VDC | $\bigcirc$ |  |
| Maximum simultaneous input points |  | 100\% (16/common) simultaneously ON | 100\% (16/common) simultaneously ON | $\bigcirc$ |  |
| ON voltage |  | 9 V or more | 9 V or more/3mA or more | $\bigcirc$ |  |
| OFF voltage |  | 4 V or less | 4 V or less/1mA or less | $\bigcirc$ |  |
| Input resistance |  | Approx. $2.4 \mathrm{k} \Omega$ | Approx. $2.7 \mathrm{k} \Omega$ | $\triangle$ | Input resistance is greater. ${ }^{* 1}$ |
| Response time | $\mathrm{OFF} \rightarrow \mathrm{ON}$ | 0.2 ms or less | 0.2 ms or less | $\bigcirc$ |  |
|  | ON $\rightarrow$ OFF | 0.2 ms or less | 0.2 ms or less | $\bigcirc$ |  |
| Interrupt condition setting |  | 1-point unit | 4-point unit | $\Delta$ | The point unit for setting whether the interrupt is processed at the rising/falling of input signals has been changed. |
| Common terminal arrangement |  | 16 points/common (common terminal: TB9,TB18) | 16 points/common (common terminal: TB9,TB18) | $\bigcirc$ |  |
| Operation indicator |  | ON indication (LED) | ON indication (LED) | $\bigcirc$ |  |
| External connection |  | 20-point terminal block connector (M3×6 screws) | 20-point terminal block connector (M3.5×7 screws) | $\times$ | Wiring must be changed. |
| Applicable wire size |  | 0.75 to $1.5 \mathrm{~mm}^{2}$ | 0.75 to $1.25 \mathrm{~mm}^{2}$ | $\times$ |  |
| Applicable solderless terminal |  | $\begin{gathered} 1.25-3,1.25-\mathrm{YS} 3 \mathrm{~A} \\ 2-\mathrm{S} 3,2-\mathrm{YS} 3 \mathrm{~A} \\ \mathrm{~V} 1.25-3, \mathrm{~V} 1.25-\mathrm{YS} 3 \mathrm{~A} \\ \mathrm{~V} 2-\mathrm{S} 3, \mathrm{~V} 2-\mathrm{YS} 3 \mathrm{~A} \end{gathered}$ | R1.25-3.5,R2-3.5, RAV1.25-3.5,RAV2-3.5 | $\times$ |  |
| Occupied points |  | 32 points (special 32 points) | 32 points (special 32 points) | $\bigcirc$ |  |
| Current consumption |  | $0.140 \mathrm{~A}$ <br> (TYP. all points ON) | $0.057 A$ <br> (TYP. all points ON) | $\bigcirc$ |  |
| External dimensions |  | $250(\mathrm{H}) \times 37.5(\mathrm{~W}) \times 121(\mathrm{D}) \mathrm{mm}$ | $130(\mathrm{H}) \times 34.5(\mathrm{~W}) \times 93.6(\mathrm{D}) \mathrm{mm}$ | $\triangle$ | The dimensions are different. |
| Weight |  | 0.40 kg | 0.20 kg | $\bigcirc$ |  |

[^5]
## (2) Specifications comparisons between the Al61-SI and the A1SI61

| Specification |  | Al61-S1 | A1SI61 | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Number of interrupt input points |  | 16 points | 16 points | $\bigcirc$ |  |
| Isolation method |  | Photocoupler | Photocoupler | $\bigcirc$ |  |
| Rated input voltage |  | 24VDC | 12/24VDC | $\bigcirc$ |  |
| Rated input current |  | 14mA | $\begin{gathered} \hline 4 \mathrm{~mA} \\ (12 \mathrm{VDC}) \\ 8 \mathrm{~mA} \\ (24 \mathrm{VDC}) \end{gathered}$ | $\triangle$ | Rated input current is smaller.* ${ }^{*}$ |
| Operating voltage range |  | 21.6 to 26.4VDC | 10.2 to 26.4VDC | $\bigcirc$ |  |
| Maximum simultaneous input points |  | 100\% (16/common) simultaneously ON | 100\% (16/common) simultaneously ON | $\bigcirc$ |  |
| ON voltage |  | 16 V or more | 9 V or more/3mA or more | $\bigcirc$ |  |
| OFF voltage |  | 9 V or less | 4 V or less/1mA or less | $\triangle$ | The OFF voltage has been reduced.*1 |
| Input resistance |  | Approx. $2.4 \mathrm{k} \Omega$ | Approx. $2.7 \mathrm{k} \Omega$ | $\triangle$ | Input resistance is greater. ${ }^{* 1}$ |
| Response time | $\mathrm{OFF} \rightarrow \mathrm{ON}$ | 2 ms or less, 8 ms or less | 0.2 ms or less | $\bigcirc$ |  |
|  | ON $\rightarrow$ OFF | 2 ms or less, 8 ms or less | 0.2 ms or less | $\bigcirc$ |  |
| Interrupt condition setting |  | 1-point unit | 4-point unit | $\triangle$ | The point unit for setting whether the interrupt is processed at the rising/falling of input signals has been changed. |
| Common terminal arrangement |  | 16 points/common (common terminal: TB9,TB18) | 16 points/common (common terminal: TB9,TB18) | $\bigcirc$ |  |
| Operation indicator |  | ON indication (LED) | ON indication (LED) | $\bigcirc$ |  |
| External connection |  | 20-point terminal block connector (M3×6 screws) | 20-point terminal block connector <br> (M3.5×7 screws) | $\times$ | Wiring must be changed. |
| Applicable wire size |  | 0.75 to $1.5 \mathrm{~mm}^{2}$ | 0.75 to $1.25 \mathrm{~mm}^{2}$ | $\times$ |  |
| Applicable solderless terminal |  | $\begin{gathered} 1.25-3,1.25-\mathrm{YS} 3 \mathrm{~A} \\ 2-\mathrm{S} 3,2-\mathrm{YS} 3 \mathrm{~A} \\ \mathrm{~V} 1.25-3, \mathrm{~V} 1.25-\mathrm{YS} 3 \mathrm{~A} \\ \mathrm{~V} 2-\mathrm{S} 3, \mathrm{~V} 2-\mathrm{YS} 3 \mathrm{~A} \end{gathered}$ | $\begin{gathered} \text { R1.25-3.5,R2-3.5, } \\ \text { RAV1.25-3.5,RAV2-3.5 } \end{gathered}$ | $\times$ |  |
| Occupied points |  | 32 points (special 32 points) | 32 points (special 32 points) | $\bigcirc$ |  |
| Current consumption |  | $0.14 \mathrm{~A}$ <br> (TYP. all points ON) | $\begin{gathered} 0.057 \mathrm{~A} \\ \text { (TYP. all points ON) } \end{gathered}$ | $\bigcirc$ |  |
| External dimensions |  | $250(\mathrm{H}) \times 37.5(\mathrm{~W}) \times 121(\mathrm{D}) \mathrm{mm}$ | $130(\mathrm{H}) \times 34.5(\mathrm{~W}) \times 93.6(\mathrm{D}) \mathrm{mm}$ | $\triangle$ | The dimensions are different. |
| Weight |  | 0.40 kg | 0.20 kg | $\bigcirc$ |  |

*1 Check the specifications of sensor or switch to connect to the A1SI61.

### 3.5 Precautions for I/O Module Replacement

## (1) Wiring

(a) Size of wire and solderless terminal

The module and terminal block of the small-sized AnS/Q2AS series are smaller than the large-sized A/QnA series, therefore the applicable size of wire and solderless terminal for terminal blocks differ between the two series.
For this reason, use the wire and solderless terminal compatible with the specifications of the smallsized AnS/Q2AS series I/O module when replacing with the small-sized AnS/Q2AS series.
(b) Change from terminal block to connecter

The 32-point I/O modules of the large-sized A/QnA series uses terminal blocks while that of the AnS/ Q2AS series uses connecters.
When using a 32 -point I/O module of the small-sized AnS/Q2AS series, shift to the wiring using connecters or convert the connecters to terminal blocks with the following method.
. Use the conversion module for the connecters and terminal block.

## (2) Connecter for external wiring

When replacing the large-sized A/QnA series $\mathrm{A} \square 82$ ( D sub-connector) with the small-sized AnS/Q2AS series A1S■82, the wiring must be changed since the external wiring connecters included in the package are different.

When directly using a D sub-connector, replace $\mathrm{A} \square 82$ with $\mathrm{A} 1 \mathrm{~S} \square 81$ (two modules).
However, confirm the wiring condition of the entire system since the number of modules will be increased.

## (3) Precautions for input modules

(a) Specifications change of rated input current

Check the specifications of sensors and switches since some of the small-sized AnS/Q2AS series input modules support lower rated input current than those of the large-sized A/QnA series.
(b) Specifications change of OFF current

Check the specifications of sensors and switches since some of the small-sized AnS/Q2AS series input modules support lower OFF current than those of the large-sized A/QnA series.
(c) Specifications change of maximum simultaneous input points

Check the specifications of sensors and switches since some of the small-sized Ans/Q2AS series input modules have less maximum simultaneous input points than those of the large-sized A/QnA series. Refer to the derating diagram and use within the range shown in the diagram when replacing with the small-sized AnS/Q2AS series.
(d) Specifications change of rated voltage value

The A1SXㅁ-S1 type DC input module of the small-sized AnS/Q2AS series is dedicated to 24VDC and cannot be used at 12VDC.
(e) Specifications change of response time

Pay attention that the response time may differ between the large-sized $A / Q n A$ series and the smallsized AnS/Q2AS series.
(f) Specifications change of common terminal arrangement

The common terminal arrangement may differ between the large-sized A/QnA series and the smallsized AnS/Q2AS series. Pay attention when applying a different voltage to each common.

## (4) Precautions for output module

(a) Specifications change of output current value

Some of the small-sized AnS/Q2AS series output modules support lower output current than those of the large-sized A/QnA series. Check the specification of the load side when using the small-sized AnS/Q2AS series output module with smaller output current.
(b) Specifications change of common terminal arrangement

The common terminal arrangement may differ between the large-sized A/QnA series and the smallsized AnS/Q2AS series. Pay attention when applying a different voltage to each common.
(c) Specifications change of common maximum load current

Check the maximum load current for one common before use, since the current for one common may differ between the large-sized A/QnA series and the small-sized AnS/Q2AS series.
(d) Specifications change of fuse blow error

On the small-sized AnS/Q2AS series, note that a fuse blow error is detected if the external supply power is not supplied to the output module with a fuse.

## POWER SUPPLY MODULE REPLACEMENTS

### 4.1 List of Power Supply Module Alternative Models

| A/QnA series model to be discontinued |  | AnS series alternative models |  |
| :---: | :---: | :---: | :---: |
| Product | Model | Model | Remarks (restrictions) |
| Power supply module | A61P * | A1S61PN | 1) External wiring change: Required <br> 2) Change in number of slots: Not required <br> 3) Change in specifications: Current capacity is smaller. |
|  | A62P | A1S62PN | 1) External wiring change: Required <br> 2) Change in number of slots: Not required <br> 3) Change in specifications: Current capacity is smaller. |
|  | A63P * | A1S63P | 1) External wiring change: Required <br> 2) Change in number of slots: Not required <br> 3) Change in specifications: Current capacity is smaller. |
|  | A61PEU | A1S61PN | 1) External wiring change: Required <br> 2) Change in number of slots: Not required <br> 3) Change in specifications: Current capacity is smaller. |
|  | A62PEU | A1S62PN | 1) External wiring change: Required <br> 2) Change in number of slots: Not required <br> 3) Change in specifications: Current capacity is smaller. |
|  | A68P | None | General-purpose switching power supply (For $\pm 15 \mathrm{VDC}$ ) |
|  | A61P-UL | A1S61PN | 1) External wiring change: Required <br> 2) Change in number of slots: Not required <br> 3) Change in specifications: Current capacity is smaller. |

* This is not a model to be discontinued.

POWER SUPPLY MODULE REPLACEMENTS

### 4.2 Power Supply Module Specifications Comparisons

## (1) Specifications comparisons between the A61P(-UL) and the A1S61PN

O: Compatible, $\Delta$ : Partial change required, $\times$ : Incompatible

| Specification |  | A61P(-UL) | A1S61PN | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Input power supply |  | $\begin{gathered} \text { 100-120VAC+10\%-15\% } \\ \text { (85 to 132VAC) } \end{gathered}$ | $\begin{aligned} & 100-240 \mathrm{VAC}+10 \%-15 \% \\ & (85 \text { to } 264 \mathrm{VAC}) \end{aligned}$ | $\bigcirc$ |  |
|  |  | 200-240VAC+10\%-15\% <br> ( 170 to 264VAC) |  | $\bigcirc$ |  |
| Input frequency |  | $50 / 60 \mathrm{~Hz} \pm 5 \%$ | $50 / 60 \mathrm{~Hz} \pm 5 \%$ | $\bigcirc$ |  |
| Input voltage distortion |  | 5\% within | 5\% within | $\bigcirc$ |  |
| Max. input apparent power |  | 130VA | 105VA | $\bigcirc$ |  |
| Inrush current |  | 20A within 8ms | 20A within 8 ms | $\bigcirc$ |  |
| Rated output current | 5VDC | 8A | 5A | $\triangle$ | Confirm the current consumption of entire system. |
|  | 24VDC | - | - | - |  |
| Overcurrent protection | 5VDC | 8.8A or more | 5.5A or more | $\bigcirc$ |  |
|  | 24VDC | - | - | - |  |
| Overvoltage protection | 5VDC | 5.5 to 6.5 V | 5.5 to 6.5 V | $\bigcirc$ |  |
|  | 24VDC | - | - | - |  |
| Efficiency |  | 65\% or more | 65\% or more | $\bigcirc$ |  |
| Power indicator |  | Power LED indication | LED indication (5VDC output: ON) | $\bigcirc$ |  |
| Terminal screw size |  | $\mathrm{M} 4 \times 0.7 \times 6$ | M $3.5 \times 7$ | $\times$ | Wiring must be changed. |
| Applicable wire size |  | 0.75 to $2 \mathrm{~mm}^{2}$ | 0.75 to $2 \mathrm{~mm}^{2}$ | $\bigcirc$ |  |
| Applicable solderless terminal |  | R1.25-4,R2-4, RAV1.25-4, RAV2-4 | RAV1.25-3.5, RAV2-3.5 | $\times$ | Wiring must be changed. |
| Applicable tightening torque |  | 98 to $137 \mathrm{~N} . \mathrm{cm}$ | 59 to $88 \mathrm{~N} . \mathrm{cm}$ | $\times$ | Tighten within the applicable tightening torque. |
| External dimension |  | $250(\mathrm{H}) \times 55(\mathrm{~W}) \times 121(\mathrm{D}) \mathrm{mm}$ | 130(H) $\times 55(\mathrm{~W}) \times 93.6$ (D) mm | $\triangle$ |  |
| Weight |  | 0.98 kg | 0.6 kg | $\bigcirc$ |  |
| Allowable momentary power failure period |  | Within 20ms | Within 20ms | $\bigcirc$ |  |
| Noise durability |  | Noise voltage 1500Vp-p | - By noise simulator of 1500 Vp p noise voltage, $1 \mu \mathrm{~s}$ noise width and 25 to 60 Hz noise frequency <br> - Noise voltage IEC801-4, 2kV | $\bigcirc$ |  |
| Dielectric withstand voltage |  | Across external AC terminal batch and ground: 1500VAC for 1 minute Across external DC terminal batch and ground: 500VAC for 1 minute | Across inputs/LG and outputs/ <br> FG <br> 2830VAC rms/3 cycles (2000m) | $\bigcirc$ |  |
| Insulation resistance |  | Across external AC terminal batch and ground: $5 \mathrm{M} \Omega$ or more by 500 VDC insulation resistance tester | Across inputs and outputs (LG and FG separated), across inputs and LG/FG, across outputs and FG/LG $10 \mathrm{M} \Omega$ or more by 500 VDC insulation resistance tester | $\bigcirc$ |  |
| Accessory |  | Spare fuse: 1 <br> Short chip for applied voltage select terminal: 1 | None | $\times$ | Fuses are not included in accessories since they are not replaceable, nor are short chip since it is unnecessary to switch operating voltage. |

## (2) Specifications comparisons between the A62P and the A1S62PN

| Specification |  | A62P | A1S62PN | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Input power supply |  | $\begin{gathered} \text { 100-120VAC+10\%-15\% } \\ \text { (85 to 132VAC) } \end{gathered}$ | $\begin{gathered} 100-240 \mathrm{VAC}+10 \%-15 \% \\ \text { (85 to 264VAC) } \end{gathered}$ | $\bigcirc$ |  |
|  |  | $200-240 \text { VAC+10\%-15\% }$ <br> ( 170 to 264VAC) |  | $\bigcirc$ |  |
| Input frequency |  | $50 / 60 \mathrm{~Hz} \pm 5 \%$ | $50 / 60 \mathrm{~Hz} \pm 5 \%$ | $\bigcirc$ |  |
| Input voltage distortion |  | 5\% within | 5\% within | $\bigcirc$ |  |
| Max. input apparent power |  | 155VA | 105VA | $\bigcirc$ |  |
| Inrush current |  | 20A within 8ms | 20A within 8ms | $\bigcirc$ |  |
| Rated output current | 5VDC | 5A | 3A | $\triangle$ | Confirm the current consumption of entire system. |
|  | 24VDC | 0.8A | 0.6A | $\triangle$ |  |
| Overcurrent protection | 5VDC | 5.5 A or more | 3.3A or more | $\bigcirc$ |  |
|  | 24VDC | 1.2A or more | 0.66 A or more | $\bigcirc$ |  |
| Overvoltage protection | 5VDC | 5.5 to 6.5 V | 5.5 to 6.5 V | $\bigcirc$ |  |
|  | 24VDC | - | - | - |  |
| Efficiency |  | 65\% or more | 65\% or more | 0 |  |
| Power indicator |  | Power LED indication | LED indication (5VDC output: ON) | $\bigcirc$ |  |
| Terminal screw size |  | $\mathrm{M} 4 \times 0.7 \times 6$ | M $3.5 \times 7$ | $\times$ | Wiring must be changed. |
| Applicable wire size |  | 0.75 to $2 \mathrm{~mm}^{2}$ | 0.75 to $2 \mathrm{~mm}^{2}$ | 0 |  |
| Applicable solderless terminal |  | R1.25-4,R2-4 <br> RAV1.25-4, RAV2-4 | RAV1.25-3.5, RAV2-3.5 | $\times$ | Wiring must be changed. |
| Applicable tightening torque |  | 48 to $137 \mathrm{~N} . \mathrm{cm}$ | 59 to $88 \mathrm{~N} . \mathrm{cm}$ | $\times$ | Tighten within the applicable tightening torque. |
| External dimensions |  | $250(\mathrm{H}) \times 55(\mathrm{~W}) \times 121(\mathrm{D}) \mathrm{mm}$ | $130(\mathrm{H}) \times 55(\mathrm{~W}) \times 93.6(\mathrm{D}) \mathrm{mm}$ | $\triangle$ |  |
| Weight |  | 0.94 kg | 0.6 kg | $\bigcirc$ |  |
| Allowable momentary power failure period |  | Within 20ms | Within 20ms | $\bigcirc$ |  |
| Noise durability |  | Noise voltage 1500Vp-p | - By noise simulator of 1500 Vp p noise voltage, $1 \mu \mathrm{~s}$ noise width and 25 to 60 Hz noise frequency <br> - Noise voltage IEC801-4, 2kV | O |  |
| Dielectric withstand voltage |  | Across external AC terminal batch and ground: 1500VAC for 1minute Across external DC terminal batch and ground: 500VAC for 1minute | Across inputs/LG and outputs/ <br> FG <br> 2830VAC rms/3 cycles (2000m) | $\bigcirc$ |  |
| Insulation resistance |  | Across external AC terminal batch and ground: $5 \mathrm{M} \Omega$ or more by 500 VDC insulation resistance tester | Across inputs and outputs (LG and FG separated), across inputs and LG/FG, across outputs and FG/LG $10 \mathrm{M} \Omega$ or more by 500 VDC insulation resistance tester | $\bigcirc$ |  |
| Accessory |  | Spare fuse: 1 <br> Short chip for applied voltage select terminal: 1 | None | $\times$ | Fuses are not included in accessories since they are not replaceable, nor are short chip since it is unnecessary to switch operating voltage. |

## (3) Specifications comparisons between the A63P and the A1S63P

O: Compatible, $\Delta$ : Partial change required, $\times$ : Incompatible

| Specification |  | A63P | A1S63P | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Input power supply |  | $\begin{aligned} & 24 \mathrm{VDC}+30 \%-35 \% \\ & \text { (15.6 to } 31.2 \mathrm{VDC} \text { ) } \end{aligned}$ | $\begin{aligned} & 24 \mathrm{VDC}+30 \%-35 \% \\ & (15.6 \text { to } 31.2 \mathrm{VDC}) \end{aligned}$ | O |  |
| Input frequency |  | - | - | - |  |
| Input voltage distortion |  | - | 5\% within | $\bigcirc$ |  |
| Max. input apparent power |  | 65W | 41W | $\bigcirc$ |  |
| Inrush current |  | 100A within 1 ms | 81A within 1ms | $\bigcirc$ |  |
| Rated output current | 5VDC | 8A | 5A | $\triangle$ | Confirm the current consumption of entire system. |
|  | 24VDC | - | - | - |  |
| Overcurrent protection | 5VDC | 8.5A or more | 5.5A or more | $\bigcirc$ |  |
|  | 24VDC | - | - | - |  |
| Overvoltage protection | 5VDC | 5.5 to 6.5 V | 5.5 to 6.5 V | $\bigcirc$ |  |
|  | 24VDC | - | - | - |  |
| Efficiency |  | 65\% or more | 65\% or more | $\bigcirc$ |  |
| Power indicator |  | Power LED indication | LED indication (5VDC output: ON) | $\bigcirc$ |  |
| Terminal screw size |  | $\mathrm{M} 4 \times 0.7 \times 6$ | M $3.5 \times 7$ | $\times$ | Wiring must be changed. |
| Applicable wire size |  | 0.75 to $2 \mathrm{~mm}^{2}$ | 0.75 to $2 \mathrm{~mm}^{2}$ | $\bigcirc$ |  |
| Applicable solderless terminal |  | R1.25-4, R2-4, RAV1.25-4, RAV2-4 | RAV1.25-3.5, RAV2-3.5 | $\times$ | Wiring must be changed. |
| Applicable tightening torque |  | 98 to 137N.cm | 59 to $88 \mathrm{~N} . \mathrm{cm}$ | $\times$ | Tighten within the applicable tightening torque. |
| External dimensions |  | $250(\mathrm{H}) \times 55(\mathrm{~W}) \times 121(\mathrm{D}) \mathrm{mm}$ | 130(H) $\times 55(\mathrm{~W}) \times 93.6(\mathrm{D}) \mathrm{mm}$ | $\triangle$ |  |
| Weight |  | 0.8 kg | 0.5 kg | $\bigcirc$ |  |
| Allowable momentary power failure period |  | Within 1ms | Within 1ms | $\bigcirc$ |  |
| Noise durability |  | Noise voltage 500Vp-p | - By noise simulator of $500 \mathrm{Vp}-\mathrm{p}$ noise voltage, $1 \mu \mathrm{~s}$ noise width and 25 to 60 Hz noise frequency | $\bigcirc$ |  |
| Dielectric withstand voltage |  | Across external DC terminal batch and ground: 500VAC for 1 minute | 500VAC across primary and 5VDC | $\bigcirc$ |  |
| Insulation resistance |  | Across external DC terminal batch and ground: $5 \mathrm{M} \Omega$ or more by 500 VDC insulation resistance tester | $5 \mathrm{M} \Omega$ or more by insulation resistance tester | $\bigcirc$ |  |
| Accessory |  | Spare fuse: 1 | None | $\times$ | Fuses are not included in accessories since they are not replaceable. |

(4) Specifications comparisons between the A61PEU and the A1S61PN

| Specification |  | A61PEU | A1S61PN | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Input power supply |  | $\begin{gathered} \text { 100-120VAC+10\%-15\% } \\ \text { (85 to 132VAC) } \end{gathered}$ | $\begin{gathered} \text { 100-240VAC+10\%-15\% } \\ \text { (85 to 264VAC) } \end{gathered}$ | $\bigcirc$ |  |
|  |  | $200-240 \text { VAC+10\%-15\% }$ <br> (170 to 264VAC) |  | $\bigcirc$ |  |
| Input frequency |  | $50 / 60 \mathrm{~Hz} \pm 5 \%$ | $50 / 60 \mathrm{~Hz} \pm 5 \%$ | $\bigcirc$ |  |
| Input voltage distortion |  | 5\% within | 5\% within | $\bigcirc$ |  |
| Max. input apparent power |  | 130VA | 105VA | $\bigcirc$ |  |
| Inrush current |  | 20A within 8 ms | 20A within 8ms | $\bigcirc$ |  |
| Rated output current | 5VDC | 8A | 5A | $\triangle$ | Confirm the current consumption of entire system. |
|  | 24VDC | - | - | - |  |
| Overcurrent protection | 5VDC | 8.8A or more | 5.5A or more | $\bigcirc$ |  |
|  | 24VDC | - | - | - |  |
| Overvoltage protection | 5VDC | 5.5 to 6.5 V | 5.5 to 6.5 V | $\bigcirc$ |  |
|  | 24VDC | - | - | - |  |
| Efficiency |  | 65\% or more | 65\% or more | $\bigcirc$ |  |
| Power indicator |  | Power LED indication | LED indication (5VDC output: ON) | $\bigcirc$ |  |
| Terminal screw size |  | $\mathrm{M} 4 \times 0.7 \times 6$ | M $3.5 \times 7$ | $\times$ | Wiring must be changed. |
| Applicable wire size |  | 0.75 to $2 \mathrm{~mm}^{2}$ | 0.75 to $2 \mathrm{~mm}^{2}$ | $\bigcirc$ |  |
| Applicable solderless terminal |  | R1.25-4, R2-4, RAV1.25-4, RAV2-4 | RAV1.25-3.5, RAV2-3.5 | $\times$ | Wiring must be changed. |
| Applicable tightening torque |  | 98 to 137N.cm | 59 to $88 \mathrm{~N} . \mathrm{cm}$ | $\times$ | Tighten within the applicable tightening torque. |
| External dimensions |  | $250(\mathrm{H}) \times 55(\mathrm{~W}) \times 121(\mathrm{D}) \mathrm{mm}$ | 130(H) $\times 55(\mathrm{~W}) \times 93.6$ (D) mm | $\triangle$ |  |
| Weight |  | 0.8 kg | 0.6 kg | $\bigcirc$ |  |
| Allowable momentary power failure period |  | Within 20ms | Within 20ms | $\bigcirc$ |  |
| Noise durability |  | - By noise simulator of 1500Vp-p noise voltage, $1 \mu \mathrm{~s}$ noise width and 25 to 60 Hz noise frequency <br> - Noise voltage IEC61000-4-4, 2kV | - By noise simulator of 1500 Vp -p noise voltage, $1 \mu \mathrm{~s}$ noise width and 25 to 60 Hz noise frequency <br> - Noise voltage IEC801-4, 2kV | $\bigcirc$ |  |
| Dielectric withstand voltage |  | Across inputs/LG and outputs/FG 2830VAC rms/3 cycles (2000m) | Across inputs/LG and outputs/FG 2830VAC rms/3 cycles (2000m) | $\bigcirc$ |  |
| Insulation resistance |  | Across inputs and outputs (LG and FG separated), across inputs and LG/FG, across outputs and FG/LG $10 \mathrm{M} \Omega$ or more by 500 VDC insulation resistance tester | Across inputs and outputs (LG and FG separated), across inputs and LG/FG, across outputs and FG/LG $10 \mathrm{M} \Omega$ or more by 500 VDC insulation resistance tester | $\bigcirc$ |  |
| Accessory |  | Spare fuse: 1 <br> Short chip for applied voltage select terminal: 1 | None | $\times$ | Fuses are not included in accessories since they are not replaceable, nor are short chip since it is unnecessary to switch operating voltage. |

## (5) Specifications comparisons between the A62PEU and the A1S62PN

O: Compatible, $\Delta$ : Partial change required, $x$ : Incompatible

| Specification |  | A62PEU | A1S62PN | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Input power supply |  | $\begin{gathered} \text { 100-120VAC+10\%-15\% } \\ (85 \text { to 132VAC }) \end{gathered}$ | $\begin{gathered} \text { 100-240VAC+10\%-15\% } \\ \text { (85 to 264VAC) } \end{gathered}$ | O |  |
|  |  | $\begin{aligned} & \text { 200-240VAC+10\%-15\% } \\ & (170 \text { to } 264 \mathrm{VAC}) \end{aligned}$ |  | $\bigcirc$ |  |
| Input frequency |  | $50 / 60 \mathrm{~Hz} \pm 5 \%$ | $50 / 60 \mathrm{~Hz} \pm 5 \%$ | $\bigcirc$ |  |
| Input voltage distortion |  | $5 \%$ within | $5 \%$ within | $\bigcirc$ |  |
| Max. input apparent power |  | 110VA | 105VA | $\bigcirc$ |  |
| Inrush current |  | 20A within 8 ms | 20A within 8 ms | $\bigcirc$ |  |
| Rated output current | 5VDC | 5A | 3A | $\triangle$ | Confirm the current consumption of entire system. |
|  | 24VDC | 0.8 A | 0.6A | $\triangle$ |  |
| Overcurrent protection | 5VDC | 5.5 A or more | 3.3A or more | $\bigcirc$ |  |
|  | 24VDC | 1.2 A or more | 0.66 A or more | $\bigcirc$ |  |
| Overvoltage protection | 5VDC | 5.5 to 6.5 V | 5.5 to 6.5 V | $\bigcirc$ |  |
|  | 24VDC | - | - | - |  |
| Efficiency |  | 65\% or more | 65\% or more | $\bigcirc$ |  |
| Power indicator |  | Power LED indication | LED indication (5VDC output: ON) | $\bigcirc$ |  |
| Terminal screw size |  | $\mathrm{M} 4 \times 0.7 \times 6$ | M $3.5 \times 7$ | $\times$ | Wiring must be changed. |
| Applicable wire size |  | 0.75 to $2 \mathrm{~mm}^{2}$ | 0.75 to $2 \mathrm{~mm}^{2}$ | $\bigcirc$ |  |
| Applicable solderless terminal |  | RAV1.25-4, RAV2-4 | RAV1.25-3.5, RAV2-3.5 | $\times$ | Wiring must be changed. |
| Applicable tightening torque |  | 118N.cm | 59 to $88 \mathrm{~N} . \mathrm{cm}$ | $\times$ | Tighten within the applicable tightening torque. |
| External dimensions |  | $250(\mathrm{H}) \times 55(\mathrm{~W}) \times 121$ (D) mm | 130(H) $\times 55(\mathrm{~W}) \times 93.6(\mathrm{D}) \mathrm{mm}$ | $\triangle$ |  |
| Weight |  | 0.9 kg | 0.6 kg | $\bigcirc$ |  |
| Allowable momentary power failure period |  | Within 20ms | Within 20ms | $\bigcirc$ |  |
| Noise durability |  | - By noise simulator of $1500 \mathrm{Vp}-\mathrm{p}$ noise voltage, $1 \mu \mathrm{~s}$ noise width and 25 to 60 Hz noise frequency <br> - Noise voltage IEC61000-4-4, 2kV | - By noise simulator of $1500 \mathrm{Vp}-\mathrm{p}$ noise voltage, $1 \mu \mathrm{~s}$ noise width and 25 to 60 Hz noise frequency <br> - Noise voltage IEC801-4, 2kV | $\bigcirc$ |  |
| Dielectric withstand voltage |  | Across inputs/LG and outputs/FG 2830VAC rms/3 cycles(2000m) | Across inputs/LG and outputs/FG 2830VAC rms/3 cycles (2000m) | $\bigcirc$ |  |
| Insulation resistance |  | Across inputs and outputs (LG and FG separated), across inputs and LG/FG, across outputs and FG/LG $10 \mathrm{M} \Omega$ or more by 500 VDC insulation resistance tester | Across inputs and outputs (LG and FG separated), across inputs and LG/FG, across outputs and $\mathrm{FG} / \mathrm{LG} 10 \mathrm{M} \Omega$ or more by 500VDC insulation resistance tester | $\bigcirc$ |  |
| Accessory |  | Spare fuse: 1 <br> Short chip for applied voltage select terminal: 1 | None | $\times$ | Fuses are not included in accessories since they are not replaceable, nor are short chip since it is unnecessary to switch operating voltage. |

(6) Specifications of the A68P

| Specification |  | A68P |
| :---: | :---: | :---: |
| Input power supply |  | $\begin{gathered} \text { 100-120VAC+10\%-15\% } \\ \text { (85 to 132VAC) } \\ \hline \end{gathered}$ |
|  |  | 200-240VAC+10\%-15\% <br> (170 to 264VAC) |
| Input frequency |  | $50 / 60 \mathrm{~Hz} \pm 5 \%$ |
| Input voltage distortion |  | - |
| Max. input apparent power |  | 95VA |
| Inrush current |  | 20A within 8 ms |
| Rated output current | +15VDC | 1.2A |
|  | -15VDC | 0.7A |
| Overcurrent protection | +15VDC | 1.64 A or more |
|  | -15VDC | 0.94 A or more |
| Efficiency |  | 65\% or more |
| Power indicator |  | Power LED indication |
| Power ON indicator |  | Contact output |
|  |  | Switched on if +15 VDC output is +14.25 V or higher or -15VDC output is -14.25 V or lower. |
|  |  | Min. contact switching load: 5VDC, 10 mA Max. contact switching load: 264VAC, 2A (R load) |
| Terminal screw size |  | $\mathrm{M} 3 \times 0.5 \times 6$ |
| Applicable wire size |  | 0.75 to $2 \mathrm{~mm}^{2}$ |
| Applicable solderless terminal |  | $\begin{gathered} \hline \text { V1.25-4, V1.25-YS4A, } \\ \text { V2-S4, V2-YS4A } \end{gathered}$ |
| Applicable tightening torque |  | $68 \mathrm{~N} . \mathrm{cm}$ |
| External dimensions |  | $250(\mathrm{H}) \times 75.5(\mathrm{~W}) \times 121(\mathrm{D}) \mathrm{mm}$ |
| Weight |  | 0.9 kg |

Substitute the general-purpose switching power supply, whose specifications are shown below, for the A68P. Choose current capacity with the result of calculating the current consumption of entire system to be used.

| Specification | General-purpose Switching Power Supply |
| :--- | :--- |
| Voltage | $+15 \mathrm{VDC} \pm 3 \%(14.55 \mathrm{~V}$ to 15.45 V$)$ |
|  | $-15 \mathrm{VDC} \pm 3 \%(-14.55 \mathrm{~V}$ to $-15.45 \mathrm{~V})$ |
| Ripple voltage | $50 \mathrm{mVp}-\mathrm{p}$ or less |
| Spike voltage | 100 mVp -p or less |
| Output voltage limit | Within $\pm 1 \mathrm{~V}$ |

### 4.3 Precautions for Power Supply Module Replacement

## (1) Power supply module selection

Current consumption differs between the AnS series and A series modules. Select the power supply module with the result of calculating the current consumption of entire system.

## (2) Wiring

Applicable wire and crimping terminals for terminal blocks differ between the AnS series and the A series. Use the wire and crimping terminals compatible with the specifications.

## BASE UNIT AND EXTENSION CABLE REPLACEMENT

### 5.1 List of Alternative Models for Base Unit and Extension Cable

| Large-sized A/QnA series model to be discontinued |  | Small-sized AnS/Q2AS series alternative model |  |
| :---: | :---: | :---: | :---: |
| Product | Model | Model | Remarks (restrictions) |
| Main base unit | A32B | A1S32B |  |
|  | A35B | A1S35B |  |
|  | A38B | A1S38B |  |
|  | A38B-UL | A1S38B |  |
|  | A32B-E | A1S32B-E |  |
|  | A35B-E | A1S35B-E |  |
|  | A38B-E | A1S38B-E |  |
|  | A32B-S1 | A1S32B |  |
|  | A38HB | A1S38HB | Cannot be used for the A2USHCPU-S1. |
|  | A38HBEU | A1S38HBEU | Cannot be used for the A2USHCPU-S1. |
| Extension base unit | A52B | A1S52B |  |
|  | A55B | A1S55B |  |
|  | A58B | A1S58B |  |
|  | A62B | A1S65B | Change in number of I/O slots: 2 slots $\rightarrow 5$ slots |
|  | A65B | A1S65B |  |
|  | A68B | A1S68B |  |
|  | A68B-UL | A1S68B |  |
| Extension cable | AC06B | A1SC07B | Cable length: $0.6 \mathrm{~m} \rightarrow 0.7 \mathrm{~m}$ |
|  | AC12B | A1SC12B |  |
|  | AC30B | A1SC30B |  |
|  | AC50B | A1SC60B | Cable length: $5.0 \mathrm{~m} \rightarrow 6.0 \mathrm{~m}$ |
|  | A1SC05NB | A1SC07B | Cable length: $0.45 \mathrm{~m} \rightarrow 0.7 \mathrm{~m}$ |
|  | A1SC07NB | A1SC07B |  |
|  | A1SC30NB | A1SC30B |  |
|  | A1SC50NB | A1SC60B | Cable length: $5.0 \mathrm{~m} \rightarrow 6.0 \mathrm{~m}$ |

### 5.2 Base Unit and Extension Cable Specifications Comparisons

### 5.2.1 Base unit specifications comparisons

(1) Main base unit
(a) Comparisons between the A32B(-E) and the A1S32B(-E)

| Item | Type |  | Precautions for replacement |
| :---: | :---: | :---: | :---: |
|  | Large-sized A/QnA series | Small-sized AnS/Q2AS series |  |
|  | A32B(-E) | A1S32B(-E) |  |
| Loaded I/O modules | 2 can be loaded. |  | Refer to Section 5.3.1 for replacement precautions. |
| Extension availability | Cannot connect extension modules. | Extendable |  |
| Mounting hole size | $\phi 6 \mathrm{~mm}$ dia. pear-shaped hole (for M5 screw) |  |  |
| External dimension | $250(\mathrm{H}) \times 247(\mathrm{~W}) \times 29$ (D) mm | $130(\mathrm{H}) \times 220(\mathrm{~W}) \times 28(\mathrm{D}) \mathrm{mm}$ |  |
| Dimension for mounting to the panel | $227 \times 200 \mathrm{~mm}$ | $200 \times 110 \mathrm{~mm}$ |  |

(b) Comparisons between the A32B-S1 and the A1S32B

| Item | Type |  | Precautions for replacement |
| :---: | :---: | :---: | :---: |
|  | Large-sized A/QnA series | Small-sized AnS/Q2AS series |  |
|  | A32B-S1 | A1S32B |  |
| Loaded I/O modules | 2 can be loaded. |  | Refer to Section 5.3.1 for replacement precautions. |
| Extension availability | Extendable |  |  |
| Mounting hole size | $\phi 6 \mathrm{~mm}$ dia. pear-shaped hole (for M5 screw) |  |  |
| External dimensions | $250(\mathrm{H}) \times 268(\mathrm{~W}) \times 29$ (D)mm | $130(\mathrm{H}) \times 220(\mathrm{~W}) \times 28(\mathrm{D}) \mathrm{mm}$ |  |
| Dimensions for mounting to the panel | $248 \times 200 \mathrm{~mm}$ | $200 \times 110 \mathrm{~mm}$ |  |

(c) Comparisons between the A35B(-E) and the A1S35B(-E)

| Item | Type |  | Precautions for replacement |
| :---: | :---: | :---: | :---: |
|  | Large-sized A/QnA series | Small-sized AnS/Q2AS series |  |
|  | A35B(-E) | A1S35B(-E) |  |
| Loaded I/O modules | 5 can be loaded. |  | Refer to Section 5.3.1 for replacement precautions. |
| Extension availability | Extendable |  |  |
| Mounting hole size | $\phi 6 \mathrm{~mm}$ dia. pear-shaped hole (for M5 screw) |  |  |
| External dimensions | $250(\mathrm{H}) \times 382(\mathrm{~W}) \times 29$ (D)mm | $130(\mathrm{H}) \times 325(\mathrm{~W}) \times 28(\mathrm{D}) \mathrm{mm}$ |  |
| Dimensions for mounting to the panel | $362 \times 200 \mathrm{~mm}$ | $305 \times 110 \mathrm{~mm}$ |  |

(d) Comparisons between the A38(-E/-UL)/A38HB/A38HBEU and the A1S38B(-E)/A1S38HB/ A1S38HBEU

| Item | Type |  | Precautions for replacement |
| :---: | :---: | :---: | :---: |
|  | Large-sized A/QnA series | Small-sized AnS/Q2AS series |  |
|  | $\begin{gathered} \text { A38B(-E/-UL)/A38HB/ } \\ \text { A38HBEU } \end{gathered}$ | $\begin{gathered} \hline \text { A1S38B(-E)/A1S38HB/ } \\ \text { A1S38HBEU } \end{gathered}$ |  |
| Loaded I/O modules | 8 can be loaded. |  | Refer to Section 5.3.1 for replacement precautions. |
| Extension availability | Extendable |  |  |
| Mounting hole size | $\phi 6 \mathrm{~mm}$ dia. pear-shaped hole (for M5 screw) |  |  |
| External dimensions | $250(\mathrm{H}) \times 480(\mathrm{~W}) \times 29$ (D) mm | $130(\mathrm{H}) \times 430(\mathrm{~W}) \times 28(\mathrm{D}) \mathrm{mm}$ |  |
| Dimensions for mounting to the panel | $460 \times 200 \mathrm{~mm}$ | $410 \times 110 \mathrm{~mm}$ |  |

## (2) Extension base unit (No power supply module required)

(a) Comparisons between the A52B and the A1S52B

| Item | Type |  | Precautions for replacement |
| :---: | :---: | :---: | :---: |
|  | Large-sized A/QnA series | Small-sized AnS/Q2AS series |  |
|  | A52B | A1S52B |  |
| Loaded I/O modules | 2 can be loaded. |  | Refer to Section 5.3.1 for replacement precautions. |
| Extension availability | Extendable | Cannot connect extension modules. |  |
| Mounting hole size | $\phi 6 \mathrm{~mm}$ dia. pear-shaped hole (for M5 screw) |  |  |
| External dimensions | $250(\mathrm{H}) \times 183(\mathrm{~W}) \times 29$ (D)mm | $130(\mathrm{H}) \times 155(\mathrm{~W}) \times 28(\mathrm{D}) \mathrm{mm}$ |  |
| Dimensions for mounting to the panel | $163 \times 200 \mathrm{~mm}$ | $135 \times 110 \mathrm{~mm}$ |  |

(b) Comparisons between the A55B and the A1S55B

| Item | Type |  | Precautions for replacement |
| :---: | :---: | :---: | :---: |
|  | Large-sized A/QnA series | Small-sized AnS/Q2AS series |  |
|  | A55B | A1S55B |  |
| Loaded I/O modules | 5 can be loaded. |  | Refer to Section 5.3.1 for replacement precautions. |
| Extension availability | Extendable | Cannot connect extension modules. |  |
| Mounting hole size | $\phi 6 \mathrm{~mm}$ dia. pear-shaped hole (for M5 screw) |  |  |
| External dimensions | $250(\mathrm{H}) \times 297(\mathrm{~W}) \times 29$ (D)mm | $130(\mathrm{H}) \times 260(\mathrm{~W}) \times 28(\mathrm{D}) \mathrm{mm}$ |  |
| Dimensions for mounting to the panel | $277 \times 200 \mathrm{~mm}$ | $240 \times 110 \mathrm{~mm}$ |  |

(c) Comparisons between the A58B and the A1S58B

| Item | Type |  | Precautions for replacement |
| :---: | :---: | :---: | :---: |
|  | Large-sized A/QnA series | Small-sized AnS/Q2AS series |  |
|  | A58B | A1S58B |  |
| Loaded I/O modules | 8 can be loaded. |  | Refer to Section 5.3.1 for replacement precautions. |
| Extension availability | Extendable | Cannot connect extension modules. |  |
| Mounting hole size | $\phi 6 \mathrm{~mm}$ dia. pear-shaped hole (for M5 screw) |  |  |
| External dimensions | $250(\mathrm{H}) \times 411(\mathrm{~W}) \times 29$ (D) mm | $130(\mathrm{H}) \times 365(\mathrm{~W}) \times 28(\mathrm{D}) \mathrm{mm}$ |  |
| Dimensions for mounting to the panel | $391 \times 200 \mathrm{~mm}$ | $345 \times 110 \mathrm{~mm}$ |  |

(3) Extension base unit (Power supply module loaded)
(a) Comparisons between the A62B and the A1S65B

| Item | Type |  | Precautions for replacement |
| :---: | :---: | :---: | :---: |
|  | Large-sized A/QnA series | Small-sized AnS/Q2AS series |  |
|  | A62B | A1S65B |  |
| Loaded I/O modules | 2 can be loaded. | 5 can be loaded. | Refer to Section 5.3.1 for replacement precautions. |
| Extension availability | Extendable | Cannot connect extension modules. |  |
| Mounting hole size | $\phi 6 \mathrm{~mm}$ dia. pear-shaped hole (for M5 screw) |  |  |
| External dimensions | $250(\mathrm{H}) \times 283(\mathrm{~W}) \times 29$ (D)mm | $130(\mathrm{H}) \times 315(\mathrm{~W}) \times 28(\mathrm{D}) \mathrm{mm}$ |  |
| Dimensions for mounting to the panel | $218 \times 200 \mathrm{~mm}$ | $295 \times 110 \mathrm{~mm}$ |  |

(b) Comparisons between the A65B and the A1S65B

| Item | Type |  | Precautions for replacement |
| :---: | :---: | :---: | :---: |
|  | Large-sized A/QnA series | Small-sized AnS/Q2AS series |  |
|  | A65B | A1S65B |  |
| Loaded I/O modules | 5 can be loaded. |  | Refer to Section 5.3.1 for replacement precautions. |
| Extension availability | Extendable | Cannot connect extension modules. |  |
| Mounting hole size | $\phi 6 \mathrm{~mm}$ dia. pear-shaped hole (for M5 screw) |  |  |
| External dimensions | $250(\mathrm{H}) \times 352(\mathrm{~W}) \times 29$ (D)mm | $130(\mathrm{H}) \times 315(\mathrm{~W}) \times 28(\mathrm{D}) \mathrm{mm}$ |  |
| Dimensions for mounting to the panel | $332 \times 200 \mathrm{~mm}$ | $295 \times 110 \mathrm{~mm}$ |  |

(c) Comparisons between the A68B (-UL) and the A1S68B

| Item | Type |  | Precautions for replacement |
| :---: | :---: | :---: | :---: |
|  | Large-sized A/QnA series | Small-sized AnS/Q2AS series |  |
|  | A68B(-UL) | A1S68B |  |
| Loaded I/O modules | 8 can be loaded. |  | Refer to Section 5.3.1 for replacement precautions. |
| Extension availability | Extendable | Cannot connect extension modules. |  |
| Mounting hole size | $\phi 6 \mathrm{~mm}$ dia. pear-shaped hole (for M5 screw) |  |  |
| External dimensions | $250(\mathrm{H}) \times 466(\mathrm{~W}) \times 29$ (D)mm | 130 (H) $\times 420$ (W) $\times 28$ (D)mm |  |
| Dimensions for mounting to the panel | $446 \times 200 \mathrm{~mm}$ | $400 \times 110 \mathrm{~mm}$ |  |

### 5.2.2 Extension cables specifications comparisons

| Item |  | Type |  |  | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | Large-sized A/QnA series |  | Small-sized AnS/Q2AS series |  |
|  |  | A Main-A <br> Extension | AnS Main-A Extension | AnS Main-AnS Extension |  |
| Cable length | 0.45m | - | A1SC05NB | A1SC07B | Refer to Section 5.3.2 for replacement precautions. |
|  | 0.6 m | AC06B | - | A1SC07B |  |
|  | 0.7 m | - | A1SC07NB | A1SC07B |  |
|  | 1.2m | AC12B | - | A1SC12B |  |
|  | 3.0 m | AC30B | A1SC30NB | A1SC30B |  |
|  | 5.0 m | AC50B | - | A1SC60B |  |
|  | 5.0 m | - | A1SC50NB | A1SC60B |  |

### 5.3 Precautions for Base Unit and Extension Cable Replacement

### 5.3.1 Precautions for base unit replacement

(1) Installation dimensions

When replacing the large-sized $A / Q n A$ series base unit with the small-sized AnS/Q2AS series, it is necessary to rework the mounting holes to fix the unit to a control panel, since the two series have different mounting hole size.

## (2) Number of extension stages

For the small-sized AnS/Q2AS series, up to one stage of the extension base unit is applicable. If the extension stages are two stages or more, consider replacing by the $Q$ series.

### 5.3.2 Precautions for extension cable replacement

## (1) Total extension distance of extension cable

The total extension distance of the small-sized AnS/Q2AS series extension cable is up to 6.0 m while that of the large-sized $A / Q n A$ series is 6.6 m . Select a suitable cable according to your system.

## MEMORY AND BATTERY REPLACEMENT

### 6.1 List of Alternative Models for Memory

(1) Large-sized A series

| Large-sized A series model to be discontinued |  | Small-sized AnS series alternative model |  |
| :---: | :---: | :---: | :---: |
| Product | Model | Model | Remarks (restrictions) |
| Memory cassette | A3NMCA-0 | Unnecessary | Built-in RAM is the alternative. For ROM operation, the A2SNMCA-30KE is required. |
|  | A3NMCA-2 | Unnecessary |  |
|  | A3NMCA-4 | Unnecessary |  |
|  | A3NMCA-8 | Unnecessary |  |
|  | A3NMCA-16 | Unnecessary |  |
|  | A3NMCA-24 | Unnecessary |  |
|  | A3NMCA-40 | Unnecessary |  |
|  | A3NMCA-56 | Unnecessary |  |
|  | A3AMCA-96 | Unnecessary |  |
|  | A4UMCA-128 | Unnecessary |  |
|  | A4UMCA-8E | Unnecessary |  |
|  | A4UMCA-32E | Unnecessary |  |
|  | A4UMCA-128E | Unnecessary |  |
| IC-RAM memory | 4KRAM | Unnecessary | Built-in RAM is the alternative. |
| $E^{2}$ PROM memory | 4KEROM | A2SNMCA-30KE | Use the memory cassette. |
| EPROM memory | 4KROM *1 | A2SNMCA-30KE | Use the memory cassette. |
|  | 8KROM *1 |  |  |
|  | 16KROM *1 |  |  |
|  | 32KROM |  |  |
|  | 64KROM |  |  |

*1 Will be discontinued in the end of September, 2008.

## (2) Large-sized QnA series

| Large-sized QnA series model to be discontinued |  | Small-sized Q2AS series alternative model |  |
| :---: | :---: | :---: | :---: |
| Product | Model | Model | Remarks (restrictions) |
| IC memory card | Q1MEM-64S | Q1MEM-64S | No. of IC memory cards is changed from two to one. |
|  | Q1MEM-128S | Q1MEM-128S |  |
|  | Q1MEM-256S | Q1MEM-256S |  |
|  | Q1MEM-512S | Q1MEM-512S |  |
|  | Q1MEM-1MS | Q1MEM-1MS |  |
|  | Q1MEM-2MS | Q1MEM-2MS |  |
|  | Q1MEM-64SE | Q1MEM-64SE |  |
|  | Q1MEM-128SE | Q1MEM-128SE |  |
|  | Q1MEM-256SE | Q1MEM-256SE |  |
|  | Q1MEM-512SE | Q1MEM-512SE |  |
|  | Q1MEM-1MSE | Q1MEM-1MSE |  |

### 6.2 Precautions for Memory and Battery Replacement

## (1) Precaution for memory replacement

(a) Necessity of memory cassette

The A2USHCPU-S1 does not need memory cassettes for the RAM operation, since its CPU module incorporates the built-in RAM. For the ROM operation, the memory cassette of the A2SNMCA-30KE is needed.
(b) Memory capacity

If the memory capacities are insufficient depending on types of CPU module or memory cassette after the replacement, consider replacing by the $Q$ series. For the memory capacity and configurations used on CPU module, refer to Section 2.4.1.

## (2) Precaution for battery replacement

All the batteries (A6BAT) for the large-sized A/QnA series and small-sized AnS/Q2AS series are common.
Refer to the users manual of each CPU module for battery life, since it varies depending on the type of CPU module and memory cassette.

## PROGRAM REPLACEMENT

This chapter explains how to replace (reuse) the programs and comments of the large-sized A/QnA series CPU with the small-sized AnS/Q2AS series, and precautions for the replacement.

## (1) Comparisons between the large-sized A series CPU and the A2USHCPU-S1



O: Compatible, $\triangle$ : Partial change required, $x$ : Incompatible

| Item | Large-sized A series CPU specification | The A2USHCPU-S1 specification and precautions for replacement | Compatibility | Reference section |
| :---: | :---: | :---: | :---: | :---: |
| Timer, Counter | - Timer and counter are processed with the END processing. | [Specification] <br> - Same specifications | $\bigcirc$ | - |
| Parameter | - Parameters are dedicated for each CPU. | [Specification] <br> - Each CPU has the dedicated parameters. <br> [Measure] <br> - When replacing by the A2USHCPU-S1, check and reset the parameters since specifications and functions differ between the two CPUs. | $\triangle$ | Section 7.2.1 |
| Special relay | - 256 points of M9000 to M9255 are provided. | [Specification] <br> - Same specifications | $\bigcirc$ | - |
| Special register | - 256 points of D9000 to D9255 are provided. | [Specification] <br> - Same specifications | $\bigcirc$ | - |
| Comment | - Comments are managed as a common comment or comment by program. <br> - The comment capacity of the ACPU is up to $127 \mathrm{k}(64 \mathrm{k}+63 \mathrm{k})$ bytes. | [Specification] <br> - Same specifications | $\bigcirc$ | - |
| Writing programs to ROM | - The ROM operation is executed with the EPROM. | [Specification] <br> - The E ${ }^{2}$ PROM cassette is installed to the CPU, and the ROM operation is executed. <br> [Measure] <br> - By replacing by $E^{2}$ PROM, writing to PLC can be made by the operation equivalent to that of RAM. | $\triangle$ | $\begin{gathered} \text { Section } \\ 7.3 .5 \end{gathered}$ |

(2) Comparisons between the large-sized QnA series CPU and the small-sized Q2AS series CPU

| Item | Large-sized QnA series CPU specification | Small-sized Q2AS series CPU specifications and precautions for replacement | Compatibility | Reference section |
| :---: | :---: | :---: | :---: | :---: |
| Sequence program SFC program | - Each program is dealt as one file. | [Specification] <br> - Same specifications | $\bigcirc$ | - |
| Instruction | - Each instruction described in the QCPU (Q mode)/QnACPU Programming Manual (Common Instructions/PID Control Instructions/SFC, etc.) is usable. | [Specification] <br> - Same specifications | $\bigcirc$ | - |
| File register | - Data is stored in a memory card. <br> - One block is set in 32K-point units. <br> - Up to two memory cards can be installed. | [Specification] <br> - Same specifications, and one memory card can be installed. <br> [Measure] <br> - Review the setting since the number of memory cards differs between the two CPUs. | $\triangle$ | Section 7.3.4 |
| Parameter | - Each CPU has the dedicated parameters. | [Specification] <br> - Same specifications | $\bigcirc$ | - |
| Special relay | - 1800 points of SM0 to SM1799 are provided. | [Specification] <br> - Same specifications | $\bigcirc$ | - |
| Special register | - 1800 points of SD0 to SD1799 are provided. | [Specification] <br> - Same specifications | $\bigcirc$ | - |
| Comment | - Comments are managed as a common comment or comment by program. | [Specification] <br> - Same specifications | $\bigcirc$ | - |
| Writing programs to ROM | - The boot run is executed with storing a program and parameter in a memory card. <br> - Up to two memory cards can be installed. | [Specification] <br> - Same specifications, and one memory card can be installed. <br> [Measure] <br> - Review the setting since the number of memory cards differs between the two CPUs. | $\triangle$ | $\begin{gathered} \text { Section } \\ 7.3 .5 \end{gathered}$ |

### 7.1 Program Replacement Procedure

The programs and comments of the large-sized A/QnA series CPU can be replaced with the small-sized AnS/Q2AS series by using "Change PLC type" of the GX Developer.

### 7.1.1 Change PLC type operation

The Change PLC type is a function that changes the target PLC type of the data read to the GX Developer.
For special function modules and network modules, review programs and parameters.
(1) Applicable range of conversion from the A/QnACPU by the GX Developer

The following table shows the applicable range of conversion from the A/QnACPU to other PLCs. As it shows, converting to all PLC CPUs is applicable.

| Product | Change source | Change destination PLC |  |  |
| :--- | :---: | :---: | :---: | :---: |
|  | PLC | ACPU | QnACPU | QCPU |
| GX Developer | ACPU | $O$ | $O$ | $O$ |
|  | QnACPU | $O$ | $O$ | $O$ |

## (2) Operation of GX Developer

(a) Select the "Change PLC type" of the "Project" menu.

(b) Specify the PLC type after conversion in the "Change PLC type" dialog box.


Click the [OK] button after setting the PLC type.


PLC type setting


### 7.1.2 Reading (Reusing) other format files

The following explains how to read (reuse) GPPQ/GPPA format files other than that of the GX Developer. Follow this procedure to convert them to the file format of the GX Developer.

## (1) Operation procedure

$$
\text { Select [Project] } \rightarrow \text { [Import file] } \longrightarrow \quad \text { [Import from GPPQ format file] }
$$

## (2) Setting screen


(a) Drive/Path, System name, Machine

Designates the location of data created in GPPQ or GPPA format.
Enter the system name and machine name of the data specified in the Drive/Path.
Clicking the [Browse] button shows the dialog box for choosing the system name and machine name. Double-click the file to be read to specify it.
(b) Source data list

Displays data created in GPPQ or GPPA format.
Check the checkbox of data name.
For the selected comments, the device comment range to be read can be set in the Common tab or Local tab.
(c) [Param+prog] button/[Select all] button

- [Param+prog] button Select only the parameter data and program data of the source data.
- [Select all] button

Select all data in the source data list.
Comment2 is selected for the A series, and the device memories of the number of data are displayed.
The first data name is selected for comments and file registers in the QnA series.
(d) [Cancel all selections] button

Cancels all the selected data.
(e) <<Common>> tab screen (A series)

Set this when specifying the range for common comments and reading data.

(f) <<Local>> tab screen (A series)

Set this when specifying the range for comments by program and reading data.

(g) Merge peripheral statement/note
(h) [Execute] button

Click this button after making the setting.

## (3) Setting procedure

(a) Data selection

1) Set a drive/path for reading in GPPQ or GPPA format.
2) Click the [Browse] button to set the system name and machine name of the project to be read.
3) Check the checkbox of the data to be selected with the [Param+prog] button, [Select all] button, or the mouse.
4) Click the [Execute] button after making necessary settings.
(b) Canceling data selection
5) When canceling the selected data arbitrarily:

Clear the checkmark ( $\checkmark$ ) in the checkbox with the mouse or space key.
2) When canceling all the selected data:

Click the [Cancel all selections] button.

## (4) Precautions for reading the other format files

|  | For A series |
| :--- | :--- |
| A6GPP, SW0S-GPPA <br> format data | Read data with the GX Developer after performing the corresponding format conversion <br> with GPPA. <br> For the operating methods, refer to the Type SW4IVD-GPPA(GPP) Operating Manual. |
| For data selection | For device comment selection, you can only choose either comment 2 or comment 1. |
| GPPA format file <br> reading | Deletes the project data on the GX Developer and reads the other format file. <br> The area in excess of the program capacity is deleted when read. <br> For the PLC type which cannot use subprograms, subprograms are deleted when read. <br> When the file includes microcomputer programs edited with other than the SFC <br> program (e.g. SWOSRX-FNUP), they are lost. |


|  | For QnA series |
| :--- | :--- |
| Ladder return <br> positions | Returning places are different between GPPQ and GX Developer. <br> Because of this, if the total of return sources and return destinations exceeds 24 lines in <br> a single ladder block, the program is not displayed properly. <br> Corrective action: Add SM400 (normally ON contact) to adjust the return positions. |
| For data selection | For the device memory and file register, you can select only one data name for each <br> item. |

### 7.2 Precautions for Parameter Replacement

### 7.2.1 Conversion from large-sized A series CPU to small-sized AnS series CPU

This section explains the parameter conversion upon replacement of the large-sized A series CPU programs with the small-sized AnS series CPU.
<Compatibility>
$O$ :Common item between the large-sized A series CPU and the small-sized AnS series CPU, that can be converted directly.
$\Delta$ :Item that requires re-setting after the conversion, since the functions/specifications are partially different
$\times$ :Items to be deleted, since there is no common item between the large-sized $A$ series and the smallsized AnS series CPU

Confirm the parameters after the conversion, and correct/reset as required.
(1) When replacing the AnNCPU by the A2USHCPU-S1

| Name |  |  | Compatibility | Remarks |
| :---: | :---: | :---: | :---: | :---: |
|  |  |  | When replacing the AnNCPU by the A2USHCPU-S1 |
|  | $\frac{\vec{U}}{2}$ | Program capacity |  | $\triangle$ | Main microcomputer program capacity is dedicated to the SFC. <br> Sub microcomputer program capacity is not converted. |
|  |  | Comment | $\bigcirc$ | Can be converted directly. |
|  |  | Expanded comment | $\bigcirc$ |  |
|  |  | File register | $\bigcirc$ |  |
|  |  | Capacity for debugging | $\times$ | Not converted since it is the online setting. |
|  |  | WDT (watchdog timer) setting | $\triangle$ | Not converted since fixed to 200ms. |
|  |  | Operation mode when these is an error | $\triangle$ | Since the setting is converted to default, resetting is required when the setting has been changed. |
|  |  | Annunciator display mode | $\times$ | No compatible function is available. |
|  | $\begin{aligned} & \stackrel{\varepsilon}{\omega} \\ & \stackrel{\rightharpoonup}{\omega} \\ & \omega \\ & 0 \\ & 0 \\ & 0 \end{aligned}$ | RUN - PAUSE contacts | $\triangle$ | Resetting is required since the setting is converted to default. |
|  |  | System interrupt settings | $\triangle$ | The caution is required since the specifications differ. |
|  |  | Output mode at STOP to RUN | $\triangle$ | Resetting is required since the setting is converted to default. |
|  |  | Data communications request batch processing | - | The new AnUCPU function |
|  | I/O assignment |  | $\triangle$ | Resetting is required since the setting is not converted. |
|  | Device |  | $\triangle$ | Resetting is required since the setting is converted to default. |


| Name |  |  |  | Compatibility | Remarks |
| :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  |  | When replacing the AnNCPU by the A2USHCPU-S1 |
|  |  |  | Network type |  | $\bigcirc$ | Can be converted directly. |
|  |  |  | Start I/O No. | $\Delta$ | The I/O No. of module installed must be changed since the start I/O No. is set to the default value of " 00 ". |
|  |  |  | Total stations | $\bigcirc$ |  |
|  |  |  | Network range assignment | $\bigcirc$ | Can be converted directly. |
|  |  |  | Refresh parameters | $\triangle$ | New setting is required. <br> For 3-tier master station, the setting is required including the link range of 2-tier local station. |
|  |  | $\stackrel{\searrow}{ \pm}$ | Network type | $\triangle$ |  |
|  |  | $\frac{.0}{0.0}$ | Start I/O No. | $\triangle$ | required. <br> If there is no 3 -tier master station, the refresh parameter setting is also required. |
|  |  | 앙 | Refresh parameters | $\Delta$ |  |
|  | MEL | CN | ET/MINI parameter | $\triangle$ | The AnU and A2USHCPU-S1 can be set in the program and the parameter. <br> The AnNCPU program can be diverted and does not require modification. <br> For changing to the parameter setting, a new setting is required. |

## (2) When replacing the AnACPU by the A2USHCPU-S1

| Name |  |  | Compatibility | Remarks |
| :---: | :---: | :---: | :---: | :---: |
|  |  |  | When replacing the AnACPU by the A2USHCPU-S1 |
|  |  | Program capacity |  | $\bigcirc$ | Can be converted directly. |
|  |  | Comment | $\bigcirc$ |  |
|  |  | Expanded comment | $\bigcirc$ |  |
|  |  | File register | $\bigcirc$ |  |
|  |  | Capacity for debugging | $\bigcirc$ | Since it is the online setting, parameter setting is not required. |  |
|  | 00000 | WDT (watchdog timer) setting | $\bigcirc$ | Fixed to 200ms. |  |
|  |  | Operation mode when these is an error | $\bigcirc$ | Can be converted directly. |  |
|  |  | Annunciator display mode | $\times$ | No compatible function is available. |  |
|  |  | RUN - PAUSE contacts | 0 | Can be converted directly. |  |
|  |  | System interrupt settings | $\bigcirc$ |  |  |
|  |  | Output mode at STOP to RUN | $\bigcirc$ |  |  |
|  |  | Data communications request batch processing | - | The new AnUCPU function |  |
|  | I/O assignment |  | $\bigcirc$ | be converted dir |  |
|  | Dev. Point |  | $\bigcirc$ | Can be converted directly. |  |


|  | Name |  |  | Compatibility | Remarks |
| :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  |  | When replacing the AnACPU by the A2USHCPU-S1 |
|  | MELSECNET (II) network parameter |  | Network type |  | $\bigcirc$ | Can be converted directly. |
|  |  |  | Start I/O No. | $\Delta$ | The I/O No. of module installed must be changed since the start I/O No. is set to the default value of " 00 ". |
|  |  |  | Total stations | $\bigcirc$ |  |
|  |  |  | Network range assignment | $\bigcirc$ |  |
|  |  |  | Refresh parameters | $\triangle$ | New setting is required. <br> For 3 -tier master station, the setting is required including the link range of 2-tier local station. |
|  |  |  | Network type | $\Delta$ |  |
|  |  | $\begin{aligned} & 0 \\ & \text { 읃 } \\ & \hline \end{aligned}$ | Start I/O No. | $\Delta$ | required. If there is no 3 -tier master station, the refresh parameter setting is also required. |
|  |  | 0 | Refresh parameters | $\Delta$ |  |
|  | MEL | CN | T/MINI parameter | $\bigcirc$ | Can be converted directly. |

## (3) When replacing the AnUCPU by the A2USHCPU-S1

| Name |  |  | Comatibility | Remarks |
| :---: | :---: | :---: | :---: | :---: |
|  |  |  | When replacing the AnUCPU by the A2USHCPU-S1 |
|  |  | Program capacity |  | $\bigcirc$ | Can be converted directly. |
|  |  | Comment | $\bigcirc$ |  |
|  |  | Expanded comment | $\bigcirc$ |  |
|  |  | File register | $\bigcirc$ |  |
|  |  | Capacity for debugging | $\bigcirc$ | Since this is the online setting, parameter setting is not required. |  |
|  |  | WDT (watchdog timer) setting | $\bigcirc$ | Fixed to 200 ms . |  |
|  |  | Operation mode when these is an error | $\bigcirc$ | Can be converted directly. |  |
|  |  | Annunciator display mode | $\times$ | No compatible function is available. |  |
|  | $\begin{aligned} & \varepsilon \\ & \stackrel{\varepsilon}{0} \\ & \omega \\ & \omega \\ & 0 \\ & 0 \\ & 0 \end{aligned}$ | RUN - PAUSE contacts | $\bigcirc$ | Can be converted directly. |  |
|  |  | System interrupt settings | $\bigcirc$ |  |  |
|  |  | Output mode at STOP to RUN | $\bigcirc$ |  |  |
|  |  | Data communications request batch processing | $\bigcirc$ |  |  |
|  | I/O assignment |  | $\bigcirc$ |  |  |
|  | Dev. Point |  | $\bigcirc$ |  |  |



### 7.2.2 Conversion from large-sized QnA series CPU to small-sized Q2AS series CPU

This section explains the parameter conversion upon replacement of the large-sized QnA series CPU programs with the small-sized Q2AS series CPU.
The symbols in the table indicate the followings.
<Compatibility>
O:Common item between the large-sized QnA series CPU and the small-sized Q2AS series CPU, that can be converted directly.
$\Delta$ :Item that requires re-setting after the conversion, since the functions/specifications are partially different
$\times$ :Items to be deleted, since there is no common item between the large-sized QnA series CPU and the small-sized Q2AS series CPU

Confirm the parameters after the conversion, and correct/re-set as required.

| Name |  |  | Compatibility | Remarks |
| :---: | :---: | :---: | :---: | :---: |
|  | PLC name |  | $\bigcirc$ | Can be converted directly. |
|  |  | Timer limit setup | $\bigcirc$ |  |
|  |  | RUN - PAUSE contacts | $\bigcirc$ |  |
|  |  | Remote reset | $\bigcirc$ |  |
|  |  | Output mode at STOP to RUN | $\bigcirc$ |  |
|  |  | Common pointer | $\bigcirc$ |  |
|  |  | General data processing | $\bigcirc$ |  |
|  |  | Number of empty slots | $\bigcirc$ |  |
|  |  | System interrupt settings | $\bigcirc$ |  |
|  |  | File register | $\bigcirc$ |  |
|  |  | Comment file used in a command | $\bigcirc$ |  |
|  |  | Device initial value | $\bigcirc$ |  |
|  |  | File for local device | $\bigcirc$ |  |
|  | Device |  | $\bigcirc$ |  |
|  |  | WDT (watchdog timer) setting | $\bigcirc$ |  |
|  |  | Error check | $\bigcirc$ |  |
|  |  | Operation mode when these is an error | $\bigcirc$ |  |
|  |  | Constant scanning | $\bigcirc$ |  |
|  |  | Annunciator display mode | $\bigcirc$ |  |
|  |  | Brakedown history | $\bigcirc$ |  |
|  |  | Low speed program execution time | $\bigcirc$ |  |
|  | I/O assignment |  | $\bigcirc$ |  |
|  | Program |  | $\bigcirc$ |  |
|  | Boot file |  | $\bigcirc$ |  |
|  | $\begin{aligned} & u \\ & \text { u } \end{aligned}$ | SFC program start mode | $\bigcirc$ |  |
|  |  | Start conditions | $\bigcirc$ |  |
|  |  | Operation mode when the block is stopped | O |  |
|  | Acknowledge X/Y assignment |  | $\bigcirc$ |  |


|  | Name | Compati- ibility | Remarks |
| :---: | :---: | :---: | :---: |
|  | MELSECNET/Ethernet | 0 | Can be converted directly. |
|  | MELSECNET/MINI | 0 |  |
|  | CC-Link | $\bigcirc$ |  |

### 7.3 Precautions for Program Replacement

### 7.3.1 List of applicable devices


*1 The number of accessible points to actual I/O modules.
*2 The number of points that can be used on the programs.
*3 The number of points used can be changed with parameters.
*4 Each 5 points of FX0 to FX4 and FY0 to FY4 can be used on the programs.
*5 "V" is used for edge relays for the QnACPU.

| A2USHCPU-S1 | AnUCPU | AnACPU | AnNCPU |
| :---: | :---: | :---: | :---: |
| A2USH-S1: 1024 points | A2U: 512 points A2U-S1: 1024 points A3U: 2048 points A4U: 4096 points | A2A: 512 points A2U-S1: 1024 points A3A: 2048 points | A1N: 256 points A2N: 512 points A2N-S1: 1024 points A3N: 2048 points |
| 8192 points |  | Same I/O device points of each CPU module |  |
| Total 8192 points*3 |  |  | Total 2048 points*3 |
| - |  |  |  |
| 2048 points |  |  | 256 points |
| - |  |  |  |
| 8192 points |  | 4096 points | 1024 points |
| 56 points |  |  |  |
| Total 2048 points |  |  | Total 256 points |
| 1024 points |  |  | 256 points |
| 8192 points |  | 6144 points | 1024 points |
| 8192 points |  | 4096 points | 1024 points |
| 56 points |  |  |  |
| - |  |  |  |
| - |  |  |  |
| 256 points |  |  |  |
| - |  |  |  |
| 256 points |  |  |  |
| - |  |  |  |
| - |  |  |  |
| 7 points (Z,Z1 to Z6) |  |  | 1 point (Z) |
| 7 points (V,V1 to V6) |  |  | 1 point (V) |
| 8192 points/block (R0 to R8191) |  |  |  |
| 2 points |  |  |  |
| 8 points |  |  |  |
| 256 points |  |  |  |
| 32 points |  |  |  |
| - |  |  |  |
| - |  |  |  |
| K-2147483648 to K2147483647 |  |  |  |
| H0 to HFFFFFFFF |  |  |  |
| - |  |  |  |
| - |  |  |  |

### 7.3.2 I/O Control method

| I/O control method |  | $\begin{gathered} \text { QnACPU } \\ \text { Q2AS(H)CPU-S1 } \end{gathered}$ | AnUCPU A2USHCPU-S1 | AnACPU | AnNCPU |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Refresh mode |  | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $0^{*}$ |
| Direct I/O method | Partial refresh instruction | $\bigcirc$ | $\bigcirc$ | 0 | $\bigcirc$ |
|  | Dedicated instruction*1 | - | $\bigcirc$ | $\bigcirc$ | - |
|  | Direct access input | $\bigcirc$ | - | - | - |
|  | Direct access output | $\bigcirc$ | - | - | - |
| Direct mode |  | - | - | - | $0^{*}$ |

*1 The direct output dedicated instructions include the DOUT, DSET and SRST instruction and do not include the direct input dedicated instructions.
*2 The DIP switch on the AnNCPU enables to switch between the refresh mode and direct mode.

### 7.3.3 Usable data format for instructions

| Setting data |  | $\begin{gathered} \text { QnACPU } \\ \text { Q2AS(H)CPU-S1 } \end{gathered}$ | AnUCPU A2USHCPU-S1 | AnACPU | AnNCPU |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Bit data | Bit device | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ |
|  | Word device | (Bit designation required) | - | - | - |
| Word data | Bit device | 0 <br> (Digit designation required) | ○ (Digit designation required) | ○ (Digit designation required) | $\bigcirc$ (Digit designation required) |
|  | Word device | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ |
| Double-word data | Bit device | 0 <br> (Digit designation required) | $\bigcirc$ (Digit designation required) | ○ (Digit designation required) | $\bigcirc$ (Digit designation required) |
|  | Word device | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ |
| Real number data |  | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\triangle$ |
| Character string data |  | $\bigcirc$ | $\bigcirc$ | - | - |

### 7.3.4 Precautions for file register replacement

This section describes precautions for replacing the large-sized A/QnA series CPU using file registers with the small-sized AnS/Q2AS CPU .
(1) Precautions for replacing the large-sized A series CPU with the small-sized AnS series CPU

| Item | Large-sized A series CPU | Small-sized AnS series CPU |
| :--- | :--- | :--- |
| Storage <br> destination | Memory cassette | Built-in RAM |
| Maximum <br> number of points | Depends on the memory cassette <br> used | Depends on the built-in RAM <br> capacity for each CPU |
| Number of points <br> for 1 block | 8k points |  |

The storage destination is different, however, the capacity setting of the parameter can be converted.
Since the file register area may not be secured when the built-in RAM capacity of CPU type is lower than the memory capacity before converted, calculate the memory capacity in advance.
The built-in RAM capacity of the A2USHCPU-S1 is 256 k bytes (equivalent to the memory cassette, the A3NMCA-32).
(2) Precautions for replacing the large-sized QnA series CPU with the small-sized Q2AS series CPU

| Item | Large-sized QnA series CPU | Small-sized Q2AS series CPU |
| :--- | :--- | :--- |
| Storage <br> destination | Memory card <br> (Up to 2 cards, 4 drives) | Memory card <br> (1 card, 2 drives) |
| Maximum <br> number of points | 1018 k points $\times 2$ <br> (When using two 2M memory cards) | 1018 k points <br> (When using a 2M memory <br> card) |
| Number of points <br> for 1 block | 32 k points |  |

(a) Changing storage destination

The applicable memory card is reduced from 2 to 1 , however, the file register setting of the parameter can be converted.
When "Memory card B" (second card) is used in the large-sized QnA series CPU, the parameter storage destination must be changed.
(b) Maximum number of points

When two memory cards are installed and used with switching files, the maximum number of points may not be secured when replacing with the small-sized Q2AS series CPU.

## POINT

Check the storage destination for the following items to be set in "PLC file" of the PLC parameter.

- Comment file used in a command
- Initial device value
- File for local device 0


### 7.3.5 Writing programs to ROM

(1) Precautions for replacing the large-sized A series CPU with the small-sized AnS series CPU
EP-ROM used in the large-sized A series CPU cannot be reused for the A2USHCPU-S1.
The E ${ }^{2}$ PROM memory cassette is included in the small-sized AnS series CPU.
This memory cassette is installed to the CPU, and the write of the parameter and main program can be made using the same operation with that of RAM by the GX Developer.
Consider replacing EP-ROM on the large-sized A series CPU with $E^{2}$ PROM.
(2) Precautions for replacing the large-sized QnA series CPU with the small-sized Q2AS series CPU
The applicable memory card is reduced from 2 to 1 , however, the boot file of the parameter can be converted.
When "Memory card B" (second card) is used in the large-sized QnA series CPU, the parameter storage destination must be changed.

COMMUNICATION AND INFORMATION MODULE REPLACEMENT

### 8.1 List of Alternative Communication and Information Module Models

| Large-sized A/QnA series models to be <br> discontinued | Alternative AnS/Q2AS series models |  |
| :--- | :--- | :--- | :--- |

### 8.2 Communication/Information Modules Comparison

### 8.2.1 Intelligent communication modules comparison

(1) Comparisons between the AD51H-S3 and A1SD51S
(a) Performance specifications comparison

O : Compatible, $\Delta$ : Partial change required, $\times$ : Incompatible

| Specification |  | AD51H-S3 | A1SD51S | $\left\lvert\, \begin{gathered} \text { Compati- } \\ \text { bility } \end{gathered}\right.$ | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Programming language |  | AD51H-BASIC |  | $\bigcirc$ |  |
| Number of tasks |  | (Max.) 8 tasks | (Max.) 2 tasks | $\times$ | Review the BASIC program, and reduce the number of tasks to two or less. |
| Task start condition |  | - Start at power on. <br> - Start by interrupt from the PLC CPU (Impossible when compiled BASIC is run.) <br> - Start by start request from another task. |  | O |  |
| Internal memory |  | Program memory: <br> Max. 384k bytes/8 tasks (16/32/48/64k bytes selectable for one task.) | Program memory: <br> Max. 64k bytes/2 tasks (16/32/48/64k bytes selectable for one task.) | $\times$ | Review the BASIC program, and reduce the memory size to 64k bytes or less. |
|  |  | Common memory: 8 k bytes |  | O |  |
|  |  | Extension register: 1024 points ( 2 k bytes) |  | $\bigcirc$ |  |
|  |  | Extension relay: 1024 points |  | $\bigcirc$ |  |
| General-purpose I/O |  | Input: 27 points Output: 23 points |  | O |  |
| Buffer memory |  | 3 k words (6k bytes) |  | $\bigcirc$ |  |
| Built-in interface | RS422I/F | RS-422 compliant, Channel 3, Connector connection, Transmission distance: Within 500 m |  | O |  |
|  | RS232I/F | RS-232C compliant, Channel 1,2, Connector connection, Transmission distance: Within 15m |  | $\Delta$ | Use a suitable connector on the other side. |
|  |  | D sub 25-pin (female), screw type | D sub 9-pin (female), screw type |  |  |
|  | Centronics I/F | Centronics compliant, Channel 4, Connector connection, Transmission distance: Within 3m | - | $\times$ | Change the external devices to the I/F devices of RS-232C/RS-422. |
|  | Memory card I/F | Applicable memory card: A6MEM-■ AW | - | $\times$ | Use the device memory of the CPU module. |
| Power failure protection |  | None <br> (The data stored in a memory card can be protected by battery) | Protected (Common memory, extension relay, extension register) | - |  |
| Storage to ROM |  | Possible <br> (For execution program only) | Not allowed (although $E^{2}$ PROM is built in the module.) | $\triangle$ | Write user program to ROM with the MSAVE command. |
| Console |  | - A7PHP • A7HGP • A7LMS <br> - IBM-PC/AT-compatible PC <br> - VG-620....... Manufactured by Victor Data Systems <br> - VT-382....... Manufactured by Digital Equipment Corporation Japan |  | O |  |
| Number of occupied I/O points |  | (I/O assignment:48 points <br> special-purpose points) | (I/O assignment:32 points <br> 32 special-purpose <br> points) | $\Delta$ | Change the start I/O No. by PLC parameter. |
| Internal current consumption (5VDC) |  | 1.0A | 0.4 A | O |  |
| External dimensions |  | $250(\mathrm{H}) \times 76(\mathrm{~W}) \times 120(\mathrm{D}) \mathrm{mm}$ | $130(\mathrm{H}) \times 34(\mathrm{~W}) \times 93.6(\mathrm{D}) \mathrm{mm}$ | $\Delta$ | The dimensions are different. |
| Weight |  | 0.9 kg | 0.3 kg | O |  |

(b) Function comparison

O : Compatible, $\Delta$ : Partial change required, $\times$ : Incompatible

| Function |  | AD51H-S3 | A1SD51S | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: | :---: |
| BASIC program functions | Sub CPU function | Uses a BASIC program to perform co that lengthen the scan time of a PLC <br> - Collection, analysis and compensat <br> - Functional operations such as sines | and function calculations <br> ent data square roots | $\bigcirc$ |  |
|  | Monitor display function | Displays the operating states (production status, operating status, and error information) on a console or terminal connected to the module. |  | 0 |  |
|  | Key <br> operation function | Allows entry of production schedules, the production quantity, operations, and setting data from the keyboard of a console or terminal connected to the module. |  | $\bigcirc$ |  |
|  | Printer function | Prints the production plans, achievements, daily reports, error details, plan data, inspection results, and test results from a printer connected to the module. |  | $\bigcirc$ |  |
|  | Data input function | Allows data entry from a bar code reader or magnetic card reader connected to the module. <br> (By using a BASIC program, the module can transmit and receive data in a free data format independently of the protocol of the communicating device.) <br> - Inputs a production lot number, product name, quantity, etc. <br> - Collects measurement values and test data |  | $\bigcirc$ |  |
|  | External device connection function | Transmits and receives data from/to a BASIC program by connecting a computer to the RS-232C or RS-422 interface. |  | $\bigcirc$ |  |
|  | File <br> manage- <br> ment function | Reads/writes data from/to a memory card installed in a memory card interface of the AD51H. | - | $\times$ | CPU module devices must be used as substitutes. |
|  | Clock function | Reads and writes clock data (year, month, day, hour, minute, second and day of the week) from/to a PLC CPU which has a clock function. |  | $\bigcirc$ |  |
| Offline programming function |  | Creates, corrects, and stores a BASIC program to a user's FD and prints it out independently by A7PHP/A7HGP/A7LMS/IBM-PC/AT-compatible PC. |  | $\bigcirc$ |  |
| Online programming function |  | Creates, executes, and corrects a BASIC program by using system commands when a console is connected to the module. Stores/reads an execution program to/from $E^{2}$ PROM using system commands. |  | O |  |
| Multitask debugging function |  | Debugs a BASIC program while executing it by connecting a console and a debugger. |  | $\bigcirc$ |  |

(c) Comparison of I/O signals for PLC CPU

Modifying sequence programs is required as the I/O signals differ.
For details of the I/O signals and sequence programs, refer to the user's manual.
O : Compatible, $\Delta$ : Partial change required, $\times$ : Incompatible


O : Compatible, $\Delta$ : Partial change required, $\times$ : Incompatible

(d) Buffer memory address comparison No special differences are identified.

## (2) Comparisons between the AD51-S3 and A1SD51S

The BASIC program of AD51-S3 is incompatible with that of A1SD51S. Analyze the existing program to create a program with AD51H-BASIC.
(a) Performance specifications comparison

O : Compatible, $\Delta$ : Partial change required, $\times$ : Incompatible

| Specification |  | AD51-S3 | A1SD51S | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Programming language |  | GPC-BASIC | AD51H-BASIC | $\times$ | Change the BASIC program to AD51HBASIC. |
| Number of tasks |  | (Max.) 8 tasks | (Max.) 2 tasks | $\times$ | Review the BASIC program, and reduce the number of tasks to two or less. |
| Task start condition |  | - Start at power on. <br> - Start by interrupt from the PLC CPU (Impossible when compiled BASIC is run). <br> - Start by real time interrupt. | - Start at power on. <br> - Start by interrupt from the PLC CPU (Impossible when compiled BASIC is run). <br> - Start by start request from another task. | $\triangle$ | When using a real time interrupt, the program must be changed. |
| Internal memory |  | Program memory: Max. 64k bytes +48 k bytes (Writing programs to ROM) | Program memory: Max. 64k bytes/2 tasks | $\times$ | Review the BASIC program, and reduce the memory size to 64 k bytes or less. |
|  |  | Common memory: 2 k bytes | Common memory: 8 k bytes | $\bigcirc$ |  |
|  |  | - | Extension register: 1024 points (2k bytes) | - |  |
|  |  | - | Extension relay: 1024 points | - |  |
| General-purpose I/O |  | Input: 13 points Output: 10 points | Input: 27 points Output: 23 points | $\triangle$ | Change the start I/O No. in the PLC parameter I/O assignment setting. |
| Buffer memory |  | 3 k words (6k bytes) |  | $\bigcirc$ |  |
| Built-in interface | RS422I/F | RS-422 compliant, Connector connection, Transmission distance: Within 500m |  |  | Change into one channel |
|  |  | Channel 1, 2 <br> D sub 25-pin (female), screw type | Channel 3 <br> D sub 25-pin (female), screw type | $\triangle$ | channels.Also, change the channel number. |
|  | RS2321/F | RS-232C compliant, Connector connection, Transmission distance: Within 15m |  | $\Delta$ | Also, change the channel number and the number of connector pins. |
|  |  | Channel 3, 4 <br> D sub 25 -pin (female), screw type | Channel 1, 2 <br> D sub 9-pin (female), screw type |  |  |
| Power failure protection |  | None | Protected (Common memory, extension relay, extension register) | $\bigcirc$ |  |
| Storage to ROM |  | Possible <br> ( 8 k ROM/16k ROM) | Not allowed (although $E^{2}$ PROM is built in the module.) | $\triangle$ | Write user program to ROM with the MSAVE command. |
| Console |  | - A6GPP • A6PHP <br> - VG-620/670... Manufactured by Victor Data Systems | - A7PHP • A7HGP • A7LMS <br> - IBM-PC/AT-compatible PC <br> - VG-620.... Manufactured by Victor Data Systems <br> - VT-382.... Manufactured by Digital Equipment Corporation Japan | $\triangle$ | Use compatible consoles. |
| Number of occupied I/O points |  | 48 points <br> (I/O assignment: 16 empty points +32 special-purpose points) | 32 points (I/O assignment: 32 special-purpose points) | $\triangle$ | Change the start I/O No. in the PLC parameter I/O assignment setting. |
| Internal power consumption (5VDC) |  | 1.3A | 0.4A | $\bigcirc$ |  |
| External dimensions |  | $250(\mathrm{H}) \times 76$ (W) $\times 120$ (D) mm | $130(\mathrm{H}) \times 34(\mathrm{~W}) \times 93.6(\mathrm{D}) \mathrm{mm}$ | $\triangle$ | The dimensions are different. |
| Weight |  | 1.1 kg | 0.3kg | $\bigcirc$ |  |

(b) Function comparison
$\bigcirc$ : Compatible, $\Delta$ : Partial change required, $\times$ : Incompatible

| Function |  | AD51-S3 | A1SD51S | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: | :---: |
| BASIC program functions | Sub CPU function | Uses a BASIC program which may result in a p <br> - Collection, analysis a <br> - Functional operations | ex numeric and function calculations $J$ scan time. easurement data garithms and square roots | O |  |
|  | Monitor display function | Displays working states (progress of production, operation status, error, etc.) on the console or terminal connected to the module. |  | $\bigcirc$ |  |
|  | Key operation function | Allows entry of production schedules, production output, operation and setting data from the keyboard of a console or terminal. |  | O |  |
|  | Printer function | Prints production plans, production records, daily reports, errors, plan data, inspection results, test records, etc. from a printer connected to the module. |  | O |  |
|  | Data input function | Allows data entry through a bar code reader or magnetic card reader connected to the module. <br> (Since a BASIC program enables the module to transmit and receive data in a free format, it can perform data communications in the protocol the communicating device uses.) <br> - Input of production lot numbers, production names, quantities, etc. <br> - Collection of measurement values and test data |  | O | The BASIC program of AD51-S3 is incompatible with that of A1SD51S. <br> Analyze the existing |
|  | External device connection function | Establishes data communications by using a BASIC program and connecting a computer to the RS-232C or RS-422/485 interface of the module. |  | O | program to create a program with AD51HBASIC. |
|  | Clock function | Reads and writes clock data (year, month, day, hour, minute, second and day of the week) from/to a PLC CPU which has a clock function. |  | $\bigcirc$ |  |
| Offline programming function |  | - | Allows the A7PHP/A7HGP/A7LMS/ IBM-PC/AT-compatible PC to create, correct and store BASIC programs on a user's FD and to print them independently. | - |  |
| Online programming function |  | Creates, executes and corrects BASIC programs on a console connected to the module using system commands. <br> Stores and reads execution programs to/from an $E^{2} P R O M$ with system commands. |  | O |  |
| Multitask debugging function |  | - | Debugs a BASIC program on a console and a debugger connected to the module while executing it by multitasking. | - |  |

## (c) Comparison of I/O signals for PLC CPU

Modifying sequence programs is required as the I/O signals differ.
For details of the I/O signals and sequence programs, refer to the user's manual.

O : Compatible, $\Delta$ : Partial change required, $\times$ : Incompatible

| Input signal | AD51-S3 | A1SD51S | Compati- | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: |
|  | Signal name | Signal name | bility |  |
| X0 | Unusable | General-purpose input | $\triangle$ | Change " X 10 to X 1 C " to " X 0 to XA" and "X10 to X1F". <br> Also, change "X1D" to "XD". |
| X1 |  |  | $\triangle$ |  |
| X2 |  |  | $\triangle$ |  |
| X3 |  |  | $\triangle$ |  |
| X4 |  |  | $\triangle$ |  |
| X5 |  |  | $\triangle$ |  |
| X6 |  |  | $\Delta$ |  |
| X7 |  |  | $\triangle$ |  |
| X8 |  |  | $\triangle$ |  |
| X9 |  |  | $\triangle$ |  |
| XA |  |  | $\triangle$ |  |
| XB |  | Start of multitasking | $\triangle$ |  |
| XC |  | Stop of multitasking | $\triangle$ |  |
| XD |  | A1SD51S system down | $\triangle$ |  |
| XE |  | Unusable | $\triangle$ |  |
| XF |  |  | $\triangle$ |  |
| X10 |  |  | $\triangle$ |  |
| X11 |  |  | $\triangle$ |  |
| X12 |  |  | $\triangle$ |  |
| X13 |  |  | $\triangle$ |  |
| X14 |  |  | $\triangle$ |  |
| X15 |  |  | $\triangle$ |  |
| X16 | General-purpose input |  | $\triangle$ |  |
| X17 |  |  | $\triangle$ |  |
| X18 |  |  | $\triangle$ |  |
| X19 |  |  | $\triangle$ |  |
| X1A |  |  | $\triangle$ |  |
| X1B |  |  | $\triangle$ |  |
| X1C |  |  | $\triangle$ |  |
| X1D | AD51-S3 system down |  | $\triangle$ |  |
| X1E | Unusable |  | $\triangle$ |  |
| X1F |  |  | $\triangle$ |  |
| $\begin{gathered} \mathrm{X} 20 \\ \text { to } \end{gathered}$ |  | Outside I/O point range | $\triangle$ |  |
| X2F |  |  |  |  |

O : Compatible, $\triangle$ : Partial change required, $\times$ : Incompatible

(d) Buffer memory address comparison

No special differences are identified.

### 8.2.2 Serial communication modules comparison

(1) Comparisons between the AJ71UC24 and A1SJ71UC24-R2/R4/PRF
(a) Performance specifications comparison

O : Compatible, $\triangle$ : Partial change required, $\times$ : Incompatible

| Specification |  | AJ71UC24 | A1SJ71 | 4-R2/R4/PRF | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Interface* |  | RS-232 compliant (D-Sub 25P) | RS-232 | liant (D-Sub 9P) | $\triangle$ | The connector on the |
|  |  | RS-422/485 compliant | RS-4 | 5 compliant | $\triangle$ | be changed. Use A1SJ71UC24-ロロ when using two channels or more. |
| Communication method | Communication using dedicated protocol | Half-duplex communication |  |  | $\bigcirc$ |  |
|  | Non-procedural/ bidirectional communication | Full duplex communication (1:1 connection) /Half-duplex communication (1:n, m:n connection) |  |  | $\bigcirc$ |  |
| Synchronization method |  | Start stop synchronization (asynchronous method) |  |  | $\bigcirc$ |  |
| Transmission speed |  | 300 to 19200 bps |  |  | $\bigcirc$ |  |
| Data format | Start bit | 1 |  |  | $\bigcirc$ |  |
|  | Data bit | 7 or 8 |  |  | $\bigcirc$ |  |
|  | Parity bit | 1 (vertical parity)/none |  |  | $\bigcirc$ |  |
|  | Stop bit | 1 or 2 |  |  | $\bigcirc$ |  |
| Access cycle | Communication using dedicated protocol | One request is processed when the mounted PLC CPU executes END processing. |  |  | $\bigcirc$ |  |
|  | Non-procedural/ bidirectional communication | Transmission is executed at each send request, and reception is available at all times. |  |  | $\bigcirc$ |  |
| Error detection | Parity check | Performed (odd/even)/none |  |  | $\bigcirc$ |  |
|  | Sum check | Performed (Dedicated protocol/Bidirectional)/none |  |  | $\bigcirc$ |  |
| Transmission control |  |  | RS-232 | RS-422/485 | $\bigcirc$ |  |
|  |  | DTR/DSR (ER/DR) control | Available | N/A |  |  |
|  |  | CD signal control | Available | N/A |  |  |
|  |  | DC1/DC3 (Xon/Xoff) control DC2/DC4 control | Available | Available |  |  |
| Line configuration (connection) | RS-232 | 1:1 |  |  | 0 |  |
|  | RS-422/485 | 1:1, 1:n, m:n (n: max.32, m+n: max.32) |  |  | $\bigcirc$ |  |
| Line configuration ( data communication) | Communication using dedicated protocol | 1:1, 1:n, m:n (n: max.32, m+n: max.32) |  |  | $\bigcirc$ | For details on linked operation between interfaces, refer to the manual. |
|  | Non-procedural communication | 1:1, 1:n (n: max.32) |  |  | $\bigcirc$ |  |
|  | Bidirectional communication | 1:1 |  |  | $\bigcirc$ |  |
| Transmission distance | RS-232 | Max. 15m |  |  | $\bigcirc$ |  |
|  | RS-422/485 | Max. 500m (overall distance) |  |  | $\bigcirc$ |  |
| Current consumption |  | 0.3A |  | 1A | $\bigcirc$ |  |
| No. of $E^{2}$ PROM writes No. of flash ROM writes |  | Max. 100,000 times on same area in $E^{2}$ PROM |  |  | $\bigcirc$ |  |
| Number of occupied I/O points |  | 32 points (I/O assignment: special 32 points) |  |  | $\bigcirc$ |  |
| External dimensions |  | $250(\mathrm{H}) \times 37.5(\mathrm{~W}) \times 131(\mathrm{D}) \mathrm{mm}$ | 130(H) $\times 34$ | W) $\times 93.6$ (D) mm | $\triangle$ | The dimensions are different. |
| Weight |  | 0.63kg | $\begin{aligned} & 0.22 \mathrm{~kg} \text { (A) } \\ & 0.25 \mathrm{~kg}(\mathrm{~A} \\ & 0.22 \mathrm{~kg}(\mathrm{~A} \end{aligned}$ | 71UC24-R2) <br> 71UC24-R4) <br> 71UC24-PRF) | $\bigcirc$ |  |

*The following table shows the AJ71UC24, A1SJ71UC24-R2, A1SJ71UC24-R4, A1SJ71UC24-PRF interface configurations.

| Model | RS-232 | RS-422/485 |
| :--- | :---: | :---: |
| AJ71UC24 | 1ch | 1ch |
| A1SJ71UC24-R2 | 1ch | - |
| A1SJ71UC24-R4 | - | 1ch |
| A1SJ71UC24-PRF | 1ch | - |

## (b) Function comparison

O : Compatible, $\Delta$ : Partial change required, $\times$ : Incompatible

| Function |  | AJ71UC24 | A1SJ71UC24-R2/R4/PRF | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: | :---: |
|  | Device memory read/write | PLC CPU data are read from and written to the external device. |  | $\bigcirc$ |  |
|  | On-demand | Data are transmitted to the external device from the PLC CPU. |  | $\bigcirc$ |  |
|  | Data transmission PLC $\rightarrow$ external device | Data are transmitted to the external device from the PLC CPU. |  | $\bigcirc$ |  |
|  | Data reception <br> PLC $\leftarrow$ external device | The PLC recieves data transmitted from the external device. |  | $\bigcirc$ |  |
|  | Data transmission <br> PLC $\rightarrow$ external device | Data are transmitted to the external device from the PLC CPU. |  | $\bigcirc$ |  |
|  | Data reception <br> PLC $\leftarrow$ external device | The PLC recieves data transmitted from the external device. |  | $\bigcirc$ |  |
| Transmission using printer function |  | Transmits messages (character strings) to the printer from the PLC CPU. |  | $\bigcirc$ |  |
|  | DTR/DSR control | Data exchange with the external device is controlled by RS-232 control signals. |  | $\bigcirc$ |  |
|  | CD signal control |  |  | $\bigcirc$ |  |
|  | DC code control | DC codes (including Xon/Xoff) are sent/received to control data exchange with the external device. |  | $\bigcirc$ |  |
| Multidrop connection with RS-232C and RS-422 mixed |  | Multidrop connection with RS-232C and RS-422 mixed is available. | Multidrop connection with RS-232C and RS-422 mixed is not available. | $\times$ | For the wiring method, refer to the manual. |

(c) Switch settings comparisons

1) Mode setting switch

The difference between the AJ71UC24 and A1SJ71UC24-R2/R4/PRF is as follows.

| Mode setting switch number | RS-232C side setting | RS-422 side setting | AJ71UC24 | $\begin{gathered} \text { A1SJ71UC24-R2/ } \\ \text { PRF } \end{gathered}$ | A1SJ71UC24-R4 |
| :---: | :---: | :---: | :---: | :---: | :---: |
| 0 | Use prohibited |  | - | - | - |
| 1 to 3 | Type n protocol | Nonprocedural mode | $\bigcirc$ | $\bigcirc$ | - |
| 4 | Type 4 protocol | Nonprocedural mode | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ |
| 5 | Nonprocedural mode | Type 1 protocol | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ |
| 6 to 8 | Nonprocedural mode | Type n protocol | $\bigcirc$ | - | $\bigcirc$ |
| 9 | Nonprocedural mode |  | $\bigcirc$ | - | - |
| A to D | Type n protocol |  | $\bigcirc$ | - | - |
| E | Use prohibited |  | - | - | - |
| F | Self-loopback test |  | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ |

2) Transmission setting switch

AJ71UC24

| Switch setting | Setting switch | Setting item | Setting switch status |  |  |  |  |  |  |  | Remarks |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  | OFF |  |  |  | ON |  |  |  |  |
|  | SW11 | Main channel setting | RS-232C |  |  |  | RS-422/485 |  |  |  | Valid when mode setting switches 9 to D are set. |
|  | SW12 | Data bit setting | 7 bits |  |  |  | 8 bits |  |  |  | Excluding parity bit |
|  |  | ( $\begin{gathered}\text { Transmission } \\ \text { speed (BPS) }\end{gathered}$Transmission speed setting | 300 | 600 | 1200 | 2400 | 4800 | 9600 | 19200 | Use prohibited | - |
| $\rightarrow \mathrm{ON} \quad \mathrm{SW} 13$ |  |  | OFF | ON | OFF | ON | OFF | ON | OFF | ON |  |
| SW12 | SW14 |  | OFF | OFF | ON | ON | OFF | OFF | ON | ON |  |
| SW14 | SW15 |  | OFF | OFF | OFF | OFF | ON | ON | ON | ON |  |
| SW16 | SW16 | Parity bit setting | None |  |  |  | Yes |  |  |  | - |
| SW17 <br> SW18 | SW17 | Even/odd parity setting | Odd |  |  |  | Even |  |  |  | Valid when parity bit is used. |
| $\mathrm{SW} 21 \rightarrow \mathrm{ON}$ | SW18 | Stop bit setting | 1 bit |  |  |  | 2 bits |  |  |  | - |
| SW22 | SW21 | Sum check | None |  |  |  | Yes |  |  |  | For dedicated protocol |
| SW24 | SW22 | Write during RUN | Disabled |  |  |  | Enabled |  |  |  |  |
|  | SW23 | Computer link/ multidrop link selection | Multidrop link |  |  |  | Computer link |  |  |  | Always set to computer link (ON). |
|  | SW24 | Master station/local station setting | Multidrop link local station |  |  |  | Multidrop link master station |  |  |  | Setting ignored for computer link. |

A1SJ71UC24-R2/PRF

| Switch setting |  | Setting switch | Setting item | Setting switch status |  |  |  |  |  |  |  | Remarks |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | ON |  | OFF |  |  |  |  |
|  |  |  | SW03 | Unused | - |  |  |  | - |  |  |  | - |
|  |  | SW04 | Write during RUN | Enabled |  |  |  | Disabled |  |  |  | For dedicated protocol |
|  | $\bigcirc \mathrm{ON} \leftarrow$ |  | ( $\begin{gathered}\text { Transmission } \\ \text { speed (BPS) }\end{gathered}$Transmission speed setting | 300 | 600 | 1200 | 2400 | 4800 | 9600 | 19200 | Use prohibited | - |
| 04 | $\mathrm{ON} \leftarrow$ | SW05 |  | OFF | ON | OFF | ON | OFF | ON | OFF | ON |  |
|  |  | SW06 |  | OFF | OFF | ON | ON | OFF | OFF | ON | ON |  |
| 06 |  | SW07 |  | OFF | OFF | OFF | OFF | ON | ON | ON | ON |  |
| 07 | $\square$ | SW08 | Data bit setting | 8 bits |  |  |  | 7 bits |  |  |  | Excluding parity bit |
| 09 | $\square$ | SW09 | Parity bit setting | Yes |  |  |  | None |  |  |  | - |
| 10 11 12 | $\square$ | SW10 | Even/odd parity setting | Even |  |  |  | Odd |  |  |  | Valid when parity bit is used. |
|  |  | SW11 | Stop bit setting | 2 bits |  |  |  | 1 bit |  |  |  | - |
|  |  | SW12 | Sum check | Yes |  |  |  | None |  |  |  | For dedicated protocol |

A1SJ71UC24-R4

3) Station number setting switch

The difference between the AJ71UC24 and A1SJ71UC24-R2/R4/PRF is as follows.

| Item | AJ71UC24 | A1SJ71UC24-R2/R4/PRF | Compati- <br> bility |
| :---: | :---: | :---: | :---: |
| Station <br> number <br> setting <br> switch | Equipped with station number setting <br> switch | No station number setting switch | $\times$ |

(d) Comparison of I/O signals for PLC CPU

There are no differences in I/O signals between the AJ71UC24 and A1SJ71UC24-R2/R4/PRF.
$\bigcirc$ : Compatible, $\triangle$ : Partial change required, $\times$ : Incompatible


(e) Buffer memory address comparison

No special differences are identified in buffer memory assignment.
O : Compatible, $\triangle$ : Partial change required, $\times$ : Incompatible

| Address |  | AJ71UC24 | A1SJ71UC24-R2/R4/PRF | $\left\lvert\, \begin{gathered} \text { Compati- } \\ \text { bility } \end{gathered}\right.$ | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: | :---: |
| HEX | DEC | Buffer memory name | Buffer memory name |  |  |
| OH | 0 | Non-procedural send data count storage area |  | $\bigcirc$ |  |
| $\begin{gathered} 1 \mathrm{H} \\ \text { to } \\ 7 \mathrm{FH} \end{gathered}$ | $\begin{gathered} 1 \\ \text { to } \\ 127 \end{gathered}$ | Send data storage area |  | O |  |
| 80 H | 128 | Non-procedura | count storage area | $\bigcirc$ |  |
| $\begin{aligned} & 81 \mathrm{H} \\ & \text { to } \\ & \text { FFH } \end{aligned}$ | $\begin{gathered} 129 \\ \text { to } \\ 255 \end{gathered}$ | Receive data storage area |  | $\bigcirc$ |  |
| 100 H | 256 | Non-procedural receive end code specification area |  | $\bigcirc$ |  |
| to | to |  |  | - |  |
| 103H | 259 | Non-procedural word/byte specification area |  | $\bigcirc$ |  |
| 104H | 260 | Non-procedural send buffer memory head address specification area |  | $\bigcirc$ |  |
| 105H | 261 | Non-procedural send buffer memory length specification area |  | $\bigcirc$ |  |
| 106H | 262 | Non-procedural receive buffer memory head address specification area |  | $\bigcirc$ |  |
| 107H | 263 | Non-procedural receive buffer memory length specification area |  | $\bigcirc$ |  |
| 108H | 264 | Non-procedural receive end data count specification area |  | $\bigcirc$ |  |
| 109H | 265 | On-demand buffer memory head address specification area |  | $\bigcirc$ |  |
| 10AH | 266 | On-demand data length specification area |  | $\bigcirc$ |  |
| 10BH | 267 | RS-232 CD terminal check setting area |  | O |  |
| to | to | to |  | - |  |
| DFFH | 3583 | - |  | $\bigcirc$ |  |

## (2) Comparisons between AJ71C22-S1 and A1SJ71UC24-R4

(a) Performance specifications comparison
$\bigcirc$ : Compatible, $\triangle$ : Partial change required, $\times$ : Incompatible

| Specification |  | AJ71C22-S1 | A1SJ71UC24-R4 | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Interfaces |  | RS-422 compliant | RS-422/485 compliant | $\bigcirc$ |  |
| Communication method |  | Half-duplex communication |  | $\bigcirc$ |  |
| Synchronization method |  | Start stop synchronization (asynchronous method) |  | $\bigcirc$ |  |
| Transmission speed |  | 38400bps | 19200, 38400bps | $\bigcirc$ |  |
| Data format | Start bit | 1 |  | $\bigcirc$ |  |
|  | Data bit | 7 |  | $\bigcirc$ |  |
|  | Parity bit | 1 (vertical parity) |  | $\bigcirc$ |  |
|  | Stop bit | 1 |  | $\bigcirc$ |  |
| Error detection |  | Parity check (Even) |  | $\bigcirc$ |  |
|  |  | BCC check |  | $\bigcirc$ |  |
| Line configuration (connection) |  | 8 local stations for 1 master station |  | $\bigcirc$ |  |
| Transmission distance |  | Max. 500 m (overall distance) |  | $\bigcirc$ |  |
| Current consumption |  | 1.4A | 0.1A | $\bigcirc$ |  |
| Number of occupied I/O points |  | 32 points(I/O assignment: 32 special-purpose points) |  | $\bigcirc$ |  |
| External dimensions |  | $250(\mathrm{H}) \times 37.5(\mathrm{~W}) \times 120(\mathrm{D}) \mathrm{mm}$ | 130(H) $\times 34.5(\mathrm{~W}) \times 93.6(\mathrm{D}) \mathrm{mm}$ | $\triangle$ | The dimensions are different. |
| Weight |  | 0.6 kg | 0.25 kg | $\bigcirc$ |  |

(b) Function comparison
$\bigcirc$ : Compatible, $\Delta$ : Partial change required, $\times$ : Incompatible

| Function | AJ71C22-S1 | A1SJ71UC24-R4 | $\begin{array}{\|c\|} \hline \text { Compati- } \\ \text { bility } \end{array}$ | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: |
| Data communication | 1) ON/OFF data is sent to and received from 422 interface. <br> 2) Maximum number of ON/OFF data transf Total of inputs/outputs for all slave station Inputs/outputs per station ...... Outputs Inputs <br> 3) For communication with a slave station, th <br> (a) Slave stations to communicate with <br> (b) Priority order of slave stations <br> (c) Number of communication data points | of 8 slave stations via the RS- <br> uts + Inputs $\leqq 512$ points <br> ay be specified for the network: | O |  |
| Communication data monitoring | Data communicated with a slave station can be monitored in units of 8 points. | - | $\times$ | A1SJ71UC24-R4 does not have the LED for slave station communication data monitoring. Directly monitor the buffer memory addresses 20 H to 3 FH and 40 H to 5 FH . |
| Self-loopback test | Communication check is conducted for the RS422 interface. |  | $\bigcirc$ |  |

(c) Comparison of I/O signals for PLC CPU

There are no differences in I/O signals between the AJ71C22-S1 and A1SJ71UC24-R4.
$\bigcirc$ : Compatible, $\Delta$ : Partial change required, $\times$ : Incompatible


O : Compatible, $\Delta$ : Partial change required, $\times$ : Incompatible

(d) Buffer memory address comparison

No special differences are identified in buffer memory assignment.
O : Compatible, $\Delta$ : Partial change required, $\times$ : Incompatible

| Address |  | AJ71C22-S1 | A1SJ71UC24-R4 | ati- | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: | :---: |
| HEX | DEC | Buffer memory name | Buffer memory name | bility |  |
| OH | 0 | Number of access slave stations |  | $\bigcirc$ |  |
| $\begin{aligned} & 1 \mathrm{H} \\ & \text { to } \\ & 8 \mathrm{H} \end{aligned}$ | $\begin{gathered} 1 \\ \text { to } \\ 8 \end{gathered}$ | Transmission priority |  | O |  |
| $\begin{gathered} \hline 9 \mathrm{H} \\ \text { to } \\ 10 \mathrm{H} \\ \hline \end{gathered}$ | $\begin{aligned} & 9 \\ & \text { to } \\ & 16 \end{aligned}$ | Number of bits received |  | $\bigcirc$ |  |
| $\begin{gathered} \hline 11 \mathrm{H} \\ \text { to } \\ 18 \mathrm{H} \end{gathered}$ | $\begin{aligned} & 17 \\ & \text { to } \\ & 24 \end{aligned}$ | Number of bits received |  | O |  |
| $\begin{gathered} 19 \mathrm{H} \\ \text { to } \\ 1 \mathrm{DH} \end{gathered}$ | $\begin{aligned} & 25 \\ & \text { to } \\ & 29 \end{aligned}$ | Use prohibited | Use prohibited | $\bigcirc$ |  |
| 1EH | 30 |  | Max. number of link points (256/512 points) | - |  |
| 1FH | 31 |  | Off-link station | - |  |
| $\begin{gathered} 20 \mathrm{H} \\ \text { to } \\ 3 \mathrm{FH} \end{gathered}$ | $\begin{aligned} & 32 \\ & \text { to } \\ & 63 \end{aligned}$ | Receive data storing area |  | O |  |
| $\begin{gathered} 40 \mathrm{H} \\ \text { to } \\ 5 \mathrm{FH} \end{gathered}$ | $\begin{aligned} & 64 \\ & \text { to } \\ & 95 \end{aligned}$ | Send data storing area |  | O |  |
| 60 H | 96 | Error code |  | $\bigcirc$ |  |
| 61H | 97 | Error slave station display |  | $\bigcirc$ |  |
| 62 H | 98 | Link time storing area (current value) |  | $\bigcirc$ |  |
| 63H | 99 | Link time storing area (maximum value) |  | $\bigcirc$ |  |
| $\begin{gathered} \text { 64H } \\ \text { to } \\ 6 \mathrm{FH} \end{gathered}$ | $\begin{gathered} 100 \\ \text { to } \\ 111 \end{gathered}$ | Use prohibited |  | O |  |
| 70 H | 112 | Error return request area |  | $\bigcirc$ |  |
| 71H | 113 | Work area | Work area | $\bigcirc$ |  |
| to | to |  |  |  |  |
| 7FFH | 2047 |  |  |  |  |
| to | to | - |  | - |  |
| DFFH | 3583 |  |  |  |  |

## NETWORK SYSTEM REPLACEMENT

### 9.1 List of Alternative Network Module Models

| Large-sized A/QnA discon | ies models to be ued |  | Alternative small-sized AnS/Q2AS series models |
| :---: | :---: | :---: | :---: |
| Product name | Model name | Model name | Remarks (restrictions) |
|  | A1NCPUP21 | A2USHCPU-S1 <br> A1SJ71AP21 |  |
|  | A1NCPUR21 | A2USHCPU-S1 A1SJ71AR21 |  |
|  | A2NCPUP21 | A2USHCPU-S1 A1SJ71AP21 |  |
|  | A2NCPUR21 | A2USHCPU-S1 <br> A1SJ71AR21 |  |
|  | A2NCPUP21-S1 | A2USHCPU-S1 <br> A1SJ71AP21 |  |
|  | A2NCPUR21-S1 | A2USHCPU-S1 <br> A1SJ71AR21 |  |
|  | A3NCPUP21 | A2USHCPU-S1 <br> A1SJ71AP21 |  |
|  | A3NCPUR21 | A2USHCPU-S1 <br> A1SJ71AR21 |  |
|  | A2ACPUP21 | $\begin{aligned} & \text { A2USHCPU-S1 } \\ & \text { A1SJ71AP21 } \end{aligned}$ |  |
|  | A2ACPUR21 | A2USHCPU-S1 <br> A1SJ71AR21 |  |
| CPU module with link | A2ACPUP21-S1 | A2USHCPU-S1 A1SJ71AP21 | Refer to Section 2.1 for the details. |
|  | A2ACPUR21-S1 | A2USHCPU-S1 A1SJ71AR21 |  |
|  | A3ACPUP21 | A2USHCPU-S1 A1SJ71AP21 |  |
|  | A3ACPUR21 | A2USHCPU-S1 <br> A1SJ71AR21 |  |
|  | A1NCPUP21-S3 | A2USHCPU-S1 <br> A1SJ71AP21-S3 |  |
|  | A2NCPUP21-S3 | $\begin{aligned} & \text { A2USHCPU-S1 } \\ & \text { A1SJ71AP21-S3 } \end{aligned}$ |  |
|  | A2NCPUP21-S4 | $\begin{aligned} & \text { A2USHCPU-S1 } \\ & \text { A1SJ71AP21-S3 } \end{aligned}$ |  |
|  | A3NCPUP21-S3 | $\begin{aligned} & \text { A2USHCPU-S1 } \\ & \text { A1SJ71AP21-S3 } \end{aligned}$ |  |
|  | A2ACPUP21-S3 | $\begin{aligned} & \text { A2USHCPU-S1 } \\ & \text { A1SJ71AP21-S3 } \end{aligned}$ |  |
|  | A2ACPUP21-S4 | $\begin{aligned} & \text { A2USHCPU-S1 } \\ & \text { A1SJ71AP21-S3 } \end{aligned}$ |  |
|  | A3ACPUP21-S3 | $\begin{aligned} & \text { A2USHCPU-S1 } \\ & \text { A1SJ71AP21-S3 } \end{aligned}$ |  |
| MELSECNET/MINI-S3 master module | AJ71PT32-S3 | A1SJ71PT32-S3 | No monitor station function |
|  | AJ71T32-S3 | A1SJ71PT32-S3 |  |
| MELSEC-I/OLINK master module | AJ51T64 | A1SJ51T64 | No special restrictions |


| Large-sized A/QnA series models to be discontinued |  | Alternative small-sized AnS/Q2AS series models |  |
| :---: | :---: | :---: | :---: |
| Product name | Model name | Model name | Remarks (restrictions) |
| MELSECNET data link module | AJ71AP21 | A1SJ71AP21 | No special restrictions |
|  | AJ71AR21 | A1SJ71AR21 |  |
| MELSECNET/10 * data link module | AJ71LP21 | A1SJ71LP21 | No special restrictions |
|  | AJ71LP21G | None | No alternative model |
|  | AJ71LR21 | A1SJ71LR21 | No special restrictions |
|  | AJ71BR11 | A1SJ71BR11 |  |
|  | AJ71QLP21 | A1SJ71QLP21 |  |
|  | AJ71QLP21S | A1SJ71QLP21S |  |
|  | AJ71QLP21G | None | No alternative model |
|  | AJ71QLR21 | A1SJ71QLR21 | No special restrictions |
|  | AJ71QBR11 | A1SJ71QBR11 |  |
| B/NET interface module | AJ71B62-S3 | A1SJ71B62-S3 | No special restrictions |
| JEMANET(OPCN-1) interface module | AJ71J92-S3 | A1SJ71J92-S3 | No special restrictions |
| CC-Link master/local module | AJ61BT11 | A1SJ61BT11 | No special restrictions |
|  | AJ61QBT11 | A1SJ61QBT11 |  |

* This is not a model to be discontinued.


### 9.2 Network Module Comparison

### 9.2.1 Replacement of CPU module with link

(1) Empty slot (32 occupied points) required

The small-sized A series include no CPU module with link function. For this reason, the existing CPU module with link function must be replaced with a CPU + a link module so that extra 1 slot (32 occupied points) is required.

When large-sized
A CPU (with link) is used


## (2) Network parameter settings are required

Network parameter settings are required. Refer to Section 7.2.

### 9.2.2 MELSECNET/MINI-S3 master module comparison

(1) Comparison between AJ71PT32-S3 (AJ71T32-S3) and A1SJ71PT32-S3 (A1SJ71T32-S3)
(a) Performance specifications comparison

O : Compatible, $\Delta$ : Partial change required, $\times$ : Incompatible

| Specification | AJ71PT32-S3 <br> (AJ71T32-S3) | A1SJ71PT32-S3 (A1SJ71T32-S3) | Compati- bilty | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: |
| Max. number of link stations | 64 |  | $\bigcirc$ |  |
| Input | 512 points |  | $\bigcirc$ |  |
| Output | 512 points |  | $\bigcirc$ |  |
| I/O refresh time | 3.2 to 18 ms |  | $\bigcirc$ |  |
| Communication speed | 1.5 Mbps |  | $\bigcirc$ |  |
| Optical transmission level (Optical cable) | -12.5 to -11.6dB |  | O |  |
| Optical receive level (Optical cable) | -34.8 to -14.0dB |  | $\bigcirc$ |  |
| Optical wave length (Optical cable) | 660nm (Visible radiation) |  | $\bigcirc$ |  |
| Max. inter-station transmission distance | 50m (Optical cable)/100m (Twisted pair cable) |  | $\bigcirc$ |  |
| Number of occupied I/O points | I/O mode: 32 <br> Extension mode: 48 |  | $\bigcirc$ |  |
| Internal current consumption <br> (5VDC) | 0.35A |  | - |  |
| External dimensions | $250(\mathrm{H}) \times 37.5(\mathrm{~W}) \times 119(\mathrm{D}) \mathrm{mm}$ | $130(\mathrm{H}) \times 34.5(\mathrm{~W}) \times 107.6(\mathrm{D}) \mathrm{mm}$ | $\Delta$ | The dimensions are different. |
| Weight | 0.6kg |  | $\bigcirc$ |  |

(b) Function comparison

O : Compatible, $\Delta$ : Partial change required, $\times$ : Incompatible

| Function | AJ71PT32-S3 <br> (AJ71T32-S3) | A1SJ71PT32-S3 (A1SJ71T32-S3) | $\begin{array}{\|c\|} \text { Compati- } \\ \text { bility } \end{array}$ | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: |
| MINI standard protocol | Standard protocol of remote terminal module |  | $\bigcirc$ |  |
| Nonprocedural protocol | Communication protocol for AJ35PTF-R2 |  | $\bigcirc$ |  |
| Dedicated protocol | Communication protocol for AJ35PTF-R2 |  | $\bigcirc$ |  |
| Tool box protocol | Communication protocol for AJ35PT-OPB-■ - |  | $\bigcirc$ |  |
| E.C.MODE | Yes |  | $\bigcirc$ |  |
| Monitor station setting | Equipped with monitor station No. setting switch | No monitor station No. setting switch | $\Delta$ | Monitor the buffer memory address 10 to 41/110 to 141 by GX Developer. |

(c) Comparison of I/O signals for PLC CPUs

No special differences are identified.
(d) Buffer memory address comparison

No special differences are identified.

### 9.2.3 MELSEC-I/OLINK master module comparison

(1) Comparisons between AJ51T64 and A1SJ51T64
(a) Performance specifications comparison

| Item |  | Specification |  | $\begin{array}{\|c} \text { Comati- } \\ \text { bility } \end{array}$ | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | AJ51T64 | A1SJ51T64 |  |  |
| Max. number of control I/O points |  | 128 points (if the same numbers are used for X and Y ) |  | $\bigcirc$ |  |
| I/O refresh time |  | Approx. 5.4 ms (regardless of the number of points) |  | 0 |  |
| Communication cable |  | $0.75 \mathrm{~mm}^{2}$ or larger twisted pair cable $0.75 \mathrm{~mm}^{2}$ or larger cabtyre cable |  | O |  |
|  | Communication speed | 38400bps |  | 0 |  |
|  | Communication method | Register insertion method |  | $\bigcirc$ |  |
|  | Synchronization method | Combination of frame synchronization and bit synchronization methods |  | O |  |
|  | Error control system | Parity check |  | $\bigcirc$ |  |
|  | Transmission channel | Bus (T-branch possible, terminal resistors unnecessary) |  | $\bigcirc$ |  |
|  | Transmission distance | Overall distance: 200 m |  | $\bigcirc$ |  |
|  | Max. number of remote I/O modules | 16 stations per master module |  | 0 |  |
| Error (RUN) indication/output |  | Indication by LEDs <br> The PLC CPU detects errors by "blown fuse". External output with RUN A/RUN B |  | $\bigcirc$ |  |
| LED |  | Communication status and error station station No. display | Communication status display | $\Delta$ | Error station station No. is not displayed. |
| Setting switch |  | On module face | Inside module | $\Delta$ | Functions are the same. However, the position of the switches differs. |
| Number of occupied I/O points |  | 64 points (I/O assignment: 64 output points)* |  | O |  |
| External power supply voltage |  | 21.6 to 27.6 VDC (for the transmission channel) |  | O |  |
| External power supply current consumption |  | 90mA (TYP 24VDC) |  | O |  |
| Internal current consumption (5VDC) |  | 115 mA |  | 0 |  |
| External dimensions |  | $250(\mathrm{H}) \times 37.5(\mathrm{~W}) \times 119$ (D) mm | $130(\mathrm{H}) \times 34.5(\mathrm{~W}) \times 93.6(\mathrm{D}) \mathrm{mm}$ | $\Delta$ | The dimensions are different. |
| Weight |  | 0.35 kg | 0.3 kg | $\bigcirc$ |  |

* If only a few remote I/O modules are used, perform I/O assignment with a peripheral device to decrease the number of occupied I/O points to 16,32 , or 48.


## (b) Comparison of I/O signals for PLC CPUs

No special differences are identified.

## SPECIAL FUNCTION MODULE REPLACEMENT

10.1 List of Alternative Special Function Module Models

| A series models to be discontinued |  | Alternative AnS series models |  |
| :---: | :---: | :---: | :---: |
| Product | Model | Model | Remarks (restrictions) |
| Analog input module | A616AD | A1S68AD | 1) External wiring change: Required to use a different terminal block <br> 2) Change in number of modules: Required (2 modules necessary) <br> 3) Program changes: Change of I/O signals and buffer memory addresses <br> 4) Performance specifications: Change to $8 \mathrm{CH} /$ module and input signal (only positive current for I) <br> 5) Function specifications: No multiplexer function |
|  | A68AD | A1S68AD | 1) External wiring change: Required to use a different terminal block <br> 2) Change in number of modules: Not required <br> 3) Program changes: Change of I/O signals and buffer memory addresses <br> 4) Performance specifications: Change in I/O characteristics <br> 5) Function specifications: Change in A/D conversion enable/disable function settings |
|  | A68AD-S2 | A1S68AD | 1) External wiring change: Required to use a different terminal block <br> 2) Change in number of modules: Not required <br> 3) Program changes: Change of I/O signals and buffer memory addresses <br> 4) Performance specifications: Change in I/O characteristics <br> 5) Function specifications: Not required |
|  | A68ADN | A1S68AD | 1) External wiring change: Required to use a different terminal block <br> 2) Change in number of modules: Not required <br> 3) Program changes: Change of I/O signals and buffer memory addresses <br> 4) Performance specifications: Change in I/O characteristics and resolution <br> 5) Function specifications: Not required |
| Multiplexer module | A60MX | None | Consider using multiple A1S68AD modules. |
|  | A60MXRN | None | Consider using the A1S68AD and isolation signal converter. |
|  | A60MXR | None | Consider using the A1S68AD and isolation signal converter. |
|  | A60MXTN | None | Consider using multiple A1S68TD modules. |
|  | A60MXT | None | Consider using multiple A1S68TD modules. |


| A series models to be discontinued |  | Alternative AnS series models |  |
| :---: | :---: | :---: | :---: |
| Product | Model | Model | Remarks (restrictions) |
| Analog output module | A616DAI | A1S68DAI | 1) External wiring change: Required to use a different terminal block <br> 2) Change in number of modules: Required (2 modules necessary) <br> 3) Program changes: Change of I/O signals and buffer memory addresses <br> 4) Performance specifications: Change to $8 \mathrm{CH} /$ module and in input current range <br> 5) Function specifications: Change in relationships between D/A conversion disable channel and conversion time |
|  | A616DAV | A1S68DAV | 1) External wiring change: Required to use a different terminal block <br> 2) Change in number of modules: Required (2 modules necessary) <br> 3) Program changes: Change of I/O signals and buffer memory addresses <br> 4) Performance specifications: Change to $8 \mathrm{CH} /$ module and in resolution . accuracy <br> 5) Function specifications: Change in relationships between D/A conversion disable channel and conversion time |
|  | A62DA | A1S62DA | 1) External wiring change: Required to use a different terminal block <br> 2) Change in number of modules: Not required <br> 3) Program changes: Change of I/O signals and buffer memory addresses <br> 4) Performance specifications: Change in I/O characteristics and conversion time <br> 5) Function specifications: Not required |
|  | A62DA-S1 | A1S62DA | 1) External wiring change: Required to use a different terminal block <br> 2) Change in number of modules: Not required <br> 3) Program changes: Change of I/O signals and buffer memory addresses <br> 4) Performance specifications: Change in I/O characteristics and conversion time <br> 5) Function specifications: Not required |
|  | A68DAI-S1 | A1S68DAI | 1) External wiring change: Required to use a different terminal block <br> 2) Change in number of modules: Not required <br> 3) Program changes: Change of I/O signals and buffer memory addresses <br> 4) Performance specifications: Change in output current range and I/O characteristics, increase of current consumption <br> 5) Function specifications: Not required |
|  | A68DAV | A1S68DAV | 1) External wiring change: Required to use a different terminal block <br> 2) Change in number of modules: Not required <br> 3) Program changes: Change of I/O signals and buffer memory addresses <br> 4) Performance specifications: Change in output current range and I/O characteristics, increase of current consumption <br> 5) Function specifications: Not required |
| Temperature input module | A616TD | A1S68TD | 1) External wiring change: Required to use a different terminal block <br> 2) Change in number of modules: Required (2 modules necessary) <br> 3) Program changes: Change of I/O signals and buffer memory addresses <br> 4) Performance specifications: Change to $8 \mathrm{CH} /$ module, and in input temperature range and conversion accuracy <br> 5)Functionspecifications: Change in relationships between conversion disable channel and conversion time |
|  | A68RD3N | A1S62RD3N | 1) External wiring change: Required to use a different terminal block <br> 2) Change in number of modules: Required (4 modules necessary) <br> 3) Program changes: Changed <br> 4) Performance specifications: Change to $2 \mathrm{CH} /$ module <br> 5) Function specifications: Not required |
|  | A68RD4N | A1S62RD4N | 1) External wiring change: Required to use a different terminal block <br> 2) Change in number of modules: Required (4 modules necessary) <br> 3) Program changes: Changed <br> 4) Performance specifications: Change to $2 \mathrm{CH} /$ module <br> 5) Function specifications: Not required |
| High-speed counter module | AD61 | A1SD62 | 1) External wiring change: Required to use a different terminal block <br> 2) Change in number of modules: Not required <br> 3) Program changes: Change of buffer memory addresses <br> 4) Performance specifications: Upward-compatibility <br> 5) Function specifications: Upward-compatibility |
|  | AD61S1 | A1SD62 | 1) External wiring change: Required to use a different terminal block <br> 2) Change in number of modules: Not required <br> 3) Program changes: Change of buffer memory addresses <br> 4) Performance specifications: Upward-compatibility <br> 5) Function specifications: Upward-compatibility |
| Position detection module | A62LS-S5 | A1S62LS | 1) External wiring change: Require of terminal block to connector <br> 2) Change in number of modules: Required ( 2 modules $\rightarrow 1$ module) <br> 3) Program changes: Not changed. Note that the data setting must be made from a sequence program or the data setting module "VS-T62" manufactured by NSD corporation. <br> 4) Performance specifications: Not required <br> 5) Function specifications: Not required |
|  | A61LS | None | No alternative model |
|  | A63LS | None |  |


| A series models to be discontinued |  | Alternative AnS series models |  |
| :---: | :---: | :---: | :---: |
| Product | Model | Model | Remarks (restrictions) |
| Positioning module | AD70 | A1SD70 | 1) External wiring change: Required to use a different terminal block <br> 2) Change in number of modules: Required (1 module $\rightarrow 2$ modules) <br> 3) Program changes: Not changed <br> 4) Performance specifications: Not required <br> 5) Function specifications: Not required |
|  | AD72 | None | No alternative model |
|  | AD75M1 | A1SD75M1 | No special restrictions <br> However, since the connector for peripheral connection is different, A1SD75-C01HA cable is required. |
|  | AD75M2 | A1SD75M2 | No special restrictions <br> However, since the connector for peripheral connection is different, A1SD75-C01HA cable is required. |
|  | AD75M3 | A1SD75M3 | No special restrictions <br> However, since the connector for peripheral connection is different, A1SD75-C01HA cable is required. |
|  | AD75P1-S3 | A1SD75P1-S3 | No special restrictions <br> However, since the connector for peripheral connection is different, A1SD75-C01HA cable is required. |
|  | AD75P2-S3 | A1SD75P2-S3 | No special restrictions <br> However, since the connector for peripheral connection is different, A1SD75-C01HA cable is required. |
|  | AD75P3-S3 | A1SD75P3-S3 | No special restrictions <br> However, since the connector for peripheral connection is different, A1SD75-C01HA cable is required. |

### 10.2 Special Function Modules Comparison

### 10.2.1 Analog input modules comparison

## (1) Comparisons between A616AD and A1S68AD

## (a) Performance specifications comparison

O: Compatible, $\triangle$ : Partial change required, $\times$ : Incompatible

| Item | A616AD |  |  |  | A1S68AD |  | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Analog input | Voltage:-10 to 0 to +10 VDC (Input resistance: $1 \mathrm{M} \Omega$ ) <br> Current: -20 to 0 to +20mADC <br> (Input resistance: $250 \Omega$ ) |  |  |  | Voltage: - 10 to 0 to +10 VDC <br> (Input resistance: $1 \mathrm{M} \Omega$ ) <br> Current: 0 to +20 mA <br> (Input resistance: $250 \Omega$ ) |  | $\triangle$ | Negative current cannot be converted. |
| Digital output | 16-bit, signed binary (Data unit: 12 bits) (-48 to 4047, -2048 to +2047 ) <br> Setting is available for each channel. |  |  |  | 16-bit, signed binary |  | $\bigcirc$ |  |
| I/O characteristics, maximum resolution | Input <br> Voltage <br> (V) |  |  |  | I/O characteristics |  | $\triangle$ | Check the I/O conversion characteristics. |
|  |  | Analog input range | Maximum resolution | Digital output value | Analog input value | Digital output value |  |  |
|  |  |  |  |  | 0 to +10 V | 0 to +4000 |  |  |
|  |  | 0 to +10 | $\begin{gathered} 2.5 \mathrm{mV} \\ (1 / 4000) \end{gathered}$ | $\begin{gathered} 0 \text { to } 4000 \\ -2000 \text { to } 2000 \end{gathered}$ | -10 to +10 V | -2000 to +2000 |  |  |
|  |  | 0 to +5 | $\begin{gathered} 1.25 \mathrm{mV} \\ (1 / 4000) \\ \hline \end{gathered}$ |  | 0 to 5 V or 0 to 20 mA | 0 to +4000 |  |  |
|  |  | +1 to +5 | $\begin{gathered} \hline 1.0 \mathrm{mV} \\ (1 / 4000) \\ \hline \end{gathered}$ |  |  |  |  |  |
|  |  | -10 to +10 | $\begin{gathered} 5.0 \mathrm{mV} \\ (1 / 4000) \end{gathered}$ |  | Maximum resolution |  |  |  |
|  |  | -5 to +5 | $\stackrel{2.5 \mathrm{mV}}{(1 / 4000)}$ |  | Analog input value |  |  |  |
|  |  | 0 to +20 | $\begin{gathered} 10 \mu \mathrm{~A} \\ (1 / 2000) \end{gathered}$ | $\begin{gathered} 0 \text { to } 2000 \\ -2000 \text { to } 0 \end{gathered}$ | -10 to +10V | 5 mV |  |  |
|  |  | 0 to +20 | $\begin{gathered} 5 \mu \mathrm{~A} \\ (1 / 4000) \end{gathered}$ | 0 to 4000 | 1 to 5 V | 1 mV |  |  |
|  | $\begin{gathered} \text { Current } \\ \text { (mA) } \end{gathered}$ | +4 to +20 | $\begin{gathered} 4 \mu \mathrm{~A} \\ (1 / 4000) \\ \hline \end{gathered}$ | -2000 to 2000 | 0 to 20 mA | $5 \mu \mathrm{~A}$ |  |  |
|  |  | -20 to +20 | $\begin{gathered} 20 \mu \mathrm{~A} \\ (112000) \end{gathered}$ | 1000 to 3000 <br> -1000 to 1000 |  |  |  |  |
|  |  | -20 to +20 | $\begin{gathered} 10 \mu \mathrm{~A} \\ (1 / 4000) \\ \hline \end{gathered}$ | $\begin{gathered} 0 \text { to } 4000 \\ -2000 \text { to } 2000 \\ \hline \end{gathered}$ |  |  |  |  |
| Overall accuracy (\%) (Accuracy to full-scale) | When using <br> 0 to 10 V , <br> -5 V to 5 <br> 0 to 5 V , <br> 0 to 20 m <br> When using in A60MXRN, th <br> 0.3\% (Digital | 616AD only <br> 0 to 10V <br> 20 to 20 mA <br> 5 V <br> 4 to 20 mA <br> mbination <br> ccuracy of <br> put value $\pm$ |  | Range: $\pm 0.3 \%$ <br> Digital value $\pm 12$ ) <br> Range: $\pm 0.6 \%$ <br> Digital value $\pm 24$ ) <br> MX, A60MXR, or ge of A616AD is $\pm$ | Within <br> (Digital output | $\begin{aligned} & 1 \% \\ & \text { alue } \pm 40) \end{aligned}$ | $\times$ | Overall accuracy differs. |
| Maximum conversion speed (ms/channel) | When using A When using w When using A <br> When using A | 6AD only A60MX MXR <br> MXRN | $\begin{aligned} & 1 \\ & 1 \\ & 1 \text { (Sam } \\ & 7.0 \text { (Di } \\ & 1 \text { (Sam } \\ & 7.0 \text { (Dii } \end{aligned}$ | pling processing ) ect access processing) pling processing ) ect access processing) | $0.5 \mathrm{~ms} / \mathrm{ch}$ (The maximum conv $1 \mathrm{~ms} /$ channel on averaging processing only one ch | nnel rsion speed is ll channels if is set even for annel.) | $\bigcirc$ |  |
| Absolute maximum input |  | Voltage Curren | $\begin{aligned} & \pm 15 \mathrm{~V} \\ & \pm 30 \mathrm{~mA} \end{aligned}$ |  | Voltage: $\pm$ Current: $\pm$ | $\begin{aligned} & 5 \mathrm{~V} \\ & \hline \mathrm{~mA} \end{aligned}$ | $\bigcirc$ |  |
| Analog input points |  | 16 chan | els/mod |  | 8 channels/ | module | $\times$ | As the number of channels is reduced, the number of modules installed may increase. In addition, the number of channels cannot be increased by connecting a multiplexer module. |

O : Compatible, $\Delta$ : Partial change required, $x$ : Incompatible

| Item | A616AD | A1S68AD | Compati- <br> bility | Precautions for <br> replacement |
| :--- | :---: | :---: | :---: | :---: |
| Isolation <br> method | Between the input terminals and PLC <br> : photocoupler isolation <br> Between channels : non-isolated <br> (1M $\Omega$ resistor isolation) | Between the input terminals and <br> PLC power <br> $:$ photocoupler isolation | O |  |

## (b) Function comparison

$\bigcirc$ : Compatible, $\Delta$ : Partial change required, $\times$ : Incompatible, - : Additional function

| Item | A616AD | A1S68AD | Compati- Precautions for |
| :--- | :--- | :--- | :--- | :--- |
| replify |  |  |  |

(c) Comparison of I/O signals for PLC CPU

Modifying sequence programs is required as the I/O signals differ.
For details of the I/O signals and sequence programs, refer to the user's manual.

| A616AD |  |  |  | A1S68AD |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Device NO. | Signal name | Device NO. | Signal name | Device NO. | Signal name | Device NO. | Signal name |
| X0 | WDT error (A616AD detection) | YO | Not used | X0 | WDT error (A1S68AD detection) | YO | Not used |
| X1 | A/D conversion READY | Y1 |  | X1 | A/D conversion READY | Y1 |  |
| X2 | Error | Y2 |  | X2 | Error | Y2 |  |
| X3 | Not used | Y3 |  | X3 | ( | Y3 |  |
| X4 |  | Y4 |  | X4 |  | Y4 |  |
| X5 |  | Y5 |  | X5 |  | Y5 |  |
| X6 |  | Y6 |  | X6 |  | Y6 |  |
| X7 |  | Y7 |  | X7 |  | Y7 |  |
| X8 |  | Y8 |  | X8 |  | Y8 |  |
| X9 |  | Y9 |  | X9 |  | Y9 |  |
| XA |  | YA |  | XA |  | YA |  |
| XB |  | YB |  | XB |  | YB |  |
| XC |  | YC |  | XC |  | YC |  |
| XD |  | YD |  | XD |  | YD |  |
| XE |  | YE |  | XE |  | YE |  |
| XF |  | YF |  | XF |  | YF |  |
| X10 |  | Y10 |  | X10 |  | Y10 |  |
| X11 |  | Y11 |  | X11 |  | Y11 |  |
| X12 |  | Y12 |  | X12 |  | Y12 | Error reset |
| X13 |  | Y13 |  | X13 |  | Y13 | Not used |
| X14 |  | Y14 |  | X14 |  | Y14 |  |
| X15 |  | Y15 |  | X15 |  | Y15 |  |
| X16 |  | Y16 |  | X16 |  | Y16 |  |
| X17 |  | Y17 |  | X17 |  | Y17 |  |
| X18 |  | Y18 | Direct access request signal | X18 |  | Y18 |  |
| X19 |  | Y19 | Not used | X19 |  | Y19 |  |
| X1A |  | Y1A |  | X1A |  | Y1A |  |
| X1B |  | Y1B |  | X1B |  | Y1B |  |
| X1C |  | Y1C |  | X1C |  | Y1C |  |
| X1D |  | Y1D |  | X1D |  | Y1D |  |
| X1E |  | Y1E |  | X1E |  | Y1E |  |
| X1F |  | Y1F |  | X1F |  | Y1F |  |

(d) Buffer memory address comparison

Modifying sequence programs is required as the assignment of buffer memory differs. For details of the buffer memory and sequence programs, refer to the user's manual.

(2) Comparisons between A68AD and A1S68AD
(a) Performance specifications comparison

O: Compatible, $\triangle$ : Partial change required, $\times$ : Incompatible

| Item | A68AD |  | A1S68AD |  | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Analog input | Voltage: -10 to 0 to +10VDC <br> (Input resistance:hardware version K or later: $1 \mathrm{M} \Omega$, hardware version J or earlier: $30 \mathrm{~K} \Omega$ ) <br> Current: +4 to +20 mADC (Input resistance: $250 \Omega$ ) <br> *Usable current input: -20 to 0 to +20 mA |  | $\begin{aligned} & \text { Voltage: }-10 \text { to } 0 \text { to }+10 \mathrm{VDC} \\ & \text { (Input resistance: } 1 \mathrm{M} \Omega \text { ) } \\ & \text { Current: } 0 \text { to }+20 \mathrm{~mA} \\ & \text { (Input resistance: } 250 \Omega \text { ) } \end{aligned}$ |  | $\triangle$ | Negative current cannot be converted. |
| Digital output | $\begin{array}{r} \text { ACPU } 16-\mathrm{b} \\ \quad(-204 \\ \text { K2ACPU sig } \end{array}$ | ned binary 2047) 16-bit binary ) | 16-bit, signed binary |  | $\bigcirc$ |  |
| I/O <br> characteristi CS | Analog input | Digital output |  |  | $\triangle$ | Check the I/O conversion characteristics. |
|  | $+10 \mathrm{~V}$ | +2000 | Analog input | Digital output |  |  |
|  | +5V or +20 mA | +1000 | 0 to +10 V | 0 to +4000 |  |  |
|  | OV or +4 mA | $\pm 0$ | -10 to 10V | -2000 to +2000 |  |  |
|  | -5V or-12mA | -1000 | Oto 5 V or 0 to 20 mA | 0 to +4000 |  |  |
|  | -10V | -2000 | 110 5Vor 41020 mA | $010+4000$ |  |  |
| Maximum resolution | Voltage: 5 mV (1/2000) |  | Analognet | Diota dout | $\bigcirc$ |  |
|  |  |  | 0 to +10 V | 2.5 mV |  |  |
|  | Current: $20 \mu \mathrm{~A}(1 / 1000)$ |  | -10 to 10 V | 5 mV |  |  |
|  |  |  | 0 to 5 V | 1.25 mV |  |  |
|  |  |  | 1 to 5 V | 1 mV |  |  |
|  |  |  | 0 to 20 mA | $5 \mu \mathrm{~A}$ |  |  |
|  |  |  | 4 to 20 mA | $4 \mu \mathrm{~A}$ |  |  |
| Overall accuracy | $\pm 1 \%( \pm 20)$ <br> (Accuracy in respect to maximum digital output value (+2000)) <br> (The same (+2000) for voltage input and current input.) |  | Within $\pm 1 \%$ <br> (Digital output value $\pm 40$ ) |  | $\bigcirc$ |  |
| Maximum conversion speed | Max. 2.5ms/channel |  | $0.5 \mathrm{~ms} /$ channel <br> (The maximum conversion speed is $1 \mathrm{~ms} /$ channel on all channels if averaging processing is set even for only one channel.) |  | $\bigcirc$ |  |
| Absolute maximum input | Voltage: $\pm 15 \mathrm{~V}$ <br> Current: $\pm 30 \mathrm{~mA}$ |  | Voltage: $\pm 35 \mathrm{~V}$ <br> Current: $\pm 30 \mathrm{~mA}$ |  | $\bigcirc$ |  |
| Analog input points | 8 channels/module |  | 8 channels/module |  | $\bigcirc$ |  |
| Isolation method | Between the input terminals and PLC power: photocoupler isolation Between channels: non-isolated |  | Between the input terminals and PLC power: photocoupler isolation <br> Between channels : non-isolated |  | $\bigcirc$ |  |
| Occupied I/ <br> O points | 32 points(I/O assignment: special 32 points) |  | 32 points (I/O assignment: special 32 points) |  | $\bigcirc$ |  |
| Connected terminal | 38-point terminal block |  | 20-point terminal block |  | $\times$ | External wiring must be changed. |
| Applicable wire size | 0.75 to $2 \mathrm{~mm}^{2}$(Applicable tightening torque $7 \mathrm{~kg} \cdot \mathrm{~cm}$ ) |  | 0.75 to $1.5 \mathrm{~mm}^{2}$ |  | $\triangle$ |  |
| Applicable solderless terminal | $\begin{gathered} \text { V1.25-3, V1.25-YS3A } \\ \text { V2-S3, V2-YS3A } \end{gathered}$ |  | $\begin{gathered} \text { R1.25-3, 1.25-YS3, } \\ \text { RAV1.25-3, V1.25-YS3A } \end{gathered}$ |  | $\triangle$ |  |
| Internal current consumption (5VDC) | Hardware version K or later: 0.39A, <br> Hardware version J or earlier: 0.9A |  | 0.4 A |  | $\triangle$ | Recalculation of internal current consumption [5VDC] is required. |
| External dimensions | $250(\mathrm{H}) \times 37.5(\mathrm{~W}) \times 131(\mathrm{D}) \mathrm{mm}$ |  | $130(\mathrm{H}) \times 34.5(\mathrm{~W}) \times 93.6(\mathrm{D}) \mathrm{mm}$ |  | $\triangle$ | The dimensions are different. |
| Weight | Hardware version K or later: 0.3 kg , Hardware version J or earlier: 0.6 kg |  | 0.27 kg |  | $\bigcirc$ |  |

## (b) Function comparison

O: Compatible, $\Delta$ : Partial change required, $x$ : Incompatible, 一: Additional function

| Item | A68AD | A1S68AD | Compati- bility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: |
| Sampling processing | Converts analog input values into digital output values successively, and stores the digital output value in the buffer memory. The length of time to store the sampled digital output value in the buffer memory differs depending on the number of channels used. <br> $($ Processing time $)=($ Number of channels used $)$ $\times 2.5$ ( $\mathrm{ms} /$ channel) <br> * $2.5(\mathrm{~ms})$ is maximum conversion speed. | Converts an analog input value to a digital value for each channel at any time, and stores it in the buffer memory as a digital output value. The length of time to store the sampled digital output value in the buffer memory differs depending on the number of the $A / D$ conversion enabled channels. <br> $($ Processing time $)=($ Number of A/D conversion enabled channels) $\times 0.5(\mathrm{~ms})$ <br> * $0.5(\mathrm{~ms})$ is maximum conversion speed. | $\bigcirc$ |  |
| Averaging processing | Executes the A/D conversion for the channel specified for averaging process by a PLC CPU for the set number of times or set time. The total values except the maximum and minimum values are averaged and stored in the buffer memory. However, when the processing number of times is no more than two, the sampling process is executed. <br> The applicable setting range is shown below: <br> Count averaging : 1 to 4000 <br> Time averaging: 20 to 10000 ms | Executes the A/D conversion for the channel specified for averaging process by a PLC CPU for the set number of times or set time. The total values except the maximum and minimum values are averaged and stored in the buffer memory. However, when the processing number of times is no more than two, the sampling process is executed. <br> The applicable setting range is shown below: <br> Count averaging : 1 to 20000 <br> Time averaging : 4 to 10000 ms | $\bigcirc$ |  |
| A/D conversion enable/ disable setting | Set the number of channels for which conversion is enabled to the buffer memory address 0 . | Allows the A/D conversion enable/disable setting for each channel by writing "1" (enable) or "0" (disable) to the buffer memory address 0 . By disabling the conversion for the channels that are not used, the sampling time cycle can be shortened. (Default: All channels are set to "enable".) | $\triangle$ | Settable for each channel |
| Input range setting | - | Allows the input range setting for each channel, and change of the I/O conversion characteristics. | - | - |
| Offset/gain setting | The I/O conversion characteristics can be changed. | - | $\times$ | No offset/gain setting function |

(c) Comparison I/O signals for PLC CPU

Modifying sequence programs is required as the I/O signals differ.
For details of the I/O signals and sequence programs, refer to the user's manual.


## (d) Buffer memory address comparison

Modifying sequence programs is required as the assignment of buffer memory differs.
For details of the buffer memory and sequence programs, refer to the user's manual.

| Address | A68AD |  |  | Address | A1S68AD |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | Name | CPU | Read/Write |  | Name | Default value | Read/Write |
| 0 | Number of channels | Common to ACPU and K2ACPU | R/W | 0 | A/D conversion enable/ disable specification | 00 FFH <br> (All channels are set to "enable".) | R/W |
| 1 | Average processing specification |  |  | 1 | Writing data error code | 0 (All channels) | R |
| 2 | CH1 Average time, count |  |  | 2 | Average processing specification | 0 (Sampling processing specified for all channels) | R/W |
| 3 | CH2 Average time, count |  |  | 3 | Not used | - | - |
| 4 | CH3 Average time, count |  |  | 4 |  |  |  |
| 5 | CH4 Average time, count |  |  | 5 |  |  |  |
| 6 | CH5 Average time, count |  |  | 6 |  |  |  |
| 7 | CH6 Average time, count |  |  | 7 |  |  |  |
| 8 | CH7 Average time, count |  |  | 8 |  |  |  |
| 9 | CH8 Average time, count |  |  | 9 |  |  |  |
| 10 | CH1 Digital output value | ACPU | R | 10 | CH1 Average time, count | 0 | R/W |
| 11 | CH2 Digital output value |  |  | 11 | CH2 Average time, count | 0 |  |
| 12 | CH3 Digital output value |  |  | 12 | CH3 Average time, count | 0 |  |
| 13 | CH4 Digital output value |  |  | 13 | CH4 Average time, count | 0 |  |
| 14 | CH5 Digital output value |  |  | 14 | CH5 Average time, count | 0 |  |
| 15 | CH6 Digital output value |  |  | 15 | CH6 Average time, count | 0 |  |
| 16 | CH7 Digital output value |  |  | 16 | CH7 Average time, count | 0 |  |
| 17 | CH8 Digital output value |  |  | 17 | CH8 Average time, count | 0 |  |
| 18 | CH1 Digital output value | K2ACPU |  | 18 | Not used | - | - |
| 19 | CH1 Positive or negative sign |  |  | 19 |  |  |  |
| 20 | CH 2 Digital output value |  |  | 20 | CH1 Digital output value | 0 | R |
| 21 | CH2 Positive or negative sign |  |  | 21 | CH2 Digital output value | 0 |  |
| 22 | CH3 Digital output value |  |  | 22 | CH3 Digital output value | 0 |  |
| 23 | CH3 Positive or negative sign |  |  | 23 | CH4 Digital output value | 0 |  |
| 24 | CH4 Digital output value |  |  | 24 | CH5 Digital output value | 0 |  |
| 25 | CH4 Positive or negative sign |  |  | 25 | CH6 Digital output value | 0 |  |
| 26 | CH5 Digital output value |  |  | 26 | CH 7 Digital output value | 0 |  |
| 27 | CH5 Positive or negative sign |  |  | 27 | CH8 Digital output value | 0 |  |
| 28 | CH6 Digital output value |  |  | 28 | A/D conversion completed | 0 | R/W |
| 29 | CH6 Positive or negative sign |  |  | 29 | Not used | - | - |
| 30 | CH7 Digital output value |  |  |  |  |  |  |
| 31 | CH7 Positive or negative sign |  |  |  |  |  |  |
| 32 | CH8 Digital output value |  |  |  |  |  |  |
| 33 | CH8 Positive or negative sign |  |  |  |  |  |  |
| 34 | Writing data error code | Common to ACPU and K2ACPU | R/W |  |  |  |  |

## (3) Comparisons between A68AD-S2 and A1S68AD

(a) Performance specifications comparison

O: Compatible, $\triangle$ : Partial change required, $\times$ : Incompatible

| Item | A68AD-S2 |  | A1S68AD |  | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Analog input | Voltage: - 10 to 0 to +10 VDC <br> (Input resistance:Hardware version K or later: $1 \mathrm{M} \Omega$, hardware version J or earlier: $30 \mathrm{k} \Omega$ ) <br> Current: +4 to +20 mADC (Input resistance: $250 \Omega$ ) <br> *Usable current input: -20 to 0 to +20 mA |  | ```Voltage: -10 to 0 to +10VDC (Input resistance: 1M \Omega ) Current: 0 to +20mA (Input resistance: 250 \Omega )``` |  | $\triangle$ | Negative current cannot be converted. |
| Digital output | ACPU $16-$ <br>  $(-2$ <br> K2ACPU sign <br>  $( \pm$ | ned binary +2047) <br> it binary | 16-bit, signed binary |  | $\bigcirc$ |  |
| I/O characteristi CS | Analog input | Digital output |  |  | $\triangle$ | Check the I/O conversion characteristics. |
|  | +10V | +2000 | Analog input | Digital output |  |  |
|  | +5 V or +20 mA | +1000 | 0 to +10 V | 0 to +4000 |  |  |
|  | OV or +4 mA | $\pm 0$ | -10 to 10 V | -2000 to +2000 |  |  |
|  | -5V or-20mA | -1000 | 0 to 5 V or 0 to 20 mA | 0 to +4000 |  |  |
|  | -10V | -2000 | 1 to 5 V or 4 to 20 mA | 0 to +4000 |  |  |
| Maximum resolution | Voltage: 5 mV (1/2000) |  | Analog input | Digital output | $\bigcirc$ |  |
|  |  |  | 0 to +10 V | 2.5 mV |  |  |
|  | Current: $20 \mu \mathrm{~A}(1 / 1000)$ |  | -10 to 10 V | 5 mV |  |  |
|  |  |  | 0 to +5 V | 1.25 mV |  |  |
|  |  |  | 1 to 5 V | 1 mV |  |  |
|  |  |  | 0 to 20 mA | $5 \mu \mathrm{~A}$ |  |  |
|  |  |  | 4 to 20 mA | $4 \mu \mathrm{~A}$ |  |  |
| Overall accuracy | $\pm 1 \%( \pm 20)$ <br> (Accuracy in respect to maximum digital output value (+2000)) <br> (The same (+2000) for voltage input and current input.) |  | Within $\pm 1 \%$ <br> (Digital output value $\pm 40$ ) |  | $\bigcirc$ |  |
| Maximum conversion speed | Max. 2.5ms/channel |  | $0.5 \mathrm{~ms} /$ channel <br> (The maximum conversion speed is $1 \mathrm{~ms} /$ channel on all channels if averaging processing is set even for only one channel.) |  | $\bigcirc$ |  |
| Absolute maximum input | Voltage: $\pm 15 \mathrm{~V}$ <br> Current: $\pm 30 \mathrm{~mA}$ |  | $\begin{aligned} & \text { Voltage: } \pm 35 \mathrm{~V} \\ & \text { Current: } \pm 30 \mathrm{~mA} \end{aligned}$ |  | $\bigcirc$ |  |
| Analog input points | 8 channels/module |  | 8 channels/module |  | $\bigcirc$ |  |
| Isolation method | Between the input terminals and PLC power: photocoupler isolation Between channels: non-isolated |  | Between the input terminals and PLC power: photocoupler isolation <br> Between channels: non-isolated |  | $\bigcirc$ |  |
| Occupied I/ O points | 32 points(I/O assignment: special 32 points) |  | 32 points <br> (I/O assignment: special 32 points) |  | $\bigcirc$ |  |
| Connected terminal | 38-point terminal block |  | 20-point terminal block |  | $\times$ |  |
| Applicable wire size | 0.75 to $2 \mathrm{~mm}^{2}$(Applicable tightening torque $7 \mathrm{~kg} \cdot \mathrm{~cm}$ ) |  | 0.75 to $1.5 \mathrm{~mm}^{2}$ |  | $\Delta$ | External wiring must be |
| Applicable solderless terminal | $\begin{gathered} \text { V1.25-3, V1.25-YS3A, } \\ \text { V2-S3, V2-YS3A } \end{gathered}$ |  | $\begin{gathered} \text { R1.25-3, 1.25-YS3, } \\ \text { RAV1.25-3, V1.25-YS3A } \end{gathered}$ |  | $\triangle$ | changed. |
| Internal current consumption (5VDC) | Hardware version K or later: 0.39A, Hardware version J or earlier: 0.9A |  | 0.4A |  | $\triangle$ | Recalculation of internal current consumption [5VDC] is required. |
| External dimensions | $250(\mathrm{H}) \times 37.5(\mathrm{~W}) \times 131$ (D) mm |  | $130(\mathrm{H}) \times 34.5(\mathrm{~W}) \times 93.6(\mathrm{D}) \mathrm{mm}$ |  | $\triangle$ | The dimensions are different. |
| Weight | Hardware version K or later: 0.3 kg , Hardware version J or earlier: 0.6 kg |  | 0.27 kg |  | $\bigcirc$ |  |

## (b) Function comparison

O: Compatible, $\triangle$ : Partial change required, $\times$ : Incompatible, —: Additional function

| Item | A68AD-S2 | A1S68AD | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: |
| Sampling processing | Converts analog input values into digital output values successively, and stores the digital output value in the buffer memory. <br> The length of time to store the sampled digital output value in the buffer memory differs depending on the number of channels used. <br> $($ Processing time $)=($ Number of channels used $)$ $\times 2.5$ ( $\mathrm{ms} /$ channel) <br> * $2.5(\mathrm{~ms})$ is maximum conversion speed. | Converts an analog input value to a digital value for each channel at any time, and stores it in the buffer memory as a digital output value. <br> The length of time to store the sampled digital output value in the buffer memory differs depending on the number of the A/D conversion enabled channels. <br> $($ Processing time $)=($ Number of A/D conversion enabled channels) $\times 0.5(\mathrm{~ms})$ <br> * $0.5(\mathrm{~ms})$ is maximum conversion speed. | $\bigcirc$ |  |
| Averaging processing | Executes the A/D conversion for the channel specified for averaging process by a PLC CPU for a set number of times or set time. The total values except the maximum and minimum values are averaged and stored in the buffer memory. However, when the processing number of times is no more than two, the sampling process is executed. <br> The applicable setting range is shown below: <br> Count averaging: 1 to 4000 <br> Time averaging : 20 to 10000 ms | Executes the A/D conversion for the channel specified for averaging process by a PLC CPU for the set number of times or set time. The total values except the maximum and minimum values are averaged and stored in the buffer memory. However, when the processing number of times is no more than two, the sampling process is executed. <br> The applicable setting range is shown below: <br> Count averaging: 1 to 20000 <br> Time averaging: 4 to 10000 ms | $\bigcirc$ |  |
| A/D conversion enable/disable setting | Set the number of channels for which the $A / D$ conversion is enabled to the buffer memory address 0 . The channel can be specified for each channel. (Used channel specification) | Allows the A/D conversion enable/disable setting for each channel by writing "1" (enable) or "0" (disable) to the buffer memory address 0 . <br> By disabling the conversion for the channels that are not used, the sampling time cycle can be shortened. <br> (Default: All channels are set to "enable".) | $\bigcirc$ |  |
| Input range setting | - | Allows input range setting for each channel, and change of the I/O conversion characteristics. | - |  |
| Offset/gain setting | The I/O conversion characteristics can be changed. | - | $\times$ | No offset/ gain setting function |

(c) Comparison of I/O signals for PLC CPU

Modifying sequence programs is required as the I/O signals differ.
For details of the I/O signals and sequence programs, refer to the user's manual.

| A68AD-S2 |  |  |  | A1S68AD |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Device NO. | Signal name | Device NO. | Signal name | Device NO. | Signal name | Device NO. | Signal name |
| X0 | WDT error (A68AD-S2 detection) | YO | Not used | X0 | WDT error (A1S68AD detection) | Y0 | Not used |
| X1 | A/D conversion READY | Y1 |  | X1 | A/D conversion READY | Y1 |  |
| X2 | Not used | Y2 |  | X2 | Error | Y2 |  |
| X3 |  | Y3 |  | X3 | Not used | Y3 |  |
| X4 |  | Y4 |  | X4 |  | Y4 |  |
| X5 |  | Y5 |  | X5 |  | Y5 |  |
| X6 |  | Y6 |  | X6 |  | Y6 |  |
| X7 |  | Y7 |  | X7 |  | Y7 |  |
| X8 |  | Y8 |  | X8 |  | Y8 |  |
| X9 |  | Y9 |  | X9 |  | Y9 |  |
| XA |  | YA |  | XA |  | YA |  |
| XB |  | YB |  | XB |  | YB |  |
| XC |  | YC |  | XC |  | YC |  |
| XD |  | YD |  | XD |  | YD |  |
| XE |  | YE |  | XE |  | YE |  |
| XF |  | YF |  | XF |  | YF |  |
| X10 |  | Y10 |  | X10 |  | Y10 |  |
| X11 |  | Y11 |  | X11 |  | Y11 |  |
| X12 |  | Y12 |  | X12 |  | Y12 | Error reset |
| X13 |  | Y13 |  | X13 |  | Y13 |  |
| X14 |  | Y14 |  | X14 |  | Y14 |  |
| X15 |  | Y15 |  | X15 |  | Y15 |  |
| X16 |  | Y16 |  | X16 |  | Y16 |  |
| X17 |  | Y17 |  | X17 |  | Y17 |  |
| X18 |  | Y18 |  | X18 |  | Y18 |  |
| X19 |  | Y19 |  | X19 |  | Y19 | Not used |
| X1A |  | Y1A |  | X1A |  | Y1A |  |
| X1B |  | Y1B |  | X1B |  | Y1B |  |
| X1C |  | Y1C |  | X1C |  | Y1C |  |
| X1D |  | Y1D |  | X1D |  | Y1D |  |
| X1E |  | Y1E |  | X1E |  | Y1E |  |
| X1F |  | Y1F |  | X1F |  | Y1F |  |

## (d) Buffer memory address comparison

Modifying sequence programs is required as the assignment of buffer memory differs.
For details of the buffer memory and sequence programs, refer to the user's manual.

| Address | A68AD-S2 |  |  | Address | A1S68AD |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | Name | CPU | Read/Write |  | Name | Default value | Read/Write |
| 0 | Used channel specification | Common to ACPU and K2ACPU | R/W | 0 | A/D conversion enable/ disable specification | 00FFH <br> (All channels are set to "enable".) | R/W |
| 1 | Average processing specification |  |  | 1 | Writing data error code | 0 (All channels) | R |
| 2 | CH 1 Average time, count |  |  | 2 | Average processing specification | 0 (Sampling processing specified for all channels) | R/W |
| 3 | CH2 Average time, count |  |  | 3 | Not used | - | - |
| 4 | CH3 Average time, count |  |  | 4 |  |  |  |
| 5 | CH4 Average time, count |  |  | 5 |  |  |  |
| 6 | CH5 Average time, count |  |  | 6 |  |  |  |
| 7 | CH6 Average time, count |  |  | 7 |  |  |  |
| 8 | CH7 Average time, count |  |  | 8 |  |  |  |
| 9 | CH8 Average time, count |  |  | 9 |  |  |  |
| 10 | CH 1 Digital output value | ACPU | R | 10 | CH1 Average time, count | 0 | R/W |
| 11 | CH2 Digital output value |  |  | 11 | CH2 Average time, count | 0 |  |
| 12 | CH3 Digital output value |  |  | 12 | CH3 Average time, count | 0 |  |
| 13 | CH4 Digital output value |  |  | 13 | CH4 Average time, count | 0 |  |
| 14 | CH5 Digital output value |  |  | 14 | CH5 Average time, count | 0 |  |
| 15 | CH6 Digital output value |  |  | 15 | CH6 Average time, count | 0 |  |
| 16 | CH7 Digital output value |  |  | 16 | CH7 Average time, count | 0 |  |
| 17 | CH8 Digital output value |  |  | 17 | CH8 Average time, count | 0 |  |
| 18 | CH1 Digital output value | K2ACPU |  | 18 | Not used | - | - |
| 19 | CH1 Positive or negative sign |  |  | 19 |  |  |  |
| 20 | CH2 Digital output value |  |  | 20 | CH1 Digital output value | 0 | R |
| 21 | CH2 Positive or negative sign |  |  | 21 | CH2 Digital output value | 0 |  |
| 22 | CH3 Digital output value |  |  | 22 | CH3 Digital output value | 0 |  |
| 23 | CH3 Positive or negative sign |  |  | 23 | CH4 Digital output value | 0 |  |
| 24 | CH4 Digital output value |  |  | 24 | CH5 Digital output value | 0 |  |
| 25 | CH4 Positive or negative sign |  |  | 25 | CH6 Digital output value | 0 |  |
| 26 | CH5 Digital output value |  |  | 26 | CH7 Digital output value | 0 |  |
| 27 | CH5 Positive or negative sign |  |  | 27 | CH8 Digital output value | 0 |  |
| 28 | CH6 Digital output value |  |  | 28 | A/D conversion completed | 0 | R/W |
| 29 | CH6 Positive or negative sign |  |  | 29 | Not used | - | - |
| 30 | CH7 Digital output value |  |  |  |  |  |  |
| 31 | CH7 Positive or negative sign |  |  |  |  |  |  |
| 32 | CH8 Digital output value |  |  |  |  |  |  |
| 33 | CH8 Positive or negative sign |  |  |  |  |  |  |
| 34 | Writing data error code | Common to ACPU and K2ACPU | R/W |  |  |  |  |
| 35 | A/D conversion completed |  | R |  |  |  |  |

(4) Comparisons between A68ADN and A1S68AD
(a) Performance specifications comparison

O: Compatible, $\triangle$ : Partial change required, $\times$ : Incompatible


O : Compatible, $\triangle$ : Partial change required, $\times$ : Incompatible

| Item | A68ADN | A1S68AD | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: |
| External dimensions | $250(\mathrm{H}) \times 37.5(\mathrm{~W}) \times 131$ (D) mm | 130(H)×34.5(W) $\times 93.6$ (D)mm | $\triangle$ | The dimensions are different. |
| Weight | 0.51 kg | 0.27 kg | $\bigcirc$ |  |

## (b) Function comparison

O: Compatible, $\Delta$ : Partial change required, $x$ : Incompatible, 一: Additional function

| Item | A68ADN | Compati- <br> bility | Precautions for <br> replacement |
| :--- | :--- | :--- | :--- | :--- |
| A/D conversion <br> enable/disable <br> setting | Sets whether to enable or disable the A/D <br> conversion for each channel. By disabling the <br> conversion for the channels that are not used, the <br> sampling time can be shortened. <br> (Default: All channels are set to "enable".) | Allows the A/D conversion enable/disable <br> setting for each channel by writing "1" (enable) <br> or "0" (disable) to the buffer memory address 0. <br> By disabling the conversion for the channels <br> that are not used, the sampling time can be <br> shortened. <br> (Default: All channels are set to "enable".) | O |

## (c) Comparison of I/O signals for PLC CPU

Modifying sequence programs is required as the I/O signals differ.
For details of the I/O signals and sequence programs, refer to the user's manual.

| A68ADN |  |  |  | A1S68AD |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Device NO. | Signal name | Device NO. | Signal name | Device NO. | Signal name | Device NO. | Signal name |
| X0 | WDT error (A68ADN detection) | YO | Not used | X0 | WDT error (A1S68AD detection) | Y0 | Not used |
| X1 | A/D conversion READY | Y1 |  | X1 | A/D conversion READY | Y1 |  |
| X2 | Error | Y2 |  | X2 | Error | Y2 |  |
| X3 | Not used | Y3 |  | X3 | Not used | Y3 |  |
| X4 |  | Y4 |  | X4 |  | Y4 |  |
| X5 |  | Y5 |  | X5 |  | Y5 |  |
| X6 |  | Y6 |  | X6 |  | Y6 |  |
| X7 |  | Y7 |  | X7 |  | Y7 |  |
| X8 |  | Y8 |  | X8 |  | Y8 |  |
| X9 |  | Y9 |  | X9 |  | Y9 |  |
| XA |  | YA |  | XA |  | YA |  |
| XB |  | YB |  | XB |  | YB |  |
| XC |  | YC |  | XC |  | YC |  |
| XD |  | YD |  | XD |  | YD |  |
| XE |  | YE |  | XE |  | YE |  |
| XF |  | YF |  | XF |  | YF |  |
| X10 |  | Y10 |  | X10 |  | Y10 |  |
| X11 |  | Y11 |  | X11 |  | Y11 |  |
| X12 |  | Y12 | Error reset | X12 |  | Y12 | Error reset |
| X13 |  | Y13 | Not used | X13 |  | Y13 | Not used |
| X14 |  | Y14 |  | X14 |  | Y14 |  |
| X15 |  | Y15 |  | X15 |  | Y15 |  |
| X16 |  | Y16 |  | X16 |  | Y16 |  |
| X17 |  | Y17 |  | X17 |  | Y17 |  |
| X18 |  | Y18 |  | X18 |  | Y18 |  |
| X19 |  | Y19 |  | X19 |  | Y19 |  |
| X1A |  | Y1A |  | X1A |  | Y1A |  |
| X1B |  | Y1B |  | X1B |  | Y1B |  |
| X1C |  | Y1C |  | X1C |  | Y1C |  |
| X1D |  | Y1D |  | X1D |  | Y1D |  |
| X1E |  | Y1E |  | X1E |  | Y1E |  |
| X1F |  | Y1F |  | X1F |  | Y1F |  |

## (d) Buffer memory address comparison

Modifying sequence programs is required as the assignment of buffer memory differs. For details of the buffer memory and sequence programs, refer to the user's manual.

| Address | A68ADN |  | Address | A1S68AD |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | Name | Default value |  | Name | Default value | Read/Write |
| 0 | A/D conversion enable/ disable specification | 00FFH <br> (All channels are set to "enable".) | 0 | A/D conversion enable/ disable specification | 00FFH <br> (All channels are set to "enable".) | R/W |
| 1 | Average processing specification | 0 (Sampling processing specified for all channels) | 1 | Writing data error code | 0 (All channels) | R |
| 2 | CH1 Average time, count | 0 | 2 | Average processing specification | 0 (Sampling processing specified for all channels) | R/W |
| 3 | CH2 Average time, count |  | 3 | Not used | - | - |
| 4 | CH3 Average time, count |  | 4 |  |  |  |
| 5 | CH4 Average time, count |  | 5 |  |  |  |
| 6 | CH5 Average time, count |  | 6 |  |  |  |
| 7 | CH6 Average time, count |  | 7 |  |  |  |
| 8 | CH7 Average time, count |  | 8 |  |  |  |
| 9 | CH8 Average time, count |  | 9 |  |  |  |
| 10 | CH1 Digital output value | 0 | 10 | CH1 Average time, count | 0 | R/W |
| 11 | CH2 Digital output value |  | 11 | CH2 Average time, count | 0 |  |
| 12 | CH3 Digital output value |  | 12 | CH3 Average time, count | 0 |  |
| 13 | CH4 Digital output value |  | 13 | CH4 Average time, count | 0 |  |
| 14 | CH5 Digital output value |  | 14 | CH5 Average time, count | 0 |  |
| 15 | CH6 Digital output value |  | 15 | CH6 Average time, count | 0 |  |
| 16 | CH7 Digital output value |  | 16 | CH7 Average time, count | 0 |  |
| 17 | CH8 Digital output value |  | 17 | CH8 Average time, count | 0 |  |
| 18 | Writing data error code | 0 (No error) | 18 | Not used | - | - |
| 19 | A/D conversion completed | 00FFH <br> (A/D conversion completed on all channels) | 19 |  |  |  |
| 20 | Resolution specification | 1(1/4000) | 20 | CH1 Digital output value | 0 | R |
|  |  |  | 21 | CH2 Digital output value | 0 |  |
|  |  |  | 22 | CH3 Digital output value | 0 |  |
|  |  |  | 23 | CH4 Digital output value | 0 |  |
|  |  |  | 24 | CH5 Digital output value | 0 |  |
|  |  |  | 25 | CH6 Digital output value | 0 |  |
|  |  |  | 26 | CH7 Digital output value | 0 |  |
|  |  |  | 27 | CH8 Digital output value | 0 |  |
|  |  |  | 28 | A/D conversion completed | 0 | R/W |
|  |  |  | 29 | Not used | - | - |

### 10.2.2 Analog output modules comparison

## (1) Comparisons between A616DAI and A1S68DAI

## (a) Performance specifications comparison

O : Compatible, $\triangle$ : Partial change required, $\times$ : Incompatible

| Item |  | A616DAI |  | A1S68DAI |  | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Digital input |  | (1) 16-bit, signed binary (Data unit: 12 bits) <br> (2) Setting range: 0 to 4095 |  | (1) 16-bit, signed binary <br> (2) Setting range: 0 to 4096 |  | $\bigcirc$ |  |
| Analog output |  | 0 to 20mADC(External load resistance: 0 to $600 \Omega$ ) |  | 4 to 20mADC(External load resistance: 0 to $600 \Omega$ ) |  | $\triangle$ | Conversion is limited to 4 to 20 mA . |
| I/O characteristics |  | Digita input | Analog output | Digital input value | Analog output value | $\triangle$ | Conversion is limited to 4 to 20 mA . |
|  |  | +4000 | +20mA | 4000 | 20 mA |  |  |
|  |  | +2000 | +12mA | 2000 | 12 mA |  |  |
|  |  | 0 | 4 mA | 0 | 4 mA |  |  |
| Digital value resolution |  | 1/4000 |  | 1/4000 |  | $\bigcirc$ |  |
| Analog value maximum resolution |  | $4 \mu \mathrm{~A}$ |  | $4 \mu \mathrm{~A}$ |  | $\bigcirc$ |  |
| Overall accuracy (Accuracy in respect to maximum value) |  | $\pm 0.6 \%( \pm 120 \mu \mathrm{~A})$ <br> (When ambient temperature is $25^{\circ} \mathrm{C}: \pm 0.3 \%$ ) $( \pm 60 \mu \mathrm{~A})$ |  | $\begin{gathered} \pm 1.0 \% \\ ( \pm 200 \mu \mathrm{~A}) \end{gathered}$ |  | $\times$ | Overall accuracy differs. |
| Sampling cycle |  | $1.5+0.5 \times$ (Number of D/A conversion enabled channels) (ms) |  | - |  | $\triangle$ | The D/A conversion time is fixed regardless of the D/A conversion value output disable channel setting. |
| Conversion time |  | 0.5 ms (Time required for conversion from 0 to 20 mA or 20 mA to 0 mA ) |  | 4 ms or less/8 channels <br> For high frequency of access from the PLC CPU using FROM/TO instructions, this can be extended up to about 6 ms . |  |  |  |
| Output short protection |  | - |  | Provided |  | $\bigcirc$ |  |
| No. of analog output channels |  | 16 channels/module |  | 8 channels/module |  | $\times$ | As the number of channels is reduced, the number of modules installed may increase. |
| Isolation method |  | Between the output terminals and PLC power: photocoupler isolation <br> Between A616DAI channels : non-isolated |  | Between the output terminals and PLC power: photocoupler isolation <br> Between output channels : non-isolated |  | $\bigcirc$ |  |
| Occupied I/O points |  | 32 points(I/O assignment: special 32 points) |  | 32 points(I/O assignment: special 32 points) |  | $\bigcirc$ |  |
| Connected terminal |  | 38-point terminal block |  | $\begin{aligned} & \begin{array}{l} \text { 20-poin } \\ \text { (M3. } \end{array} \end{aligned}$ | nal block rews) | $\times$ | External wiring must be changed. |
| Applicable wire size |  | 0.75 to $2 \mathrm{~mm}^{2}$ |  | 0.75 to $1.5 \mathrm{~mm}^{2}$ |  | $\triangle$ |  |
| Applicable solderless terminal |  | $\begin{gathered} \text { V1.25-3, V1.25-YS3A } \\ \text { V2-S3, V2-YS3A } \end{gathered}$ |  | $\begin{gathered} \text { R1.25-3, 1.25-YS3, } \\ \text { RAV1.25-3, V1.25-YS3A } \end{gathered}$ |  | $\triangle$ |  |
| Internal current consumption (5VDC) |  | 0.3A |  | 0.85A |  | $\triangle$ | Recalculation of internal current consumption [5VDC] is required. |
| External power supply | Voltage | +15VDC/-15VDC |  | - |  | $\triangle$ | External power supply is not required. |
|  | Current | +15VDC $\cdots 0.53 \mathrm{~A} /-15 \mathrm{VDC} \cdot \cdots 0.125 \mathrm{~V}$ |  | - |  |  |  |
| External dimensions |  | $250(\mathrm{H}) \times 37.5(\mathrm{~W}) \times 131(\mathrm{D}) \mathrm{mm}$ |  | $130(\mathrm{H}) \times 34.5(\mathrm{~W}) \times 93.6(\mathrm{D}) \mathrm{mm}$ |  | $\triangle$ | The dimensions are different. |
| Weight |  | 0.69kg |  | 0.28 kg |  | $\bigcirc$ |  |

## (b) Function comparison

| Item | A616DAI | A1S68DAI | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: |
| Analog output <br> HOLD/CLEAR <br> setting <br> (For all channels) | Using the jumper, specify whether to hold (HOLD) or clear (CLEAR) the analog output before STOP when the ACPU is set to STOP by the RUN key switch, etc. When the ACPU stops operation on detection of an error, the analog output value is 0 mA independently of the analog output HOLD/CLEAR setting. | Allows users to select whether to hold the last analog value output at each channel or clear (outputs 0 or 4 mA ), when the PLC CPU goes into the STOP status, or when digital-to-analog conversion is stopped by an A1S68DAI error: one setting is made for all channels using the HLD/CLR terminal on the front face of the module. | $\triangle$ | Check the analog output status combination list. |
| D/A conversion disable channel setting (For each channel ) | Using the sequence program, specify the channel for which D/A conversion is to be disabled in order to shorten the sampling cycle. (D/A conversion of all 16 channels is enabled when the ACPU is powered up or reset.) <br> (a) D/A conversion enable channel: Performs D/A conversion of the digital value specified from the ACPU. <br> (b) D/A conversion disable channel: Does not perform D/A conversion of the digital value specified from the ACPU. | Allows users to specify whether to output a D/A conversion value of each channel or 4 mA by setting the D/A conversion value output enable flag to ON/OFF in the sequence program. The D/A conversion time (conversion speed) is fixed regardless of setting the D/A conversion value output disable channel setting. (D/A conversion value output enable flag) | $\triangle$ | The D/A conversion time (conversion speed) is fixed regardless of the D/A conversion value output disable channel setting. |
| Analog output disable channel setting (For each channel) | Using the sequence program, specify the channel for which analog value output is to be disabled. <br> (Analog output of all 16 channels is enabled when the ACPU is powered up or reset.) <br> (a) Analog output enable channel: Outputs the offset value or an analog value converted. <br> (b) Analog output disable channel: Outputs 0 mA . | Allows users to specify whether to enable or disable the output of analog values to external devices by writing $0 / 1$ to address 0 for each channel in the sequence program. (Analog output enable/disable setting) | $\bigcirc$ |  |
| Offset/gain setting | The I/O conversion characteristics can be changed. | - | $\times$ | No offset/gain setting function. Fixed to 4 to 20 mA output. Adjust output with the digital input. |

## (c) Comparison of I/O signals for PLC CPU

Modifying sequence programs is required as the I/O signals differ.
For details of the I/O signals and sequence programs, refer to the user's manual.

| A616DAI |  |  |  | A1S68DAI |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Device NO. | Signal name | Device NO. | Signal name | Device NO. | Signal name | Device NO. | Signal name |
| X0 | WDT error | YO | Not used | X0 | WDT error (A1S68DAI detection) | YO | Not used |
| X1 | D/A conversion READY | Y1 |  | X1 | D/A conversion READY | Y1 |  |
| X2 | Error | Y2 |  | X2 | Error | Y2 |  |
| X3 | (er used | Y3 |  | X3 | ( | Y3 |  |
| X4 |  | Y4 |  | X4 |  | Y4 |  |
| X5 |  | Y5 |  | X5 |  | Y5 |  |
| X6 |  | Y6 |  | X6 |  | Y6 |  |
| X7 |  | Y7 |  | X7 |  | Y7 |  |
| X8 |  | Y8 |  | X8 |  | Y8 |  |
| X9 |  | Y9 |  | X9 |  | Y9 |  |
| XA |  | YA |  | XA |  | YA |  |
| XB |  | YB |  | XB |  | YB |  |
| XC |  | YC |  | XC |  | YC |  |
| XD |  | YD |  | XD |  | YD |  |
| XE |  | YE |  | XE |  | YE |  |
| XF |  | YF |  | XF |  | YF |  |
| X10 |  | Y10 |  | X10 |  | Y10 | D/A conversion value output enable |
| X11 |  | Y11 |  | X11 |  | Y11 |  |
| X12 |  | Y12 |  | X12 |  | Y12 |  |
| X13 |  | Y13 |  | X13 |  | Y13 |  |
| X14 |  | Y14 |  | X14 |  | Y14 |  |
| X15 |  | Y15 |  | X15 |  | Y15 |  |
| X16 |  | Y16 |  | X16 |  | Y16 |  |
| X17 |  | Y17 |  | X17 |  | Y17 |  |
| X18 |  | Y18 |  | X18 |  | Y18 | Error reset |
| X19 |  | Y19 |  | X19 |  | Y19 | Not used |
| X1A |  | Y1A |  | X1A |  | Y1A |  |
| X1B |  | Y1B | Output batch enable | X1B |  | Y1B |  |
| X1C |  | Y1C | Not used | X1C |  | Y1C |  |
| X1D |  | Y1D |  | X1D |  | Y1D |  |
| X1E |  | Y1E |  | X1E |  | Y1E |  |
| X1F |  | Y1F |  | X1F |  | Y1F |  |

(d) Buffer memory address comparison

Modifying sequence programs is required as the assignment of buffer memory differs. For details of the buffer memory and sequence programs, refer to the user's manual.

| Address | A616DAI |  | Address | A1S68DAI |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
|  | Name | Read/Write |  | Name | Default value |
| 0 | D/A conversion enable/disable channel | R/W | 0 | Analog output enable/disable channel | 0000 H (All channels are set to "enable".) |
| 1 | Analog output enable/disable channel |  | 1 | CH1 Digital value | 0 |
| 2 | Not used | - | 2 | CH 2 Digital value |  |
| 3 |  |  | 3 | CH3 Digital value |  |
| 4 |  |  | 4 | CH 4 Digital value |  |
| 5 |  |  | 5 | CH5 Digital value |  |
| 6 |  |  | 6 | CH6 Digital value |  |
| 7 |  |  | 7 | CH7 Digital value |  |
| 8 |  |  | 8 | CH8 Digital value |  |
| 9 |  |  | 9 |  | - |
| 10 |  |  | 10 | CH 1 Set value check code | 0 |
| 11 |  |  | 11 | CH 2 Set value check code |  |
| 12 |  |  | 12 | CH3 Set value check code |  |
| 13 |  |  | 13 | CH 4 Set value check code |  |
| 14 |  |  | 14 | CH5 Set value check code |  |
| 15 |  |  | 15 | CH6 Set value check code |  |
| 16 | CH0 Digital value | R/W | 16 | CH7 Set value check code |  |
| 17 | CH1 Digital value |  | 17 | CH8 Set value check code |  |
| 18 | CH 2 Digital value |  |  |  |  |
| 19 | CH3 Digital value |  |  |  |  |
| 20 | CH 4 Digital value |  |  |  |  |
| 21 | CH5 Digital value |  |  |  |  |
| 22 | CH6 Digital value |  |  |  |  |
| 23 | CH 7 Digital value |  |  |  |  |
| 24 | CH8 Digital value |  |  |  |  |
| 25 | CH9 Digital value |  |  |  |  |
| 26 | CHA Digital value |  |  |  |  |
| 27 | CHB Digital value |  |  |  |  |
| 28 | CHC Digital value |  |  |  |  |
| 29 | CHD Digital value |  |  |  |  |
| 30 | CHE Digital value |  |  |  |  |
| 31 | CHF Digital value |  |  |  |  |
| 32 |  |  |  |  |  |
| to | Not used | - |  |  |  |
| 47 |  |  |  |  |  |
| 48 | CHO Set value check code | R/W |  |  |  |
| 49 | CH 1 Set value check code |  |  |  |  |
| 50 | CH 2 Set value check code |  |  |  |  |
| 51 | CH3 Set value check code |  |  |  |  |
| 52 | CH 4 Set value check code |  |  |  |  |
| 53 | CH5 Set value check code |  |  |  |  |
| 54 | CH6 Set value check code |  |  |  |  |
| 55 | CH7 Set value check code |  |  |  |  |
| 56 | CH8 Set value check code |  |  |  |  |
| 57 | CH9 Set value check code |  |  |  |  |
| 58 | CHA Set value check code |  |  |  |  |
| 59 | CHB Set value check code |  |  |  |  |
| 60 | CHC Set value check code |  |  |  |  |
| 61 | CHD Set value check code |  |  |  |  |
| 62 | CHE Set value check code |  |  |  |  |
| 63 | CHF Set value check code |  |  |  |  |

## (2) Comparisons between A616DAV and A1S68DAV

(a) Performance specifications comparison

O: Compatible, $\Delta$ : Partial change required, $x$ : Incompatible

| Item |  | A616DAV |  |  | A1S68DAV |  | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Digital input |  | (1) 16-bit, signed binary (Data unit :12 bits) <br> (2) Setting range: -4096 to 4095 |  |  | (1) 16-bit signed binary <br> (2) Setting range:- 2048 to 2047 |  | $\triangle$ | Setting range differs. |
| Analog output |  | (1)When output voltage range setting is $10 \mathrm{~V} \cdots-10 \mathrm{~V}$ to 0 V to +10 V (External load resistance: $2 \mathrm{k} \Omega$ to $1 \mathrm{M} \Omega$ ) <br> (2)When output voltage range setting is $5 \mathrm{~V} \cdots-5 \mathrm{~V}$ to 0 V to +5 V (External load resistance: $2 \mathrm{k} \Omega$ to $1 \mathrm{M} \Omega$ ) |  |  | -10 to 0 to 10 VDC <br> (External load resistance: $2 \mathrm{k} \Omega$ to $1 \mathrm{M} \Omega$ ) |  | $\triangle$ | No $\pm 5 \mathrm{~V}$ range setting |
| I/O characteristics |  | Digital input | Analog output |  |  |  | $\triangle$ | Digital input value differs. |
|  |  | Digital input |  |  | Analog output |  |  |
|  |  | +4000 | +5V | +10V | 2000 | 10 V |  |  |
|  |  | +2000 | +2.5V | +5V | 1000 | 5 V |  |  |
|  |  | 0 | OV | ov | 0 | OV |  |  |
|  |  | -2000 | -2.5V | -5V | -1000 | -5V |  |  |
|  |  | -4000 | -5.0V | -10V | -2000 | -10V |  |  |
| Digital value resolution |  |  | 1/4000 |  |  | 1/2000 |  | $\times$ | Resolution is different. |
| Analog v maximum | solution | Output voltage setting $10 \mathrm{~V}: 2.5 \mathrm{mV}$, Output voltage setting $5 \mathrm{~V}: 1.25 \mathrm{mV}$ |  |  | 5 mV |  | $\times$ | Resolution is different. |
| Overall accuracy |  | Output voltage setting 10 V |  | $\begin{aligned} & \pm 60 \mathrm{mV})(\text { Ambient } \\ & \text { ure } \left.0 \text { to } 55^{\circ} \mathrm{C}\right) \\ & \pm 30 \mathrm{mV} \text { ) (Ambient } \\ & \text { are } 25^{\circ} \mathrm{C} \text { ) } \end{aligned}$ | $\begin{gathered} \pm 1.0 \% \\ ( \pm 100 \mu \mathrm{~A}) \end{gathered}$ |  | $\times$ | Overall accuracy differs. |
|  |  | Output voltage setting 5 V |  | $\pm 0.6 \%( \pm 30 \mathrm{mV})$ (Ambient temperature 0 to $55^{\circ} \mathrm{C}$ ) $\pm 0.3 \%( \pm 15 \mathrm{mV})$ (Ambient temperature $25^{\circ} \mathrm{C}$ ) |  |  |  |  |
| Sampling |  | $1.5+0.5 \times$ (Number of D/A conversion enabled channels) (ms) |  |  |  |  | $\triangle$ | The D/A conversion time is fixed regardless of the D/A conversion value output disable channel setting. |
| Conversion time |  | 0.5 ms <br> (Time required for conversion from -10 V to +10 V or +10 V to -10 V ) |  |  | 4 ms or less/8 channels For high frequency of access from the PLC CPU using FROM/TO instructions, this can be extended up to about 6 ms . |  |  |  |
| Absolute maximum output |  | 15V |  |  | - |  | $\bigcirc$ |  |
| No. of analog output channels |  | 16 channels/module |  |  | 8 channels/module |  | $\times$ | As the number of channels is reduced, the number of modules installed may increase. |
| Output sh protection |  | - |  |  | Provided |  | $\bigcirc$ |  |
| Isolation method |  | Between the output terminals and PLC power supply: photocoupler isolation Between A616DAV channels : non-isolated |  |  | Between the output terminals and <br> PLC power supply: <br> photocoupler isolation <br> Between output channels : nonisolated |  | $\bigcirc$ |  |
| Occupied I/O points |  | (1/O assignment: special 32 points ${ }^{\text {points) }}$ |  |  | (I/O assignm | ts <br> ecial 32 points) | $\bigcirc$ |  |
| Connecte | erminal | 38-point terminal block |  |  | $\begin{array}{r} \text { 20-poi } \\ \text { (M3 } \\ \hline \end{array}$ | nal block crews) | $\times$ | External wiring must be changed. |
| Applicable wire size |  | 0.75 to $2 \mathrm{~mm}^{2}$ |  |  |  | $\mathrm{mm}^{2}$ | $\triangle$ |  |
| Applicable terminal | olderless | $\begin{gathered} \text { V1.25-3, V1.25-YS3A, } \\ \text { V2-S3,V2-YS3A } \end{gathered}$ |  |  | $\begin{array}{r} \text { R1.2 } \\ \text { RAV1.2 } \end{array}$ | $\begin{aligned} & 5-Y S 3, \\ & 25-Y S 3 A \end{aligned}$ | $\triangle$ |  |
| Internal current consumption (5VDC) |  | 0.38A |  |  | 0.85A |  | $\triangle$ | Recalculation of internal current consumption [5VDC] is required. |
| External power supply | Voltage | +15VDC/-15VDC |  |  | - |  | $\triangle$ | External power supply is not required. |
|  | Current | +15VDC $\cdots 0.2 \mathrm{~A} /-15 \mathrm{VDC} \cdots 0.17 \mathrm{~A}$ |  |  |  |  |  |  |


| Item | A616DAV | A1S68DAV | Compati- <br> bility | Precautions for <br> replacement |
| :--- | :---: | :---: | :---: | :---: |
| External dimensions | $250(\mathrm{H}) \times 37.5(\mathrm{~W}) \times 131(\mathrm{D}) \mathrm{mm}$ | $130(\mathrm{H}) \times 34.5(\mathrm{~W}) \times 93.6(\mathrm{D}) \mathrm{mm}$ | $\Delta$ | The dimensions are <br> different. |
| Weight | 0.65 kg | 0.28 kg | O |  |

(b) Function comparison

O: Compatible, $\Delta$ : Partial change required, $x$ : Incompatible, 一: Additional function

| Item | A616DAV | A1S68DAV | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: |
| Analog output HOLD/CLEAR setting (For all channels) | Using the jumper, specify whether to HOLD or CLEAR the analog output before STOP when the ACPU is set to STOP by the RUN key switch, etc. When the ACPU stops operation on detection of an error, the analog output value is 0 V independently of the analog output HOLD/CLEAR setting. | Allows users to select whether to hold the last analog value output at each channel or clear (outputs 0 or 4 mA ), when the PLC CPU goes into the STOP status, or when digital-to-analog conversion is stopped by an A1S68DAV error: one setting is made for all channels using the HLD/CLR terminal on the front face of the module. | $\triangle$ | Check the analog output status combination list. |
| D/A conversion disable channel setting (For each channel) | Using the sequence program, specify the channel for which D/A conversion is to be disabled in order to shorten the sampling cycle. (D/A conversion of all 16 channels is enabled when the ACPU is powered up or reset.) <br> (a) D/A conversion enable channel: Performs D/A conversion of the digital value specified from the ACPU. <br> (b) D/A conversion disable channel: Does not perform D/A conversion of the digital value specified from the ACPU. | Allows users to specify whether to output a D/A conversion value of each channel or 0 V by setting the D/A conversion value output enable flag to ON/OFF in the sequence program. The D/A conversion time (conversion speed) is fixed regardless of the D/A conversion value output disable channel setting. <br> (D/A conversion value output enable flag) | $\Delta$ | The D/A conversion time (conversion speed) is fixed regardless of the D/A conversion value output disable channel setting. |
| Analog output disable channel setting (For each channel) | Using the sequence program, specify the channel for which analog value output is to be disabled. (Analog output of all 16 channels is enabled when the ACPU is powered up or reset.) <br> (a) Analog output enable channel: Outputs the offset value or an analog value converted. <br> (b) Analog output disable channel: Outputs 0V. | Allows users to specify whether to enable or disable the output of analog values to external devices by writing $0 / 1$ to address 0 for each channel in the sequence program. (Analog output enable/disable setting) | $\bigcirc$ |  |
| Offset/gain setting | The I/O conversion characteristics can be changed. | - | $\times$ | No offset/gain setting function. Fixed to -10 to 10V output. Adjust output with the digital input. |

(c) Comparison of I/O signals for PLC CPU

Modifying sequence programs is required as the I/O signals differ.
For details of the I/O signals and sequence programs, refer to the user's manual.

| A616DAV |  |  |  | A1S68DAV |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Device NO. | Signal name | Device NO. | Signal name | Device NO. | Signal name | $\begin{gathered} \text { Device } \\ \text { NO. } \end{gathered}$ | Signal name |
| X0 | WDT error | Y0 | Not used | X0 | WDT error <br> (A1S68DAV detection) | Y0 | Not used |
| X1 | D/A conversion READY | Y1 |  | X1 | D/A conversion READY | Y1 |  |
| X2 | Error | Y2 |  | X2 | Error | Y2 |  |
| X3 | Not used | Y3 |  | X3 | Not used | Y3 |  |
| X4 |  | Y4 |  | X4 |  | Y4 |  |
| X5 |  | Y5 |  | X5 |  | Y5 |  |
| X6 |  | Y6 |  | X6 |  | Y6 |  |
| X7 |  | Y7 |  | X7 |  | Y7 |  |
| X8 |  | Y8 |  | X8 |  | Y8 |  |
| X9 |  | Y9 |  | X9 |  | Y9 |  |
| XA |  | YA |  | XA |  | YA |  |
| XB |  | YB |  | XB |  | YB |  |
| XC |  | YC |  | XC |  | YC |  |
| XD |  | YD |  | XD |  | YD |  |
| XE |  | YE |  | XE |  | YE |  |
| XF |  | YF |  | XF |  | YF |  |
| X10 |  | Y10 |  | X10 |  | Y10 | D/A conversion value output enable |
| X11 |  | Y11 |  | X11 |  | Y11 |  |
| X12 |  | Y12 |  | X12 |  | Y12 |  |
| X13 |  | Y13 |  | X13 |  | Y13 |  |
| X14 |  | Y14 |  | X14 |  | Y14 |  |
| X15 |  | Y15 |  | X15 |  | Y15 |  |
| X16 |  | Y16 |  | X16 |  | Y16 |  |
| X17 |  | Y17 |  | X17 |  | Y17 |  |
| X18 |  | Y18 |  | X18 |  | Y18 | Error reset |
| X19 |  | Y19 |  | X19 |  | Y19 | Not used |
| X1A |  | Y1A |  | X1A |  | Y1A |  |
| X1B |  | Y1B | Output batch enable | X1B |  | Y1B |  |
| X1C |  | Y1C | Not used | X1C |  | Y1C |  |
| X1D |  | Y1D |  | X1D |  | Y1D |  |
| X1E |  | Y1E |  | X1E |  | Y1E |  |
| X1F |  | Y1F |  | X1F |  | Y1F |  |

## (d) Buffer memory address comparison

Modifying sequence programs is required as the assignment of buffer memory differs. For details of the buffer memory and sequence programs, refer to the user's manual.

| Address | A616DAV |  | Address | A1S68DAV |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
|  | Name | Read/Write |  | Name | Default value |
| 0 | D/A conversion enable/disable channel | R/W | 0 | Analog output enable/disable channel | 0000 H (All channels are set to "enable".) |
| 1 | Analog output enable/disable channel |  | 1 | CH1 Digital value | 0 |
| 2 | Not used | - | 2 | CH 2 Digital value | 0 |
| 3 |  |  | 3 | CH 3 Digital value | 0 |
| 4 |  |  | 4 | CH4 Digital value | 0 |
| 5 |  |  | 5 | CH5 Digital value | 0 |
| 6 |  |  | 6 | CH6 Digital value | 0 |
| 7 |  |  | 7 | CH7 Digital value | 0 |
| 8 |  |  | 8 | CH8 Digital value | 0 |
| 9 |  |  | 9 | Not used | - |
| 10 |  |  | 10 | CH 1 Set value check code | 0 |
| 11 |  |  | 11 | CH 2 Set value check code | 0 |
| 12 |  |  | 12 | CH3 Set value check code | 0 |
| 13 |  |  | 13 | CH 4 Set value check code | 0 |
| 14 |  |  | 14 | CH5 Set value check code | 0 |
| 15 |  |  | 15 | CH6 Set value check code | 0 |
| 16 | CH0 Digital value | R/W | 16 | CH 7 Set value check code | 0 |
| 17 | CH1 Digital value |  | 17 | CH8 Set value check code | 0 |
| 18 | CH 2 Digital value |  |  |  |  |
| 19 | CH3 Digital value |  |  |  |  |
| 20 | CH4 Digital value |  |  |  |  |
| 21 | CH5 Digital value |  |  |  |  |
| 22 | CH6 Digital value |  |  |  |  |
| 23 | CH7 Digital value |  |  |  |  |
| 24 | CH8 Digital value |  |  |  |  |
| 25 | CH9 Digital value |  |  |  |  |
| 26 | CHA Digital value |  |  |  |  |
| 27 | CHB Digital value |  |  |  |  |
| 28 | CHC Digital value |  |  |  |  |
| 29 | CHD Digital value |  |  |  |  |
| 30 | CHE Digital value |  |  |  |  |
| 31 | CHF Digital value |  |  |  |  |
| $\begin{aligned} & 32 \\ & \text { to } \\ & 47 \end{aligned}$ | Not used | - |  |  |  |
| 48 | CHO Set value check code | R/W |  |  |  |
| 49 | CH1 Set value check code |  |  |  |  |
| 50 | CH2 Set value check code |  |  |  |  |
| 51 | CH3 Set value check code |  |  |  |  |
| 52 | CH4 Set value check code |  |  |  |  |
| 53 | CH5 Set value check code |  |  |  |  |
| 54 | CH6 Set value check code |  |  |  |  |
| 55 | CH7 Set value check code |  |  |  |  |
| 56 | CH8 Set value check code |  |  |  |  |
| 57 | CH9 Set value check code |  |  |  |  |
| 58 | CHA Set value check code |  |  |  |  |
| 59 | CHB Set value check code |  |  |  |  |
| 60 | CHC Set value check code |  |  |  |  |
| 61 | CHD Set value check code |  |  |  |  |
| 62 | CHE Set value check code |  |  |  |  |
| 63 | CHF Set value check code |  |  |  |  |

(3) Comparisons between A62DA and A1S62DA
(a) Performance specifications comparison

O: Compatible, $\Delta$ : Partial change required, $x$ : Incompatible

| Item |  | A62DA |  |  | A1S62DA |  |  |  |  |  | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Digital input |  | Maximum setting value <br> Voltage: $\pm 2000$ <br> Current: $\pm 1000$ |  |  | Voltage: -4000 to 4000 Current: 0 to 4000 <br> Voltage: -8000 to 8000 Current: 0 to 8000 <br> Voltage: -12000 to 12000 Current: 0 to 12000 |  |  |  |  |  | $\triangle$ | Check the I/O conversion characteristics to set the digital value. |
| Analog output |  | Voltage : - 10 to 0 to +10VDC <br> (external load resistance $500 \Omega$ to $1 \mathrm{M} \Omega$ ) <br> Current : +4 to +20 mADC <br> (external load resistance $0 \Omega$ to $600 \Omega$ ) <br> Current outputs are usable by -20 to 0 to 20 mA . |  |  |  |  | e:-10 <br> ad resis <br> rent: 0 <br> oad res | 0 to + <br> ance 2 <br> 20 mA <br> tance | $\begin{aligned} & \mathrm{VDC} \\ & \Omega \text { to } 1 \mathrm{~N} \\ & \mathrm{C} \\ & \text { to } 600 \end{aligned}$ |  | $\triangle$ | External load resistance must be checked at voltage output. Negative current cannot be output. |
| I/O characteristics |  |  |  |  |  |  | Resolution |  | Volta |  | $\Delta$ | Check the I/O conversion characteristics. |
|  |  |  | 1/4000 | 1/8000 | 1/12000 | $\begin{aligned} & \text { output } \\ & \text { value } \end{aligned}$ | value ${ }^{\text {2 }}$ |  |  |
|  |  | Digital input | Voltage | Current |  | 4000 | 8000 | 12000 | 10 V | 20 mA |  |  |
|  |  | +2000 | $+10 \mathrm{~V}$ | - |  | 2000 | 4000 | 6000 | 5 V | 12 mA |  |  |
|  |  | 0 | OV | 4 mA |  | 0 | 0 | 0 | 0 | 4 mA |  |  |
|  |  | -1000 | -5V | -12mA |  | -2000 | -4000 | -6000 | $-5 \mathrm{~V}$ |  |  |  |
|  |  | -2000 | -10V | $-$ |  | -4000 | -8000 | -12000 | -10V |  |  |  |
|  |  |  |  |  | *1. Offset for voltage output: OV, Gain: 10V (Factory-set) <br> *2. Offset for current output: 4mA, Gain: 20mA |  |  |  |  |  |  |  |
| Maximum resolution |  |  |  |  | Voltage: 5 mV (1/2000) <br> Current: $20 \mu \mathrm{~A}(1 / 1000)$ |  |  |  |  | $\begin{array}{r} 2.5 \mathrm{mV}(10 \mathrm{~V}) \\ 1.25 \mathrm{mV}(10 \mathrm{~V}) \\ 0.83 \mathrm{mV}(10 \mathrm{~V}) \\ \hline \end{array}$ |  | $\begin{array}{r} 5 \mu \mathrm{~A}(20 \mathrm{~mA}) \\ 2.5 \mu \mathrm{~A}(20 \mathrm{~mA}) \\ 1.7 \mu \mathrm{~A}(20 \mathrm{~mA}) \end{array}$ |  | $\bigcirc$ |  |
| Overall (Accurac respect to maximum output va | curacy in <br> analog <br> e) | $\begin{gathered} \pm 1 \% \\ \text { (Voltage: } \pm 0.1 \mathrm{~V}, \text { current: } \pm 0.2 \mathrm{~mA} \text { ) } \end{gathered}$ |  |  | $\begin{gathered} \pm 1 \% \\ (\text { Voltage: } \pm 100 \mathrm{mV} \text {, current: } \pm 200 \mu \mathrm{~A}) \end{gathered}$ |  |  |  |  |  | $\bigcirc$ |  |
| Maximum conversion speed |  | Within $15 \mathrm{~ms} / 2$ channels (same for 1 channel) <br> Note) Time period from digital input write until specified analog voltage(current) reached. |  |  | Within $25 \mathrm{~ms} / 2$ channels (same for 1 channel) |  |  |  |  |  | $\times$ | Conversion speed is reduced. |
| Absolute maximum output |  | Voltage: $\pm 12 \mathrm{~V}$ <br> Current: $\pm 28 \mathrm{~mA}$ <br> Note) The voltage or current exceeding the above is not output by output protection circuit. |  |  | Voltage: $\pm 12 \mathrm{~V}$ <br> Current: +28 mA |  |  |  |  |  | $\bigcirc$ |  |
| Output short protection |  | Provided |  |  | Provided |  |  |  |  |  | $\bigcirc$ |  |
| Number output po | analog ts | 2 channels/module |  |  | 2 channels/module |  |  |  |  |  | $\bigcirc$ |  |
| Isolation method |  | Between the output terminals and PLC power supply: photocoupler isolation Between channels: non-isolated |  |  | Between the output terminals and PLC power supply: photocoupler isolation <br> Between channels : non-isolated |  |  |  |  |  | $\bigcirc$ |  |
| Occupied I/O points |  | 32 points(I/O assignment: special 32 points) |  |  | 32 points(I/O assignment: special 32 points) |  |  |  |  |  | $\bigcirc$ |  |
| Connected terminal |  | 20-point terminal block |  |  | 20-point terminal block |  |  |  |  |  | $\triangle$ | External wiring must be changed. |
| Applicable wire size |  | 0.75 to $2 \mathrm{~mm}^{2}$(Applicable tightening torque:39 to $59 \mathrm{~N} . \mathrm{cm}$ ) |  |  | 0.75 to $1.5 \mathrm{~mm}^{2}$ |  |  |  |  |  | $\triangle$ |  |
| Applicable solderless terminal |  | $\begin{gathered} \hline \text { V1.25-3, V1.25-YS3A, } \\ \text { V2-S3, V2-YS3A } \end{gathered}$ |  |  | $\begin{gathered} \hline 1.25-3,1.25-\mathrm{YS} 3 \mathrm{~A}, \\ \mathrm{~V} 1.25-3, \mathrm{~V} 1.25-\mathrm{YS} 3 \mathrm{~A} \end{gathered}$ |  |  |  |  |  | $\triangle$ |  |
| Internal current consumption (5VDC) |  | 0.6A |  |  | 0.8A |  |  |  |  |  | $\triangle$ | Recalculation of internal current consumption [ 5 VDC ] is required. |
| External | Voltage | 21.6 to 26.4VDC |  |  | - |  |  |  |  |  | $\triangle$ | External power supply is not required. |
| supply | Current | 0.35A |  |  |  |  |  |  |  |  |  |  |
| Inrush current |  | 2.4A |  |  | - |  |  |  |  |  |  |  |
| External dimensions |  | $250(\mathrm{H}) \times 37.5(\mathrm{~W}) \times 121(\mathrm{D}) \mathrm{mm}$ |  |  | $130(\mathrm{H}) \times 34.5(\mathrm{~W}) \times 93.6(\mathrm{D}) \mathrm{mm}$ |  |  |  |  |  | $\triangle$ | The dimensions are different. |


| Item | A62DA | A1S62DA | $\begin{gathered} \text { Compati- } \\ \text { bility } \end{gathered}$ | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: |
| Weight | 0.5 kg | 0.32 kg | $\bigcirc$ |  |

(b) Function comparison
$\bigcirc$ : Compatible, $\Delta$ : Partial change required, $\times$ : Incompatible, - : Additional function

| Item | A62DA | A1S62DA | $\begin{gathered} \text { Compati- } \\ \text { bility } \end{gathered}$ | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: |
| Output HOLD/CLEAR setting | - | Allows users to select whether to hold the last analog value output at each channel or clear (offset value or $0 \mathrm{~V} / 0 \mathrm{~mA}$ output), when the PLC CPU is in STOP, or when the D/A conversion is stopped by this module because of an error. One setting can be set for all channels with the HOLD/CLEAR terminal. | - | - |
| D/A conversion execute/ non-execute setting function | - | Allows users to specify whether to output a D/A conversion value for each channel or an offset value by setting the D/A conversion value output enable flag to ON/OFF in the sequence program. <br> The D/A conversion time (conversion speed) is fixed regardless of the D/A conversion value output disable channel setting. | - | - |
| Analog value external output enable/disable setting function | - | Allows users to specify whether to enable or disable the output of analog values to external devices by writing $0 / 1$ to address 0 for each channel in the sequence program. | - | - |

## (c) Comparison of I/O signals for PLC CPU

Modifying sequence programs is required as the I/O signals differ.
For details of the I/O signals and sequence programs, refer to the user's manual.

| A62DA |  |  |  | A1S62DA |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Device NO. | Signal name | Device NO. | Signal name | Device NO. | Signal name | Device NO. | Signal name |
| X0 | WDT error (A62DA detection) | Y0 | Not used | X0 | WDT error (A1S62DA detection) | Y0 | Not used |
| X1 | D/A conversion READY | Y1 |  | X1 | D/A conversion READY | Y1 |  |
| X2 | Not used | Y2 |  | X2 | Error | Y2 |  |
| X3 |  | Y3 |  | X3 | Not used | Y3 |  |
| X4 |  | Y4 |  | X4 |  | Y4 |  |
| X5 |  | Y5 |  | X5 |  | Y5 |  |
| X6 |  | Y6 |  | X6 |  | Y6 |  |
| X7 |  | Y7 |  | X7 |  | Y7 |  |
| X8 |  | Y8 |  | X8 |  | Y8 |  |
| X9 |  | Y9 |  | X9 |  | Y9 |  |
| XA |  | YA |  | XA |  | YA |  |
| XB |  | YB |  | XB |  | YB |  |
| XC |  | YC |  | XC |  | YC |  |
| XD |  | YD |  | XD |  | YD |  |
| XE |  | YE |  | XE |  | YE |  |
| XF |  | YF |  | XF |  | YF |  |
| X10 |  | Y10 |  | X10 |  | Y10 | CH1 D/A conversion output enable |
| X11 |  | Y11 |  | X11 |  | Y11 | CH2 D/A conversion output enable |
| X12 |  | Y12 |  | X12 |  | Y12 | Not used |
| X13 |  | Y13 |  | X13 |  | Y13 |  |
| X14 |  | Y14 |  | X14 |  | Y14 |  |
| X15 |  | Y15 |  | X15 |  | Y15 |  |
| X16 |  | Y16 |  | X16 |  | Y16 |  |
| X17 |  | Y17 |  | X17 |  | Y17 |  |
| X18 |  | Y18 | CPU select signal | X18 |  | Y18 | Error reset |
| X19 |  | Y19 | Sign of CH 1 digital input | X19 |  | Y19 | Not used |
| X1A |  | Y1A | Sign of CH 2 digital input | X1A |  | Y1A |  |
| X1B |  | Y1B | Output enable | X1B |  | Y1B |  |
| X1C |  | Y1C | Not used | X1C |  | Y1C |  |
| X1D |  | Y1D |  | X1D |  | Y1D |  |
| X1E |  | Y1E |  | X1E |  | Y1E |  |
| X1F |  | Y1F |  | X1F |  | Y1F |  |

(d) Buffer memory address comparison

Modifying sequence programs is required as the assignment of buffer memory differs. For details of the buffer memory and sequence programs, refer to the user's manual.

| Address | A62DA |  | Address | A1S62DA |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | Name | Read/Write |  | Name | Default value | Read/Write |
| 0 | CH1 Digital value | R/W | 0 | Analog output enable/disable channel | 0 |  |
| 1 | CH 2 Digital value |  | 1 | CH1 Digital value | 0 | R/W |
| 2 | CH1 Voltage set value check code |  | 2 | CH 2 Digital value | 0 |  |
| 3 | CH2 Voltage set value check code |  | 3 | Not used | - | - |
| 4 | CH1 Current set value check code |  | 4 |  |  |  |
| 5 | CH 2 Current set value check code |  | 5 |  |  |  |
|  |  |  | 6 |  |  |  |
|  |  |  | 7 |  |  |  |
|  |  |  | 8 |  |  |  |
|  |  |  | 9 | Resolution of digital value | 1(×1) | R/W |
|  |  |  | 10 | CH1 Set value check code | 0 |  |
|  |  |  | 11 | CH 2 Set value check code | 0 |  |
|  |  |  | 12 | Not used | - | - |
|  |  |  | 13 |  |  |  |
|  |  |  | 14 |  |  |  |
|  |  |  | 15 |  |  |  |
|  |  |  | 16 |  |  |  |
|  |  |  | 17 |  |  |  |

## (4) Comparisons between A62DA-S1 and A1S62DA

(a) Performance specifications comparison

O: Compatible, $\Delta$ : Partial change required, $x$ : Incompatible

|  | tem | A62DA-S1 |  |  |  |  |  | A1S62DA |  |  |  |  |  | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Digital input |  | 0 to +4000 |  |  |  |  |  | Voltage: -4000 to 4000 Current: 0 to 4000 <br> Voltage: -8000 to 8000 Current: 0 to 8000 <br> Voltage: -12000 to 12000 Current: 0 to 12000 |  |  |  |  |  | $\triangle$ | Check the I/O conversion characteristics to set the digital value. |
| Analog output |  | Voltage: 0 to +10 VDC <br> (external load resistance: $500 \Omega$ to $1 \mathrm{M} \Omega$ ) <br> Current : +4 to +20 mADC <br> (external load resistance $0 \Omega$ to $600 \Omega$ ) <br> Current outputs are usable by 0 up to 20 mA . |  |  |  |  |  | Voltage: -10 to 0 to +10 VDC <br> (external load resistance $2 \mathrm{k} \Omega$ to $1 \mathrm{M} \Omega$ ) <br> Current : 0 to +20 mADC <br> (external load resistance 0 to $600 \Omega$ ) |  |  |  |  |  | $\triangle$ | External load resistance must be checked at voltage output. |
| 1/0 characteristics |  | Output range | Digital |  | Analog output |  |  |  |  | Resolution |  | Voltage | Curre | $\Delta$ | Check the I/O conversion characteristics |
|  |  |  |  |  |  | $1 / 4000$ | 18800 | 1/12000 | output | output |  |  |
|  |  | 0 to 10 V | +4000 |  |  |  | +10V |  |  | Digital input value | 4000 | 8000 | 12000 |  |  | 10 V | 20 mA |
|  |  |  | 0 | OV |  |  | 2000 | 4000 | 6000 |  | 5 V | 12 mA |  |  |
|  |  | 0 to 5 V 0 to 20 mA |  | 4000 | +5 V or +20 n |  |  | 0 | 0 |  | 0 | 0 | 4 mA |  |  |
|  |  | 0 to 20 mA |  | 0 | OV or OmA |  |  | -2000 | -4000 |  | -6000 | -5V |  |  |  |
|  |  | 1 to 5 V 4 to 20 mA |  | 4000 | +5V or +20 |  |  | -4000 | -8000 |  | -12000 | -10V |  |  |  |
|  |  | 4 to 20 mA |  | 0 | +1V or +4 mA |  |  | *1. Offset for voltage output : OV, Gain: 10V (Factory-set) <br> *2. Offset for current output: 4mA, Gain: 20mA |  |  |  |  |  |  |  |
|  | Voltage |  | 1 to 5 V $: 1 \mathrm{mV}$ $(1 / 4000)$ <br> 0 to 5 V $: 1.25 \mathrm{mV}$ $(1 / 4000)$ <br> 0 to 10 V $: 2.5 \mathrm{mV}$ $(1 / 4000)$ |  |  |  |  |  | $1 / 4000$ $2.5 \mathrm{mV}(10 \mathrm{~V})$ <br> $1 / 8000$ $1.25 \mathrm{mV}(10 \mathrm{~V})$ <br> $1 / 12000$ $0.83 \mathrm{mV}(10 \mathrm{~V})$ |  |  |  |  |  | $\bigcirc$ |  |
|  | Current | 4 to $20 \mathrm{~mA}: 4 \mu \mathrm{~A}(1 / 4000)$ 0 to $20 \mathrm{~mA}: 5 \mu \mathrm{~A}(1 / 4000)$ |  |  |  |  |  | $\begin{array}{ccc} 1 / 4000 & 5 \mu \mathrm{~A}(20 \mathrm{~mA}) \\ 1 / 8000 & 2.5 \mu \mathrm{~A}(20 \mathrm{~mA}) \\ 1 / 12000 & 1.7 \mu \mathrm{~A}(20 \mathrm{~mA}) \end{array}$ |  |  |  |  |  | $\bigcirc$ |  |
| Overall accuracy (Accuracy in respect to maximum value) |  |  |  |  |  |  |  | $\begin{gathered} \pm 1 \% \\ \text { (Voltage: } \pm 100 \mathrm{mV} \text {, current: } \pm 200 \mu \mathrm{~A} \text { ) } \end{gathered}$ |  |  |  |  |  | $\triangle$ | Fixed regardless of the output range. |
|  |  | Temperature range |  | 0 to 5 V | 0 to 10V | $\begin{array}{r} 4 \mathrm{to} \\ 20 \mathrm{~mA} \\ \hline \end{array}$ | $\begin{gathered} \hline 0 \text { to } \\ 20 \mathrm{~mA} \\ \hline \end{gathered}$ |  |  |  |  |  |  |  |  |  |
|  |  | $\begin{gathered} 25^{\circ} \mathrm{C} \\ ( \pm \text { Within } 0.5 \%) \end{gathered}$ |  | $\pm 25 \mathrm{mV}$ | $\pm 50 \mathrm{mV}$ | $\pm 0.1 \mathrm{~mA}$ | $\pm 0.1 \mathrm{~mA}$ |  |  |  |  |  |  |  |  |  |
|  |  | $\begin{gathered} 0 \text { to } 55^{\circ} \mathrm{C} \\ ( \pm \text { Within } 1 \%) \\ \hline \end{gathered}$ |  | $\pm 50 \mathrm{mV}$ | $\pm 100 \mathrm{mV}$ | $\pm 0.2 \mathrm{~mA}$ | $\pm 0.2 \mathrm{~mA}$ |  |  |  |  |  |  |  |  |  |
| Maximum conversion speed |  | Within $15 \mathrm{~ms} / 2$ channels (Same for 1channel) <br> Note)Time period from digital input write until specified analog voltage(current) reached. |  |  |  |  |  | Within $25 \mathrm{~ms} / 2$ channels <br> (Period for 1channel is also the same) |  |  |  |  |  | $\times$ | Conversion speed is reduced. |
| Absolute maximum output |  | Voltage: 0 to +12 V <br> Current: 0 to +28 mA <br> Note)The voltage or current exceeding the above is not output due to output protection circuit. |  |  |  |  |  | Voltage: $\pm 12 \mathrm{~V}$ <br> Current: +28mA |  |  |  |  |  | $\bigcirc$ |  |
| Out | ut short ction | Provided |  |  |  |  |  | Provided |  |  |  |  |  | $\bigcirc$ |  |
| Num anal poin | er of g output | 2 channels/module |  |  |  |  |  | 2 channels/module |  |  |  |  |  | $\bigcirc$ |  |
| Isolation method |  | Between the output terminals and PLC power supply: photocoupler isolation Between channels: non-isolated |  |  |  |  |  | Between the output terminals and PLC power supply: <br> photocoupler isolation <br> Between channels : non-isolated |  |  |  |  |  | $\bigcirc$ |  |
| $\begin{aligned} & \text { Occu } \\ & \text { point } \end{aligned}$ | ied I/O | 32 points (I/O assignment: special 32 points) |  |  |  |  |  | 32 points (I/O assignment: special 32 points) |  |  |  |  |  | $\bigcirc$ |  |

O: Compatible, $\Delta$ : Partial change required, $x$ : Incompatible

| Item |  | A62DA-S1 | A1S62DA | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Connected terminal |  | 20-point terminal block | 20-point terminal block | $\triangle$ | External wiring must be changed. |
| Applicable wire size |  | 0.75 to $2 \mathrm{~mm}^{2}$ (Applicable tightening torque 39 to $59 \mathrm{~N} \cdot \mathrm{~cm}$ ) | 0.75 to $1.5 \mathrm{~mm}^{2}$ | $\triangle$ |  |
| Applicable solderless terminal |  | $\begin{gathered} \text { V1.25-3, V1.25-YS3A, } \\ \text { V2-S3, V2-YS3A } \end{gathered}$ | $\begin{gathered} \text { 1.25-3, 1.25-YS3A, } \\ \text { V1.25-3, V1.25-YS3A } \end{gathered}$ | $\triangle$ |  |
| Internal current consumption (5VDC) |  | 0.6A | 0.8A | $\triangle$ | Recalculation of internal current consumption [5VDC] is required. |
|  | Voltage | 21.6 to 26.4 VDC | - | $\triangle$ | External power supply is not required. |
|  | Current | 0.35A |  |  |  |
| Inrush current |  | 2.4 A | - |  |  |
| External dimensions |  | $250(\mathrm{H}) \times 37.5(\mathrm{~W}) \times 121(\mathrm{D}) \mathrm{mm}$ | $130(\mathrm{H}) \times 34.5(\mathrm{~W}) \times 93.6$ (D) mm | $\triangle$ | The dimensions are different. |
| Weight |  | 0.5 kg | 0.32 kg | $\bigcirc$ |  |

(b) Function comparison

O: Compatible, $\Delta$ : Partial change required, $x$ : Incompatible, —: Additional function

| Item | A62DA-S1 | A1S62DA | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: |
| Output HOLD/ CLEAR function | The HOLD/CLEAR of analog output can be set by the setting pin. <br> 1) HOLD side: <br> Holds the value before the output enable signal is OFF. <br> 2) CLEAR side: <br> Outputs the offset value. | When the PLC CPU goes into STOP, or when D/A conversion of this module is stopped by an error, whether to hold the last analog value output from each channel or to clear it (offset value or $0 \mathrm{~V} / 0 \mathrm{~mA}$ output) can be selected. This is selected for all channels with the HOLD/CLEAR terminal. | $\triangle$ | Check the analog output status combination list. |
| D/A conversion enable/disable function | - | Allows users to specify whether to output a D/A conversion value for each channel or an offset value by setting the D/A conversion value output enable flag to ON/OFF in the sequence program. <br> The D/A conversion time (conversion speed) is fixed regardless of the D/A conversion value output disable channel setting. | - | - |
| Analog output enable/disable function | - | Allows users to specify whether to enable or disable the output of analog values to external devices by writing $0 / 1$ to address 0 for each channel in the sequence program. | - | - |

## (c) Comparison of I/O signals for PLC CPU

Modifying sequence programs is required as the I/O signals differ.
For details of the I/O signals and sequence programs, refer to the user's manual.

| A62DA-S1 |  |  |  | A1S62DA |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Device NO. | Signal name | Device NO. | Signal name | Device NO. | Signal name | Device NO. | Signal name |
| X0 | WDT error (A62DA-S1 detection) | YO | Not used | X0 | WDT error (A1S62DA detection) | YO | Not used |
| X1 | D/A conversion READY | Y1 |  | X1 | D/A conversion READY | Y1 |  |
| X2 | Not used | Y2 |  | X2 | Error | Y2 |  |
| X3 |  | Y3 |  | X3 | Not used | Y3 |  |
| X4 |  | Y4 |  | X4 |  | Y4 |  |
| X5 |  | Y5 |  | X5 |  | Y5 |  |
| X6 |  | Y6 |  | X6 |  | Y6 |  |
| X7 |  | Y7 |  | X7 |  | Y7 |  |
| X8 |  | Y8 |  | X8 |  | Y8 |  |
| X9 |  | Y9 |  | X9 |  | Y9 |  |
| XA |  | YA |  | XA |  | YA |  |
| XB |  | YB |  | XB |  | YB |  |
| XC |  | YC |  | XC |  | YC |  |
| XD |  | YD |  | XD |  | YD |  |
| XE |  | YE |  | XE |  | YE |  |
| XF |  | YF |  | XF |  | YF |  |
| X10 |  | Y10 |  | X10 |  | Y10 | CH1 D/A conversion output enable |
| X11 |  | Y11 |  | X11 |  | Y11 | CH2 D/A conversion output enable |
| X12 |  | Y12 |  | X12 |  | Y12 | Not used |
| X13 |  | Y13 |  | X13 |  | Y13 |  |
| X14 |  | Y14 |  | X14 |  | Y14 |  |
| X15 |  | Y15 |  | X15 |  | Y15 |  |
| X16 |  | Y16 |  | X16 |  | Y16 |  |
| X17 |  | Y17 |  | X17 |  | Y17 |  |
| X18 |  | Y18 |  | X18 |  | Y18 | Error reset |
| X19 |  | Y19 |  | X19 |  | Y19 | Not used |
| X1A |  | Y1A |  | X1A |  | Y1A |  |
| X1B |  | Y1B | Output enable | X1B |  | Y1B |  |
| X1C |  | Y1C | Not used | X1C |  | Y1C |  |
| X1D |  | Y1D |  | X1D |  | Y1D |  |
| X1E |  | Y1E |  | X1E |  | Y1E |  |
| X1F |  | Y1F |  | X1F |  | Y1F |  |

## (d) Buffer memory address comparison

Modifying sequence program is required as the assignment of buffer memory differs. For details of the buffer memory and sequence programs, refer to the user's manual.

| Address | A62DA-S1 |  | Address | A1S62DA |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | Name | Read/Write |  | Name | Default value | Read/Write |
| 0 | CH1 Digital value | R/W | 0 | Analog output enable/disable channel | 0 |  |
| 1 | CH 2 Digital value |  | 1 | CH1 Digital value | 0 | R/W |
| 2 | CH1 Upper limit check code |  | 2 | CH 2 Digital value | 0 |  |
| 3 | CH1 Lower limit check code |  | 3 | Not used | - | - |
| 4 | CH2 Upper limit check code |  | 4 |  |  |  |
| 5 | CH2 Lower limit check code |  | 5 |  |  |  |
|  |  |  | 6 |  |  |  |
|  |  |  | 7 |  |  |  |
|  |  |  | 8 |  |  |  |
|  |  |  | 9 | Resolution of digital value | $1(\times 1)$ | R/W |
|  |  |  | 10 | CH1 Set value check code | 0 |  |
|  |  |  | 11 | CH 2 Set value check code | 0 |  |
|  |  |  | 12 | Not used | - | - |
|  |  |  | 13 |  |  |  |
|  |  |  | 14 |  |  |  |
|  |  |  | 15 |  |  |  |
|  |  |  | 16 |  |  |  |
|  |  |  | 17 |  |  |  |

(5) Comparisons between A68DAI-S1 and A1S68DAI
(a) Performance specifications comparison

O: Compatible, $\Delta:$ Partial change required, $x$ : Incompatible

$\bigcirc$ : Compatible, $\Delta$ : Partial change required, $\times$ : Incompatible

| Item |  | A68DAl-S1 | A1S68DAI | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: | :---: |
| External power supply | ® <br> $\frac{\pi}{\#}$ | 21.6 to 26.4VDC | - | $\triangle$ | External power supply is not required. |
|  | $\stackrel{\rightharpoonup}{0}$ Ǔ U | 0.4A |  |  |  |
| External dimensions |  | $250(\mathrm{H}) \times 37.5(\mathrm{~W}) \times 131(\mathrm{D}) \mathrm{mm}$ | $130(\mathrm{H}) \times 34.5(\mathrm{~W}) \times 93.6$ (D) mm | $\triangle$ | The dimensions are different. |
| Weight |  | 0.65 kg | 0.28 kg | $\bigcirc$ |  |

(b) Function comparison

O: Compatible, $\Delta$ : Partial change required, $x$ : Incompatible, —: Additional function

| Item | A68DAl-S1 | A1S68DAI | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: |
| HOLD/ CLEAR setting | Allows users to select whether to hold the last analog value output from each channel or to clear it (offset value or OmA output), when the PLC CPU goes into the STOP status, or when digital-to-analog conversion is stopped by an A68DAI-S1 error: one setting is made for all channels using the HLD/CLR terminal on the front face of the module. | Allows users to select whether to hold the last analog value output from each channel or to clear it ( 0 or 4 mA output), when the PLC CPU goes into the STOP status, or when digital-toanalog conversion is stopped by an A1S68DAI error: one setting is made for all channels using the HLD/CLR terminal on the front face of the module. | $\triangle$ | Since A1S68DAI does not have the offset adjustment, the offset value is not output, and $0 \mu \mathrm{~A}$ is output when cleared. |
| D/A conversion value output enable flag | Allows users to specify whether to output a D/A conversion value for each channel or an offset value by setting the D/A conversion value output enable flag to ON/OFF in the sequence program. <br> The D/A conversion time (conversion speed) is fixed regardless of the D/A conversion value disable channel setting. | Allows users to specify whether to output a D/A conversion value for each channel or 4 mA by setting the D/A conversion value output enable flag to ON/OFF in the sequence program. <br> The D/A conversion time (conversion speed) is fixed regardless of the D/A conversion value disable channel setting. | $\triangle$ | Check the analog output status combination list. |
| Analog output enable/ disable | Allows specifying whether to enable or disable analog value output to external devices by writing $0 / 1$ to address 0 for each channel in the sequence program. |  | $\bigcirc$ |  |
| Offset/gain setting | The I/O conversion characteristics can be changed. | - | $\times$ | No offset/gain setting function. Fixed to 4 to 20 mA output. Adjust output with the digital input. |

## (c) Comparison of I/O signals for PLC CPU

Modifying sequence programs is required as the I/O signals differ.
For details of the I/O signals and sequence programs, refer to the user's manual.

| A68DAl-S1 |  |  |  | A1S68DAI |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| $\begin{gathered} \text { Device } \\ \text { NO. } \end{gathered}$ | Signal name | Device NO. | Signal name | Device NO. | Signal name | Device NO. | Signal name |
| X0 | WDT error (A68DAI-S1 detection) | YO | Not used | X0 | WDT error (A1S68DAI detection) | Y0 | Not used |
| X1 | D/A conversion READY | Y1 |  | X1 | D/A conversion READY | Y1 |  |
| X2 | Not used | Y2 |  | X2 | Error | Y2 |  |
| X3 |  | Y3 |  | X3 | Not used | Y3 |  |
| X4 |  | Y4 |  | X4 |  | Y4 |  |
| X5 |  | Y5 |  | X5 |  | Y5 |  |
| X6 |  | Y6 |  | X6 |  | Y6 |  |
| X7 |  | Y7 |  | X7 |  | Y7 |  |
| X8 |  | Y8 |  | X8 |  | Y8 |  |
| X9 |  | Y9 |  | X9 |  | Y9 |  |
| XA |  | YA |  | XA |  | YA |  |
| XB |  | YB |  | XB |  | YB |  |
| XC |  | YC |  | XC |  | YC |  |
| XD |  | YD |  | XD |  | YD |  |
| XE |  | YE |  | XE |  | YE |  |
| XF |  | YF |  | XF |  | YF |  |
| X10 |  | Y10 | D/A conversion output enable | X10 |  | Y10 | D/A conversion output enable |
| X11 |  | Y11 |  | X11 |  | Y11 |  |
| X12 |  | Y12 |  | X12 |  | Y12 |  |
| X13 |  | Y13 |  | X13 |  | Y13 |  |
| X14 |  | Y14 |  | X14 |  | Y14 |  |
| X15 |  | Y15 |  | X15 |  | Y15 |  |
| X16 |  | Y16 |  | X16 |  | Y16 |  |
| X17 |  | Y17 |  | X17 |  | Y17 |  |
| X18 |  | Y18 | Error reset | X18 |  | Y18 | Error reset |
| X19 |  | Y19 | Not used | X19 |  | Y19 | Not used |
| X1A |  | Y1A |  | X1A |  | Y1A |  |
| X1B |  | Y1B |  | X1B |  | Y1B |  |
| X1C |  | Y1C |  | X1C |  | Y1C |  |
| X1D |  | Y1D |  | X1D |  | Y1D |  |
| X1E |  | Y1E |  | X1E |  | Y1E |  |
| X1F |  | Y1F |  | X1F |  | Y1F |  |

(d) Buffer memory address comparison

Modifying sequence programs is required as the assignment of buffer memory differs. For details of the buffer memory and sequence programs, refer to the user's manual.

| Address | A68DAl-S1 |  | A1S68DAI |  |
| :---: | :---: | :---: | :---: | :---: |
|  | Buffer memory name | Default value | Name | Default value |
| 0 | Analog output enable/disable channel | 0000H <br> (All channels are set to "enable".) | Analog output enable/disable channel | 0000H <br> (All channels are set to "enable".) |
| 1 | CH1 Digital value | 0 | CH1 Digital value | 0 |
| 2 | CH 2 Digital value |  | CH 2 Digital value |  |
| 3 | CH3 Digital value |  | CH3 Digital value |  |
| 4 | CH 4 Digital value |  | CH 4 Digital value |  |
| 5 | CH5 Digital value |  | CH5 Digital value |  |
| 6 | CH6 Digital value |  | CH6 Digital value |  |
| 7 | CH7 Digital value |  | CH7 Digital value |  |
| 8 | CH8 Digital value |  | CH8 Digital value |  |
| 9 | Resolution of digital value | 1(1/4000) | Not used | - |
| 10 | CH1 Set value check code | 0 | CH1 Set value check code | 0 |
| 11 | CH 2 Set value check code |  | CH 2 Set value check code |  |
| 12 | CH3 Set value check code |  | CH3 Set value check code |  |
| 13 | CH4 Set value check code |  | CH 4 Set value check code |  |
| 14 | CH5 Set value check code |  | CH5 Set value check code |  |
| 15 | CH6 Set value check code |  | CH6 Set value check code |  |
| 16 | CH 7 Set value check code |  | CH7 Set value check code |  |
| 17 | CH8 Set value check code |  | CH8 Set value check code |  |

(6) Comparisons between A68DAV and A1S68DAV
(a) Performance specifications comparison

O: Compatible, $\Delta$ : Partial change required, $x$ : Incompatible

| Item | A68DAV |  |  |  |  |  | 8DAV | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Digital input | (1) 16 -bit, signed binary value <br> (2) Setting range: |  |  |  |  | (1) 16 -bit, signed binary value <br> (2) Setting range: -2048 to 2047 |  | $\triangle$ | Only -2048 to 2047 for setting range |
|  | Settingresolution |  |  | Setting range |  |  |  |  |  |
|  | 1/4000 |  |  | to 4000 |  |  |  |  |  |
|  | 188000 |  |  | 000 8000 |  |  |  |  |  |
|  | 1/12000 -12000 to 12000 |  |  |  |  |  |  |  |  |
| Analog output | -10 to 0 to 10 VDC(External load resistance: $2 \mathrm{k} \Omega$ to $1 \mathrm{M} \Omega$ ) |  |  |  |  | (External load re | $010 \text { VDC }$ <br> ance: $2 \mathrm{k} \Omega$ to $1 \mathrm{M} \Omega$ ) | $\bigcirc$ |  |
| I/O characteristics |  | Digital value resolution |  |  | *Analog output value |  |  | $\triangle$ | I/O conversion characteristics are fixed as in the left. |
|  |  | 1/4000 | 1/8000 | 1/12000 |  | Digital input value | $\begin{gathered} \text { Analog output } \\ \text { value } \end{gathered}$ |  |  |
|  |  | 4000 | 8000 | 12000 | +10V | 2000 | 10 V |  |  |
|  |  | 2000 | 4000 | 6000 | +5V | 1000 | 5 V |  |  |
|  |  | 0 | 0 | 0 | ov | 0 | ov |  |  |
|  |  | -2000 | -4000 | -6000 | -5V | -1000 | $-5 \mathrm{~V}$ |  |  |
|  |  | -4000 | -8000 | -12000 | -10V | -2000 | -10V |  |  |
|  | ${ }^{*}$ When offset value 0 V , gain value 10 V settings |  |  |  |  |  |  |  |  |
| Maximum resolution of analog value | 2.5 mV $(1 / 4000)$ <br> 1.25 mV $(1 / 8000)$ <br> 0.83 mV $(1 / 12000)$ |  |  |  |  | 5 mV |  | $\times$ | Resolution is different. |
| Overall accuracy (Accuracy in respect to maximum value) | $\pm 1.0 \%( \pm 100 \mathrm{mV})$ |  |  |  |  | $\pm 1.0 \%( \pm 100 \mathrm{mV})$ |  | $\bigcirc$ |  |
| Conversion speed | Within $40 \mathrm{~ms} / 8$ channel (Same for 1 channel) Note) <br> Time period from digital input write until specified analog voltage (current) reached. |  |  |  |  | Within $4 \mathrm{~ms} / 8$ channels <br> For high frequency of access from the PLC CPU using FROM/TO instructions, this can be extended up to about 6 ms . |  | $\bigcirc$ |  |
| Absolute maximum output | $-12 \text { to }+12 \mathrm{~V}$ <br> Note) <br> The voltage exceeding the above is not output due to output protection circuit. |  |  |  |  |  |  | $\bigcirc$ |  |
| Number of analog output points | 8 channels/module |  |  |  |  | 8 channels/module |  | $\bigcirc$ |  |
| Output short protection | - |  |  |  |  |  | ided | - |  |
| Isolation method | Between the output terminals and PLC power supply: <br> photocoupler isolation <br> Between channels: non-isolated |  |  |  |  | Between the output <br> photoco <br> Between output | minals and PLC power ply: <br> er isolation <br> nnels: non-isolated | $\bigcirc$ |  |
| Occupied I/O points | 32 points (I/O assignment: special 32 points) |  |  |  |  | (I/O assignm | oints <br> special 32 points) | $\bigcirc$ |  |
| Connected terminal | 38-point terminal block |  |  |  |  | $\begin{array}{r} \text { 20-point } \\ \text { (M3.5 } \end{array}$ | minal block <br> screws) | $\times$ | External wiring must be changed. |
| Applicable wire size | 0.75 to $2 \mathrm{~mm}^{2}$(Applicable tightening torque 39 to $59 \mathrm{~N} \cdot \mathrm{~cm}$ ) |  |  |  |  | 0.7 | $1.5 \mathrm{~mm}^{2}$ | $\triangle$ |  |
| Applicable solderless terminal | $\begin{gathered} \text { V1.25-3, V1.25-YS3A } \\ \text { V2-S3, V2-YS3A } \end{gathered}$ |  |  |  |  | $\begin{array}{r} \mathrm{R} 1.25-3 \\ \text { RAV1.25 } \end{array}$ | $\begin{aligned} & 1.25-\mathrm{YS} 3 \mathrm{~A}, \\ & \text { V1.25-YS3A } \end{aligned}$ | $\triangle$ |  |
| Internal power consumption (5VDC) | 0.15A |  |  |  |  | 0.65A |  | $\Delta$ | Recalculation of internal current consumption [5VDC] is required. |

$\bigcirc$ : Compatible, $\Delta$ : Partial change required, $x$ : Incompatible

| Item |  | A68DAV | A1S68DAV | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: | :---: |
| External power supply | ¢ \% $\stackrel{\pi}{0}$ $>$ | 21.6 to 26.4VDC | - | $\triangle$ | External power supply is not required. |
|  |  | 0.2A |  |  |  |
| External dimensions |  | $250(\mathrm{H}) \times 37.5(\mathrm{~W}) \times 131$ (D) mm | 130(H) $\times 34.5(\mathrm{~W}) \times 93.6$ (D)mm | $\triangle$ | The dimensions are different. |
| Weight |  | 0.6 kg | 0.28 kg | $\bigcirc$ |  |

## (b) Function comparison

O: Compatible, $\Delta$ : Partial change required, $\times$ : Incompatible, - : Additional function

| Item | A68DAV | A1S68DAV | Compatibility | precautions for replacement |
| :---: | :---: | :---: | :---: | :---: |
| HOLD/ CLEAR setting | When the PLC CPU is in STOP, or when D/A conversion is stopped in A68DAV by an error, whether to hold the last analog value output or to clear it (offset value or OV output) can be selected for all channels with the HOLD/CLEAR terminal on the front face. | When the PLC CPU is in STOP, or when D/A conversion is stopped in A1S68DV by an error, whether to hold the last analog value output or to clear it ( 0 V output) may be selected for all channels with the HOLD/ CLEAR terminal on the front face. | $\triangle$ | Since A1S68DAI does not have the offset adjustment, the offset value is not output, and 0 V is output when cleared. |
| D/A <br> conversion <br> value output <br> enable flag | Allows users to specify whether to output a D/A conversion value or an offset value by setting the D/A conversion value output enable flag to ON/OFF in the sequence program. <br> The D/A conversion time (conversion speed) is fixed regardless of the D/A conversion value disable channel setting. | Allows users to specify whether to output a D/A conversion value or 0 V by setting the D/A conversion value output enable flag to ON/OFF in the sequence program. The D/A conversion time (conversion speed) is fixed regardless of the D/A conversion value disable channel setting. | $\triangle$ | Check the analog output status combination list. |
| Analog output enable/disable setting | Allows specifying whether to enable or disable analog value output to external devices by writing $0 / 1$ to address 0 for each channel in the sequence program. |  | $\bigcirc$ |  |
| Offset/gain setting | Provided | Not provided | $\times$ | No offset/gain setting function. Fixed to -10 to 10 V output. Adjust output with the digital input. |

(c) Comparison of I/O signals for PLC CPU

Modifying sequence programs is required as the I/O signals differ.
For details of the I/O signals and sequence programs, refer to the user's manual.

| A68DAV |  |  |  | A1S68DAV |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Device NO. | Signal name | Device NO. | Signal name | Device NO. | Signal name | Device NO. | Signal name |
| X0 | WDT error (A68DAV detection) | YO | Not used | X0 | WDT error (A1S68DAV detection) | Y0 | Not used |
| X1 | D/A conversion READY | Y1 |  | X1 | D/A conversion READY | Y1 |  |
| X2 | Error | Y2 |  | X2 | Error | Y2 |  |
| X3 | Not used | Y3 |  | X3 | Not used | Y3 |  |
| X4 |  | Y4 |  | X4 |  | Y4 |  |
| X5 |  | Y5 |  | X5 |  | Y5 |  |
| X6 |  | Y6 |  | X6 |  | Y6 |  |
| X7 |  | Y7 |  | X7 |  | Y7 |  |
| X8 |  | Y8 |  | X8 |  | Y8 |  |
| X9 |  | Y9 |  | X9 |  | Y9 |  |
| XA |  | YA |  | XA |  | YA |  |
| XB |  | YB |  | XB |  | YB |  |
| XC |  | YC |  | XC |  | YC |  |
| XD |  | YD |  | XD |  | YD |  |
| XE |  | YE |  | XE |  | YE |  |
| XF |  | YF |  | XF |  | YF |  |
| X10 |  | Y10 | D/A conversion output enable | X10 |  | Y10 | D/A conversion output enable |
| X11 |  | Y11 |  | X11 |  | Y11 |  |
| X12 |  | Y12 |  | X12 |  | Y12 |  |
| X13 |  | Y13 |  | X13 |  | Y13 |  |
| X14 |  | Y14 |  | X14 |  | Y14 |  |
| X15 |  | Y15 |  | X15 |  | Y15 |  |
| X16 |  | Y16 |  | X16 |  | Y16 |  |
| X17 |  | Y17 |  | X17 |  | Y17 |  |
| X18 |  | Y18 | Error reset | X18 |  | Y18 | Error reset |
| X19 |  | Y19 | Not used | X19 |  | Y19 | Not used |
| X1A |  | Y1A |  | X1A |  | Y1A |  |
| X1B |  | Y1B |  | X1B |  | Y1B |  |
| X1C |  | Y1C |  | X1C |  | Y1C |  |
| X1D |  | Y1D |  | X1D |  | Y1D |  |
| X1E |  | Y1E |  | X1E |  | Y1E |  |
| X1F |  | Y1F |  | X1F |  | Y1F |  |

## (d) Buffer memory address comparison

Modifying sequence programs is required as the assignment of buffer memory differs. For details of the buffer memory and sequence programs, refer to the user's manual.

| Address | A68DAV |  | A1S68DAV |  |
| :---: | :---: | :---: | :---: | :---: |
|  | Name | Default value | Name | Default value |
| 0 | Analog output enable/disable channel | 0000H <br> (All channels are set to "enable".) | Analog output enable/disable channel | $0000 \mathrm{H}$ <br> (All channels are set to "enable".) |
| 1 | CH1 Digital value | 0 | CH1 Digital value | 0 |
| 2 | CH2 Digital value |  | CH 2 Digital value |  |
| 3 | CH3 Digital value |  | CH 3 Digital value |  |
| 4 | CH4 Digital value |  | CH4 Digital value |  |
| 5 | CH5 Digital value |  | CH5 Digital value |  |
| 6 | CH6 Digital value |  | CH6 Digital value |  |
| 7 | CH7 Digital value |  | CH7 Digital value |  |
| 8 | CH8 Digital value |  | CH8 Digital value |  |
| 9 | Resolution of digital value | 1(1/4000) | Not used | - |
| 10 | CH 1 Set value check code | 0 | CH 1 Set value check code | 0 |
| 11 | CH 2 Set value check code |  | CH 2 Set value check code |  |
| 12 | CH3 Set value check code |  | CH3 Set value check code |  |
| 13 | CH4 Set value check code |  | CH4 Set value check code |  |
| 14 | CH5 Set value check code |  | CH5 Set value check code |  |
| 15 | CH6 Set value check code |  | CH6 Set value check code |  |
| 16 | CH7 Set value check code |  | CH 7 Set value check code |  |
| 17 | CH8 Set value check code |  | CH8 Set value check code |  |

### 10.2.3 Temperature input module comparison

(1) Comparisons between A616TD and A1S68TD
(a) Performance specifications comparison

|  | Item | A616TD <br> (A60MXT, A60MXTN when using the combination.) | A1S68TD |  |  |  | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Temperature sensor input |  | -200 to $1800\left[{ }^{\circ} \mathrm{C}\right.$ ] | 0 to $1700\left[{ }^{\circ} \mathrm{C}\right]$ |  |  |  | $\triangle$ | Input temperature range differs. |
| $\begin{aligned} & \text { 亳 } \\ & \text { B } \end{aligned}$ | Digital output value | $\begin{aligned} & \text { 16-bit, signed binary } \\ & \text { (0 to } 4000 \text { ) } \\ & \text { (data unit: } 12 \text { bits) } \end{aligned}$ | 16-bit, signed binary (0 to 2000) *Scaling value |  |  |  | $\times$ | Changes 0 to $4000 \rightarrow 0$ to 2000. |
|  | Detected temperature value | 16-bit, signed binary (-2000 to 18000: <br> value to 1 decimal place $\times 10$ ) | 16-bit, signed binary <br> ( 0 to 17000: value to 1 decimal place $\times 10$ ) |  |  |  | $\triangle$ | Detected temperature range differs. |
| Applicable thermocouple |  | Refer to (e) Applicable thermocouples and measured temperature range accuracies. | Refer to (e) Applicable thermocouples and measured temperature range accuracies. |  |  |  | $\triangle$ | Check the applicable thermocouple. |
| Measured temperature range accuracies |  | Refer to (e) Applicable thermocouples and measured temperature range accuracies. | Refer to (e) Applicable thermocouples and measured temperature range accuracies. |  |  |  | $\times$ | Conversion accuracy differs. |
| Overall accuracy [\%] |  | Shown in the list of (e) Applicable thermocouples and measured temperature range accuracies. Measured temperature range accuracies $\pm 0.5^{\circ} \mathrm{C}$ | Conversion accuracy + Temperature characteristic $\times$ Operating ambient temperature variation $+ \pm 1^{\circ} \mathrm{C}$ (Cold junction compensation accuracy) |  |  |  |  |  |
| Cold junction compensation accuracy range |  | -20 to $80\left[{ }^{\circ} \mathrm{C}\right]$ <br> (RTD Pt100 included) | - |  |  |  | - |  |
| Maximum conversion speed |  | 50ms/channel | $400 \mathrm{~ms} / 8$ channel |  |  |  | $\Delta$ | Sampling period is not changed according to No. of channels used. |
| Isolation method |  | Between the input terminals and PLC: <br> photocoupler isolation <br> Between channels: <br> non-isolated ( $1 \mathrm{M} \Omega$ resistor isolation) | Specific isolation area | Isolation method | Dielectric withstand voltage | Insulation resistance | $\bigcirc$ |  |
|  |  |  | Transformer isolation | 500VAC for 1 minute | $5 \mathrm{M} \Omega$ or higher with a 500VDC insulation resistance tester |  |  |  |
|  |  | Between thermocouple input channels channels |  |  |  |  |  |  |
|  |  | Between the cold junction compensation input (Pt100) and PLC power supply | Not isolated | - | - |  |  |  |
| Temperature sensor input points |  |  | 15point/A60MXT, A60MXTN (Up to 7 A60MXT/A60MXTN can be connected to each A616TD.) | 8 channels+Pt100 connection channel/module |  |  |  | $\times$ | Since the number of channels is reduced, the number of modules installed may increase. In addition, the number of channels cannot be increased by connecting a multiplexer module. |
| Occupied I/O points |  |  | 32 points (I/O assignment: special 32 points) | 32 points(I/O assignment: special 32 points) |  |  |  | $\bigcirc$ |  |
| Connected terminal |  |  | 38-point terminal block | 20-point terminal block |  |  |  | $\times$ | External wiring must be changed. |
| Applicable wire size |  | 0.75 to $2 \mathrm{~mm}^{2}$ <br> (Applicable tightening torque 39 to $59 \mathrm{~N} . \mathrm{cm}$ ) | 0.75 to $1.5 \mathrm{~mm}^{2}$ |  |  |  | $\Delta$ |  |  |
| Applicable solderless terminal |  | $\begin{gathered} \text { V1.25-3, V1.25-YS3A, } \\ \text { V2-S3, V2-YS3A } \end{gathered}$ | R1.25-3, 1.25-YS3A, RAV1.25-3, V1.25-YS3A |  |  |  | $\triangle$ |  |  |

O: Compatible, $\Delta$ : Partial change required, $\times$ : Incompatible

| Item | A616TD <br> (A60MXT, A60MXTN when using <br> the combination.) | A1S68TD | Compati- <br> bility | Precautions for <br> replacement |
| :--- | :---: | :---: | :---: | :---: |
| Internal current <br> consumption <br> $(5 \mathrm{VDC})$ | 1.0 A | 0.32 A | O |  |
| External <br> dimensions | $250(\mathrm{H}) \times 37.5(\mathrm{~W}) \times 131(\mathrm{D}) \mathrm{mm}$ | $130(\mathrm{H}) \times 34.5(\mathrm{~W}) \times 93.6(\mathrm{D}) \mathrm{mm}$ | $\Delta$ | The dimensions are <br> different. |
| Weight | 0.85 kg | 0.28 kg | O |  |

## (b) Function comparison

O: Compatible, $\Delta$ : Partial change required, $\times$ : Incompatible, —: Additional function


## (c) Comparison of I/O signals for PLC CPU

Modifying sequence programs is required as the I/O signals differ.
For details of the I/O signals and sequence programs, refer to the user's manual.

| Signal direction: A616TD $\rightarrow$ PLC CPU |  | Signal direction: PLC CPU $\rightarrow$ A616TD |  | Signal direction:$\text { A1S68TD } \rightarrow \text { CPU }$ |  | Signal direction: CPU $\rightarrow$ A1S68TD |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Device NO. | Signal name | Device NO. | Signal name | Device NO. | Signal name | Device NO. | Signal name |
| X0 | WDT error (A616TD detection) | Y0 | Not used | X0 | WDT error | Y0 | Not used |
| X1 | A/D conversion READY | Y1 |  | X1 | A/D conversion READY | Y1 |  |
| X2 | Error | Y2 |  | X2 | Error | Y2 |  |
| X3 | Wire break error detection | Y3 |  | X3 | Disconnection detection | Y3 |  |
| X4 | Digital output value out-of-range detected | Y4 |  | X4 | Exceeding measurement range | Y4 |  |
| X5 | Temperature output value out-of-range detected | Y5 |  | X5 | Not used | Y5 |  |
| X6 | Not used | Y6 |  | X6 |  | Y6 |  |
| X7 |  | Y7 |  | X7 |  | Y7 |  |
| X8 |  | Y8 |  | X8 |  | Y8 |  |
| X9 |  | Y9 |  | X9 |  | Y9 |  |
| XA |  | YA |  | XA |  | YA |  |
| XB |  | YB |  | XB |  | YB |  |
| XC |  | YC |  | XC |  | YC |  |
| XD |  | YD |  | XD |  | YD |  |
| XE |  | YE |  | XE |  | YE |  |
| XF |  | YF |  | XF |  | YF |  |
| X10 |  | Y10 | Detected temperature value LED display request signal | X10 |  | Y10 |  |
| X11 |  | Y11 | Not used | X11 |  | Y11 | Set lower/upper limit value update instruction |
| X12 |  | Y12 |  | X12 |  | Y12 | Error reset |
| X13 |  | Y13 |  | X13 |  | Y13 | Not used |
| X14 |  | Y14 |  | X14 |  | Y14 |  |
| X15 |  | Y15 |  | X15 |  | Y15 |  |
| X16 |  | Y16 |  | X16 |  | Y16 |  |
| X17 |  | Y17 |  | X17 |  | Y17 |  |
| X18 |  | Y18 |  | X18 |  | Y18 |  |
| X19 |  | Y19 |  | X19 |  | Y19 |  |
| X1A |  | Y1A |  | X1A |  | Y1A |  |
| X1B |  | Y1B |  | X1B |  | Y1B |  |
| X1C |  | Y1C |  | X1C |  | Y1C |  |
| X1D |  | Y1D |  | X1D |  | Y1D |  |
| X1E |  | Y1E |  | X1E |  | Y1E |  |
| X1F |  | Y1F |  | X1F |  | Y1F |  |

(d) Buffer memory address comparison

Modifying sequence programs is required as the assignment of buffer memory differs. For details of the buffer memory and sequence programs, refer to the user's manual.

| Address | A616TD |  | Read/Write | Address | A1S68TD | Read/Write |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| 0 | Data format selection |  | R/W | 0 | A/D conversion enable/disable setting | R/W |
| 1 | Error code storage |  |  | 1 | Error code | R |
| 2 | Faulty A60MX $\square$ CONNECT No. storage |  |  | 2 | Disconnection detection flag |  |
| 3 | Thermocouple-type setting error and channel number storage |  |  | 3 | Exceeding measurement range |  |
| 4 | Current sampling cycle storage |  | R | 4 | Not used | - |
| 5 | Not used |  |  | 5 |  |  |
| 6 |  |  | 6 |  |  |
| 7 |  |  | 7 |  |  |
| 8 |  |  | 8 |  |  |
| 9 |  |  | 9 |  |  |
| 10 |  |  | 10 | CH 1 Converted temperature value ( $0.1{ }^{\circ} \mathrm{C}$ units) | R |  |
| 11 |  |  | 11 | CH 2 Converted temperature value ( $0.1^{\circ} \mathrm{C}$ units) |  |  |
| 12 |  |  | 12 | CH 3 Converted temperature value ( $0.1{ }^{\circ} \mathrm{C}$ units) |  |  |
| 13 |  |  | 13 | CH 4 Converted temperature value ( $0.1{ }^{\circ} \mathrm{C}$ units) |  |  |
| 14 |  |  | 14 | CH 5 Converted temperature value ( $0.1{ }^{\circ} \mathrm{C}$ units) |  |  |
| 15 | Conversion enable/ disable designation | A616TD |  | R/W |  | 15 | CH 6 Converted temperature value ( $0.1^{\circ} \mathrm{C}$ units) |
| 16 |  | Multiplex module |  |  |  | 16 | CH 7 Converted temperature value ( $0.1{ }^{\circ} \mathrm{C}$ units) |
| 17 |  |  |  |  |  | 17 | CH 8 Converted temperature value ( $0.1^{\circ} \mathrm{C}$ units) |
| 18 |  |  |  |  | 18 | Not used | - |
| 19 |  |  |  |  | 19 |  |  |
| 20 |  |  |  |  | 20 | CH 1 Scaling value | R |
| 21 |  |  |  |  | 21 | CH 2 Scaling value |  |
| 22 |  |  |  |  | 22 | CH3 Scaling value |  |
| 23 |  |  |  |  | 23 | CH 4 Scaling value |  |
| 24 | Set data setting request |  | 24 |  | CH5 Scaling value |  |  |
| 25 | Not used |  | R | 25 | CH6 Scaling value |  |  |
| 26 |  |  | 26 | CH 7 Scaling value |  |  |  |
| 27 |  |  | 27 | CH8 Scaling value |  |  |  |
| 28 |  |  | 28 | A/D conversion completion |  |  |  |
| 29 |  |  | 29 | Not used | - |  |  |
| 30 |  |  | 30 | CH 1 Lower limit value ( $0.1{ }^{\circ} \mathrm{C}$ units) | R/W |  |  |
| 31 |  |  | 31 | CH 1 Upper limit value ( $0.1{ }^{\circ} \mathrm{C}$ units) |  |  |  |
| 32 | Disconnection detection enable/disable designation |  |  | R/W |  | 32 | CH 2 Lower limit value ( $0.1{ }^{\circ} \mathrm{C}$ units) |
| 33 |  |  | 33 |  |  | CH 2 Upper limit value ( $0.1{ }^{\circ} \mathrm{C}$ units) |  |
| 34 |  |  | 34 |  |  | CH 3 Lower limit value ( $0.1{ }^{\circ} \mathrm{C}$ units) |  |
| 35 |  |  | 35 |  |  | CH 3 Upper limit value ( $0.1{ }^{\circ} \mathrm{C}$ units) |  |
| 36 |  |  | 36 |  |  | CH 4 Lower limit value ( $0.1{ }^{\circ} \mathrm{C}$ units) |  |
| 37 |  |  | 37 |  |  | CH 4 Upper limit value ( $0.1{ }^{\circ} \mathrm{C}$ units) |  |
| 38 |  |  | 38 |  |  | CH5 Lower limit value ( $0.1{ }^{\circ} \mathrm{C}$ units) |  |
| 39 |  |  | 39 |  |  | CH5 Upper limit value ( $0.1{ }^{\circ} \mathrm{C}$ units) |  |
| 40 | Not used |  |  | R |  | 40 | CH6 Lower limit value ( $0.1{ }^{\circ} \mathrm{C}$ units) |
| 41 |  |  | 41 |  |  | CH6 Upper limit value ( $0.1{ }^{\circ} \mathrm{C}$ units) |  |
| 42 |  |  | 42 |  |  | CH 7 Lower limit value ( $0.1{ }^{\circ} \mathrm{C}$ units) |  |
| 43 |  |  | 43 |  |  | CH 7 Upper limit value ( $0.1{ }^{\circ} \mathrm{C}$ units) |  |
| 44 |  |  | 44 |  |  | CH8 Lower limit value ( $0.1{ }^{\circ} \mathrm{C}$ units) |  |
| 45 |  |  | 45 |  |  | CH8 Upper limit value ( $0.1{ }^{\circ} \mathrm{C}$ units) |  |
| 46 |  |  | 46 |  |  |  |  |
| 47 |  |  | 47 |  |  |  |  |
| $\begin{gathered} \hline 48 \\ \text { to } \\ 63 \\ \hline \end{gathered}$ | Digital output value temperature setting |  |  | R/W | 48 | Not used | - |
| $\begin{aligned} & 64 \\ & \text { to } \\ & 71 \end{aligned}$ | Disconnection detection channel number storage |  |  |  | 49 |  |  |


| Address | A616TD | Read/Write |
| :---: | :---: | :---: |
| $\begin{aligned} & 72 \\ & \text { to } \\ & 79 \end{aligned}$ | Not used | R |
| $\begin{aligned} & 80 \\ & \text { to } \\ & 87 \end{aligned}$ | No. of the channel where the digital output value is out of range | R/W |
| $\begin{aligned} & 88 \\ & \text { to } \\ & 95 \end{aligned}$ | Not used | R |
| $\begin{gathered} 96 \\ \text { to } \\ 103 \end{gathered}$ | No. of the channel where the detected temperature is out of range | R/W |
| $\begin{gathered} 104 \\ \text { to } \\ 111 \end{gathered}$ | Not used |  |
| $\begin{gathered} 112 \\ \text { to } \\ 127 \end{gathered}$ | Digital output value of INPUT channel | R |
| $\begin{gathered} 128 \\ \text { to } \\ 255 \\ \hline \end{gathered}$ | Error compensation settings | R/W |
| $\begin{gathered} 256 \\ \text { to } \\ 383 \end{gathered}$ | Thermocouple type setting |  |
| $\begin{gathered} 384 \\ \text { to } \\ 511 \end{gathered}$ | Digital output value of MX CH.channel |  |
| $\begin{gathered} 512 \\ \text { to } \\ 639 \end{gathered}$ | Detected temperature value of MX CH.channel |  |

(e) Applicable thermocouples and measured temperature range accuracies

| A616TD |  |  |  |  |  |  |  |  | A1S68TD |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| JIS | ANSI | DIN | BS | Measured range no. | 1 | 2 | 3 | 4 | JIS | - | - |
|  |  |  |  | Permitted input voltage range [mV] | -12.5 to 12.5 | 0 to 25 | 0 to 50 | 0 to 100 |  |  |  |
| B | B | - | $\begin{aligned} & \text { PtRh30 } \\ & \text {-PtRh6 } \end{aligned}$ | Temperature input range $\left[{ }^{\circ} \mathrm{C}\right.$ ] | 100 to 1500 | 100 to 1800 | 100 to 1800 | 100 to 1800 | B | Temperature input range $\left[{ }^{\circ} \mathrm{C}\right.$ ] | 800 to 1700 |
|  |  |  |  | Accuracy at $25^{\circ} \mathrm{C}$ [\%] | - | $\pm 0.5$ | - | - |  | Conversion accuracy at $25 \pm 5^{\circ} \mathrm{C}$ | $\pm 2.5^{\circ} \mathrm{C}$ |
|  |  |  |  | Temperature drift [\%/ ${ }^{\circ} \mathrm{C}$ ] |  | $\pm 0.013$ |  |  |  | Temperature characteristics | $\pm 0.4{ }^{\circ} \mathrm{C}$ |
| R | R | - | $\begin{aligned} & \text { PtRh13 } \\ & \text {-Pt } \end{aligned}$ | Temperature input range[ ${ }^{\circ} \mathrm{C}$ ] | 0 to 1000 | 0 to 1700 | 0 to 1700 | 0 to 1700 | R | Temperature input range[ ${ }^{\circ} \mathrm{C}$ ] | 300 to 1600 |
|  |  |  |  | Accuracy at $25^{\circ} \mathrm{C}$ [\%] | - | $\pm 0.4$ | - | - |  | Conversion accuracy at $25 \pm 5^{\circ} \mathrm{C}$ | $\pm 2^{\circ} \mathrm{C}$ |
|  |  |  |  | Temperature drift [\%/ ${ }^{\circ} \mathrm{C}$ ] |  | $\pm 0.011$ |  |  |  | Temperature characteristics | $\pm 0.3{ }^{\circ} \mathrm{C}$ |
| S | S | $\begin{gathered} \text { PtRh } \\ -\mathrm{Pt} \end{gathered}$ | PtRh10 | Temperature input range ${ }^{\circ} \mathrm{C}$ ] | 0 to 1200 | 0 to 1700 | 0 to 1700 | 0 to 1700 | S | Temperature input range ${ }^{\circ} \mathrm{C}$ ] | 300 to 1600 |
|  |  |  |  | Accuracy at $25^{\circ} \mathrm{C}$ [\%] | - | $\pm 0.4$ | - | - |  | Conversion accuracy at $25 \pm 5^{\circ} \mathrm{C}$ | $\pm 2^{\circ} \mathrm{C}$ |
|  |  |  |  | Temperature drift [\%/ ${ }^{\circ} \mathrm{C}$ ] |  | $\pm 0.011$ |  |  |  | Temperature characteristics | $\pm 0.3{ }^{\circ} \mathrm{C}$ |
| K | K | $\begin{gathered} \mathrm{NiCr} \\ -\mathrm{Ni} \end{gathered}$ | $\begin{gathered} \mathrm{NiCr} \\ \text {-NiAl } \end{gathered}$ | Temperature input range[ ${ }^{\circ} \mathrm{C}$ ] | -200 to 250 | 0 to 500 | 0 to 1000 | 0 to 1300 | K | Temperature input range[ ${ }^{\circ} \mathrm{C}$ ] | 0 to 1200 |
|  |  |  |  | Accuracy at $25^{\circ} \mathrm{C}$ [\%] | $\pm 0.4$ | $\pm 0.3$ | $\pm 0.3$ | $\pm 0.5$ |  | Conversion accuracy at $25 \pm 5^{\circ} \mathrm{C}$ | $\begin{aligned} & \pm 0.5^{\circ} \mathrm{C} \text { or } \\ & \pm 0.25 \% \text { of } \end{aligned}$ <br> measured temperature, whichever is greater. |
|  |  |  |  | Temperature drift [\%/ ${ }^{\circ} \mathrm{C}$ ] | $\pm 0.011$ | $\pm 0.01$ | $\pm 0.01$ | $\pm 0.013$ |  | Temperature characteristics | $\begin{aligned} & \pm 0.07^{\circ} \mathrm{C} \text { or } \\ & \pm 0.02 \% \text { of } \end{aligned}$ <br> measured temperature, whichever is greater. |
| E | E | - | $\begin{gathered} \mathrm{NiCr} \\ -\mathrm{CuNi} \end{gathered}$ | Temperature input range[ ${ }^{\circ} \mathrm{C}$ ] | -200 to 150 | 0 to 300 | 0 to 600 | 0 to 1000 | E | Temperature input range $\left[{ }^{\circ} \mathrm{C}\right.$ ] | 0 to 800 |
|  |  |  |  | Accuracy at $25^{\circ} \mathrm{C}$ [\%] | $\pm 0.4$ | $\pm 0.3$ | $\pm 0.3$ | $\pm 0.4$ |  | Conversion accuracy at $25 \pm 5^{\circ} \mathrm{C}$ | $\begin{aligned} & \pm 0.5^{\circ} \mathrm{C} \text { or } \\ & \pm 0.25 \% \text { of } \end{aligned}$ measured temperature, whichever is greater. |
|  |  |  |  | Temperature drift [\%/ ${ }^{\circ} \mathrm{C}$ ] | $\pm 0.011$ | $\pm 0.01$ | $\pm 0.01$ | $\pm 0.011$ |  | Temperature characteristics | $\begin{aligned} & \pm 0.07^{\circ} \mathrm{C} \text { or } \\ & \pm 0.02 \% \text { of } \end{aligned}$ measured temperature, whichever is greater. |
| J | J | - | $\begin{gathered} \mathrm{Fe} \\ -\mathrm{CuNi} \end{gathered}$ | Temperature input range $\left[{ }^{\circ} \mathrm{C}\right.$ ] | -200 to 200 | 0 to 400 | 0 to 800 | 0 to 1200 | J | Temperature input range[ ${ }^{\circ} \mathrm{C}$ ] | 0 to 750 |
|  |  |  |  | Accuracy at $25^{\circ} \mathrm{C}$ [\%] | $\pm 0.4$ | $\pm 0.3$ | $\pm 0.3$ | $\pm 0.4$ |  | Conversion accuracy at $25 \pm 5^{\circ} \mathrm{C}$ | $\begin{aligned} & \pm 0.5^{\circ} \mathrm{C} \text { or } \\ & \pm 0.25 \% \text { of } \end{aligned}$ measured temperature, whichever is greater. |
|  |  |  |  | Temperature drift [\%/ ${ }^{\circ} \mathrm{C}$ ] | $\pm 0.011$ | $\pm 0.01$ | $\pm 0.01$ | $\pm 0.011$ |  | Temperature characteristics | $\begin{aligned} & \pm 0.07^{\circ} \mathrm{C} \text { or } \\ & \pm 0.02 \% \text { of } \end{aligned}$ <br> measured temperature, whichever is greater. |


| A616TD |  |  |  |  |  |  |  |  | A1S68TD |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| JIS | ANSI | DIN | BS | Measured range no. | 1 | 2 | 3 | 4 | JIS | - | - |
|  |  |  |  | Permitted input voltage range [mV] | -12.5 to 12.5 | 0 to 25 | 0 to 50 | 0 to 100 |  |  |  |
|  | T | - | $\begin{gathered} \mathrm{Cu} \\ -\mathrm{CuNi} \end{gathered}$ | Temperature input range $\left[{ }^{\circ} \mathrm{C}\right.$ ] | -200 to 200 | 0 to 400 | 0 to 400 | 0 to 400 | T | Temperature input range $\left[{ }^{\circ} \mathrm{C}\right.$ ] | 0 to 350 |
| T |  |  |  | Accuracy at $25^{\circ} \mathrm{C}$ [\%] | $\pm 0.5$ | $\pm 0.3$ | - | - |  | Conversion accuracy at $25 \pm 5^{\circ} \mathrm{C}$ | $\begin{aligned} & \pm 0.5^{\circ} \mathrm{C} \text { or } \\ & \pm 0.25 \% \text { of } \end{aligned}$ measured <br> temperature, whichever is greater. |
|  |  |  |  | Temperature drift [\%/ ${ }^{\circ} \mathrm{C}$ ] | $\pm 0.013$ | $\pm 0.01$ |  |  |  | Temperature characteristics | $\begin{aligned} & \pm 0.07^{\circ} \mathrm{C} \text { or } \\ & \pm 0.02 \% \text { of } \end{aligned}$ <br> measured temperature, whichever is greater. |
| - | - | Fe -CuNi | - | Temperature input range [ ${ }^{\circ} \mathrm{C}$ ] | -100 to 200 | 0 to 400 | 0 to 800 | 0 to 900 |  |  |  |
|  |  |  |  | Accuracy at $25^{\circ} \mathrm{C}$ [\%] | - | $\pm 0.3$ | $\pm 0.3$ | $\pm 0.5$ |  |  |  |
|  |  |  |  | Temperature drift [\%/ ${ }^{\circ} \mathrm{C}$ ] |  | $\pm 0.01$ | $\pm 0.01$ | $\pm 0.013$ |  |  |  |
| - | - | $\begin{gathered} \mathrm{Cu} \\ -\mathrm{CuNi} \end{gathered}$ | - | Temperature input range [ ${ }^{\circ} \mathrm{C}$ ] | -100 to 200 | 0 to 400 | 0 to 600 | 0 to 600 |  |  |  |
|  |  |  |  | Accuracy at $25^{\circ} \mathrm{C}$ [\%] | - | $\pm 0.3$ | $\pm 0.4$ | - |  |  |  |
|  |  |  |  | Temperature drift [\%/ ${ }^{\circ} \mathrm{C}$ ] |  | $\pm 0.01$ | $\pm 0.011$ |  |  |  |  |

## (2) Comparisons between A68RD3N and A1S62RD3N

(a) Performance specifications comparison

O: Compatible, $\Delta$ : Partial change required, $x$ : Incompatible

| Item |  | A68RD3N | A1S62RD3N | Compati bility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Measuring method |  | 3 -wire type |  | $\bigcirc$ |  |
| Output (detected temperature value) |  | 16-bit, signed binary <br> (-1800 to 6000: Value to one decimal place $\times 10$ ) 32-bit, signed binary <br> 180000 to 600000: Value to three decimal places $\times 1000$ ) |  | $\bigcirc$ |  |
| Applicable platinum RTD |  | Pt100 (JIS C1604-1997, IEC 751-am2, JIS C1604-1989, DIN 43760-1980), JPt100 (JIS C1604-1981) |  | $\bigcirc$ |  |
| Temperature input range | Pt100 | -180 to $600^{\circ} \mathrm{C}(27.10$ to $313.71 \Omega$ ) |  |  |  |
|  | JPt100 | -180 to $600^{\circ} \mathrm{C}(25.80$ to $317.28 \Omega$ ) |  |  |  |
| Accuracy |  | $\pm 1 \%$ (accuracy relative to full-scale) |  | $\bigcirc$ |  |
| Resolution |  | $0.025^{\circ} \mathrm{C}$ |  | $\bigcirc$ |  |
| Conversion speed |  | $40 \mathrm{~ms} /$ channel |  | $\bigcirc$ |  |
| Analog input points |  | 8 channels/module | 2 channels/module | $\times$ | The number of channels has decreased. <br> Using multiple A1S62RD3Ns is recommended. |
| Output current for temperature detection |  | 1 mA |  | $\bigcirc$ |  |
| Isolation method |  | Between platinum RTD input and PLC power supply: photocoupler isolation Between platinum RTD input and channels: non-isolated |  | $\bigcirc$ |  |
| Dielectric withstand voltage |  | Between platinum RTD input and PLC power supply: 500VAC for 1 minute |  | $\bigcirc$ |  |
| Disconnection detection |  | Detected for each channel |  | $\bigcirc$ |  |
| Occupied I/O points |  | 32 points (I/O assignment: special 32 points) |  | $\bigcirc$ |  |
| Connected terminal |  | 38-point terminal block | 20-point terminal block | $\times$ | External wiring must be changed. |
| Applicable wire size |  | 0.75 to $2 \mathrm{~mm}^{2}$ | 0.75 to $1.5 \mathrm{~mm}^{2}$ | $\triangle$ |  |
| Applicable solderless terminal |  | $\begin{gathered} \text { V1.25-3, V1.25-YS3A, } \\ \text { V2-S3, V2-YS3A } \end{gathered}$ |  | $\triangle$ |  |
| Cables between RD3N and platinum RTD |  | Conductor resistance between Pt100 and A68RD3N/A1S62RD3N must be $10 \Omega$ or less per wire. All channels have the same specifications. |  | $\bigcirc$ |  |
| Internal current consumption (5VDC) |  | 0.94A | 0.49A | $\bigcirc$ |  |
| External dimensions |  | $250(\mathrm{H}) \times 37.5(\mathrm{~W}) \times 121(\mathrm{D}) \mathrm{mm}$ | $130(\mathrm{H}) \times 34.5(\mathrm{~W}) \times 93.6$ (D) mm | $\triangle$ | The dimensions are different. |
| Weight |  | 0.43 kg | 0.27 kg | $\bigcirc$ |  |

(b) Function comparison

O: Compatible, $\Delta$ : Partial change required, $\times$ : Incompatible, - : Additional function

| Item | A68RD3N |  | A1S62RD3N | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Conversion enable/ disable setting for each channel | Temperature detection is enabled or disabled for each channel. <br> - Conversion enable... Loads external temperature, and detects the disconnection. <br> - Conversion disable... Does not load external temperature, and not detect any disconnection. |  |  | $\bigcirc$ |  |
| Sampling/average processing setting | 1) Sampling processing <br> For each sampling time, the detected temperature value is stored in the buffer memory. <br> 2) Time-average processing <br> For each sampling time, the detected temperature value is loaded for the time of the set value ( 320 to 32000 ms ), and the average of the total excluding the maximum and minimum values is stored in the buffer memory. <br> 3) Count-average processing For each sampling time, the detected temperature value is loaded for the set number of times ( 1 to 800 times) and the average of the total exculding the maximum and minimum value is stored in the buffer memory. |  | 1)Sampling processing <br> For each sampling time, the detected temperature value is stored in the buffer memory. <br> 2) Time-average processing For each sampling time, the detected temperature value is loaded for the time of the set value ( 80 to 32000 ms ), and the average of the total excluding the maximum and minimum values is stored in the buffer memory. <br> 3) Count-average processing For each sampling time, the detected temperature value is loaded for the set number of times ( 1 to 800 times) and the average of the total exculding the maximum and minimum value is stored in the buffer memory. | $\bigcirc$ |  |
| Storage of detected temperature values | Values rounded to one and three decimal places are stored in the butter memory. <br> - Value rounded to one decimal place (16-bit signed binary) Example: 53.8( $\left.{ }^{\circ} \mathrm{C}\right) \rightarrow 538$ <br> - Value rounded to three decimal places (32-bit signed binary) Example: $216.025\left({ }^{\circ} \mathrm{C}\right) \rightarrow 216025$ |  |  | $\bigcirc$ |  |
| Disconnection detection | Disconnection of Pt100 or cable is detected. Disconnection can be detected at each channel and the disconnection-detected flag that corresponds to that channel turns ON when detected. |  |  | $\bigcirc$ |  |
| Platinum RTD type setting | The type of the platinum RTD to be used is set. There are two kinds of platinum RTDs: |  |  | $\bigcirc$ |  |
|  | PlatinuPt100 | latinum RTD type name | Specification |  |  |
|  |  | 1997JIS type | JIS C1604-1997, IEC 751-am2 |  |  |
|  |  | 1989JIS type | JIS C1604-1989, DIN 43760-1980 |  |  |
|  | JPt100 | Old JIS type | JIS C1604-1981 |  |  |

(c) Comparison of I/O signals for PLC CPU

Modifying sequence programs is required as the I/O signals differ.
For details of the I/O signals and sequence programs, refer to the user's manual.

| Signal direction: A68RD3N $\rightarrow$ PLC CPU |  | Signal direction: <br> PLC CPU $\rightarrow$ A68RD3N |  | Signal direction: <br> A1S62RD3N $\rightarrow$ PLC CPU |  | Signal direction: <br> PLC CPU $\rightarrow$ A1S62RD3N |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Device NO. | Signal name | Device NO. | Signal name | Device NO. | Signal name | Device NO. | Signal name |
| X0 | WDT error | Y0 | Not used | X0 | WDT error | Y0 | Not used |
| X1 | READY | Y1 |  | X1 | READY | Y1 |  |
| X2 | Write data error | Y2 |  | X2 | Write data error | Y2 |  |
| X3 | CH1: Disconnection detected | Y3 |  | X3 | CH 1 : Disconnection detected | Y3 |  |
| X4 | CH2: Disconnection detected | Y4 |  | X4 | CH2: Disconnection detected | Y4 |  |
| X5 | CH3: Disconnection detected | Y5 |  | X5 | Not used | Y5 |  |
| X6 | CH4: Disconnection detected | Y6 |  | X6 |  | Y6 |  |
| X7 | CH5: Disconnection detected | Y7 |  | X7 |  | Y7 |  |
| X8 | CH6: Disconnection detected | Y8 |  | X8 |  | Y8 |  |
| X9 | CH7: Disconnection detected | Y9 |  | X9 |  | Y9 |  |
| XA | CH8: Disconnection detected | YA |  | XA |  | YA |  |
| XB | Not used | YB |  | XB |  | YB |  |
| XC |  | YC |  | XC |  | YC |  |
| XD |  | YD |  | XD |  | YD |  |
| XE |  | YE |  | XE |  | YE |  |
| XF |  | YF |  | XF |  | YF |  |
| X10 |  | Y10 |  | X10 |  | Y10 |  |
| X11 |  | Y11 |  | X11 |  | Y11 |  |
| X12 |  | Y12 | Error code reset | X12 |  | Y12 | Error code reset |
| X13 |  | Y13 | Not used | X13 |  | Y13 | Not used |
| X14 |  | Y14 |  | X14 |  | Y14 |  |
| X15 |  | Y15 |  | X15 |  | Y15 |  |
| X16 |  | Y16 |  | X16 |  | Y16 |  |
| X17 |  | Y17 |  | X17 |  | Y17 |  |
| X18 |  | Y18 |  | X18 |  | Y18 |  |
| X19 |  | Y19 |  | X19 |  | Y19 |  |
| X1A |  | Y1A |  | X1A |  | Y1A |  |
| X1B |  | Y1B |  | X1B |  | Y1B |  |
| X1C |  | Y1C |  | X1C |  | Y1C |  |
| X1D |  | Y1D |  | X1D |  | Y1D |  |
| X1E |  | Y1E |  | X1E |  | Y1E |  |
| X1F |  | Y1F |  | X1F |  | Y1F |  |

## (d) Buffer memory address comparison

Modifying sequence programs is required as the assignment of buffer memory differs.
For details of the buffer memory and sequence programs, refer to the user's manual.

| Address | A68RD3N | Read/Write | Address | A1S62RD3N | Read/Write |
| :---: | :---: | :---: | :---: | :---: | :---: |
| 0 | Conversion enable/disable specification | R/W | 0 | Conversion enable/disable specification | R/W |
| 1 | Averaging processing specification |  | 1 | Averaging processing specification |  |
| 2 | CH1 Averaging time/count |  | 2 | CH1 Averaging time/count |  |
| 3 | CH2 Averaging time/count |  | 3 | CH2 Averaging time/count |  |
| 4 | CH3 Averaging time/count |  | 4 | Not used | - |
| 5 | CH4 Averaging time/count |  | 5 |  |  |
| 6 | CH5 Averaging time/count |  | 6 |  |  |
| 7 | CH6 Averaging time/count |  | 7 |  |  |
| 8 | CH7 Averaging time/count |  | 8 |  |  |
| 9 | CH8 Averaging time/count |  | 9 |  |  |
| 10 | CH1 Detected temperature value (16bit) | R | 10 | CH1 Detected temperature value (16bit) |  |
| 11 | CH 2 Detected temperature value (16bit) |  | 11 | CH2 Detected temperature value (16bit) |  |
| 12 | CH3 Detected temperature value (16bit) |  | 12 | Not used | - |
| 13 | CH4 Detected temperature value (16bit) |  | 13 |  |  |
| 14 | CH5 Detected temperature value (16bit) |  | 14 |  |  |
| 15 | CH6 Detected temperature value (16bit) |  | 15 |  |  |
| 16 | CH7 Detected temperature value (16bit) |  | 16 |  |  |
| 17 | CH8 Detected temperature value (16bit) |  | 17 |  |  |
| 18 | CH1 Detected temperature value (L) |  | 18 | CH 1 Detected temperature value (L) | R |
| 19 | (32bit) (H) |  | 19 | (32bit) (H) |  |
| 20 | CH 2 Detected temperature value (L) |  | 20 | CH 2 Detected temperature value (32bit) |  |
| 21 | (32bit) (H) |  | 21 |  |  |
| 22 | CH3 Detected temperature value (L) |  | 22 | Not used | - |
| 23 | (32bit) (H) |  | 23 |  |  |
| 24 | CH 4 Detected temperature value (L) |  | 24 |  |  |
| 25 | (32bit) (H) |  | 25 |  |  |
| 26 | CH5 Detected temperature value (L) |  | 26 |  |  |
| 27 | (32bit) (H) |  | 27 |  |  |
| 28 | CH6 Detected temperature value (32bit) |  | 28 |  |  |
| 29 |  |  | 29 |  |  |
| 30 | CH 7 Detected temperature value (32bit) |  | 30 |  |  |
| 31 |  |  | 31 |  |  |
| 32 | CH8 Detected temperature value (L) <br> (32bit) (H) |  | 32 |  |  |
| 33 |  |  | 33 |  |  |
| 34 | Write data error code | R/W | 34 | Write data error code | R/W |
| 35 | Conversion completed | R | 35 | Conversion completed | R |
| 36 | Type specification of a platinum RTD | R/W | 36 | Type specification of a platinum RTD | R/W |

## (3) Comparisons between A68RD4N and A1S62RD4N

(a) Performance specifications comparison

O: Compatible, $\triangle$ : Partial change required, $x$ : Incompatible

| Item |  | A68RD4N | A1S62RD4N | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Measuring method |  | 4-wire type |  | $\bigcirc$ |  |
| Output (detected temperature value) |  | 16-bit, signed binary <br> (-1800 to 6000: Value to one decimal place $\times 10$ ) 32-bit, signed binary <br> (-180000 to 600000: Value to three decimal places $\times 1000$ ) |  | $\bigcirc$ |  |
| Applicable platinum RTD |  | Pt100 (JIS C1604-1997, IEC 751-am2, JIS C1604-1989, DIN 43760-1980), JPt100 (JIS C1604-1981) |  | $\bigcirc$ |  |
| Temperature input range | Pt100 | -180 to $600^{\circ} \mathrm{C}(27.10$ to $313.71 \Omega$ ) |  |  |  |
|  | JPt100 | -180 to $600^{\circ} \mathrm{C}(25.80$ to $317.28 \Omega$ ) |  |  |  |
| Accuracy |  | $\pm 1 \%$ (accuracy relative to full-scale) |  | $\bigcirc$ |  |
| Resolution |  | $0.025^{\circ} \mathrm{C}$ |  | $\bigcirc$ |  |
| Conversion speed |  | $40 \mathrm{~ms} /$ channel |  | $\bigcirc$ |  |
| Analog input points |  | 8 channels/module | 2 channels/module | $\times$ | The number of channels decreases. Using multiple A1S62RD4Ns is recommended. |
| Output current for temperature detection |  | 1 mA |  | $\bigcirc$ |  |
| Isolation method |  | Between platinum RTD input and PLC power supply: photocoupler isolation Between platinum RTD input and channels : non-isolated |  | $\bigcirc$ |  |
| Dielectric withstand voltage |  | Between platinum RTD input and PLC power supply: 500VAC for 1 minute |  | $\bigcirc$ |  |
| Disconnection detection |  | Batch-detected on all channels |  | $\bigcirc$ |  |
| Occupied I/O points |  | 32 points (I/O assignment: special 32 points) |  | $\bigcirc$ |  |
| Connected terminal |  | 38-point terminal block | 20-point terminal block | $\times$ | External wiring must be changed. |
| Applicable wire size |  | 0.75 to $2 \mathrm{~mm}^{2}$ | 0.75 to $1.5 \mathrm{~mm}^{2}$ | $\triangle$ |  |
| Applicable solderless terminal |  | V1.25-3, V1.25-YS3A, V2-S3, V2-YS3A |  | $\Delta$ |  |
| Cable across RD3N and platinum RTD |  | Total resistance value of live conductors must be $70 \Omega$ or less. |  | $\bigcirc$ |  |
| Internal current consumption (5VDC) |  | 0.41A | 0.39A | $\bigcirc$ |  |
| External dimensions |  | $250(\mathrm{H}) \times 37.5(\mathrm{~W}) \times 121(\mathrm{D}) \mathrm{mm}$ | $130(\mathrm{H}) \times 34.5(\mathrm{~W}) \times 93.6(\mathrm{D}) \mathrm{m}$ | $\triangle$ | The dimensions are different. |
| Weight |  | 0.43 kg | 0.27 kg | $\bigcirc$ |  |

## (b) Function comparison

O: Compatible, $\Delta$ : Partial change required, $\times$ : Incompatible, - : Additional function

| Item | A68RD4N |  | A1S62RD4N | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Conversion enable/ disable setting for each channel | Temperature detection is enabled or disabled for each channel. <br> - Conversion enable... Loads external temperature, and detects the disconnection. <br> - Conversion disable...Does not load external temperature, and not detect any disconnection. |  |  | $\bigcirc$ |  |
| Sampling laverage processing setting | 1) Sampling processing <br> For each sampling time, the detected temperature value is stored in the buffer memory. <br> 2) Time-average processing <br> For each sampling time, the detected temperature value is loaded for the time of the set value ( 320 to 32000 ms ) and the average of the total excluding the maximum and minimum values is stored in the buffer memory. <br> 3) Count-average processing For each sampling time, the detected temperature value is loaded for the set number of times ( 1 to 800 times) and the average of the total excluding the maximum and minimum values is stored in the buffer memory. |  | 1) Sampling processing <br> For each sampling time, the detected temperature value is stored in the buffer memory. <br> 2) Time-average processing For each sampling time, the detected temperature value is loaded for the time of the set value ( 80 to 32000 ms ) and the average of the total excluding the maximum and minimum values is stored in the buffer memory. <br> 3) Count-average processing For each sampling time, the detected temperature value is loaded for the set number of times ( 1 to 800 times) and the average value of the total exculding the maximum and minimum value is stored in the buffer memory. | $\bigcirc$ |  |
| Storage of detected temperature values | Values rounded to one and three decimal places are stored in the buffer memory. <br> - Value rounded to one decimal place (16-bit signed binary) Example: $53.8\left({ }^{\circ} \mathrm{C}\right) \rightarrow 538$ <br> - Value rounded three decimal places (32-bit signed binary) Example: $216.025\left({ }^{\circ} \mathrm{C}\right) \rightarrow 216025$ |  |  | $\bigcirc$ |  |
| Disconnection detection | Disconnection of Pt100 or cable is detected. If either channel disconnection is detected, the $\Sigma$ disconnection-detected flag turns ON. |  |  | $\bigcirc$ |  |
| Platinum RTD type setting | The type of the platinum RTD to be used is set. There are two kinds of platinum RTDs: |  |  | $\bigcirc$ |  |
|  | Platinum RTD type name |  | Specification |  |  |
|  | Pt100 | 1997JIS type | JIS C1604-1997, IEC 751-am2 |  |  |
|  |  | 1989JIS type | JIS C1604-1989, DIN 43760-1980 |  |  |
|  | JPt100 | Old JIS type | JIS C1604-1981 |  |  |

(c) Comparison of I/O signals for PLC CPU

Modifying sequence programs is required as the I/O signals differ.
For details of the I/O signals and sequence programs, refer to the user's manual.

| Signal direction: A68RD4N $\rightarrow$ PLC CPU |  | Signal direction: <br> PLC CPU $\rightarrow$ A68RD4N |  | Signal direction: <br> A1S62RD4N $\rightarrow$ PLC CPU |  | Signal direction: <br> PLC CPU $\rightarrow$ A1S62RD4N |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Device NO. | Signal name | Device NO. | Signal name | Device NO. | Signal name | Device NO. | Signal name |
| X0 | WDT error | Y0 | Not used | X0 | WDT error | Y0 | Not used |
| X1 | READY | Y1 |  | X1 | READY | Y1 |  |
| X2 | Write data error | Y2 |  | X2 | Write data error | Y2 |  |
| X3 | $\begin{aligned} & \sum \text { disconnection } \\ & \text { detection } \\ & \text { (CH1 to } \mathrm{CH} 8 \text { ) } \end{aligned}$ | Y3 |  | X3 | $\begin{aligned} & \sum \text { disconnection } \\ & \text { detection } \\ & \text { (CH1, } \mathrm{CH} 2) \end{aligned}$ | Y3 |  |
| X4 | Not used | Y4 |  | X4 | Not used | Y4 |  |
| X5 |  | Y5 |  | X5 |  | Y5 |  |
| X6 |  | Y6 |  | X6 |  | Y6 |  |
| X7 |  | Y7 |  | X7 |  | Y7 |  |
| X8 |  | Y8 |  | X8 |  | Y8 |  |
| X9 |  | Y9 |  | X9 |  | Y9 |  |
| XA |  | YA |  | XA |  | YA |  |
| XB |  | YB |  | XB |  | YB |  |
| XC |  | YC |  | XC |  | YC |  |
| XD |  | YD |  | XD |  | YD |  |
| XE |  | YE |  | XE |  | YE |  |
| XF |  | YF |  | XF |  | YF |  |
| X10 |  | Y10 |  | X10 |  | Y10 |  |
| X11 |  | Y11 |  | X11 |  | Y11 |  |
| X12 |  | Y12 | Error code reset | X12 |  | Y12 | Error code reset |
| X13 |  | Y13 | Not used | X13 |  | Y13 | Not used |
| X14 |  | Y14 |  | X14 |  | Y14 |  |
| X15 |  | Y15 |  | X15 |  | Y15 |  |
| X16 |  | Y16 |  | X16 |  | Y16 |  |
| X17 |  | Y17 |  | X17 |  | Y17 |  |
| X18 |  | Y18 |  | X18 |  | Y18 |  |
| X19 |  | Y19 |  | X19 |  | Y19 |  |
| X1A |  | Y1A |  | X1A |  | Y1A |  |
| X1B |  | Y1B |  | X1B |  | Y1B |  |
| X1C |  | Y1C |  | X1C |  | Y1C |  |
| X1D |  | Y1D |  | X1D |  | Y1D |  |
| X1E |  | Y1E |  | X1E |  | Y1E |  |
| X1F |  | Y1F |  | X1F |  | Y1F |  |

## (d) Buffer memory address comparison

Modifying sequence programs is required as the assignment of buffer memory differs. For details of the buffer memory and sequence programs, refer to the user's manual.

| Address | A68RD4N | Read/Write | Address | A1S62RD4N | Read/Write |
| :---: | :---: | :---: | :---: | :---: | :---: |
| 0 | Conversion enable/disable specification | R/W | 0 | Conversion enable/disable specification | R/W |
| 1 | Averaging processing specification |  | 1 | Averaging processing specification |  |
| 2 | CH1 Averaging time/count |  | 2 | CH1 Averaging time/count |  |
| 3 | CH2 Averaging time/count |  | 3 | CH2 Averaging time/count |  |
| 4 | CH3 Averaging time/count |  | 4 | Not used | - |
| 5 | CH4 Averaging time/count |  | 5 |  |  |
| 6 | CH5 Averaging time/count |  | 6 |  |  |
| 7 | CH6 Averaging time/count |  | 7 |  |  |
| 8 | CH7 Averaging time/count |  | 8 |  |  |
| 9 | CH8 Averaging time/count |  | 9 |  |  |
| 10 | CH1 Detected temperature value (16bit) | R | 10 | CH 1 Detected temperature value (16bit) | R |
| 11 | CH 2 Detected temperature value (16bit) |  | 11 | CH 2 Detected temperature value (16bit) |  |
| 12 | CH3 Detected temperature value (16bit) |  | 12 | Not used | - |
| 13 | CH4 Detected temperature value (16bit) |  | 13 |  |  |
| 14 | CH5 Detected temperature value (16bit) |  | 14 |  |  |
| 15 | CH6 Detected temperature value (16bit) |  | 15 |  |  |
| 16 | CH7 Detected temperature value (16bit) |  | 16 |  |  |
| 17 | CH8 Detected temperature value (16bit) |  | 17 |  |  |
| 18 | CH1 Detected temperature value (L) |  | 18 | CH 1 Detected temperature value (L) |  |
| 19 | (32bit) (H) |  | 19 | (32bit) (H) |  |
| 20 | CH2 Detected temperature value (L) |  | 20 | CH2 Detected temperature value (L) |  |
| 21 | (32bit) (H) |  | 21 | (32bit) (H) |  |
| 22 | CH 3 Detected temperature value (L) |  | 22 | Not used | - |
| 23 | (32bit) (H) |  | 23 |  |  |
| 24 | CH 4 Detected temperature value (L) |  | 24 |  |  |
| 25 | (32bit) (H) |  | 25 |  |  |
| 26 | CH5 Detected temperature value (L) |  | 26 |  |  |
| 27 | (32bit) (H) |  | 27 |  |  |
| 28 | CH6 Detected temperature value (32bit) |  | 28 |  |  |
| 29 |  |  | 29 |  |  |
| 30 | CH 7 Detected temperature value (L) <br> (32bit) (H) |  | 30 |  |  |
| 31 |  |  | 31 |  |  |
| 32 | CH8 Detected temperature value (L) <br> (32bit) (H) |  | 32 |  |  |
| 33 |  |  | 33 |  |  |
| 34 | Write data error code | R/W | 36 | Write data error code | R/W |
| 35 | Conversion completed | R | 37 | Conversion completed | R |
| 36 | Type specification of a platinum RTD | R/W | 39 | Type specification of a platinum RTD | R/W |

### 10.2.4 High-speed counter module comparison

(1) Comparisons between AD61 and A1SD62
(a) Performance specifications comparison

O: Compatible, $\Delta$ : Partial change required, $\times$ : Incompatible, 一:Additional function


## (b) Function comparison

O: Compatible, $\Delta$ : Partial change required, $x$ : Incompatible, 一: Additional function

| Item | AD61 | A1SD62 | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: |
| Preset function | Changes the present counter value to a given value (initial value). The latch function is not provided for the AD61 memory. When the power is turned OFF, or when the CPU is reset, the AD61 memories (counter value, present value, set value and preset value) are initialized. <br> Storing the present counter value into the data register of the CPU in continuous operation allows counting from the stored value in the next operation. | Changes the present counter value to a given value, which is a preset value. This function can be used to start the pulse count from the set value. <br> There are two methods for the function, "preset in sequence program" and "preset by external control signal (applying voltage to external terminal)". | $\bigcirc$ |  |
| Disable function | Turning ON the count enable signal shown in the PLC I/O signal assignment starts counting on the AD61. ( $\mathrm{CH} 1=\mathrm{Y} 14, \mathrm{CH} 2=\mathrm{Y} 1 \mathrm{~B}$ ). When voltage is applied to the DIS (disable) terminal of the external input terminal block, the AD61 stops counting. Therefore, counting can be started and stopped by external input regardless of the scan time. | Stops counting while the count enable command is OFF. | $\bigcirc$ |  |
| Ring counter function | Automatically presets the value when the counter value and the set value are equal with the ring counter setting pin set to ON on the AD61 board. Used for cyclic control such as constant-rate feeding. | Repeats counting between the preset value and the ring counter value with the ring counter command. Used for control such as constant-rate feeding. | $\bigcirc$ |  |
| Latch counter function | - | Latches the present value at the time a signal is input. | - |  |
| Sampling counter function | - | Counts the pulse input at the sampling time set. The setting unit of the sampling time is 10 ms , and the accuracy is less than 1 count. | - |  |
| Periodic pulse counter function | - | Allows storing the present value and the previous value in the corresponding periodic pulse counter value areas at the specified intervals. The setting unit is 10 ms , and the accuracy is less than 1 count. | - |  |
| Coincidence output function | Outputs an ON/OFF signal, comparing the set value with the present value of the counter. | Outputs an signal when the specified counter value is matched with the present value of the counter as a result of comparison. Tow-point setting is available. | $\bigcirc$ | 2 points can be set. |

(c) Comparison of I/O signals for PLC CPU

Modifying sequence programs is required as the I/O signals differ.
For details of the I/O signals and sequence programs, refer to the user's manual.

| AD61 |  |  |  | A1SD62 |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Device NO. | Signal name | Device NO. | Signal name | Device NO. | Signal name | Device NO. | Signal name |
| X0 | CH 1 Counter value greater | Y0 | Not used | X0 | CH 1 Counter value greater (point No.1) | Y0 | Not used |
| X1 | CH 1 Counter value matched | Y1 |  | X1 | CH 1 Counter value matched (point No.1) | Y1 |  |
| X2 | CH1 Counter value smaller | Y2 |  | X2 | CH 1 Counter value smaller (point No.1) | Y2 |  |
| X3 | CH1 External preset request detection | Y3 |  | X3 | CH1 External preset request detection | Y3 |  |
| X4 | CH 2 Counter value greater | Y4 |  | X4 | CH 2 Counter value greater (point No.1) | Y4 |  |
| X5 | CH 2 Counter value matched | Y5 |  | X5 | CH 2 Counter value matched (point No.1) | Y5 |  |
| X6 | CH 2 Counter value smaller | Y6 |  | X6 | CH 2 Counter value smaller (point No.1) | Y6 |  |
| X7 | CH2 External preset request detection | Y7 |  | X7 | CH2 External preset request detection | Y7 |  |
| X8 | Not used | Y8 |  | X8 | CH 1 Counter value greater (point No.2) | Y8 |  |
| X9 |  | Y9 |  | X9 | CH1 Counter value matched (point No.2) | Y9 |  |
| XA |  | YA |  | XA | CH 1 Counter value smaller (point No.2) | YA |  |
| XB |  | YB |  | XB | CH 2 Counter value greater (point No.2) | YB |  |
| XC |  | YC |  | XC | CH 2 Counter value matched (point No.2) | YC |  |
| XD |  | YD |  | XD | CH 2 Counter value smaller (point No.2) | YD |  |
| XE |  | YE |  | XE | Fuse/External power cutoff detection | YE |  |
| XF |  | YF |  | XF | Not used | YF |  |
| X10 |  | Y10 | CH 1 Coincidence signal reset command | X10 |  | Y10 | CH 1 Coincidence signal reset command |
| X11 |  | Y11 | CH1 Preset command | X11 |  | Y11 | CH1 Preset command |
| X12 |  | Y12 | CH1 Coincidence signal output enable | X12 |  | Y12 | CH1 Coincidence signal output enable command |
| X13 |  | Y13 | CH1 Down count command | X13 |  | Y13 | CH1 Down count command |
| X14 |  | Y14 | CH1 Count enable | X14 |  | Y14 | CH1 Count enable command |
| X15 |  | Y15 | CH1 Present value read request | X15 |  | Y15 | CH 1 Count value read request |
| X16 |  | Y16 | CH1 External preset detection reset command | X16 |  | Y16 | CH 1 Counter function selection start command |
| X17 |  | Y17 | CH2 Coincidence signal reset command | X17 |  | Y17 | CH2 Coincidence signal reset command |
| X18 |  | Y18 | CH2 Preset command | X18 |  | Y18 | CH2 Preset command |
| X19 |  | Y19 | CH 2 Coincidence signal output enable | X19 |  | Y19 | CH2 Coincidence signal output enable command |
| X1A |  | Y1A | CH2 Down count command | X1A |  | Y1A | CH2 Down count command |
| X1B |  | Y1B | CH2 Count enable | X1B |  | Y1B | CH2 Count enable command |
| X1C |  | Y1C | CH 2 Present value read request | X1C |  | Y1C | CH 2 Count value read request |
| X1D |  | Y1D | CH2 External preset detection reset command | X1D |  | Y1D | CH2 Counter function selection start command |
| X1E |  | Y1E | Not used | X1E |  | Y1E | Not used |
| X1F |  | Y1F |  | X1F |  | Y1F |  |

## (d) Buffer memory address comparison

Modifying sequence programs is required as the assignment of buffer memory differs. For details of the buffer memory and sequence programs, refer to the user's manual.

| Address |  | AD61 |  | Address |  | A1SD62 |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| CH1 | CH2 | Name | Read/Write | CH1 | CH2 | Name |  | Read/Write |
| 1 | 33 | Preset value write (Lower and middle) Preset value write (Upper) | W | 1 | 33 | Preset value setting | (L) <br> (H) | R/W |
| (2) | (34) |  |  | 2 | 34 |  |  |  |
| 3 | 35 | Mode register | R/W | 3 | 35 | Pulse input mode setting |  |  |
| 4 | 36 | Present value read (Lower and middle) Present value read (Upper) | R | 4 | 36 | Present value | $\begin{aligned} & (\mathrm{L}) \\ & (\mathrm{H}) \end{aligned}$ | R |
| (5) | (37) |  |  | 5 | 37 |  |  |  |
| 6 | 38 | Set value read/write (Lower and middle) Set value read/write (Upper) | R/W | 6 | 38 | Coincident output point setting No. 1 | (L)(H) | R/W |
| (7) | (39) |  |  | 7 | 39 |  |  |  |
| Address in parentheses in the above table indicates that of the upper 8 bits in the 24-bit data. |  |  |  | 8 | 40 | Counter function selection setting |  |  |
|  |  |  |  | 9 | 41 | Sampling/Cycle setting 1 to 65535 [10ms increments] |  |  |
|  |  |  |  | 10 | 42 | External preset detection reset command |  | W |
|  |  |  |  | 11 | 43 | Point No. 2 coincidence signal reset command |  |  |
|  |  |  |  | 12 | 44 | Coincident output point setting No. 2 | $\begin{aligned} & (\mathrm{L}) \\ & (\mathrm{H}) \\ & \hline \end{aligned}$ | R/W |
|  |  |  |  | 13 | 45 |  |  |  |
|  |  |  |  | 14 | 46 | Latch count value | (L) | R |
|  |  |  |  | 15 | 47 |  |  |  |
|  |  |  |  | 16 | 48 | Sampling count value | (L) |  |
|  |  |  |  | 17 | 49 |  |  |  |
|  |  |  |  | 18 | 50 | Periodic pulse counter previous value | $\begin{aligned} & \text { (L) } \\ & \text { (H) } \end{aligned}$ |  |
|  |  |  |  | 19 | 51 |  |  |  |
|  |  |  |  | 20 | 52 | Periodic pulse counter present value | (L)(H) |  |
|  |  |  |  | 21 | 53 |  |  |  |
|  |  |  |  | 22 |  | Sampling/Cycle counter (for both CH 1 and CH 2 ) |  |  |

(2) Comparisons between AD61S1 and A1SD62
(a) Performance specifications comparison

O: Compatible, $\Delta$ : Partial change required, $x$ : Incompatible


## (b) Function comparison

O: Compatible, $\Delta$ : Partial change required, $\times$ : Incompatible, - : Additional function

| Item | AD61S1 | A1SD62 | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: |
| Preset function | Changes the present counter value to a given value (initial value). The latch function is not provided for the AD61 memory. When the power is turned OFF, or when the CPU is reset, the AD61 memories (counter value, present value, set value and preset value) are initialized. <br> Storing the present counter value into the data register of the CPU in continuous operation allows counting from the stored value in the next operation. | Changes the present counter value to a given value, which is a preset value. This function can be used to start the pulse count from the set value. <br> There are two methods for the function, "preset in sequence program" and "preset by external control signal (applying voltage to external terminal)". | $\bigcirc$ |  |
| Disable function | Turning ON the count enable signal shown in the PLC I/O signal assignment starts counting on the AD61. (CH1 = Y 14, CH2=Y1B) <br> When voltage is applied to the DIS (disable) terminal of the external input terminal block, the AD61 stops counting. Therefore, counting can be started and stopped by external input regardless of the scan time. | Stops counting while the count enable command is OFF. | $\bigcirc$ |  |
| Ring counter function | Automatically presets the value when the counter value and the set value are equal with the ring counter setting pin set to ON on the AD61 board. Used for cyclic control such as constant-rate feeding. | Repeats counting between the preset value and the ring counter value with the ring counter command. Used for control such as constant-rate feeding. | $\bigcirc$ |  |
| Ring counter function | - | Latches the present value at the time a signal is input. | - |  |
| Sampling counter function | - | Counts the pulse input at the sampling time set.The setting unit of the sampling time is 10 ms , and the accuracy is less than 1 count. | - |  |
| Periodicpulse counter function | - | Allows storing the present value and the previous value in the corresponding periodic pulse counter value areas at the specified intervals. <br> The setting unit is 10 ms , and the accuracy is less than 1 count. | - |  |
| Coincidence output function | Outputs an ON/OFF signal, comparing the set value with the present value of the counter. | Outputs an signal when the specified counter value is matched with the present value of the counter as a result of comparison. Two-point setting is available. | $\bigcirc$ | 2 points can be set. |

## (c) Comparison of I/O signals for PLC CPU

Modifying sequence program is required as the I/O signals differ.
For details of the I/O signals and sequence programs, refer to the user's manual.

| AD61S1 |  |  |  | A1SD62 |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Device NO. | Signal name | Device NO. | Signal name | Device NO. | Signal name | Device NO. | Signal name |
| X0 | CH1 Counter value greater | Y0 | Not used | X0 | CH1 Counter value greater (point No.1) | Y0 | Not used |
| X1 | CH1 Counter value matched | Y1 |  | X1 | CH1 Counter value matched (point No.1) | Y1 |  |
| X2 | CH 1 Counter value smaller | Y2 |  | X2 | CH1 Counter value smaller (point No.1) | Y2 |  |
| X3 | CH1 External preset request detection | Y3 |  | X3 | CH1 External preset request detection | Y3 |  |
| X4 | CH2 Counter value greater | Y4 |  | X4 | CH2 Counter value greater (point No.1) | Y4 |  |
| X5 | CH 2 Counter value matched | Y5 |  | X5 | CH2 Counter value matched (point No.1) | Y5 |  |
| X6 | CH 2 Counter value smaller | Y6 |  | X6 | CH2 Counter value smaller (point No.1) | Y6 |  |
| X7 | CH2 External preset request detection | Y7 |  | X7 | CH2 External preset request detection | Y7 |  |
| X8 | Not used | Y8 |  | X8 | CH1 Counter value greater (point No.2) | Y8 |  |
| X9 |  | Y9 |  | X9 | CH1 Counter value matched (point No.2) | Y9 |  |
| XA |  | YA |  | XA | CH1 Counter value smaller (point No.2) | YA |  |
| XB |  | YB |  | XB | CH 2 Counter value greater (point No.2) | YB |  |
| XC |  | YC |  | XC | CH2 Counter value matched (point No.2) | YC |  |
| XD |  | YD |  | XD | CH2 Counter value smaller (point No.2) | YD |  |
| XE |  | YE |  | XE | Fuse/External power cutoff detection flag | YE |  |
| XF |  | YF |  | XF | Not used | YF |  |
| X10 |  | Y10 | CH1 Coincidence signal reset command | X10 |  | Y10 | CH1 Coincidence signal reset command |
| X11 |  | Y11 | CH1 Preset command | X11 |  | Y11 | CH1 Preset command |
| X12 |  | Y12 | CH1 Coincidence signal output enable | X12 |  | Y12 | CH 1 Coincidence signal output enable command |
| X13 |  | Y13 | CH1 Down count command | X13 |  | Y13 | CH1 Down count command |
| X14 |  | Y14 | CH1 Count enable | X14 |  | Y14 | CH1 Count enable command |
| X15 |  | Y15 | CH 1 Present value read request | X15 |  | Y15 | CH 1 Count value read request |
| X16 |  | Y16 | CH1 External preset detection reset command | X16 |  | Y16 | CH 1 Counter function selection start command |
| X17 |  | Y17 | CH2 Coincidence signal reset command | X17 |  | Y17 | CH2 Coincidence signal reset command |
| X18 |  | Y18 | CH2 Preset command | X18 |  | Y18 | CH2 Preset command |
| X19 |  | Y19 | CH2 Coincidence signal output enable | X19 |  | Y19 | CH2 Coincidence signal output enable command |
| X1A |  | Y1A | CH2 Down count command | X1A |  | Y1A | CH2 Down count command |
| X1B |  | Y1B | CH2 Count enable | X1B |  | Y1B | CH 2 Count enable command |
| X1C |  | Y1C | CH2 Present value read request | X1C |  | Y1C | CH 2 Count value read request |
| X1D |  | Y1D | CH2 External preset detection reset command | X1D |  | Y1D | CH2 Counter function selection start command |
| X1E |  | Y1E | Not used | X1E |  | Y1E | Not used |
| X1F |  | Y1F |  | X1F |  | Y1F |  |

## (d) Buffer memory address comparison

Modifying sequence programs is required as the assignment of buffer memory differs. For details of the buffer memory and sequence programs, refer to the user's manual.

| Address |  | AD61S1 |  | Address |  | A1SD62 |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| CH1 | CH2 | Name | Read/Write | CH1 | CH2 | Name |  | Read/Write |
| 1 | 33 | Preset value write (Lower and middle) Preset value write (Upper) | W | 1 | 33 | Preset value setting | $\begin{aligned} & \text { (L) } \\ & (\mathrm{H}) \end{aligned}$ | R/W |
| (2) | (34) |  |  | 2 | 34 |  |  |  |
| 3 | 35 | Mode register | R/W | 3 | 35 | Pulse input mode setting |  |  |
| 4 | 36 | Present value read (Lower and middle) <br> Present value read (Upper) | R | 4 | 36 | Present value | (L)(H) | R |
| (5) | (37) |  |  | 5 | 37 |  |  |  |
| 6 | 38 | Set value read/write (Lower and middle) <br> Set value read/write (Upper) | R/W | 6 | 38 | Coincident output point setting No. 1 | $\begin{gathered} (\mathrm{L}) \\ (\mathrm{H}) \end{gathered}$ | R/W |
| (7) | (39) |  |  | 7 | 39 |  |  |  |
| Address in parentheses in the above table indicates that of the upper 8 bits in the 24 -bit data. |  |  |  | 8 | 40 | Counter function selection setting |  |  |
|  |  |  |  | 9 | 41 | Sampling/Cycle setting 1 to 65535 [10ms increments] |  |  |
|  |  |  |  | 10 | 42 | External preset detection reset command |  | W |
|  |  |  |  | 11 | 43 | Point No. 2 coincidence signal reset command |  |  |
|  |  |  |  | 12 | 44 | Coincident output point setting No. 2 | (L)(H) | R/W |
|  |  |  |  | 13 | 45 |  |  |  |
|  |  |  |  | 14 | 46 | Latch count value | (L) | R |
|  |  |  |  | 15 | 47 |  |  |  |
|  |  |  |  | 16 | 48 | Sampling count value | (L) <br> (H) |  |
|  |  |  |  | 17 | 49 |  |  |  |
|  |  |  |  | 18 | 50 | Periodic pulse counter previous value | (L)(H) |  |
|  |  |  |  | 19 | 51 |  |  |  |
|  |  |  |  | 20 | 52 | Periodic pulse count present value | $\begin{aligned} & (\mathrm{L}) \\ & (\mathrm{H}) \end{aligned}$ |  |
|  |  |  |  | 21 | 53 |  |  |  |
|  |  |  |  | 22 |  | Sampling/Cycle counter (for both CH 1 and CH 2 ) |  |  |

### 10.2.5 Position detection module comparison

(1) Comparisons between A62LS-S5 and A1S62LS
(a) Performance specifications comparison

O: Compatible, $\Delta$ : Partial change required, $\times$ : Incompatible


O: Compatible, $\Delta$ : Partial change required, $x$ : Incompatible

| Item | Specifications |  | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: |
|  | A62LS-S5 | A1S62LS |  |  |
| Minimum position setting unit | 0.00001 |  | $\bigcirc$ |  |
| Current position value setting function | Current position value setting, Current position value preset setting |  | $\bigcirc$ |  |
| JOG operation function | JOG operation executed by JOG FWD/RVS signal inputs. |  | $\bigcirc$ |  |
| Sampling time | 1 ms |  | $\bigcirc$ |  |
|  | 2 ms |  | $\bigcirc$ |  |
| ® Current <br> value output |  |  | $\bigcirc$ |  |
| $$ | Depends on the parameter setting, 4, 8, 16, 32 or 64 |  | $\bigcirc$ |  |
|  | 117 |  | $\bigcirc$ |  |
| Number of occupied I/O points | 48 points (I/O assignment: empty $16+$ special 32 points) | 32 points (I/O assignment: special 32 points) | $\triangle$ | Change the start <br> I/O No. by PLC parameter. |
| Internal current consumption (5VDC) | 1.5A | 0.55A | $\bigcirc$ |  |
| External dimensions | $250(\mathrm{H}) \times 75(\mathrm{~W}) \times 121(\mathrm{D}) \mathrm{mm}$ | 130(H) $\times 34(\mathrm{~W}) \times 93$ (D) mm | $\triangle$ | The dimensions are different. |
| Weight | 1.1 kg | 0.5 kg | $\bigcirc$ |  |

(b) External input/output specifications comparison

1) Input specifications

O: Compatible, $\triangle$ : Partial change required, $x$ : Incompatible

| Item |  | A62LS-S5 | A1S62LS | Compatiibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Number of input points |  | Current position value preset input: 2 points |  | $\bigcirc$ |  |
| Isolation method |  | Photo-coupler |  | $\bigcirc$ |  |
| Rated input voltage |  | 12/24VDC |  | $\bigcirc$ |  |
| Rated input current |  | 4/10 mA |  | $\bigcirc$ |  |
| Operating input voltage range |  | 10.2 to 30 VDC |  | $\bigcirc$ |  |
| ON voltage |  | 10 VDC or more |  | $\bigcirc$ |  |
| OFF voltage |  | 4VDC or more |  | $\bigcirc$ |  |
| Response time | $\mathrm{OFF} \rightarrow \mathrm{ON}$ | 0.04 ms (At input voltage of 24 V ) |  | $\bigcirc$ |  |
|  | $\mathrm{ON} \rightarrow$ OFF | 0.2 ms (At input voltage of 24 V ) |  | $\bigcirc$ |  |
| Common connections |  | 1 common for 2 points (common terminal: TB19) | 1 common for 2 points (common terminal: B1, B2) | $\times$ | Wiring must be changed. |
| External cable connection |  | 20-point terminal block connector (M3×6 screws) | 24-pin connector |  |  |
| Applicable wire size |  | 0.75 to $2 \mathrm{~mm}^{2}$ | $0.3 \mathrm{~mm}^{2}$ |  |  |

## 2) Output specifications

O: Compatible, $\Delta$ : Partial change required, $\times$ : Incompatible

| Item |  | A62LS-S5 |  | A1S62LS |  | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Number of output points |  | For limit SW output function only | 16 points | For current value detection function only | Not output | $\bigcirc$ |  |
|  |  | For limit SW output function only |  | 16 points | $\bigcirc$ |  |
|  |  | For limit SW output and positioning functions | Limit SW output: 8 points <br> Positioning signal output: 8 points | For limit SW output and positioning functions | Limit SW output: 8 points <br> Positioning signal output: 8 points | $\bigcirc$ |  |
| Isolation method |  |  | Photo-coupler |  |  |  | $\bigcirc$ |  |
| Rated load voltage |  | 12/24VDC |  |  |  | $\bigcirc$ |  |
| Operating load voltage range |  | 10.2 to 30VDC |  |  |  | $\bigcirc$ |  |
| Max. load current |  | 100 mA |  |  |  | $\bigcirc$ |  |
| Max. inrush current |  | 0.4 A |  |  |  | $\bigcirc$ |  |
| Current leakage when OFF |  | 0.1 mA or less |  |  |  | $\bigcirc$ |  |
| Max. voltage drop when ON |  | 0.5 V (at 100 mA ) |  | 1.0 V (at 50 mA ) |  | $\triangle$ | Check the external device specifications as the external output specifications differ. |
| Response time | $\mathrm{OFF} \rightarrow \mathrm{ON}$ | 0.04 ms(when load current is 100 mA ) |  | 1 ms(when load current is 50 mA ) |  |  |  |
|  | $\mathrm{ON} \rightarrow$ OFF | 0.2 ms(when load current is 100 mA ) |  | 1 ms(when load current is 50 mA ) |  |  |  |
| Common connections |  | 1 common for 16 points (common terminal: TB20) |  | 1 common for 16 points (common terminal: A1, A2) |  | $\times$ | Wiring must be changed. |
| External cable connection |  | 20-point terminal block connector (M3 $\times 6$ screws) |  | 24-pin connector |  |  |  |
| Applicable wire size |  | 0.75 to $2 \mathrm{~mm}^{2}$ |  | $0.3 \mathrm{~mm}^{2}$ |  |  |  |

## (c) Comparison of I/O signals for PLC CPU

Modifying sequence programs is required as the I/O signals differ.
For details of the I/O signals and sequence programs, refer to the user's manual.

| A62LS-S5 |  |  |  | A1S62LS |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Device NO. | Signal name | Device NO. | Signal name | Device NO. | Signal name |  |  |  | $\begin{aligned} & \text { Device } \\ & \text { NO. } \end{aligned}$ | Signal name |
| $\begin{gathered} \text { X0 } \\ \text { to } \\ \text { XF } \end{gathered}$ | Not used | $\begin{aligned} & \mathrm{YO} \\ & \text { to } \\ & \mathrm{YF} \end{aligned}$ | Not used | X0 | A1S62LS operation status |  |  |  | $\begin{aligned} & \mathrm{YO} \\ & \text { to } \\ & \mathrm{YF} \end{aligned}$ | Not used |
|  |  |  |  | X1 |  |  |  |  |  |  |
|  |  |  |  | X2 |  | per limit ove | rave | ' detection |  |  |
|  |  |  |  | X3 |  | wer limit ove | rave | ' detection |  |  |
|  |  |  |  | X4 |  | nsor error de | ectio |  |  |  |
|  |  |  |  | X5 | 'Excessive correction amount' detection |  |  |  |  |  |
|  |  |  |  | X6 | 'Excessive current position change' detection |  |  |  |  |  |
|  |  |  |  | X7 | Error detection |  |  |  |  |  |
|  |  |  |  | X8 |  | Channel 0 |  |  |  |  |
|  |  |  |  | X9 |  | Channel 1 |  |  |  |  |
|  |  |  |  | XA |  | Channel 2 | $\begin{aligned} & \text { 을 } \\ & \text { (0) } \end{aligned}$ | Channel 10 <br> (High-speed) |  |  |
|  |  |  |  | XB |  | Channel 3 |  | Channel 11 (Low- or mediumspeed) |  |  |
|  |  |  |  | XC |  | Channel 4 | $$ | Channel 12 <br> (Brake <br> release) |  |  |
|  |  |  |  | XD |  | Channel 5 |  | Channel 13 <br> (In-position) |  |  |
|  |  |  |  | XE |  | Channel 6 |  | Channel 14 (Positioning in progress) |  |  |
|  |  |  |  | XF |  | Channel 7 |  | Channel 15 (Operation error) |  |  |



A62LS-S5

| A62LS-S5 |  |  |  |
| :---: | :---: | :---: | :---: |
| Device NO. | Signal name | Device NO. | Signal name |
| $\begin{gathered} \mathrm{X} 20 \\ \text { to } \\ \mathrm{X} 2 \mathrm{~F} \end{gathered}$ | Not used | Y20 | PLC ready |
|  |  | Y21 | Positioning START (leading edge detection) |
|  |  | Y22 | Positioning STOP (leading edge detection) |
|  |  | Y23 | FWD (forward) JOG (operation occurs during ON) |
|  |  | Y24 | RVS (reverse) JOG (operation occurs during ON) |
|  |  | Y25 | Limit SW output enabled |
|  |  | Y26 | Error reset |
|  |  | Y27 | Current position preset 1 disabled Current position preset command (leading edge detection) |
|  |  | Y28 | Current position preset 2 disabled |
|  |  | $\begin{gathered} \mathrm{Y} 29 \\ \text { to } \\ \mathrm{Y} 2 \mathrm{~F} \end{gathered}$ | Not used |

(d) Buffer memory address comparison

No special differences are identified.

### 10.2.6 Positioning module comparison

(1) Comparisons between AD70 and A1SD70
(a) Performance specifications comparison (Compatible for other than items shown in the following list.)

O : Compatible, $\Delta$ : Partial change required, $\times$ : Incompatible

| Item |  | AD70 | A1SD70 | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Number of I/O points |  | 32 points <br> (I/O assignment: 32 special-purpose points) | 48 points (I/O assignment: 16 empty points + 32 special-purpose points) | $\triangle$ | Change the start I/O No. by PLC parameter. ${ }^{*}$ |
| Number of I/O slots |  | 1 slot occupied | 2 slots occupied | $\times$ | Since 1 more slot is occupied, the number of base slots must be reviewed. ${ }^{*}$ |
| Connector for external connection | For control signal connection | 9-pin connector | 9-pin connector | O |  |
|  | For drive module connection | 15-pin connector | 15-pin connector | O |  |
| External power supply | Voltage | +15VDC/-15VDC | +15VDC/-15VDC | $\bigcirc$ |  |
|  | Current | $+15 \mathrm{VDC} \cdots 0.2 \mathrm{~A} /-15 \mathrm{VDC} \cdots 0.02 \mathrm{~A}$ | +15VDC $\cdots 0.2 \mathrm{~A} /-15 \mathrm{VDC} \cdots 0.02 \mathrm{~A}$ | $\bigcirc$ |  |
|  | Terminal screw size | M4 screw | M3.5 screw | $\times$ | Wiring must be changed. |
| Internal current consumption |  | 5VDC 0.3A | 5VDC 0.3A | O |  |
| External dimentions |  | $250(\mathrm{H}) \times 37.5(\mathrm{~W}) \times 119$ (D) (mm) | $130(\mathrm{H}) \times 69.5(\mathrm{~W}) \times 93.6$ (D) $(\mathrm{mm})$ | $\triangle$ | The dimensions are different. |
| Weight |  | 0.5 kg | 0.4 kg | $\bigcirc$ |  |

*1 Since 2 slots are occupied for A1SD70, the device NO. differs from that of AD70. Therefore, the existing program must be modified.
In the parameter I/O assignment of GX Developer, setting the first slot of A1SD70 to "0 empty point" enables reuse of the existing program. I/O assignment setting is recommended.

I/O assignment for AD70
When AD70 is set to I/O slot No. 0 of main base

| I/O slot |  | 0 | 1 | 2 | 3 | 4 | 5 | 6 | 7 |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\stackrel{\rightharpoonup}{\mathrm{O}}$ | 을 |  |  |  |  |  |  |  |
| (I/O number) X/Y 0 to 1F |  |  |  |  |  |  |  |  |  |

I/O assignment for A1SD70
When A1SD70 is set to I/O slot No. 0 and 1 of main base

(I/O number)

|  | Slot 0 | Slot 1 |
| :--- | :---: | :---: |
| Without I/O | 16 points | 32 points |
| assignment | $(X / Y 0$ to $F)$ | $(X / Y 10$ to $2 F)$ |
| With I/O <br> assignment | 0 point |  |
| $(-)$ | 32 points |  |
| $(X / Y 0$ to $1 F)$ |  |  |

(b) Function comparison

No special differences are identified.
(c) Comparison of I/O signals for PLC CPU

Compatibility is supported in the setting marked.. ${ }^{* 1}$

(d) Buffer memory address comparison

No special differences are identified.

## EXTERNAL DIMENSIONS

11.1 Large-sized A/QnA Series External Dimensions and Mounting Dimensions


Unit: mm

| Base unit | Dimensions |  |  | Dimensions for mounting |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | H | W | D*1 | H1 | H2 | W1 | W2 |
| A32B | 250 | 247 | $130^{*}$ | 200 | 25 | 227 | 10 |
| A32B-S1 |  | 268 |  |  |  | 248 |  |
| A35B |  | 382 |  |  |  | 362 |  |
| A38B |  | 480 |  |  |  | 460 |  |
| A38HB |  | 480 |  |  |  | 460 |  |
| A38HBEU |  | 480 |  |  |  | 460 |  |
| A32RB |  | 494 |  |  |  | 474 |  |
| A33RB |  | 570 |  |  |  | 550 |  |
| A37RHB |  | 497 |  |  |  | 477 |  |
| A52B |  | 183 |  |  |  | 163 |  |
| A55B |  | 297 |  |  |  | 277 |  |
| A58B |  | 411 |  |  |  | 391 |  |
| A62B |  | 238 |  |  |  | 218 |  |
| A65B |  | 352 |  |  |  | 332 |  |
| A68B |  | 466 |  |  |  | 446 |  |
| A68RB |  | 522 |  |  |  | 502 |  |

*1 $\quad \mathrm{D}$ (depth) varies depending on the module to be mounted. Therefore, confirm the external dimensions of each module.
*2 For mounting A series module of which depth is 121 mm .

### 11.2 Small-sized AnS/Q2AS Series External Dimensions and Mounting Dimensions

(1) A1SH, A2SH, A2AS, A2AS-S1, A2USH-S1


* A memory cassette is installed from the front face for A2AS(-S1) and A2USH-S1 and the left face for A1SH and A2SH.
(2) Q2AS

Width (W)

| A1S32B | A1S33B | A1S35B | A1S38B $\cdot$ A1S38HB $\cdot$ A1S38HBEU | A1S52B | A1S55B | A1S 58 B | A1S65B | A1S68B |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| 220 | 255 | 325 | 430 | 155 | 260 | 365 | 315 | 420 |

## APPENDICES

## Appendix 1 Spare Parts Storage

(1) The general specifications of PLCs are as follows. Please do not store spare parts under a high temperature or high humidity condition, even within the range guaranteed by the specifications.

| Storage ambient temperature | -20 to $75^{\circ} \mathrm{C}$ |
| :---: | :--- |
| Storage ambient humidity | 10 to $90 \%$, no condensation |

(2) Store in a place avoiding direct sunlight.
(3) Store under a condition with no dust or corrosive gas.
(4) The battery capacity of a A6BAT battery or a lithium-coin battery (commercially available) for memory card will be decreased by its self-discharging even when it is not used. Replace it with new one in 5 years as a guideline.
(5) Among power supply modules or CPU modules with built-in power supply that use any aluminum electrolytic capacitor, the characteristics of the modules listed below will be deteriorated if they are left un-energized for a long time. Therefore, take the following measures.

| Product |  |
| :---: | :--- |
| CPU module | Model |
| (Power supply module built-in type) | A1NCPU, A1NCPUP21, A1NCPUR21, A1NCPUP21-S3 <br> A2CCPU, A2CCPUP21, A2CCPUR21, A2CCPUC24 <br> A2CCPUC24-PRF |
|  | A61P, A61PEU, A61P-UL, A62P, A62PEU, A63P, A68P <br> A61RP, A67RP, A2CJ66P |

[Measures for preventing aluminum electrolytic capacitor characteristics deterioration]
Once every 2 or 3 years, increase the voltage gradually from 0 V to the rated voltage spending 10 minutes or more and keep the voltage for several hours to activate the aluminum electrolytic capacitor. Or, rotate products at the periodic inspection (in every 1 to 2 years).
[Reference]
The life of an aluminum electrolytic capacitor, even if not used and under a normal temperature, decreases approximately 4 times slowly than the case when it is energized.

## Appendix 2 Related Manuals

## Appendix 2.1 Replacement Handbooks

| No. | Manual name | Manual No. | Model <br> name code |
| :---: | :--- | :--- | :---: |
| 1 | Transition from MELSEC-A/QnA (Large Type) Series to Q Series <br> Handbook (Fundamentals) | L-08043ENG | - |
| 2 | Transition from MELSEC-A/QnA (Large Type) Series to Q Series <br> Handbook (Intelligent Function Modules) | L-08046ENG | - |
| 3 | Transition from MELSEC-A/QnA (Large Type) Series to Q Series <br> Handbook (Network Modules) | L-08048ENG | - |
| 4 | Transition from MELSEC-A/QnA (Large Type) Series to Q Series <br> Handbook (Communications) | L-08050ENG | - |
| 5 | Transition from MELSEC-AOJ2H Series to Q Series Handbook | L-08060ENG | - |
| 6 | Transition from MELSECNET/MINI-S3,A2C(I/O) to CC-Link Handbook | L-08061ENG | - |
| 7 | Transition from MELSEC-I/OLINK to CC-Link/LT Handbook | L-08062ENG | - |
| 8 | Transition from MELSEC-A/QnA Large Type Series to AnS/Q2AS Small <br> Type Series Handbook | L-08064ENG | - |

## Appendix 2.2 Large-sized A/QnA Series

| No. | Manual name | Manual No. | Model name code |
| :---: | :---: | :---: | :---: |
| 1 | MELSEC-A/QnA Catalog | L-08033E | - |
| 2 | MELSEC-A/QnA Data Book | L-08029E | - |
| 3 | Type A1N/A2N(S1) / A3NCPU User's Manual | IB-66543 | 13JE83 |
| 4 | Type A2A (S1) / A3ACPU User's Manual | IB-66544 | 13JE84 |
| 5 | Type A2U (S1) / A3U/A4UCPU User's Manual | IB-66436 | 13JE25 |
| 6 | Q2ACPU (S1) / Q3ACPU/Q4ACPU User's Manual | IB-66608 | 13J821 |
| 7 | Type A2CCPU (P21/R21), A2CCPU-DC24V, A2CCPUC24 (-PRF), A2CJCPU User's Manual | IB-66545 | 13JE85 |
| 8 | Type ACPU/QCPU-A (A Mode) (Fundamentals) Programming Manual | IB-66249 | 13 J 40 |
| 9 | Type ACPU/QCPU-A (A Mode) (Common Instructions) Programming Manual | IB-66250 | 13J741 |
| 10 | Type AnSHCPU/AnACPU/AnUCPU/QCPU-A (A Mode) Programming Manual (Dedicated Instructions) | IB-66251 | 13 J 742 |
| 11 | Type AnACPU/AnUCPU/QCPU-A (A Mode) Programming Manual (PID Control Instructions) | IB-66258 | 13J744 |
| 12 | Type MELSAP-II(SFC) Programming Manual | IB-66361 | 13JF40 |
| 13 | QnACPU Programming Manual (Fundamentals) | IB-66614 | 13JF46 |
| 14 | QnACPU Programming Manual (Special Function Module) | SH-4013 | 13JF56 |
| 15 | QCPU(Q Mode)/QnACPU Programming Manual (Common Instructions) | SH-080039 | 13JF58 |
| 16 | QCPU(Q Mode)/QnACPU Programming Manual (PID Control Instructions) | SH-080040 | 13JF59 |
| 17 | QCPU(Q Mode)/QnACPU Programming Manual (SFC) | SH-080041 | 13JF60 |
| 18 | I/O module type Building block User's Manual | IB-66140 | 13 J 643 |
| 19 | A/D converter module type A68AD User's Manual | IB-66054 | 13 J 607 |
| 20 | A/D converter module type A68AD-S2 User's Manual | IB-66213 | 13 J 647 |
| 21 | Analog-Digital Converter Module type A68ADN User's Manual | IB-66307 | 13 J 668 |
| 22 | Analog-Digital Converter Module type A616AD User's Manual | IB-66171 | 13 J 645 |
| 23 | D/A converter module type A62DA User's Manual | IB-66053 | 13 J 608 |
| 24 | D/A converter module type A62DA-S1 User's Manual | IB-66177 | 13 J 648 |
| 25 | Digital-Analog Converter Module type A68DAV/DAI(S1) User's Manual | IB-66285 | 13 J 667 |
| 26 | Digital-Analog Converter Module type A616DAV User's Manual | IB-66172 | 13 J 650 |
| 27 | Digital-Analog Converter Module type A616DAI User's Manual | IB-66173 | 13 J 651 |
| 28 | Pt100 input module type A68RD3/4 User's Manual | IB-66308 | 13 J 670 |
| 29 | Type A68RD3N/4N, A1S62RD3N/4N Pt100 Input Module User's Manual | SH-080193 | 13JR46 |
| 30 | Temperature-Digital Converter Module type A616TD User's Manual | IB-66174 | 13J654 |
| 31 | High speed counter module type AD61(-S1) User's Manual | IB-66052 | 13 J 610 |
| 32 | Positioning module type AD70 User's Manual | IB-66309 | 13 J 663 |
| 33 | Positioning Module Type AD72 User's Manual | IB-66095 | 13 J 622 |
| 34 | A1SD75P1-S3/P2-S3/P3-S3/AD75P1-S3/P2-S3/P3-S3 Positioning Module User's Manual | IB-66716 | 13 J 871 |
| 35 | Positioning module type A1SD75M1/M2/M3, AD75M1/M2/M3 User's Manual | IB-66715 | 13 J 70 |
| 36 | Type MELSECNET, MELSECNET/B Data Link System Reference Manual | IB-66350 | 13JF70 |
| 37 | Control \& Communication Link System Master/Local Module Type AJ61BT11/A1SJ61BT11 User's Manual | IB-66721 | 13 J 872 |
| 38 | For A Ethernet Interface Module User's Manual | SH-080192 | 13JR45 |
| 39 | For QnA Ethernet Interface Module User's Manual | SH-080146 | 13JR33 |
| 40 | Computer Link Module (Com.link func./Print. func.) User's Manual | SH-3511 | 13JF77 |
| 41 | Serial Communications Module User's Manual (Modem Function Additional Version) | SH-66612 | 13J825 |
| 42 | Intelligent Communication Module type AD51-S3 User's Manual | IB-66189 | 13J655 |


| No. | Manual name | Manual No. | Model <br> name code |
| :---: | :--- | :--- | :--- |
| 43 | Intelligent communication module type AD51H-S3 User's Manual | IB-66401 | 13JE16 |
| 44 | MELSECNET/MINI-S3 Master Module Type AJ71PT32-S3, AJ71T32-S3, <br> A1SJ71PT32-S3, A1SJ71T32-S3 User's Manual | SH-66565 | 13JE64 |
| 45 | MELSEC-I/O LINK Remote I/O System Master Module type AJ51T64/ <br> A1SJ51T64 User's Manual | SH-66574 | 13J748 |
| 46 | Type MELSECNET/10 Network system (PLC to PLC network) Reference <br> Manual | IB-66440 | 13JE33 |
| 47 | For QnA/Q4AR MELSECNET/10 Network System Reference Manual | IB-66690 | 13JF78 |
| 48 | Control \& Communication Link System Master/Local Module type <br> AJ61QBT11/A1SJ61QBT11 User's Manual | IB-66722 | 13J873 |
| 49 | Positioning Module Type AD71(S1/S2/S7)/A1SD71-S2(S7) User's <br> Manual | IB-66563 | 13JE98 |
| 50 | PC fault detection module type AS91, A1SS91, A0J2-S91 User's Manual | IB-66626 | 13J828 |

## Appendix 2.3 Small-sized AnS/Q2AS Series

| No. | Manual name | Manual No. | Model name code |
| :---: | :---: | :---: | :---: |
| 1 | Type A2USHCPU-S1 User's Manual | IB-66789 | 13JL30 |
| 2 | Model Q2AS (H) CPU (S1) User's Manual | SH-3599 | 13 J 858 |
| 3 | Type ACPU/QCPU-A (A Mode) Programming Manual (Fundamentals) | IB-66249 | 13 J 440 |
| 4 | Type ACPU/QCPU-A (A Mode) Programming Manual (Common Instructions) | IB-66250 | 13 J 741 |
| 5 | Type AnSHCPU/AnACPU/AnUCPU/QCPU-A (A Mode) Programming Manual (Dedicated Instructions) | IB-66251 | 13 J 742 |
| 6 | Type AnACPU/AnUCPU/QCPU-A (A Mode) Programming Manual (PID Control Instructions) | IB-66258 | 13J744 |
| 7 | Type MELSAP-II (SFC) Programming Manual | IB-66361 | 13JF40 |
| 8 | QnACPU Programming Manual (Fundamentals) | IB-66614 | 13JF46 |
| 9 | QnACPU Programming Manual (Special Function Module) | SH-4013 | 13JF56 |
| 10 | QCPU (Q Mode)/QnACPU Programming Manual (Common Instructions) | SH-080039 | 13JF58 |
| 11 | QCPU (Q Mode)/QnACPU Programming Manual (PID Control Instructions) | SH-080040 | 13JF59 |
| 12 | QCPU (Q Mode)/QnACPU Programming Manual (SFC) | SH-080041 | 13JF60 |
| 13 | AnS Module type I/O User's Manual | IB-66541 | 13JE81 |
| 14 | Analog-Digital Converter Module type A1S68AD User's Manual | IB-66576 | 13 J 757 |
| 15 | D/A converter module type A1S62DA User's Manual | IB-66335 | 13 J 673 |
| 16 | Thermocouple input module type A1S68TD User's Manual | IB-66571 | 13 J 781 |
| 17 | Digital-Analog Converter Module type A1S68DAV/DAI User's Manual | IB-66587 | 13 J 810 |
| 18 | Pt100 input module type A1S62RD3/4 User's Manual | IB-66338 | 13J675 |
| 19 | High speed counter module type A1SD62, A1SD62E, A1SD62D(S1) User's Manual | IB-66593 | 13 J 816 |
| 20 | Positioning module type A1SD70 User's Manual | IB-66367 | 13JE04 |
| 21 | Positioning module type A1SD75M1/M2/M3, AD75M1/M2/M3 User's Manual | IB-66715 | 13 J 870 |
| 22 | A1SD75P1-S3/P2-S3/P3-S3, AD75P1-S3/P2-S3/P3-S3 Positioning Module User's Manual | IB-66716 | 13 J 871 |
| 23 | Type A1S62LS User's Manual | IB-66647 | 13 J 837 |
| 24 | Type A1SD51S Intelligent communication module User's Manual | IB-66551 | 13JE90 |

## Appendix 2.4 Programming Tool

| No. | Manual name | Manual No. | Model code |
| :---: | :--- | :--- | :--- |
| 1 | GX Developer Version 8 Operating Manual | SH-080373E | $13 \mathrm{JU41}$ |
| 2 | GX Developer Version 8 Operating Manual (SFC) | SH-080374E | $13 \mathrm{JU42}$ |
| 3 | GX Simulator Version 7 Operating Manual | SH-080468ENG | 13JU51 |
| 4 | Type SW4IVD-GPPA (GPP) Operating Manual | IB-66855 | 13JL62 |

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## WARRANTY

Please confirm the following product warranty details before using this product.

## 1. Gratis Warranty Term and Gratis Warranty Range

If any faults or defects (hereinafter "Failure") found to be the responsibility of Mitsubishi occurs during use of the product within the gratis warranty term, the product shall be repaired at no cost via the sales representative or Mitsubishi Service Company.
However, if repairs are required onsite at domestic or overseas location, expenses to send an engineer will be solely at the customer's discretion. Mitsubishi shall not be held responsible for any re-commissioning, maintenance, or testing on-site that involves replacement of the failed module.

## [Gratis Warranty Term]

The gratis warranty term of the product shall be for one year after the date of purchase or delivery to a designated place. Note that after manufacture and shipment from Mitsubishi, the maximum distribution period shall be six (6) months, and the longest gratis warranty term after manufacturing shall be eighteen (18) months. The gratis warranty term of repair parts shall not exceed the gratis warranty term before repairs.

## [Gratis Warranty Range]

(1) The range shall be limited to normal use within the usage state, usage methods and usage environment, etc., which follow the conditions and precautions, etc., given in the instruction manual, user's manual and caution labels on the product.
(2) Even within the gratis warranty term, repairs shall be charged for in the following cases.

1. Failure occurring from inappropriate storage or handling, carelessness or negligence by the user. Failure caused by the user's hardware or software design.
2. Failure caused by unapproved modifications, etc., to the product by the user.
3. When the Mitsubishi product is assembled into a user's device, Failure that could have been avoided if functions or structures, judged as necessary in the legal safety measures the user's device is subject to or as necessary by industry standards, had been provided.
4. Failure that could have been avoided if consumable parts (battery, backlight, fuse, etc.) designated in the instruction manual had been correctly serviced or replaced.
5. Failure caused by external irresistible forces such as fires or abnormal voltages, and Failure caused by force majeure such as earthquakes, lightning, wind and water damage.
6. Failure caused by reasons unpredictable by scientific technology standards at time of shipment from Mitsubishi.
7. Any other failure found not to be the responsibility of Mitsubishi or that admitted not to be so by the user.

## 2. Onerous repair term after discontinuation of production

(1) Mitsubishi shall accept onerous product repairs for seven (7) years after production of the product is discontinued. Discontinuation of production shall be notified with Mitsubishi Technical Bulletins, etc.
(2) Product supply (including repair parts) is not available after production is discontinued.

## 3. Overseas service

Overseas, repairs shall be accepted by Mitsubishi's local overseas FA Center. Note that the repair conditions at each FA Center may differ.

## 4. Exclusion of loss in opportunity and secondary loss from warranty liability

Regardless of the gratis warranty term, Mitsubishi shall not be liable for compensation of damages caused by any cause found not to be the responsibility of Mitsubishi, loss in opportunity, lost profits incurred to the user by Failures of Mitsubishi products, special damages and secondary damages whether foreseeable or not, compensation for accidents, and compensation for damages to products other than Mitsubishi products, replacement by the user, maintenance of on-site equipment, start-up test run and other tasks.

## 5. Changes in product specifications

The specifications given in the catalogs, manuals or technical documents are subject to change without prior notice.

## 6. Product application

(1) In using the Mitsubishi MELSEC programmable logic controller, the usage conditions shall be that the application will not lead to a major accident even if any problem or fault should occur in the programmable logic controller device, and that backup and fail-safe functions are systematically provided outside of the device for any problem or fault.
(2) The Mitsubishi programmable logic controller has been designed and manufactured for applications in general industries, etc. Thus, applications in which the public could be affected such as in nuclear power plants and other power plants operated by respective power companies, and applications in which a special quality assurance system is required, such as for Railway companies or Public service purposes shall be excluded from the programmable logic controller applications.
In addition, applications in which human life or property that could be greatly affected, such as in aircraft, medical applications, incineration and fuel devices, manned transportation, equipment for recreation and amusement, and safety devices, shall also be excluded from the programmable logic controller range of applications.
However, in certain cases, some applications may be possible, providing the user consults their local Mitsubishi representative outlining the special requirements of the project, and providing that all parties concerned agree to the special circumstances, solely at the users discretion.

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[^0]:    *1: Not available for the A1NCPU (P21/R21).

[^1]:    *1 For large-sized QnA series, up to two cards can be installed. For small-sized Q2AS series, only one card can be installed.
    *2 Maximum value when the Flash memory card (Q1MEM- $\square$ SF) is used. (discontinued in August, 2002.)

[^2]:    *1 Check the specifications of sensor or switch to connect to the A1SX10EU.

[^3]:    *1 Check the specifications of sensor or switch to connect to the A1SX20EU.
    *2 The figure on the right shows derating.

[^4]:    *1 Connect a varistor to reduce external noise.
    *2 Mount a fuse on every external terminal to prevent external devices and modules from burning out upon load short circuit.

[^5]:    *1 Check the specifications of sensor or switch to connect to the A1SI61.

