# MITSUBISHI

Mitsubishi Programmable Logic Controller

Transition from MELSEC-A/QnA Large Type

Series to AnS/Q2AS Small Type Series Handbook



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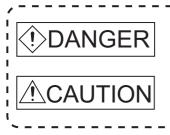
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## SAFETY PRECAUTIONS

(Be sure to read these instructions before use.)

Before using the product, read this and relevant manuals carefully and handle the product correctly with full attention to safety.

In this manual, **SAFETY PRECAUTIONS** are classified into 2 levels: "DANGER" and "CAUTION".



Indicates that incorrect handling may cause hazardous conditions, resulting in death or severe injury.

\_ \_ \_ \_ \_ \_

Indicates that incorrect handling may cause hazardous conditions, resulting in minor or moderate injury and/or property damage.

Under some circumstances, failure to observe the ACAUTION level instructions may also lead to serious results.

\_ \_ \_ \_ \_ \_ \_ \_ \_ \_

Be sure to observe the instructions of both levels to ensure the safety.

Please keep this manual in a safe place for future reference and also pass this manual on to the end user.

### [Design Precautions]

# DANGER

- Create a safety circuit outside the PLC to ensure the whole system will operate safely even if an external power failure or a PLC failure occurs. Otherwise, incorrect output or malfunction may cause an accident.
  - (1) For an emergency stop circuit, protection circuit and interlock circuit that is designed for incompatible actions such as forward/reverse rotation or for damage prevention such as the upper/lower limit setting in positioning, any of them must be created outside the PLC.
  - (2) When the PLC detects the following error conditions, it stops the operation and turn off all the outputs.
    - The overcurrent protection device or overvoltage protection device of the power supply module is activated.
    - The PLC CPU detects an error such as a watchdog timer error by the self-diagnostics function.

In the case of an error of a part such as an I/O control part that cannot be detected by the PLC CPU, all the outputs may turn on. In order to make all machines operate safely in such a case, set up a fail-safe circuit or a specific mechanism outside the PLC. For a fail-safe circuit example, refer to "LOADING AND INSTALLATION" in the User's Manual of the relevant CPU.

(3) Depending on the failure of the output module's relay or transistor, the output status may remain ON or OFF incorrectly. For output signals that may lead to a serious accident, create an external monitoring circuit.

### [Design Precautions]

# DANGER

If load current more than the rating or overcurrent due to a short circuit in the load has flowed in the output module for a long time, it may cause a fire and smoke. Provide an external safety device such as a fuse.
Design a circuit so that the external power will be supplied after power-up of the PLC. Activating the external power supply prior to the PLC may result in an accident due to incorrect output or malfunction.
<ul> <li>For the operation status of each station at a communication error in data link, refer to the respective data link manual.</li> <li>Otherwise, incorrect output or malfunction may cause an accident.</li> </ul>
<ul> <li>When controlling a running PLC (data modification) by connecting a peripheral device to the CPU module or a PC to an intelligent/special function module, create an interlock circuit on sequence programs so that the whole system functions safely all the time.</li> <li>Also, before performing any other controls (e.g. program modification, operating status change (status control)), read the manual carefully and ensure the safety.</li> <li>In these controls, especially the one from an external device to a PLC in a remote location, some PLC side problem may not be resolved immediately due to failure of data communications.</li> <li>To prevent this, create an interlock circuit on sequence programs and establish corrective procedures for communication failure between the external device and the PLC CPU.</li> </ul>
<ul> <li>When setting up the system, do not allow any empty slot on the base unit.</li> <li>If any slot is left empty, be sure to use a blank cover (A1SG60) or a dummy module (A1SG62) for it.</li> <li>When using the extension base unit, A1S52B, A1S55B, A1S58B, A1S52B(S1), A1S55B(S1) or</li> <li>A1S58B(S1), attach the included dustproof cover to the module in slot 0.</li> <li>Otherwise, internal parts of the module may be flied in the short circuit test or when an overcurrent or</li> </ul>

overvoltage is accidentally applied to the external I/O section.

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Do not install the control lines or communication cables together with the main circuit or power lines, or bring them close to each other.

Keep a distance of 100mm or more between them.

Failure to do so may cause malfunctions due to noise.

When an output module is used to control the lamp load, heater, solenoid valve, etc., a large current (ten times larger than the normal one) may flow at the time that the output status changes from OFF to ON. Take some preventive measures such as replacing the module with the one of a suitable current rating.

### [Installation Precautions]

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Use the PLC under the environment specified in the user's manual. Otherwise, it may cause electric shocks, fires, malfunctions, product deterioration or damage. • Insert the module fixing projection into the fixing hole in the base unit to mount the module, and tighten the module fixing screws with the specified torque. Even correct mounting may cause malfunctions, a failure or a drop of the module if no screw is tightened. Tightening the screw excessively may damage the screw and/or the module, resulting in a drop of the module, a short circuit or malfunctions. Connect the extension cable to the connector of the base unit or module. Check for incomplete connection after installing it. Poor electrical contact may cause incorrect inputs and/or outputs. Insert the memory cassette and fully press it to the memory cassette connector. Check for incomplete connection after installing it. Poor electrical contact may cause malfunctions. Be sure to shut off all phases of the external power supply used by the system before mounting or removing the module. Failure to do so may damage the module. Do not directly touch the conductive part or electronic components of the module. Doing so may cause malfunctions or a failure of the module.

### [Wiring Precautions]

# DANGER

- Be sure to shut off all phases of the external power supply used by the system before wiring. Failure to do so may result in an electric shock or damage of the product.
- Before energizing and operating the system after wiring, be sure to attach the terminal cover supplied with the product.

Failure to do so may cause an electric shock.

### [Wiring Precautions]

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- Ground the FG and LG terminals correctly.
   Failure to do so may cause an electric shock or malfunctions.
- Wire the module correctly after confirming the rated voltage and terminal layout.
   Connecting a power supply of a different voltage rating or incorrect wiring may cause a fire or failure.
- Do not connect multiple power supply modules to one module in parallel.
   The power supply modules may be heated, resulting in a fire or failure.
- Press, crimp or properly solder the connector for external connection with the specified tool. Incomplete connection may cause a short circuit, fire or malfunctions.
- Tighten terminal screws within the specified torque range.
   If the screw is too loose, it may cause a short circuit, fire or malfunctions.
   If too tight, it may damage the screw and/or the module, resulting in a short circuit or malfunctions.
- Carefully prevent foreign matter such as dust or wire chips from entering the module.
   Failure to do so may cause a fire, failure or malfunctions.

### [Startup and Maintenance Precautions]

<ul> <li>Do not touch any terminal during power distribution.</li> <li>Doing so may cause an electric shock.</li> </ul>
<ul> <li>Properly connect batteries.</li> <li>Do not charge, disassemble, heat or throw them into the fire and do not make them short-circuited and soldered.</li> <li>Incorrect battery handling may cause personal injuries or a fire due to exothermic heat, burst and/or ignition.</li> </ul>
<ul> <li>Be sure to shut off all phases of the external power supply used by the system before cleaning or retightening the terminal screws or module mounting screws.</li> <li>Failure to do so may result in an electric shock.</li> <li>If they are too loose, it may cause a short circuit or malfunctions.</li> <li>Tightening the screw excessively may damage the screw and/or the module, resulting in a drop of the module, a short circuit or malfunctions.</li> </ul>

### [Startup and Maintenance Precautions]

# 

When performing online operations (especially, program modification, forced output or operating status change) by connecting a peripheral device to the running CPU module, read the manual carefully and ensure the safety.

Incorrect operation will cause mechanical damage or accidents.

- Do not disassemble or modify each of modules.
   Doing so may cause failure, malfunctions, personal injuries and/or a fire.
- When using a wireless communication device such as a mobile phone, keep a distance of 25cm (9.84inch) or more from the PLC in all directions.
   Failure to do so may cause malfunctions.
- Be sure to shut off all phases of the external power supply used by the system before mounting or removing the module.

Failure to do so may result in failure or malfunctions of the module.

When replacing the fuse, use a fuse specified by the manufacturer.
 Using the one for the high-rated current or an electric wire may cause a fire.

### [Startup and Maintenance Precautions]

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Do not drop or apply any impact to the battery.

Doing so may damage the battery, resulting in electrolyte spillage inside the battery. If any impact has been applied, discard the battery and never use it.

Before handling modules, touch a grounded metal object to discharge the static electricity from the human body.

Failure to do so may cause failure or malfunctions of the module.

### [Disposal Precautions]

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When disposing of the product, treat it as an industrial waste.

### [Transportation Precautions]

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 When transporting lithium batteries, make sure to treat them based on the transportation regulations. (Refer to the User's Manual of each CPU for details of the relevant models.)

#### REVISIONS

* The handbook number is given on the bottom left of the back cover.
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# INTRODUCTION

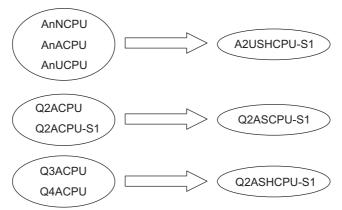
# 1.1 Suggestions for Transition from Large-sized A/QnA Series to Small-sized AnS/Q2AS Series

#### 1.1.1 Concepts for alternative models

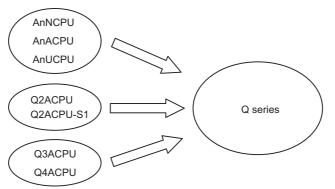
For transition from large-sized A/QnA series to small-sized AnS/Q2AS series, the A2USHCPU-S1, Q2ASCPU-S1 or Q2ASHCPU-S1 is suggested as an alternative model.

Consider adopting the Q series when transition to the A2USHCPU-S1, Q2ASCPU-S1 or Q2ASHCPU-S1 is difficult for the following reasons: the number of extension stages used is two or more, the I/O points used is 1024 or more, the program capacity exceeds 60K steps, etc.

#### Alternative models suggested



When replacement is difficult



The AnNCPU indicates A1N, A2N,A2N-S1, A3N, A1NP21/R21, A2NP21/R21, A2NP21/R21-S1, A3NP21/R21, A1NP21-S3, A2NP21-S3, A2NP21-S4 and A3NP21-S3. The AnACPU indicates A2A, A2A-S1, A3A,A2AP21/R21, A2AP21/R21-S1, A3AP21/R21, A2AP21-S3, A2AP21-S4 and A3AP21-S3. The AnUCPU indicates A2U, A2U-S1, A3U and A4U. At the first, figure out if each model can be used or not in reference to the basic specifications shown below. As a result of this, if a replacement is available, check the detailed specifications.

Selectio	n item	A2USHCPU-S1	Q2ASCPU-S1	Q2ASHCPU-S1	QnHCPU
Number of extension	1 stage	0	0	0	0
stages	7 stages	×	×	×	O <sup>*1</sup>
Number of modules	16 within	0	0	0	0
mounted	More than 16	×	×	×	0
I/O points	1024 within	0	0	0	0
1/O points	More than 1024	×	×	×	0
	30k steps within	0	0	0	0
Program capacity	60k steps within	×	0	0	O <sup>*1</sup>
	More than 60k steps	×	×	×	O <sup>*1</sup>
I/O control method	Refresh mode	0	0	0	0
	Direct mode	△*2	△*2	△*2	△*2
Processing speed LD: $\mu$ s	3	0.09	0.2	0.075	0.079 to 0.034
Timer/counter update	END processing	0	_	_	_
timing	Execution of OUT Tn instruction	_	0	0	0
File register	Up to 120k points	O <sup>*3*4</sup>	O <sup>*5</sup>	O <sup>*5</sup>	O <sup>*6</sup>
File register (including the extension	More than 120k points	×	O <sup>*5</sup>	O <sup>*5</sup>	O <sup>*6</sup>
file register)	Up to 1018k points	×	O <sup>*5</sup>	O <sup>*5</sup>	O <sup>*6</sup>
	NET (II) compatibility	0	0	0	×
MELSECNET	3-tier master station support	0	0	0	×
Number of	1 module	0	0	0	×
Number of MELSECNET modules mounted	2 modules (3-tier master station)	0	0	0	×
mounted	NET/10 compatibility	0	0	0	O <sup>*7</sup>
Number of computer link	6 within	0	0	0	0
modules mounted *8	More than 6	×	0	0	0
Bus connection to GOT	O <sup>*9</sup>	O <sup>*10</sup>	O* <sup>10</sup>	0	
Microcomputer program (	except for SFC)	×	×	×	×
	CC-Link	0	∆ <sup>*11</sup>	△*11	△*11
Dedicated instruction	PID/Real constant/ Character string/ Others	0	0	0	0

 $\bigcirc$  : Usable,  $\bigtriangleup$  : Alternatives are available,  $\,\times$  : Unusable

# 

- \*1 : Differs according to the CPU type.
- \*2 : Applicable using partial refresh instructions and direct access instructions.
- \*3 : Decreases according to the parameter settings.
- \*4 : Stored in the built-in memory (not extensible).
- \*5 : A memory card is required.
- \*6 : The standard RAM + memory card must be used.
- \*7: The MELSECNET/H mode is not available when any CPU other than Qn(H)CPU are used together.

- \*8: Up to 6 A series A1SJ71UC24S (-R2, -R4) can be mounted.
- \*9: Included in the number of computer link modules.
- \*10: Equivalent to the A series module.
- \*11: No program is required since the network parameters are used.

#### 1.1.2 Advantages of transition from large-sized A/QnA Series to small-sized AnS/ Q2AS Series

#### (1) RAM memory featured as standard

The RAM memory with 112K to 256K byte memory capacity is built in as a standard feature.

The A2USHCPU-S1 can store file registers and comments since the RAM more than program capacity is featured as standard.

For the Q2ASCPU-S1 and Q2ASHCPU-S1, the RAM featured as standard has a capacity equivalent to the program amount.

#### (2) E<sup>2</sup>PROM operation

Installing a memory cassette (when using the A2USHCPU-S1) or a memory card (when using the Q2ASCPU-S1 or Q2ASHCPU-S1) enables the  $E^2$ PROM operation. (Equivalent to the conventional ROM operation)

Previously, the ROM operation required a dedicated device such as ROM writer. However, programs can be written by the PLC write operation in the same way as for the RAM, and the program is not lost even without a battery.

#### (3) Compact size

The external dimensions are 130 (H)  $\times$  430(W)  $\times$  110(D)mm (when using a base with 8 slots). The mounting area becomes smaller than that of the large-sized A/QnA series.

#### (4) Extension connectors equipped on the right and left sides of the main base unit

An extension connector is equipped on each of the right and left sides to allow connection of the extension base unit in any position.

Bus connection to GOT is available even when an extension base is attached.

#### (5) DIN rail installation

The A1S base unit has screw holes, and a DIN rail installation mechanism on the rear face.

#### 1.1.3 Precautions for transition from large-sized A/QnA series to small-sized AnS/ Q2AS series

- (1) Be sure to confirm the functions, specifications and handling instructions before using each small-sized AnS/Q2AS series module by referring to the relevant manual.
- (2) Be sure to check the operation of whole system before starting the actual operation.
- (3) Screws must be tightened for the module mounting.

# **2** CPU MODULE REPLACEMENT

### 2.1 List of CPU Module Alternative Models

	nA series model to be continued		Small-sized AnS/Q2AS series alternative models				
Product	Model	Model	Remarks (restrictions)				
	A1NCPU	A2USHCPU-S1	1) I/O control: Refresh/Direct switching $\rightarrow$ Refresh only 2) Processing speed (LD instruction): For refresh $1.0 \mu s \rightarrow 0.09 \mu s$ 3) PC MIX value: $0.2 \rightarrow 2.0$				
	A1NCPUP21	A2USHCPU-S1 A1SJ71AP21	<ul> <li>4) I/O points: 256 points → 1024 points</li> <li>5) Program capacity: 6k steps → 30k steps</li> <li>6) File register points: 0 points → 8k points</li> </ul>				
	A1NCPUR21	A2USHCPU-S1 A1SJ71AR21	<ul> <li>7) Extension stage: 1 stage → 1 stage</li> <li>8) Applicable memory: 4KRAM/4KROM/4KEROM → built-in RAM</li> <li>9) Microcomputer program: Available → Not available</li> </ul>				
	A2NCPU	A2USHCPU-S1	1) I/O control: Refresh/Direct switching $\rightarrow$ Refresh only 2) Processing speed (LD instruction): For refresh $1.0 \mu s \rightarrow 0.09 \mu s$ 3) PC MIX value: $0.2 \rightarrow 2.0$				
	A2NCPUP21	A2USHCPU-S1 A1SJ71AP21	<ul> <li>4) I/O points: 512 points → 1024 points</li> <li>5) Program capacity: 14k steps → 30k steps</li> <li>6) File register points: 4k points → 8k points</li> <li>7) Extension stage: 3 stages → 1 stage</li> </ul>				
CPU module	A2NCPUR21	A2USHCPU-S1 A1SJ71AR21	<ul> <li>8) Applicable memory: Depending on the memory cassette → built-in RAM</li> <li>9) Microcomputer program: Available → Not available</li> </ul>				
	A2NCPU-S1	A2USHCPU-S1	1) I/O control: Refresh/Direct switching $\rightarrow$ Refresh only 2) Processing speed (LD instruction): For refresh $1.0 \mu s \rightarrow 0.09 \mu s$ 3) PC MIX value: $0.2 \rightarrow 2.0$				
	A2NCPUP21-S1	A2USHCPU-S1 A1SJ71AP21	<ul> <li>4) I/O points: 1024 points → 1024 points</li> <li>5) Program capacity: 14k steps → 30k steps</li> <li>6) File register points: 4k points → 8k points</li> <li>7) Extension stage: 7 stages → 1 stage</li> </ul>				
	A2NCPUR21-S1	A2USHCPU-S1 A1SJ71AR21	<ul> <li>8) Applicable memory: Depending on the memory cassette → built-in RAM</li> <li>9) Microcomputer program: Available → Not available</li> </ul>				
	A3NCPU	A2USHCPU-S1	1) I/O control: Refresh/Direct switching $\rightarrow$ Refresh only 2) Processing speed (LD instruction): For refresh $1.0 \mu s \rightarrow 0.09 \mu s$ 3) PC MIX value: $0.2 \rightarrow 2.0$				
	A3NCPUP21	A2USHCPU-S1 A1SJ71AP21	<ul> <li>4) I/O points: 2048 points → 1024 points</li> <li>5) Program capacity: 30k × 2 steps → 30k steps</li> <li>6) File register points: 8k points → 8k points</li> <li>7) Extension stage: 7 stages → 1 stage</li> </ul>				
	A3NCPUR21	A2USHCPU-S1 A1SJ71AR21	<ul> <li>8) Applicable memory: Depending on the memory cassette → built-in RAM</li> <li>9) Microcomputer program: Available → Not available</li> </ul>				

	AnA series model to be continued	Small-sized AnS/Q2AS series alternative models				
Product	Model	Model	Remarks (restrictions)			
	A2ACPU	A2USHCPU-S1	1) I/O control: Refresh only 2) Processing speed (LD instruction): $0.2 \mu \text{s} \rightarrow 0.09 \mu \text{s}$ 3) PC MIX value: $0.9 \rightarrow 2.0$			
	A2ACPUP21	A2USHCPU-S1 A1SJ71AP21	4) I/O points: 512 points $\rightarrow$ 1024 points 5) Program capacity: 14k steps $\rightarrow$ 30k steps 6) File register points: 8k points $\rightarrow$ 8k points			
	A2ACPUR21	A2USHCPU-S1 A1SJ71AR21	7) Extension stage: 3 stages $\rightarrow$ 1 stage 8) Applicable memory: Depending on the memory cassette $\rightarrow$ built-in RAM			
	A2ACPU-S1	A2USHCPU-S1	1) I/O control: Refresh only 2) Processing speed (LD instruction): $0.2 \mu \text{s} \rightarrow 0.09 \mu \text{s}$ 3) PC MIX value: $0.9 \rightarrow 2.0$			
	A2ACPUP21-S1	A2USHCPU-S1 A1SJ71AP21	4) I/O points: 1024 points $\rightarrow$ 1024 points 5) Program capacity: 14k steps $\rightarrow$ 30k steps 6) File register points: 8k points $\rightarrow$ 8k points			
	A2ACPUR21-S1	A2USHCPU-S1 A1SJ71AR21	<ul> <li>7) Extension stage: 7 stages → 1 stage</li> <li>8) Applicable memory: Depending on the memory cassette → built-in RAM</li> </ul>			
	A3ACPU	A2USHCPU-S1	1) I/O control: Refresh only 2) Processing speed (LD instruction): $0.15 \mu \text{s} \rightarrow 0.09 \mu \text{s}$ 3) PC MIX value: $1.2 \rightarrow 2.0$			
	A3ACPUP21	A2USHCPU-S1 A1SJ71AP21	<ul> <li>3) FC MiX value. 1.2 → 2.0</li> <li>4) I/O points: 2048 points → 1024 points</li> <li>5) Program capacity: 30k × 2 steps → 30k steps</li> <li>6) File register points: 8k points → 8k points</li> </ul>			
	A3ACPUR21	A2USHCPU-S1 A1SJ71AR21	<ul> <li>7) Extension stage: 7stages → 1 stage</li> <li>8) Applicable memory: Depending on the memory cassette → built-in RAM</li> </ul>			
CPU module	A2UCPU	A2USHCPU-S1	<ol> <li>I/O control: Refresh only</li> <li>Processing speed (LD instruction): 0.2 µ s → 0.09 µ s</li> <li>PC MIX value: 0.9 → 2.0</li> <li>I/O points: 512 points → 1024 points</li> <li>Program capacity: 14k steps → 30k steps</li> <li>File register points: 8k points → 8k points</li> <li>Extension stage: 3 stages → 1 stage</li> <li>Applicable memory: Depending on the memory cassette → built-in RAM</li> </ol>			
	A2UCPU-S1	A2USHCPU-S1	<ol> <li>I/O control: Refresh only</li> <li>Processing speed (LD instruction): 0.2 µ s → 0.09 µ s</li> <li>PC MIX value: 0.9 → 2.0</li> <li>I/O points: 1024 points → 1024 points</li> <li>Program capacity: 14k steps → 30k steps</li> <li>File register points: 8k points → 8k points</li> <li>Extension stage: 7 stages → 1 stage</li> <li>Applicable memory: Depending on the memory cassette → built-in RAM</li> </ol>			
	A3UCPU	A2USHCPU-S1	<ol> <li>I/O control: Refresh only</li> <li>Processing speed (LD instruction): 0.15 µ s → 0.09 µ s</li> <li>PC MIX value: 1.2 → 2.0</li> <li>I/O points: 2048 points → 1024 points</li> <li>Program capacity: 30k × 2 steps → 30k steps</li> <li>File register points: 8k points → 8k points</li> <li>Extension stage: 7 stages → 1 stage</li> <li>Applicable memory: Depending on the memory cassette → built-in RAM</li> </ol>			
	A4UCPU	A2USHCPU-S1	<ol> <li>I/O control: Refresh only</li> <li>Processing speed (LD instruction): 0.15 µ s → 0.09 µ s</li> <li>PC MIX value: 1.2 → 2.0</li> <li>I/O points: 4096 points → 1024 points</li> <li>Program capacity: 30k × 4 steps → 30k steps</li> <li>File register points: 8k points → 8k points</li> <li>Extension stage: 7 stages → 1 stage</li> <li>Applicable memory: Depending on the memory cassette → built-in RAM</li> </ol>			

	nA series model to be continued		Small-sized AnS/Q2AS series alternative models
Product	Model	Model	Remarks (restrictions)
	A1NCPUP21-S3	A2USHCPU-S1 A1SJ71AP21-S3	<ol> <li>I/O control: Refresh/Direct switching → Refresh only</li> <li>Processing speed (LD instruction): For refresh 1.0 µ s → 0.09 µ s</li> <li>PC MIX value: 0.2 → 2.0</li> <li>I/O points: 256 points → 1024 points</li> <li>Program capacity: 6k steps → 30k steps</li> <li>File register points: 0 points → 8k points</li> <li>Extension stage: 1 stage → 1 stage</li> <li>Applicable memory: 4KRAM/4KROM/4KEROM → built-in RAM</li> <li>Microcomputer program: Available → Not available</li> </ol>
	A2NCPUP21-S3	A2USHCPU-S1 A1SJ71AP21-S3	<ol> <li>I/O control: Refresh/Direct switching → Refresh only</li> <li>Processing speed (LD instruction): For refresh 1.0 µ s → 0.09 µ s</li> <li>PC MIX value: 0.2 → 2.0</li> <li>I/O points: 512 points → 1024 points</li> <li>Program capacity: 14k steps → 30k steps</li> <li>File register points: 4k points → 8k points</li> <li>Extension stage: 3 stages → 1 stage</li> <li>Applicable memory: Depending on the memory cassette → built-in RAM</li> <li>Microcomputer program: Available → Not available</li> </ol>
	A2NCPUP21-S4	A2USHCPU-S1 A1SJ71AP21-S3	<ol> <li>I/O control: Refresh/Direct switching → Refresh only</li> <li>Processing speed (LD instruction): For refresh 1.0 µ s → 0.09 µ s</li> <li>PC MIX value: 0.2 → 2.0</li> <li>I/O points: 1024 points → 1024 points</li> <li>Program capacity: 14k steps → 30k steps</li> <li>File register points: 4k points → 8k points</li> <li>Extension stage: 7 stages → 1 stage</li> <li>Applicable memory: Depending on the memory cassette → built-in RAM</li> <li>Microcomputer program: Available → Not available</li> </ol>
CPU module	A3NCPUP21-S3	A2USHCPU-S1 A1SJ71AP21-S3	<ol> <li>I/O control: Refresh/Direct switching → Refresh only</li> <li>Processing speed (LD instruction): For refresh 1.0 µ s → 0.09 µ s</li> <li>PC MIX value: 0.2 → 2.0</li> <li>I/O points: 2048 points → 1024 points</li> <li>Program capacity: 30k × 2 steps → 30k steps</li> <li>File register points: 8k points → 8k points</li> <li>Extension stage: 7 stages → 1 stage</li> <li>Applicable memory: Depending on the memory cassette → built-in RAM</li> <li>Microcomputer program: Available → Not available</li> </ol>
	A2ACPUP21-S3	A2USHCPU-S1 A1SJ71AP21-S3	<ol> <li>I/O control: Refresh only</li> <li>Processing speed (LD instruction): 0.2 µ s → 0.09 µ s</li> <li>PC MIX value: 0.9 → 2.0</li> <li>I/O points: 512 points → 1024 points</li> <li>Program capacity: 14k step → 30k steps</li> <li>File register points: 8k points → 8k points</li> <li>Extension stage: 3 stages → 1 stage</li> <li>Applicable memory: Depending on the memory cassette → built-in RAM</li> </ol>
	A2ACPUP21-S4	A2USHCPU-S1 A1SJ71AP21-S3	<ol> <li>I/O control: Refresh only</li> <li>Processing speed (LD instruction): 0.2 µ s → 0.09 µ s</li> <li>PC MIX value: 0.9 → 2.0</li> <li>I/O points: 1024 points → 1024 points</li> <li>Program capacity: 14k steps → 30k steps</li> <li>File register points: 8k points → 8k points</li> <li>Extension stage: 7 stages → 1 stage</li> <li>Applicable memory: Depending on the memory cassette → built-in RAM</li> </ol>
	A3ACPUP21-S3	A2USHCPU-S1 A1SJ71AP21-S3	<ol> <li>I/O control: Refresh only</li> <li>Processing speed (LD instruction): 0.15 µ s → 0.09 µ s</li> <li>PC MIX value: 1.2 → 2.0</li> <li>I/O points: 2048 points → 1024 points</li> <li>Program capacity: 30k × 2 steps → 30k steps</li> <li>File register points: 8k points → 8k points</li> <li>Extension stage: 7 stages → 1 stage</li> <li>Applicable memory: Depending on the memory cassette → built-in RAM</li> </ol>

	anA series model to be continued	Small-sized AnS/Q2AS series alternative models				
Product	Model	Model	Remarks (restrictions)			
	Q2ACPU	Q2ASCPU-S1	<ol> <li>I/O control: Refresh only</li> <li>Processing speed (LD instruction): 0.2 µ s → 0.2 µ s</li> <li>PC MIX value: 1.3 → 1.3</li> <li>I/O points: 512 points → 1024 points</li> <li>Program capacity: 28k steps → 60k steps</li> <li>File register points: 1018k points × 2 → 1018k points</li> <li>Extension stage: 3 stages → 1 stage</li> <li>Number of memory cards: 2 cards → 1 card</li> <li>Max. memory card SRAM capacity: 2M bytes × 2 cards → 2M bytes × 1 card</li> </ol>			
	Q2ACPU-S1	Q2ASCPU-S1	<ol> <li>I/O control: Refresh only</li> <li>Processing speed (LD instruction): 0.2 µ s → 0.2 µ s</li> <li>PC MIX value: 1.3 → 1.3</li> <li>I/O points: 1024 points → 1024 points</li> <li>Program capacity: 60k steps → 60k steps</li> <li>File register points: 1018k points × 2 → 1018k points</li> <li>Extension stage: 7 stages → 1 stage</li> <li>Number of memory cards: 2 cards → 1 card</li> <li>Max. memory card SRAM capacity: 2M bytes × 2 cards → 2M bytes × 1 card</li> </ol>			
CPU module	Q3ACPU	Q2ASHCPU-S1	1) I/O control: Refresh only 2) Processing speed (LD instruction): $0.15 \mu \text{s} \rightarrow 0.075 \mu \text{s}$ 3) PC MIX value: $1.8 \rightarrow 3.8$ 4) I/O points: 2048 points $\rightarrow 1024$ points 5) Program capacity: 92k steps $\rightarrow 60k$ steps 6) File register points: 1018k points $\times 2 \rightarrow 1018k$ points 7) Extension stage: 7 stages $\rightarrow 1$ stage 8) Number of memory cards: 2 cards $\rightarrow 1$ card 9) Max. memory card SRAM capacity: 2M bytes $\times 2$ cards $\rightarrow 2M$ bytes $\times 1$ card			
	Q4ACPU	Q2ASHCPU-S1	<ol> <li>I/O control: Refresh only</li> <li>Processing speed (LD instruction): 0.075 µ s → 0.075 µ s</li> <li>PC MIX value: 3.8 → 3.8</li> <li>I/O points: 4096 points → 1024 points</li> <li>Program capacity: 124k steps → 60k steps</li> <li>File register points: 1018k points × 2 → 1018k points</li> <li>Extension stage: 7 stages → 1 stage</li> <li>Number of memory cards: 2 cards → 1 card</li> <li>Max. memory card SRAM capacity: 2M bytes × 2 cards → 2M bytes × 1 card</li> </ol>			

## 2.2 CPU Module Specifications Comparisons

						$\bigcirc$ : Usable, $\triangle$ : Partially different in spec. (eg. setting methods)			nod), ×: Not usable	
			Large-sized	A/QnA series	(	Small-sized Ar	S/Q2AS series		Reference	
Function	Contents	AnN CPU	AnA CPU	AnU CPU	QnA CPU	A2USH CPU-S1	Q2ASH CPU-S1	Precautions for replacement	section	
Control method	Repetitive operation of a stored program	0	0	0	0	0	0	_	_	
I/O control method	Refresh mode/ Direct mode	O*1	O*2	O*2	O*2	O*2	O*2	Use the direct I/O instructions to input/output in the direct mode, as the A2USHCPU-S1/ Q2AS(H)CPU-S1 supports the refresh mode only.	Section 7.3.2	
Programming language	Language dedicated to sequence control (Relay symbol, logic symbol, MELSAP language)	0	0	0	0	0	0	The MELSAP language for the A/AnS series is MELSAP-II and that for the QnA/ Q2AS series is MELSAP3.	_	
Processing speed	Sequence instructions ( µ s/step)	1.0	0.15	0.15	0.075	0.09	0.075	—	_	
Watchdog timer (WDT)	Watchdog timer (WDT) (ms)	10 to 2000	200	200	10 to 2000	200	10 to 2000	_	_	
Memory capacity	User memory capacity (bytes)	Max. 448k (Memory cassette)	Max. 768k (Memory cassette)	Max. 1024k (Memory cassette)	Max. 2036k × 2 (SRAM card)	256k (built-in RAM)	Max. 2036k (SRAM card)	A memory cassette is required for the A series as user memory, while the user memory is included in the AnS series as standard equipment.	Section 2.4.1	
_	Sequence program (steps)	Max. 30k × 2	Max. 30k × 2	Max. 30k × 4	Max. 124k	Max. 30k	Max. 60k	If the program capacity is insufficient, consider replacing by the Q series.	_	
Program capacity	Microcomputer program (bytes)	Max. 58k	×	×	×	×	×	The AnA, AnU, QnA, Q2AS series and A2USHCPU-S1 do not include microcomputer program. Therefore, consider use of sequence program, etc., as the substitution.	_	
Number of I/O points	Number of I/O points (points) *3	256 to 2048	512 to 2048	512 to 4096	512 to 4096	1024	1024	If the I/O points are insufficient, consider replacing by the Q series.	_	
	Input device (X) (points) *4	256 to 2048	512 to 2048	8192	8192	8192	8192	_	_	
	Output device (Y) (points) *4	256 to 2048	512 to 2048	8192	8192	8192	8192	—	_	
	Internal relay (M) (points)				8192		8192	—	_	
	Latch relay (L) (points)	Total 2048	Total 8192	Total 8192	8192	Total 8192	8192	—	_	
	Step relay (S) (points)				8192 <sup>*5</sup>		8192 <sup>*5</sup>	—	_	
Number of	Annunciator (F) (points)	256	2048	2048	2048	2048	2048	—	_	
device points	Edge relay (V) (points)	×	×	×	2048	×	2048	—	_	
	Link relay (B) (points)	1024	4096	8192	8192	8192	8192	_	_	
	Timer (T) (points)	256	2048	2048	2048	2048	2048	_	_	
	Counter (C) (points)	256	1024	1024	1024	1024	1024	—		
	Data register (D) (points)	1024	6144	8192	12288	8192	12288	_		
	Link register (W) (points)	1024	4096	8192	8192	8192	8192	_	_	

							O: Usable, ∆	: Partially differ	ent in spec. (eg. setting method), $\times$ : N	lot usable
			Large-sized A/QnA series				Small-sized AnS/Q2AS series			
Function	Con	Contents		AnA CPU	AnU CPU	QnA CPU	A2USH CPU-S1	Q2ASH CPU-S1	Precautions for replacement	Reference section
	File registe (points)	r (R)	8192	8192	8192	32768	8192	32768	—	_
	Accumulator (A) (points)		2	2	2	×	2	×	Accumulators are converted to the special registers (SD718, SD719) upon $A \rightarrow QnA$ program conversion as they are not included in the QnA and Q2AS series.	_
	Index	(Z) (points)	1	7	7	16	7	16	—	_
Number of device points	register	(V) (points)	1	7	7	×	7	×	This is used as edge relay for the QnA and Q2AS series.	_
	Nesting (N) (points)		8	8	8	15	8	15	—	_
	Pointer (P) (points)		256	256	256	4096	256	4096	—	_
	Special relay (M) (points)		256	256	256	2048	256	2048	_	_
	Special register (D) (points)		256	256	256	2048	256	2048	—	_
Comment points	Comment p (points) <sup>*6</sup>	points	Max. 4032	Max. 4032	Max. 4032	Max. approx. 50k	Max. 4032	Max. approx. 50k	_	_
diagnostics detection,		mory error	0	0	0	0	0	0	_	_
Operation mode at error occurrence	Stop/contin	ue setting	0	0	0	0	0	0	_	_
Output mode switching at changing from STOP to RUN	Re-output of status before selection of operation e	, re STOP/ f output after	0	0	0	0	0	0	_	_

\*1: Direct I/O is also selectable with the I/O control method setting switch.

\*2: Basically, only the refresh mode is applicable, but some instructions/devices can be input/output in the direct mode.

\*3: This number means the number of applicable points for the access to actual I/O modules. \*4: This number means the number of usable points on the program.

\*5: The step replays (S) of the QnA and Q2AS series are dedicated to the SFC.

\*6: Comment points are the points that can be written to CPU.

### 2.3 CPU Module Functional Comparisons

### 2.3.1 Functional comparisons between the AnNCPU and the A2USHCPU-S1

			O: Usable, ∆:	Partially different ir	n spec. (eg. setting method), $\times$ : N	lot usable
	Function	Contents	Large-sized A series AnNCPU	Small-sized AnS series A2USHCPU-S1	Precautions for replacement	Reference section
	Constant scan	Executes the sequence program at constant time intervals regardless of the processing time of the program.		0	_	
	Latch (power backup)	Holds the data of devices in the event of power OFF, resetting, and a momentary power failure longer than 20ms.	0	0	_	_
	Remote RUN/ STOP	Executes the remote RUN/ STOP using external switches and peripheral devices.	0	0	_	_
	PAUSE	Stops operations while holding the output status.	0	0	_	_
	Interrupt processing	Executes the program that corresponds to the cause when an interrupt cause occurs.	0	Δ	For the A3NCPU, an interrupt program is required for each main program and sub- program separately. For the A2USHCPU-S1, only one main program is available.	_
lo.	Microcomputer mode	Executes various controls and operations over utility programs and user created microcomputer programs stored in the microcomputer program area by calling them from the sequence program.	0	×	Consider use of sequence program, etc., as the substitution.	_
Control	Display priority of ERROR LED	The settings for ON/OFF of ERROR LED at the occurrence of error.	×	0	Target errors vary by model, but there is no functional difference.	_
	ROM operation	Enables operation with parameters and programs stored in ROMs in order not to lose user programs due to battery exhaustion.	0	Δ	For the A2USHCPU-S1, use the E <sup>2</sup> PROM memory cassette.	Section 7.3.5
	Data protection function (System protect, Keyword registration/ Password registration)	Prohibits peripheral devices from reading/writing programs and comments in the memory cassettes, the memory card, and built-in memory, etc. of a CPU module.	0	0	_	_
	The settings of output status at changing from STOP to RUN	The settings for the output status at changing from STOP to RUN (Y) between "re-output operation status before STOP" and "output after operation execution".	0	0	_	_
	Clock function	A CPU includes a clock, of which data can be read and written. The clock data consists of year, month, date, hour, minute, second and a day of the week.	0	0	_	_
	Write during RUN	Changes (writes to) the program of a CPU in the RUN status.	0	0	—	_
	Status latch	Stores the data of all devices in the memory cassette or built-in memory at the occurrence of an error for monitoring by the peripheral device.	O <sup>*1</sup>	0	—	_
Debug	Sampling trace	Stores the data of specified devices at the specified intervals for monitoring by the peripheral device.	O <sup>*1</sup>	0	_	_
Del	Sampling operation (skip operation/ partial operation)	Stops the execution of a sequence program at the specified step.	0	0	_	_
	Off-line switch	Skips the devices used for the OUT instruction in the operation processing of sequence program.	0	×	The A2USHCPU-S1 does not include the off-line switch function.	_
Maintenance	Online I/O module replacement	Enables I/O modules to be replaced while the CPU is in RUN.	0	×	The A2USHCPU-S1 does not include the online I/O module replacement function.	_
Main	Self-diagnostics function	Executes self-diagnostics to check for errors and stop a CPU, etc.	0	0	_	_

\*1: Not available for the A1NCPU (P21/R21).

### 2.3.2 Functional comparisons between the AnACPU, AnUCPU and the A2USHCPU-S1

	Function	Contents	Large-sized A series			nt in spec. (eg. setting method), ×: Precautions for replacement	Reference
			AnACPU	AnUCPU	A2USHCPU-S1		
	Constant scan	Executes the sequence program at constant time intervals regardless of the processing time of the program.	0	0	0	_	_
	Latch (power backup)	Holds the data of devices in the event of power OFF, resetting, and a momentary power failure longer than 20ms.	0	0	0	_	_
	Remote RUN/ STOP	Executes the remote RUN/ STOP using external switches and peripheral devices.	0	0	0	_	_
	PAUSE	Stops operations while holding the output status.	0	0	0	—	_
	Interrupt processing	Executes the program that corresponds to the cause when an interrupt cause occurs.	0	0	Δ	For the A3A/A3U/A4UCPU, an interrupt program is required for each main program and sub- program separately. For the A2USHCPU-S1, only one main program is available.	_
lo	Display priority of ERROR LED	The settings for ON/OFF of ERROR LED at the occurrence of error.	0	0	0	—	-
Control	ROM operation	Enables operation with parameters and programs stored in ROMs in order not to lose user programs due to battery exhaustion.	0	0	Δ	For the A2USHCPU-S1, use the E <sup>2</sup> PROM memory cassette.	Section 7.3.5
	Data protection function (System protect, Keyword registration/ Password registration)	Prohibits peripheral devices from reading/writing programs and comments in the memory cassettes, the memory card, and built-in memory, etc. of a CPU module.	0	0	0	_	_
	The settings of output status at changing from STOP to RUN	The settings for the output status at changing from STOP to RUN (Y) between "re-output operation status before STOP" and "output after operation execution".	0	0	0	_	_
	Clock function	A CPU includes a clock, of which data can be read and written. The clock data consists of year, month, date, hour, minute, second and a day of the week.	0	0	0	_	_
	Write during RUN	Changes (writes to) the program of a CPU in the RUN status.	0	0	0	—	-
Debug	Status latch	Stores the data of all devices in the memory cassette or built-in memory at the occurrence of an error for monitoring by the peripheral device.	0	0	0	_	_
Det	Sampling trace	Stores the data of specified devices at the specified intervals for monitoring by the peripheral device.	0	0	0	_	_
	Step operation (skip operation/ partial operation)	Stops the execution of a sequence program at the specified step.	0	0	0	_	_
e	Online I/O module replacement	Enables I/O modules to be replaced while the CPU is in RUN.	0	0	×	The A2USHCPU-S1 does not include the online I/O module replacement function.	_
Maintenance	Self-diagnostics function	Executes self-diagnostics to check for errors and stop a CPU, etc.	0	0	0		_
Mainte	Error history	Stores errors detected by the diagnostics function into the CPU. Error details can be monitored from peripheral devices.	0	0	0	_	_

 $\bigcirc$ : Usable,  $\triangle$ : Partially different in spec. (eg. setting method),  $\times$ : Not usable

### 2.3.3 Functional comparisons between the QnACPU and the Q2AS(H)CPU-S1

		C	$\bigcirc:$ Usable, $\bigtriangleup:$ Partially different in spec. (eg. setting method), $\times:$ Not usable					
	Function	Contents	Large-sized QnA series	Q2AS series	Precautions for replacement	Reference section		
	Constant scan	Executes the sequence program at constant time intervals regardless of the processing time of the program.	QnACPU O	Q2AS(H)CPU-S1	_	_		
	Latch (power backup)	Holds the data of devices in the event of power OFF, resetting, and a momentary power failure longer than 20ms.	0	0	_	_		
	Remote RUN/ STOP	Executes the remote RUN/ STOP using external switches and peripheral devices.	0	0	_	_		
	PAUSE	Stops operations while holding the output status.	0	0	_	_		
	Interrupt processing	Executes the program that corresponds to the cause when an interrupt cause occurs.	0	0	_	-		
	Display priority of ERROR LED	The settings for ON/OFF of ERROR LED at the occurrence of error.	0	0		_		
	File management	Manages all of parameters, sequence programs, device comments, file registers, etc as files.	0	Δ	Some memory configurations differ between the QnACPU and the Q2AS(H)CPU-S1. (For the Q2AS(H)CPU-S1, only one memory card is installed.)	Section 2.4.1 Section 7.3.4		
Control	Structured program	Selects a suitable execution type for program application, and divides each program by designer, process or others.	0	0	_	_		
	I/O assignment	Performs the I/O assignment to any individual module regardless of its mounted position.	0	Δ	Only 1 stage of the extension base unit can be connected though there is no restriction on the I/O assignment.	_		
	Boot run (ROM operation)	Executes the sequence program after reading it from the memory card to the CPU built-in memory when the CPU goes to the RUN status.	0	Δ	Some memory configurations differ between the QnACPU and the Q2AS(H)CPU-S1. (For the Q2AS(H)CPU-S1, only one memory card is installed.)	Section 2.4.1 Section 7.3.5		
	Data protection (System protect, Keyword registration/ Password registration)	Prohibits peripheral devices from reading/writing programs and comments in the memory cassettes, the memory card, and built-in memory, etc. of a CPU module.	0	0	_	_		
	Initial device value	Sets the initial value of device memory, file registers, and special function modules, etc. when the CPU has become the RUN status.	0	Δ	Some memory configurations differ between the QnACPU and the Q2AS(H)CPU-S1. (For the Q2AS(H)CPU-S1, only one memory card is installed.)	Section 2.4.1 Section 7.3.5		
	The settings of output status at changing from STOP to RUN	The settings for the output status at changing from STOP to RUN (Y) between "re-output operation status before STOP" and "output after operation execution".	0	0	_	_		
	Number of general data processing	Sets the number of general data processing executed in one END processing.	0	0	_	_		
	Clock function	A CPU incorporates a clock, which can be read/written. The clock data consists of year, month, day, hour, minute, second and a day of the week.	0	0	_	_		

		O:	Usable, <u>∧</u> : Pa	artially different	in spec. (eg. setting method), $\times$ : N	lot usable
	Function	Contents		Small-sized Q2AS series	Precautions for replacement	Reference section
			QnACPU	Q2AS(H)CPU-S1		section
	Write during RUN	Changes (writes to) the program of a CPU in the RUN status.	0	0	—	_
	Status latch	Stores the data of all devices in the memory card at the occurrence of an error for monitoring by the peripheral device.	0	0	_	_
	Sampling trace	Stores the data of specified devices at the specified intervals for monitoring by the peripheral device.	O <sup>*1</sup>	O <sup>*1</sup>	_	_
	Program trace	Collects the execution status of specified programs and steps, and stores them in a file.	O <sup>*1*2</sup>	O <sup>*1*2</sup>	_	_
Debug	Simulation function	Detaches I/O modules or special modules from the CPU module and test-operates the program upon the step operation.	O <sup>*2</sup>	O <sup>*2</sup>	_	_
	Step operation (skip operation partial operation)	Stops the execution of a sequence program at the specified step.	0	0	—	_
	Execution time measurement (Program list monitor, scan time measurement)	Measures the operation time for each program.	0	0	_	_
	Module access interval reading	Monitors the access interval of special function modules or peripheral devices.	0	0	_	-
JCe	Online I/O module replacement	Enables I/O modules to be replaced while the CPU is in RUN.	0	×	The Q2AS(H)CPU-S1 does not include the online I/O module replacement function.	_
Maintenance	Self-diagnostics function	Executes self-diagnostics to check for errors and stop a CPU, etc.	0	0	_	_
Mai	Error history	Stores errors, which are detected with the diagnostics function, in a CPU or memory card. The stored history can be monitored with peripheral devices.	0	0	_	_

\*1: The SRAM card is required.

\*2: GPPQ is required. This is not applicable to GX Developer.

### 2.4 Precautions for CPU Module Replacement

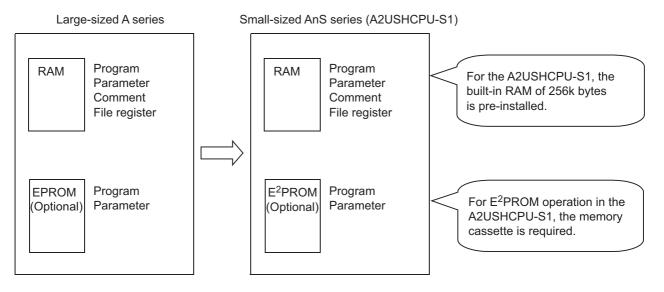
#### 2.4.1 Memory for CPU module

The memory configuration is shown in (1). Examine the following points depending on the memory capacity before replacement and applications. •Memory to store

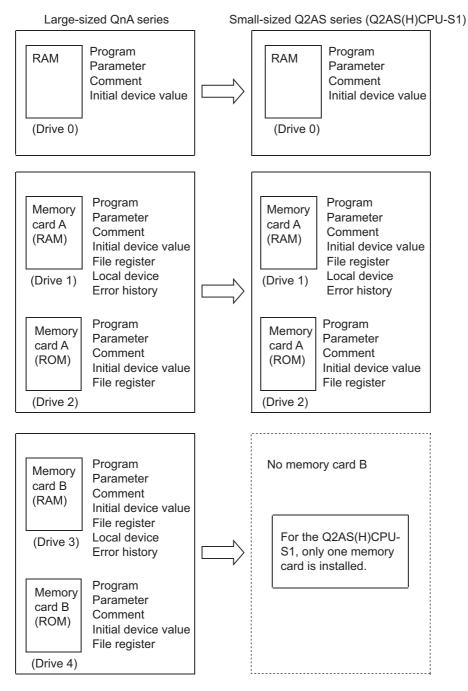
•To use or not use a memory card

#### (1) Memory configuration and data that can be stored

1) Large-sized A series and small-sized AnS series



#### 2) Large-sized QnA series and small-sized Q2AS series



#### (2) Capacity of each memory

The following table shows the memory of CPU modules, in which the user program, etc. is stored, together with its capacity.

	Item		Model					
			Large-sized A series	Small-sized AnS series A2USHCPU-S1	Large-sized QnA series	Small-sized Q2AS series Q2AS(H)CPU-S1		
		RAM	Max. 1024k bytes	—	—	—		
Memory cassette		EPROM	Max. 256k bytes	—	—	—		
		E <sup>2</sup> PROM	Max. 256k bytes	Max. 64k bytes	—	_		
Built-in RA	M		_	256k bytes	Max. 496k bytes (Program memory)	Max. 240k bytes (Program memory)		
	SRAM card		—	—	Max. 2M bytes	Max. 2M bytes		
Memory card <sup>*1</sup>	E <sup>2</sup> PF	ROM card	—	—	Max. 512k bytes	Max. 512k bytes		
ouru	Flash	h card	_	—	Max. 1M bytes <sup>*2</sup>	Max. 1M bytes <sup>*2</sup>		

\*1 For large-sized QnA series, up to two cards can be installed. For small-sized Q2AS series, only one card can be installed.

\*2 Maximum value when the Flash memory card (Q1MEM-□SF) is used. (discontinued in August, 2002.)

# 3 I/O MODULE REPLACEMENT

### 3.1 List of I/O Module Alternative Models

A series model to be discontinued			AnS series alternative model
Product	Model	Model	Remarks (restrictions)
	AX10	A1SX10	<ol> <li>External wiring change: Required Screw size: M3 → M3.5</li> <li>Change in number of modules: Not required</li> <li>Program changes Change in number of occupied I/O points: Not required</li> <li>Specification changes Change in rated input voltage: Not required Change in rated input current: Required Change in ON voltage/ON current: Required Change in OFF voltage/OFF current: Required Change in input resistance: Required</li> <li>Change in functions: Not required</li> </ol>
Input module	AX11	A1SX10	<ol> <li>1) External wiring change: Required Screw size: M3 → M3.5</li> <li>2) Change in number of modules: Required (2 modules necessary)</li> <li>3) Program changes Change in number of occupied I/O points: Not required (32=16×2)</li> <li>4) Specification changes Change in rated input voltage: Not required Change in rated input current: Required Change in rol voltage/ON current: Required Change in OFF voltage/OFF current: Required Change in input resistance: Required</li> <li>5) Change in functions: Not required</li> </ol>
	AX11EU	A1SX10EU	<ol> <li>1) External wiring change: Required</li> <li>2) Change in number of modules: Required (2 modules necessary)</li> <li>3) Program changes         <ul> <li>Change in number of occupied I/O points: Not required (32=16×2)</li> </ul> </li> <li>4) Specification changes             <ul> <li>Change in rated input voltage: Not required</li> <li>Change in rated input current: Required</li> <li>Change in ON voltage/ON current: Required</li> <li>Change in OFF voltage/OFF current: Required</li> <li>Change in input resistance: Required</li> <li>S) Change in functions: Not required</li> </ul> </li> </ol>
	AX20	A1SX20	<ol> <li>External wiring change: Required Screw size: M3 → M3.5</li> <li>Change in number of modules: Not required</li> <li>Program changes Change in number of occupied I/O points: Not required</li> <li>Specification changes Change in rated input voltage: Not required Change in rated input current: Required Change in ON voltage/ON current: Required Change in OFF voltage/OFF current: Required Change in input resistance: Required</li> <li>Change in functions: Not required</li> </ol>

A series model to	be discontinued		AnS series alternative model
Product	Model	Model	Remarks (restrictions)
	AX21	A1SX20	<ol> <li>External wiring change: Required Screw size: M3 → M3.5</li> <li>Change in number of modules: Required (2 modules necessary)</li> <li>Program changes Change in number of occupied I/O points: Not required (32=16×2)</li> <li>Specification changes Change in rated input voltage: Required Change in rated input current: Required Change in rated input current: Required Change in ON voltage/ON current: Required Change in OFF voltage/OFF current: Required Change in input resistance: Required</li> <li>Change in functions: Not required</li> </ol>
	AX21EU	A1SX20EU	<ol> <li>1) External wiring change: Required</li> <li>2) Change in number of modules: Required (2 modules necessary)</li> <li>3) Program changes         <ul> <li>Change in number of occupied I/O points: Not required (32=16×2)</li> </ul> </li> <li>4) Specification changes         <ul> <li>Change in rated input voltage: Not required</li> <li>Change in rated input current: Required</li> <li>Change in ON voltage/ON current: Required</li> <li>Change in OFF voltage/OFF current: Required</li> <li>Change in input resistance: Required</li> <li>S) Change in functions: Not required</li> </ul> </li> </ol>
Input module	AX31	A1SX30	<ol> <li>1) External wiring change: Required Screw size: M3 → M3.5</li> <li>2) Change in number of modules: Required (2 modules necessary)</li> <li>3) Program changes Change in number of occupied I/O points: Not required (32=16×2)</li> <li>4) Specification changes Change in rated input voltage: Required Change in rated input current: Required Change in rol voltage/ON current: Required Change in OFF voltage/OFF current: Required Change in input resistance: Required</li> <li>5) Change in functions: Not required</li> </ol>
	AX31-S1	A1SX41	<ol> <li>1) External wiring change: Required (Connector terminal block must be converted.)</li> <li>Screw size: M3 → M3.5</li> <li>2) Change in number of modules: Not required</li> <li>3) Program changes         <ul> <li>Change in number of occupied I/O points: Not required</li> <li>4) Specification changes</li> <li>Change in rated input voltage: Not required</li> <li>Change in rated input current: Required</li> <li>Change in ON voltage/ON current: Required</li> <li>Change in input resistance: Required</li> <li>5) Change in functions: Not required</li> </ul> </li> </ol>
	AX40	A1SX40	<ol> <li>1) External wiring change: Required Screw size: M3 → M3.5</li> <li>2) Change in number of modules: Not required</li> <li>3) Program changes Change in number of occupied I/O points: Not required</li> <li>4) Specification changes Change in rated input voltage: Not required Change in rated input voltage: Not required Change in rated input current: Required Change in ON voltage/ON current: Required Change in OFF voltage/OFF current: Required Change in input resistance: Required</li> <li>5) Change in functions: Not required</li> </ol>

	be discontinued		AnS series alternative model
Product	Model	Model	Remarks (restrictions)
	AX40-UL	A1SX40	<ol> <li>1) External wiring change: Required</li> <li>2) Change in number of modules: Not required</li> <li>3) Program changes         <ul> <li>Change in number of occupied I/O points: Not required</li> <li>Specification changes</li> <li>Change in rated input voltage: Not required</li> <li>Change in rated input voltage: Not required</li> <li>Change in rated input current: Required</li> <li>Change in ON voltage/ON current: Required</li> <li>Change in OFF voltage/OFF current: Required</li> <li>Change in input resistance: Required</li> <li>S) Change in functions: Not required</li> </ul> </li> </ol>
	AX41	A1SX41	<ol> <li>External wiring change: Required (Connector terminal block must be converted.)</li> <li>Change in number of modules: Not required</li> <li>Program changes         <ul> <li>Change in number of occupied I/O points: Not required</li> <li>Specification changes</li> <li>Change in rated input voltage: Not required</li> <li>Change in rated input voltage: Not required</li> <li>Change in rated input current: Required</li> <li>Change in ON voltage/ON current: Required</li> <li>Change in OFF voltage/OFF current: Required</li> <li>Change in input resistance: Required</li> <li>Specifications: Not required</li> </ul> </li> </ol>
Input module	AX41-S1	A1SX41-S1	<ol> <li>External wiring change: Required (Connector terminal block must be converted.)</li> <li>Change in number of modules: Not required</li> <li>Program changes         <ul> <li>Change in number of occupied I/O points: Not required</li> <li>Specification changes</li> <li>Change in rated input voltage: Required (12VDC cannot be used.)</li> <li>Change in rated input current: Required</li> <li>Change in ON voltage/ON current: Required</li> <li>Change in OFF voltage/OFF current: Required</li> <li>Change in input resistance: Required</li> <li>Specification: Not required</li> </ul> </li> </ol>
	AX41-UL	A1SX41	<ol> <li>External wiring change: Required (Connector terminal block must be converted.)</li> <li>Change in number of modules: Not required</li> <li>Program changes         <ul> <li>Change in number of occupied I/O points: Not required</li> </ul> </li> <li>Specification changes             <ul> <li>Change in rated input voltage: Not required</li> <li>Change in rated input voltage: Not required</li> <li>Change in rated input current: Required</li> <li>Change in ON voltage/ON current: Required</li> <li>Change in OFF voltage/OFF current: Required</li> <li>Change in input resistance: Required</li> <li>S) Change in functions: Not required</li> </ul> </li> </ol>
	AX42	A1SX42	<ol> <li>1) External wiring change: Not required</li> <li>2) Change in number of modules: Not required</li> <li>3) Program changes         <ul> <li>Change in number of occupied I/O points: Not required</li> <li>4) Specification changes                 Change in rated input voltage: Not required                 Change in rated input voltage: Not required                 Change in rated input current: Required                 Change in ON voltage/ON current: Required                Change in OFF voltage/OFF current: Required                 Change in input resistance: Required                 Shore in functions: Not required</li> </ul> </li> </ol>
	AX42-S1	A1SX42-S1	<ol> <li>1) External wiring change: Not required</li> <li>2) Change in number of modules: Not required</li> <li>3) Program changes         <ul> <li>Change in number of occupied I/O points: Not required</li> <li>4) Specification changes                       Change in rated input voltage: Required (12VDC cannot be used.)                       Change in rated input current: Required                       Change in ON voltage/ON current: Required                       Change in OFF voltage/OFF current: Required                       Change in input resistance: Required</li>                       S) Change in functions: Not required</ul></li></ol>

A series model to	be discontinued		AnS series alternative model
Product	Model	Model	Remarks (restrictions)
	AX50-S1	None	<ul> <li>Alternating with the A1SX40 is recommended.</li> <li>1) External wiring change: Required Connect a 3.3k Ω (1W or more) resistor to the external signal wire serially.</li> <li>2) Change in number of modules: Not required</li> <li>3) Program changes Change in number of occupied I/O points: Not required</li> <li>4) Specification changes Change in rated input voltage: Required Change in rated input voltage: Required Change in rated input current: Required Change in ON voltage/ON current: Required Change in OFF voltage/OFF current: Required Change in input resistance: Required</li> <li>5) Change in functions: Not required</li> </ul>
	AX60-S1	None	<ul> <li>Alternating with the A1SX40 is recommended.</li> <li>1) External wiring change: Required Connect a 15k Ω (3W or more) resistor to the external signal wire serially.</li> <li>2) Change in number of modules: Not required</li> <li>3) Program changes Change in number of occupied I/O points: Not required</li> <li>4) Specification changes Change in rated input voltage: Required Change in rated input voltage: Required Change in rated input current: Required Change in ON voltage/ON current: Required Change in OFF voltage/OFF current: Required</li> <li>5) Change in functions: Not required</li> </ul>
Input module	AX70	A1SX71	<ol> <li>External wiring change: Required (Connector terminal block must be converted.)</li> <li>Change in number of modules: Not required</li> <li>Program changes         <ul> <li>Change in number of occupied I/O points: Required</li> <li>Specification changes</li> <li>Change in rated input voltage: Not required</li> <li>Change in rated input voltage: Not required</li> <li>Change in rated input current: Required</li> <li>Change in ON voltage/ON current: Required</li> <li>Change in OFF voltage/OFF current: Required</li> <li>Change in input resistance: Required</li> <li>Change in functions: Not required</li> </ul> </li> </ol>
	AX71	A1SX71	<ol> <li>External wiring change: Required (Connector terminal block must be converted.)</li> <li>Change in number of modules: Not required</li> <li>Program changes         <ul> <li>Change in number of occupied I/O points: Not required</li> </ul> </li> <li>Specification changes             <ul> <li>Change in rated input voltage: Not required</li> <li>Change in rated input voltage: Not required</li> <li>Change in rated input current: Required</li> <li>Change in ON voltage/ON current: Required</li> <li>Change in oFF voltage/OFF current: Required</li> <li>Change in input resistance: Required</li> <li>So Change in functions: Not required</li> </ul> </li> </ol>
	AX80	A1SX80	<ol> <li>1) External wiring change: Required Screw size: M3 → M3.5</li> <li>2) Change in number of modules: Not required</li> <li>3) Program changes Change in number of occupied I/O points: Not required</li> <li>4) Specification changes Change in rated input voltage: Not required Change in rated input current: Required Change in ON voltage/ON current: Required Change in OFF voltage/OFF current: Required Change in input resistance: Required</li> <li>5) Change in functions: Not required</li> </ol>

A series model to	be discontinued		AnS series alternative model
Product	Model	Model	Remarks (restrictions)
	AX80E	A1SX80-S1	<ol> <li>1) External wiring change: Required Screw size: M3 → M3.5</li> <li>2) Change in number of modules: Not required</li> <li>3) Program changes Change in number of occupied I/O points: Not required</li> <li>4) Specification changes Change in rated input voltage: Required (12VDC cannot be used.) Change in rated input current: Required Change in ON voltage/ON current: Required Change in OFF voltage/OFF current: Required Change in input resistance: Required</li> <li>5) Change in functions: Not required</li> </ol>
	AX81	A1SX81	<ol> <li>External wiring change: Required (Connector terminal block must be converted.)</li> <li>Change in number of modules: Not required</li> <li>Program changes         <ul> <li>Change in number of occupied I/O points: Not required</li> </ul> </li> <li>Specification changes             <ul> <li>Change in rated input voltage: Not required</li> <li>Change in rated input voltage: Not required</li> <li>Change in rated input current: Required</li> <li>Change in ON voltage/ON current: Required</li> <li>Change in OFF voltage/OFF current: Required</li> <li>Change in input resistance: Required</li> <li>Change in functions: Not required</li> </ul> </li> </ol>
Input module	AX81B	None	<ul> <li>Alternating with the A1SX81 is recommended.</li> <li>1) External wiring change: Required (Connector terminal block must be converted.)</li> <li>2) Change in number of modules: Not required</li> <li>3) Program changes Change in number of occupied I/O points: Required</li> <li>4) Specification changes Change in rated input voltage: Not required Change in rated input voltage: Not required Change in rated input current: Required Change in ON voltage/ON current: Required Change in OFF voltage/OFF current: Required Change in input resistance: Required</li> <li>5) Functions: The wire breakage detection function is not provided.</li> </ul>
	AX81-S1	A1SX81	<ol> <li>1) External wiring change: Required (Connector terminal block must be converted.)</li> <li>2) Change in number of modules: Not required</li> <li>3) Program changes         Change in number of occupied I/O points: Not required</li> <li>4) Specification changes         Change in rated input voltage: Not required         Change in rated input current: Required         Change in ON voltage/ON current: Required         Change in OFF voltage/OFF current: Required         Change in input resistance: Required         S) Change in functions: Not required</li> </ol>
	AX81-S2	None	<ul> <li>Alternating with the A1SX81 is recommended.</li> <li>1) External wiring change: Required (Connector terminal block must be converted.)</li> <li>Connect a 3.3k Ω (1W or more) or 5.6k Ω (2W or more) resistor serially to the external signal wire at 48VDC or 60VDC, respectively.</li> <li>2) Change in number of modules: Not required</li> <li>3) Program changes <ul> <li>Change in number of occupied I/O points: Not required</li> </ul> </li> <li>4) Specification changes <ul> <li>Change in rated input voltage: Required</li> <li>Change in rated input current: Required</li> <li>Change in ON voltage/ON current: Required</li> <li>Change in OFF voltage/OFF current: Required</li> <li>Change in input resistance: Required</li> </ul> </li> <li>5) Change in functions: Not required</li> </ul>

A series model to be discontinued		AnS series alternative model	
Product	Product Model		Remarks (restrictions)
Input module	AX81-S3	A1SX80-S1	<ol> <li>1) External wiring change: Required Screw size: M3 → M3.5</li> <li>2) Change in number of modules: Required (2 modules necessary)</li> <li>3) Program changes Change in number of occupied I/O points: Required</li> <li>4) Specification changes Change in rated input voltage: Required (12VDC cannot be used.) Change in rated input current: Required Change in ON voltage/ON current: Required Change in OFF voltage/OFF current: Required Change in input resistance: Required</li> <li>5) Change in functions: Not required</li> </ol>
	AX82	A1SX82-S1	<ol> <li>External wiring change: Required (D sub → FCN connector)</li> <li>Change in number of modules: Not required</li> <li>Program changes         <ul> <li>Change in number of occupied I/O points: Not required</li> </ul> </li> <li>Specification changes         <ul> <li>Change in rated input voltage: Required (12VDC cannot be used.)</li> <li>Change in rated input current: Required</li> <li>Change in ON voltage/ON current: Required</li> <li>Change in OFF voltage/OFF current: Required</li> <li>Change in input resistance: Required</li> </ul> </li> </ol>

A series model to	be discontinued		AnS series alternative model
Product	Model	Model	Remarks (restrictions)
	AY10	A1SY10	<ol> <li>1) External wiring change: Required Screw size: M3 → M3.5</li> <li>2) Change in number of modules: Not required</li> <li>3) Program changes Change in number of occupied I/O points: Not required</li> <li>4) Specification changes Change in rated output voltage: Not required Change in rated output current: Not required (Note that a contact life is half.)</li> <li>5) Change in functions: Not required</li> </ol>
	AY10A	A1SY18A	<ol> <li>1) External wiring change: Required Screw size: M3 → M3.5</li> <li>2) Change in number of modules: Required (2 modules necessary)</li> <li>3) Program changes Change in number of occupied I/O points: Required</li> <li>4) Specification changes Change in rated output voltage: Not required Change in rated output current: Not required</li> <li>5) Change in functions: Not required</li> </ol>
	AY11	A1SY10	<ol> <li>1) External wiring change: Required Screw size: M3 → M3.5</li> <li>2) Change in number of modules: Not required</li> <li>3) Program changes Change in number of occupied I/O points: Not required</li> <li>4) Specification changes Change in rated output voltage: Not required Change in rated output current: Not required (Note that a contact life is half.)</li> <li>5) Change in functions: Required (No varistor, relay not replaceable)</li> </ol>
Output module	AY11A	A1SY18A	<ol> <li>1) External wiring change: Required Screw size: M3 → M3.5</li> <li>2) Change in number of modules: Required (2 modules necessary)</li> <li>3) Program changes Change in number of occupied I/O points: Required</li> <li>4) Specification changes Change in rated output voltage: Not required Change in rated output current: Not required</li> <li>5) Change in functions: Required (No varistor)</li> </ol>
	AY11AEU	A1SY18AEU	<ol> <li>1) External wiring change: Required</li> <li>2) Change in number of modules: Required (2 modules necessary)</li> <li>3) Program changes         <ul> <li>Change in number of occupied I/O points: Required</li> <li>Specification changes                       Change in rated output voltage: Not required                       Change in rated output current: Not required</li>                       S) Change in functions: Required (No varistor)</ul></li> </ol>
	AY11E	A1SY10	<ol> <li>1) External wiring change: Required Screw size: M3 → M3.5</li> <li>2) Change in number of modules: Not required</li> <li>3) Program changes Change in number of occupied I/O points: Not required</li> <li>4) Specification changes Change in rated output voltage: Not required Change in rated output voltage: Not required Change in rated output current: Not required (Note that a contact life is half.)</li> <li>5) Change in functions: Required (No fuse, no varistor)</li> </ol>
	AY11EEU	A1SY10EU	<ol> <li>1) External wiring change: Required Screw size: M3 → M3.5</li> <li>2) Change in number of modules: Not required</li> <li>3) Program changes Change in number of occupied I/O points: Not required</li> <li>4) Specification changes Change in rated output voltage: Not required Change in rated output voltage: Not required Change in rated output current: Not required (Note that a contact life is half.)</li> <li>5) Change in functions: Required (No fuse, no varistor)</li> </ol>

	be discontinued		AnS series alternative model
Product	Model	Model	Remarks (restrictions)
	AY11-UL	A1SY10	<ol> <li>1) External wiring change: Required</li> <li>2) Change in number of modules: Not required</li> <li>3) Program changes         Change in number of occupied I/O points: Not required</li> <li>4) Specification changes         Change in rated output voltage: Not required         Change in rated output voltage: Not required         Change in rated output current: Not required (Note that a contact life is         half.)</li> <li>5) Change in functions: Required (No varistor)</li> </ol>
	AY13	A1SY10	<ul> <li>1) External wiring change: Required Screw size: M3 → M3.5</li> <li>2) Change in number of modules: Required (2 modules necessary)</li> <li>3) Program changes Change in number of occupied I/O points: Not required (32=16×2)</li> <li>4) Specification changes Change in rated output voltage: Not required Change in rated output voltage: Not required Change in rated output current: Not required (Note that a contact life is half.)</li> <li>5) Change in functions: Not required</li> </ul>
	AY13E	A1SY10	<ol> <li>1) External wiring change: Required Screw size: M3 → M3.5</li> <li>2) Change in number of modules: Required (2 modules necessary)</li> <li>3) Program changes Change in number of occupied I/O points: Not required (32=16×2)</li> <li>4) Specification changes Change in rated output voltage: Not required Change in rated output voltage: Not required Change in rated output current: Not required (Note that a contact life is half.)</li> <li>5) Change in functions: Required (No fuse)</li> </ol>
Output module	AY13EU	A1SY10EU	<ol> <li>1) External wiring change: Required</li> <li>2) Change in number of modules: Required (2 modules necessary)</li> <li>3) Program changes         Change in number of occupied I/O points: Not required (32=16×2)</li> <li>4) Specification changes         Change in rated output voltage: Not required         Change in rated output current: Not required         Change in rated output current: Not required (Note that a contact life is         half.)</li> <li>5) Change in functions: Not required</li> </ol>
	AY15EU A	A1SY14EU	<ol> <li>External wiring change: Required</li> <li>External wiring change: Required</li> <li>Change in number of modules: Required (2 modules necessary)</li> <li>Program changes         <ul> <li>Change in number of occupied I/O points: Not required (32=16×2)</li> <li>Specification changes                 <ul> <li>Change in rated output voltage: Not required</li> <li>Change in rated output current: Not required (Note that a contact life is half.)</li> </ul> </li> </ul> </li> <li>Change in functions: Not required</li> </ol>
	AY22	A1SY22	<ol> <li>External wiring change: Required Screw size: M3 → M3.5</li> <li>Change in number of modules: Not required</li> <li>Program changes Change in number of occupied I/O points: Not required</li> <li>Specification changes Change in rated output voltage: Not required Change in rated output voltage: Not required Change in rated output current: Required (Output 2A → 0.6A)</li> <li>Change in functions: Required (No fuse, no varistor)</li> </ol>
	AY23	A1SY22	<ol> <li>1) External wiring change: Required Screw size: M3 → M3.5</li> <li>2) Change in number of modules: Required (2 modules necessary)</li> <li>3) Program changes Change in number of occupied I/O points: Not required (32=16×2)</li> <li>4) Specification changes Change in rated output voltage: Not required Change in rated output current: Not required</li> <li>5) Change in functions: Required (No fast-blow fuse)</li> </ol>

A series model to	be discontinued		AnS series alternative model		
Product	Model	Model	Remarks (restrictions)		
	AY40	A1SY40	<ol> <li>1) External wiring change: Required Screw size: M3 → M3.5</li> <li>2) Change in number of modules: Not required</li> <li>3) Program changes Change in number of occupied I/O points: Not required</li> <li>4) Specification changes Change in rated output voltage: Not required Change in rated output voltage: Not required</li> <li>5) Change in functions: Not required</li> </ol>		
	AY40A	A1SY68A	<ol> <li>1) External wiring change: Required Screw size: M3 → M3.5</li> <li>2) Change in number of modules: Required (2 modules necessary)</li> <li>3) Program changes Change in number of occupied I/O points: Required</li> <li>4) Specification changes Change in rated output voltage: Not required Change in rated output current: Not required Response: Slow</li> <li>5) Change in functions: Not required</li> </ol>		
	AY41	A1SY41	<ol> <li>External wiring change: Required (Connector terminal block must be converted.)</li> <li>Change in number of modules: Not required</li> <li>Program changes         <ul> <li>Change in number of occupied I/O points: Not required</li> </ul> </li> <li>Specification changes             <ul> <li>Change in rated output voltage: Not required</li> <li>Specification changes</li> <li>Change in rated output voltage: Not required</li> <li>Specification changes</li> <li>Change in rated output voltage: Not required</li> <li>Change in functions: Not required</li> </ul> </li> </ol>		
Output module	AY41-UL	A1SY41	<ol> <li>External wiring change: Required (Connector terminal block must be converted.)</li> <li>Change in number of modules: Not required</li> <li>Program changes         <ul> <li>Change in number of occupied I/O points: Not required</li> </ul> </li> <li>Specification changes             <ul> <li>Change in rated output voltage: Not required</li> <li>Change in rated output current: Not required</li> <li>Change in functions: Not required</li> </ul> </li> </ol>		
	AY42	A1SY42P	<ol> <li>1) External wiring change: Not required</li> <li>2) Change in number of modules: Not required</li> <li>3) Program changes         <ul> <li>Change in number of occupied I/O points: Not required</li> </ul> </li> <li>4) Specification changes             <ul> <li>Change in rated output voltage: Not required</li> <li>Change in rated output current: Not required</li> <li>Change in functions: Not required</li> </ul> </li> </ol>		
	AY42-S1	A1SY42P	<ol> <li>1) External wiring change: Not required</li> <li>2) Change in number of modules: Not required</li> <li>3) Program changes         <ul> <li>Change in number of occupied I/O points: Not required</li> </ul> </li> <li>4) Specification changes             <ul> <li>Change in rated output voltage: Not required</li> <li>Change in rated output current: Not required</li> <li>Change in rated output current: Not required</li> <li>Response time: Required (0.3ms or less → 1ms or less)</li> <li>5) Change in functions: Not required</li> </ul> </li> </ol>		
	AY42-S3	A1SY42P	<ol> <li>1) External wiring change: Not required</li> <li>2) Change in number of modules: Not required</li> <li>3) Program changes         Change in number of occupied I/O points: Not required</li> <li>4) Specification changes         Change in rated output voltage: Not required         Change in rated output voltage: Not required</li> <li>5) Change in functions: Required (The short protection function is         equivalent to that of a fuse.)</li> </ol>		

A series model to	be discontinued		AnS series alternative model
Product	Model	Model	Remarks (restrictions)
	AY42-S4	A1SY42P	<ol> <li>1) External wiring change: Required (External supply power is required.)</li> <li>2) Change in number of modules: Not required</li> <li>3) Program changes         <ul> <li>Change in number of occupied I/O points: Not required</li> <li>4) Specification changes</li></ul></li></ol>
	AY50	A1SY50	<ol> <li>1) External wiring change: Required Screw size: M3 → M3.5</li> <li>2) Change in number of modules: Not required</li> <li>3) Program changes Change in number of occupied I/O points: Not required</li> <li>4) Specification changes Change in rated output voltage: Not required Change in rated output current: Not required</li> <li>5) Change in functions: Required (Fuse not replaceable)</li> </ol>
	AY51	A1SY50	<ol> <li>1) External wiring change: Required Screw size: M3 → M3.5</li> <li>2) Change in number of modules: Required (2 modules necessary)</li> <li>3) Program changes Change in number of occupied I/O points: Not required (32=16×2)</li> <li>4) Specification changes Change in rated output voltage: Not required Change in rated output voltage: Not required</li> <li>5) Change in functions: Not required</li> </ol>
Output module	AY51-S1	A1SY50	<ol> <li>1) External wiring change: Required Screw size: M3 → M3.5</li> <li>2) Change in number of modules: Required (2 modules necessary)</li> <li>3) Program changes Change in number of occupied I/O points: Not required (32=16×2)</li> <li>4) Specification changes Change in rated output voltage: Not required Change in rated output current: Not required</li> <li>5) Change in functions: Required (Fuse not replaceable)</li> </ol>
	AY51-UL	A1SY50	<ol> <li>1) External wiring change: Required</li> <li>2) Change in number of modules: Required (2 modules necessary)</li> <li>3) Program changes         <ul> <li>Change in number of occupied I/O points: Not required (32=16×2)</li> <li>4) Specification changes</li></ul></li></ol>
	AY60	A1SY60	<ol> <li>1) External wiring change: Required Screw size: M3 → M3.5</li> <li>2) Change in number of modules: Not required</li> <li>3) Program changes Change in number of occupied I/O points: Required</li> <li>4) Specification changes Change in rated input voltage: Required (48VDC cannot be used) Change in rated output current: Not required</li> <li>5) Change in functions: Not required</li> </ol>
	AY60E	A1SY60E	<ol> <li>1) External wiring change: Required Screw size: M3 → M3.5</li> <li>2) Change in number of modules: Not required</li> <li>3) Program changes Change in number of occupied I/O points: Required</li> <li>4) Specification changes Change in rated input voltage: Required (48VDC cannot be used) Change in rated output current: Not required</li> <li>5) Change in functions: Not required</li> </ol>

A series model to	be discontinued		AnS series alternative model
Product	Model	Model	Remarks (restrictions)
	AY60S	A1SY60	<ol> <li>1) External wiring change: Required Screw size: M3 → M3.5</li> <li>2) Change in number of modules: Not required</li> <li>3) Program changes Change in number of occupied I/O points: Required</li> <li>4) Specification changes Change in rated input voltage: Required (48VDC not applicable) Change in rated output current: Not required</li> <li>5) Change in functions: Not required</li> </ol>
	AY70	A1SY71	<ol> <li>External wiring change: Required (Connector terminal block must be converted.)</li> <li>Change in number of modules: Not required</li> <li>Program changes         <ul> <li>Change in number of occupied I/O points: Required</li> <li>Specification changes</li></ul></li></ol>
	AY71	A1SY71	<ol> <li>External wiring change: Required (Connector terminal block must be converted.)</li> <li>Change in number of modules: Not required</li> <li>Program changes         <ul> <li>Change in number of occupied I/O points: Not required</li> <li>Specification changes</li></ul></li></ol>
Output module	AY72	A1SY71	<ol> <li>1) External wiring change: Not required</li> <li>2) Change in number of modules: Required (2 modules necessary)</li> <li>3) Program changes         <ul> <li>Change in number of occupied I/O points: Not required (64=32×2)</li> <li>4) Specification changes</li></ul></li></ol>
	AY80	A1SY80	<ol> <li>1) External wiring change: Required Screw size: M3 → M3.5</li> <li>2) Change in number of modules: Not required</li> <li>3) Program changes Change in number of occupied I/O points: Not required</li> <li>4) Specification changes Change in rated output voltage: Not required Change in rated output current: Not required</li> <li>5) Change in functions: Required (Fuse not replaceable)</li> </ol>
	AY81	A1SY81	<ol> <li>External wiring change: Required (Connector terminal block must be converted.)</li> <li>Change in number of modules: Not required</li> <li>Program changes         <ul> <li>Change in number of occupied I/O points: Not required</li> <li>Specification changes</li></ul></li></ol>
	AY82-EP	A1SY82	<ol> <li>1) External wiring change: Required (D sub → FCN connector)</li> <li>2) Change in number of modules: Not required</li> <li>3) Program changes         <ul> <li>Change in number of occupied I/O points: Not required</li> <li>4) Specification changes</li></ul></li></ol>

A series model to I	be discontinued		AnS series alternative model
Product	Model	Model	Remarks (restrictions)
I/O module	AH42	A1SH42	<ol> <li>1) External wiring change: Not required</li> <li>2) Change in number of modules: Not required</li> <li>3) Program changes         <ul> <li>Change in rated input voltage: Required (32 points occupied)</li> <li>4) Specification changes                 <ul></ul></li></ul></li></ol>
Dynamic scan I/O module	A42XY	A1S42X A1S42Y	<ol> <li>1) External wiring change: Required</li> <li>2) Change in number of modules: Required (Input and output modules are separately required.)</li> <li>3) Program changes         Change in rated input voltage: Required (32 points occupied)</li> <li>4) Specification changes         Change in rated input voltage: Required (12VDC cannot be used.)         Change in rated input current: Required         Change in ON voltage/ON current: Required         Change in OFF voltage/OFF current: Required         Change in input resistance: Required</li> <li>5) Change in functions: Not required</li> </ol>
	Al61	A1SI61	<ol> <li>External wiring change: Required Screw size: M3 → M3.5</li> <li>Change in number of modules: Not required</li> <li>Program changes Change in number of occupied I/O points: Not required</li> <li>Specification changes Change in rated input voltage: Not required Change in rated input voltage: Not required Change in rated input current: Required Change in ON voltage/ON current: Required Change in OFF voltage/OFF current: Required Change in input resistance: Required</li> <li>Change in functions: Required (The interrupt processing is set in 4-point units.)</li> </ol>
Interrupt module	Al61-S1	A1SI61	<ol> <li>1) External wiring change: Required Screw size: M3 → M3.5</li> <li>2) Change in number of modules: Not required</li> <li>3) Program changes Change in rated input voltage: Required (16 points occupied)</li> <li>4) Specification changes Change in rated input voltage: Not required Change in rated input voltage: Not required Change in rated input current: Required Change in ON voltage/ON current: Required Change in OFF voltage/OFF current: Required</li> <li>Change in input resistance: Required</li> <li>5) Change in functions: Required (The interrupt processing is set in 4-point units.)</li> <li>6) Others: The response time is different.</li> </ol>

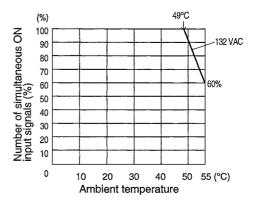
# **3.2 Specifications Comparisons between I/O Modules**

# 3.2.1 Specifications comparisons between input modules

#### (1) Specifications comparisons between the AX10 and the A1SX10

 $\bigcirc$ : Compatible,  $\triangle$ : Partial change required,  $\times$ : Incompatible Precautions for Compati-Specification AX10 A1SX10 bility replacement Number of input points 16 points 16 points 0 Isolation method Photocoupler Photocoupler Ο 100-120VAC 50/60Hz 100-120VAC 50/60Hz Rated input voltage 0 Input voltage distortion Within 5% Within 5% 0 10mA Approx. 6mA Rated input current is Rated input current Δ smaller.\*1 (100VAC, 60Hz) (100VAC, 60Hz) Max. 300mA within 0.3ms Max. 200mA within 1ms Inrush current 0 (At 132VAC) (At 132VAC) Operating voltage 85 to 132VAC 85 to 132VAC 0 (50/60Hz±5%) range  $(50/60Hz \pm 5\%)$ Maximum Use within the range 100% (16 points) simultaneously Refer to the derating chart. \*2 simultaneous input shown in the derating Δ ON points chart. ON voltage/ON current 80VAC or more/6mA or more 80VAC or more/5mA or more 0 OFF voltage/OFF OFF current is 40VAC or less/4mA or less 30VAC or less/1.4mA or less Δ current smaller.\*1 Approx. 10kΩ (60Hz) Approx. 18kΩ (60Hz) Input resistance is Input resistance Δ Approx. 12kΩ (50Hz) Approx. 21k Q (50Hz) greater.\*1 20ms or less OFF→ON 15ms or less Δ (100VAC, 60Hz) Response The response times time 35ms or less differ. ON→OFF 25ms or less Δ (100VAC, 60Hz) Common terminal 16 points/common 16 points/common 0 arrangement (common terminal: TB9,TB18) (common terminal: TB9,TB18) Operation indicator ON indication (LED) ON indication (LED) 0 20-point terminal block connector 20-point terminal block connector External connection × (M3×6 screws) (M3.5×7 screws) Wiring must be Applicable wire size 0.75 to 2mm<sup>2</sup> 0.75 to 1.25mm<sup>2</sup> × changed. Applicable solderless R1.25-3, R2-3, R1.25-3.5, R2-3.5, × terminal RAV1.25-3, RAV2-3 RAV1.25-3.5, RAV2-3.5 0.055A 0.050A Current consumption Ο (TYP. all points ON) (TYP. all points ON) The dimensions are External dimensions 250(H)×37.5(W)×121(D)mm 130(H) × 34.5(W) × 93.6(D)mm  $\triangle$ different. Weight 0.39kg 0.21kg  $\cap$ 

\*1 Check the specifications of sensor or switch to connect to the A1SX10.

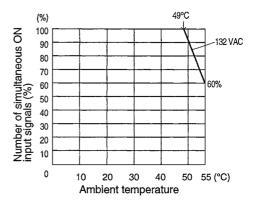


### (2) Specifications comparisons between the AX11 and the A1SX10

 $\bigcirc$ : Compatible,  $\triangle$ : Partial change required, ×: Incompatible

Specifi	cation	AX11	A1SX10	Compati- bility	Precautions for replacement
Number of input points		32 points	16 points	×	when seventeen or more points are used, use two of the A1SX10 modules.
Isolation me	thod	Photocoupler	Photocoupler	0	
Rated input	voltage	100-120VAC 50/60Hz	100-120VAC 50/60Hz	0	
Input voltage	e distortion	Within 5%	Within 5%	0	
Rated input	current	10mA (100VAC, 60Hz)	Approx. 6mA (100VAC, 60Hz)	Δ	Rated input current is smaller. <sup>*1</sup>
Inrush curre	nt	Max. 300mA within 0.3ms (At 132VAC)	Max. 200mA within 1ms (At 132VAC)	0	
Operating vo range	oltage	85 to 132VAC (50/60Hz±5%)	85 to 132VAC (50/60Hz±5%)	0	
Maximum simultaneou points	s input	60% (20 points) simultaneously ON	Refer to the derating chart. *2	0	
ON voltage/	ON current	80VAC or more/6mA or more	80VAC or more/5mA or more	0	
OFF voltage current	/OFF	40VAC or less/4mA or less	30VAC or less/1.4mA or less	Δ	OFF current is smaller.*1
Input resista	nce	Approx. 10k Ω (60Hz) Approx. 12k Ω (50Hz)	Approx. 18k Ω (60Hz) Approx. 21k Ω (50Hz)	Δ	Input resistance is greater.*1
Response	OFF→ON	15ms or less	20ms or less (100VAC, 60Hz)	Δ	The response times
time	ON→OFF	25ms or less	35ms or less (100VAC, 60Hz)	Δ	differ.
Common ter arrangemen		32 points/common (common terminal: TB9,TB18,TB27,TB36)	16 points/common (common terminal: TB9,TB18)	0	
Operation in	dicator	ON indication (LED)	ON indication (LED)	0	
External con	inection	38-point terminal block connector (M3×6 screws)	20-point terminal block connector (M3.5×7 screws)	×	
Applicable w	vire size	0.75 to 2mm <sup>2</sup>	0.75 to 1.25mm <sup>2</sup>	×	Wiring must be changed.
Applicable solderless terminal		R1.25-3, R2-3, RAV1.25-3, RAV2-3	R1.25-3.5, R2-3.5, RAV1.25-3.5, RAV2-3.5	×	
Current cons	sumption	0.11A (TYP. all points ON)	0.05A (TYP. all points ON)	0	
External dim	ensions	250(H)×37.5(W)×131(D)mm	130(H)×34.5(W)×93.6(D)mm		The dimensions are different.
Weight		0.49kg	0.21kg	0	

\*1 Check the specifications of sensor or switch to connect to the A1SX10.



## (3) Specifications comparisons between the AX11EU and the A1SX10EU

O: Compatible, ∆: Partial change require					
Specific	cation	AX11EU	A1SX10EU	Compati- bility	Precautions for replacement
Number of input points		32 points	16 points	×	when seventeen or more points are used, use two of the A1SX10EU modules.
Isolation method		Photocoupler	Photocoupler	0	
Rated input v	voltage	100-120VAC 50/60Hz	100-120VAC 50/60Hz	0	
Input voltage	distortion	Within 5%	Within 5%	0	
Rated input o	current	Approx. 12mA (120VAC, 60Hz)	Approx. 7mA (120VAC, 60Hz)	Δ	Rated input current is smaller. <sup>*1</sup>
Inrush currer	nt	Max. 300mA within 1ms (At 132VAC)	Max. 200mA within 1ms (At 132VAC)	0	
Operating vo range	ltage	85 to 132VAC (50/60Hz±5%)	85 to 132VAC (50/60Hz±5%)	0	
Maximum simultaneous points	s input	60% (20 points) simultaneously ON	100% simultaneously ON	0	
ON voltage/0	ON current	79VAC or more/6mA or more	80VAC or more/5mA or more	0	
OFF voltage/ current	/OFF	40VAC or less/4mA or less	30VAC or less/1.4mA or less	Δ	OFF current is smaller. <sup>*1</sup>
Input resistar	nce	Approx. 10kΩ (60Hz) Approx. 12kΩ (50Hz)	Approx. 18kΩ (60Hz) Approx. 21kΩ (50Hz)	Δ	Input resistance is greater.*1
Response	OFF→ON	15ms or less (100VAC, 60Hz)	20ms or less (100VAC, 60Hz)	Δ	The response times
time	ON→OFF	25ms or less (100VAC, 60Hz)	35ms or less (100VAC, 60Hz)	Δ	differ.
Common terr arrangement		32 points/common (common terminal: TB9,TB18,TB27,TB36)	16 points/common (common terminal: TB9,TB18)	0	
Operation inc	dicator	ON indication (LED)	ON indication (LED)	0	
External con	nection	38-point terminal block connector (M3.5×6 screws)	20-point terminal block connector (M3.5×7 screws)	×	
Applicable w	ire size	0.75 to 2mm <sup>2</sup> (AWG14 to AWG19)	0.75 to 1.25mm <sup>2</sup> (AWG16 to AWG19)	×	Wiring must be changed.
Applicable so terminal	olderless	RAV1.25-3.5,RAV2-3.5	RAV1.25-3.5	×	
Current cons	umption	0.15A (TYP. all points ON)	0.05A (TYP. all points ON)	0	
Dielectric withstand voltage (Across external circuit and internal circuit)		1780VAC rms/3cycles (altitude 2000m)	1780VAC rms/3cycles (altitude 2000m)	0	
Insulation res	sistance	10MΩ or more by insulation resistance tester	10MΩ or more by insulation resistance tester	0	
External dim	ensions	250(H)×37.5(W)×131(D)mm	130(H)×34.5(W)×93.6(D)mm	Δ	The dimensions are different.
Weight		0.50kg	0.21kg	0	

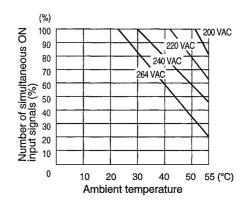
\*1 Check the specifications of sensor or switch to connect to the A1SX10EU.

## (4) Specifications comparisons between the AX20 and the A1SX20

O: Compatible ∧ .	Partial change required	x · Incompatible
$O$ . computible, $\Delta$ .	i unua onunge requirea	, A. moomputible

Specifi	cation	AX20	A1SX20	Compati- bility	Precautions for replacement
Number of input points		16 points	16 points	0	
Isolation me	thod	Photocoupler	Photocoupler	0	
Rated input	voltage	200-240VAC 50/60Hz	200-240VAC 50/60Hz	0	
Input voltage	e distortion	Within 5%	Within 5%	0	
Rated input	current	10mA (200VAC, 60Hz)	Approx. 9mA (200VAC, 60Hz)	Δ	Rated input current is smaller.*1
Inrush curre	nt	Max. 600mA within 0.12ms (At 264VAC)	Max. 500mA within 1ms (At 264VAC)	0	
Operating vo range	oltage	170 to 264VAC (50/60Hz±5%)	170 to 264VAC (50/60Hz±5%)	0	
Maximum simultaneou points	s input	100% (16 points) simultaneously ON	Refer to the derating chart. *2	Δ	Use within the range shown in the derating chart.
ON voltage/	ON current	160VAC or more/5.5mA or more	80VAC or more/4mA or more	0	
OFF voltage current	e/OFF	70VAC or less/3.5mA or less	30VAC or less/1mA or less	Δ	OFF current is smaller. <sup>*1</sup>
Input resista	ince	Approx. 22kΩ (60Hz) Approx. 24kΩ (50Hz)	Approx. 22kΩ (60Hz) Approx. 27kΩ (50Hz)	0	
Response	OFF→ON	15ms or less	30ms or less (200VAC, 60Hz)	Δ	The response times differ.
time	ON→OFF	25ms or less	55ms or less (200VAC, 60Hz)	Δ	
Common ter arrangemen		16 points/common (common terminal: TB9,TB18)	16 points/common (common terminal: TB9,TB18)	0	
Operation in	dicator	ON indication (LED)	ON indication (LED)	0	
External cor	nnection	20-point terminal block connector (M3×6 screws)	20-point terminal block connector (M3.5×7 screws)	×	
Applicable wire size		0.75 to 2mm <sup>2</sup>	0.75 to 1.25mm <sup>2</sup>	×	Wiring must be changed.
Applicable solderless terminal		R1.25-3, R2-3, RAV1.25-3, RAV2-3	R1.25-3.5, R2-3.5, RAV1.25-3.5, RAV2-3.5	×	
Current con	sumption	0.055A (TYP. all points ON)	0.050A (TYP. all points ON)	0	
External dim	nensions	250(H)×37.5(W)×121(D)mm	130(H)×34.5(W)×93.6(D)mm	Δ	The dimensions are different.
Weight		0.38kg	0.23kg	0	

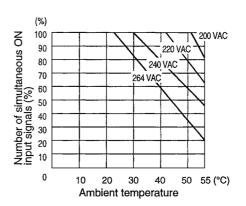
\*1 Check the specifications of sensor or switch to connect to the A1SX20.



#### (5) Specifications comparisons between the AX21 and the A1SX20

			O: Compatible, ∆: Par	tial change	e required, ×: Incompatible
Specifi	cation	AX21	A1SX20	Compati- bility	Precautions for replacement
Number of input points		32 points	16 points	×	When seventeen or more points are used, use two of the A1SX20 modules.
Isolation me	thod	Photocoupler	Photocoupler	0	
Rated input	voltage	200-240VAC 50/60Hz	200-240VAC 50/60Hz	0	
Input voltage	distortion	Within 5%	Within 5%	0	
Rated input	current	10mA (220VAC, 60Hz)	Approx. 9mA (200VAC, 60Hz)	Δ	Rated input current is smaller.*1
Inrush curre	nt	Max. 600mA within 0.12ms (At 264VAC)	Max. 500mA within 1ms (At 264VAC)	0	
Operating vo range	oltage	170 to 264VAC (50/60Hz±5%)	170 to 264VAC (50/60Hz±5%)	0	
Maximum simultaneous points	s input	60% (20 points) simultaneously ON	Refer to the derating chart. <sup>*2</sup>		Use within the range shown in the derating chart.
ON voltage/0 current	NC	160VAC or more/5.5mA or more	80VAC or more/4mA or more	0	
OFF voltage current	/OFF	70VAC or less/3.5mA or less	30VAC or less/1mA or less	Δ	OFF current is smaller.*1
Input resista	nce	Approx. 22k Ω (60Hz) Approx. 24k Ω (50Hz)	Approx. 22kΩ (60Hz) Approx. 27kΩ (50Hz)	0	
Response	OFF→ON	15ms or less	30ms or less (200VAC, 60Hz)	Δ	The response times
time	ON→OFF	25ms or less	55ms or less (200VAC, 60Hz)	Δ	differ.
Common ter arrangement		32 points/common (common terminal: TB9,TB18,TB27,TB36)	16 points/common (common terminal: TB9,TB18)	0	
Operation in	dicator	ON indication (LED)	ON indication (LED)	0	
External con	nection	38-point terminal block connector (M3×6 screws)	20-point terminal block connector (M3.5×7 screws)	×	
Applicable wire size		0.75 to 2mm <sup>2</sup>	0.75 to 1.25mm <sup>2</sup>	×	Wiring must be changed.
Applicable solderless terminal		R1.25-3, R2-3, RAV1.25-3, RAV2-3	R1.25-3.5, R2-3.5, RAV1.25-3.5, RAV2-3.5	×	
Current cons	sumption	0.11A (TYP. all points ON)	0.05A (TYP. all points ON)	0	
External dim	ensions	250(H)×37.5(W)×131(D)mm	130(H)×34.5(W)×93.6(D)mm	Δ	The dimensions are different.
Weight		0.50kg	0.23kg	0	

\*1 Check the specifications of sensor or switch to connect to the A1SX20.
\*2 The figure on the right shows derating.

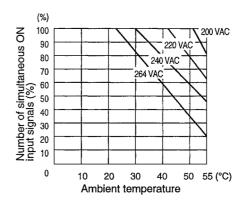


## (6) Specifications comparisons between the AX21EU and the A1SX20EU

 $\bigcirc$ : Compatible,  $\triangle$ : Partial change required, ×: Incompatible

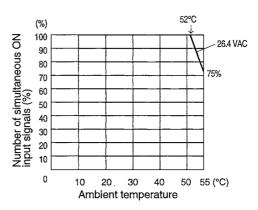
Specifi	cation	AX21EU	A1SX20EU	Compati-	
opeen	oution			bility	replacement
Number of input points		32 points	16 points	×	When seventeen or more points are used, use two of the A1SX20EU modules.
Isolation me	ethod	Photocoupler	Photocoupler	0	
Rated input	voltage	200-240VAC 50/60Hz	200-240VAC 50/60Hz	0	
Input voltag	e distortion	Within 5%	Within 5%	0	
Rated input	current	Approx. 12mA (240VAC, 60Hz)	Approx. 11mA (240VAC, 60Hz)	Δ	Rated input current is smaller.*1
Inrush curre	ent	Max. 600mA within 0.5ms (At 264VAC)	Max. 500mA within 1ms (At 264VAC)	0	
Operating v range	roltage	170 to 264VAC (50/60Hz±5%)	170 to 264VAC (50/60Hz±5%)	0	
Maximum simultaneou points	us input	60% (20 points) simultaneously ON	Refer to the derating chart. $^{*2}$	0	
ON voltage	/ON	160VAC or more/5.5mA or more	80VAC or more/4mA or more	0	
OFF voltage/OFF current		70VAC or less/3.5mA or less	30VAC or less/1mA or less	Δ	OFF current is smaller.*1
Input resistance		Approx. 22kΩ (60Hz) Approx. 24kΩ (50Hz)	Approx. 22kΩ (60Hz) Approx. 27kΩ (50Hz)	0	
Response	OFF→ON	15ms or less (200VAC, 60Hz)	30ms or less (200VAC, 60Hz)	Δ	The response times differ.
time	ON→OFF	25ms or less (200VAC, 60Hz)	55ms or less (200VAC, 60Hz)	Δ	The response times time.
Common te arrangemer		32 points/common (common terminal: TB9,TB18,TB27,TB36)	16 points/common (common terminal: TB9,TB18)	0	
Operation in	ndicator	ON indication (LED)	ON indication (LED)	0	
External co	nnection	38-point terminal block connector (M3.5×6 screws)	20-point terminal block connector (M3.5×7 screws)	×	
Applicable wire size		0.75 to 2mm <sup>2</sup> (AWG14 to AWG19)	0.75 to 1.25mm <sup>2</sup> (AWG16 to AWG19)	×	Wiring must be changed.
Applicable solderless terminal		RAV1.25-3.5,RAV2-3.5	RAV1.25-3.5	×	
Current cor	sumption	0.15A (TYP. all points ON)	0.05A (TYP. all points ON)	0	
External dir	nensions	250(H)×37.5(W)×131(D)mm	130(H)×34.5(W)×93.6(D)mm		The dimensions are different.
Weight		0.50kg	0.23kg	0	

\*1 Check the specifications of sensor or switch to connect to the A1SX20EU.



## (7) Specifications comparisons between the AX31 and the A1SX30

				Compati-	Precautions for
Specification		AX31	A1SX30	bility	replacement
Number of i	nput points	32 points	16 points	×	When seventeen or more points are used, use two of the A1SX30 modules.
Isolation me	ethod	Photocoupler	Photocoupler	0	
Rated input	voltage	12/24VDC 12/24VAC (50/60Hz)	12/24VDC 12/24VAC (50/60Hz)	0	
Rated input	current	8.5mA (24VDC/AC) 4.0mA (12VDC/AC)	8.5mA (24VDC/AC) 4.0mA (12VDC/AC)	0	
Operating v range	oltage	10.2 to 26.4VDC (ripple ratio within 5%), 10.2 to 26.4VAC (50/60Hz±5%)	DC10.2 to 26.4V (ripple ratio within 5%), 10.2 to 26.4VAC (50/60Hz±5%)	0	
Maximum simultaneous input points		100% simultaneously ON	Refer to the derating chart. <sup>*1</sup>		Use within the range shown in the derating chart.
ON voltage/ON current		7VDC/AC or more/2mA or more	7VDC/AC or more/2mA or more	0	
OFF voltage/OFF current		2.5VDC/AC or less/0.7mA or less	2.7VDC/AC or less/0.7mA or less	0	
Input resistance		Approx. 2.7kΩ	Approx. 2.7k Ω	0	
OF	OFF→ON	20ms or less (12/24VDC), 25ms or less (12/24VAC, 60Hz)	20ms or less (12/24VDC), 25ms or less (12/24VAC, 60Hz)	0	
time	ON→OFF	20ms or less (12/24VDC), 20ms or less (12/24VAC, 60Hz)	20ms or less (12/24VDC), 20ms or less (12/24VAC, 60Hz)	0	
Common te arrangemer		32 points/common (common terminal: TB9,TB18,TB27,TB36)	16 points/common (common terminal: TB9,TB18)	0	
Operation ir	ndicator	ON indication (LED)	ON indication (LED)	0	
External connection		38-point terminal block connector (M3×6 screws)	20-point terminal block connector (M3.5×7 screws)	×	
Applicable v	vire size	0.75 to 2mm <sup>2</sup>	0.75 to 1.25mm <sup>2</sup>	×	Wiring must be changed
Applicable solderless terminal		R1.25-3,R2-3, RAV1.25-3,RAV2-3	R1.25-3.5,R2-3.5, RAV1.25-3.5,RAV2-3.5	×	
Current con	sumption	0.11A (TYP. all points ON)	0.05A (TYP. all points ON)	0	
External din	nensions	250(H)×37.5(W)×131(D)mm	130(H)×34.5(W)×93.6(D)mm	Δ	The dimensions are different.
Weight		0.49kg	0.20kg	0	



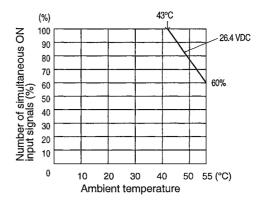
#### (8) Specifications comparisons between the AX31-S1 and the A1SX41

O: Compatible, ∠	· Part	al change	required	× · In	compatible
O. Compatible, Z	<u>.</u>	a onungo	required,	··· · ·	loomputible

Specification		AX31-S1	A1SX41	Compati- bility	Precautions for replacement
Number of input points		32 points	32 points	0	
Isolation me	ethod	Photocoupler	Photocoupler	0	
Rated input	voltage	24VDC	12/24VDC	0	
Rated input	current	8.5mA	Approx. 3mA/Approx. 7mA	Δ	Rated input current is smaller.*1
Operating v range	oltage	19.2 to 26.4VDC (ripple ratio within 5%)	10.2 to 26.4VDC (ripple ratio within 5%)	0	
Maximum simultaneou points	us input	100% simultaneously ON	Refer to the derating chart. *2	Δ	Use within the range shown in the derating chart.
ON voltage/ current	/ON	16VDC or more/5mA or more	8VDC or more/2mA or more	0	
OFF voltage/OFF current		8VDC or less/2mA or less	4VDC or less/1 mA or less		OFF current is smaller. <sup>*1</sup>
Input resistance		Approx. 2.7kΩ	Approx. 3.3kΩ		Input resistance is greater.*1
Response OFF→ON		10ms or less	10ms or less (24VDC)	0	
time	ON→OFF	10ms or less	10ms or less (24VDC)	0	
Common terminal arrangement		32 points/common (common terminal: TB9,TB18,TB27,TB36)	32 points/common (common terminal: B1, B2)	0	
Operation in	ndicator	ON indication (LED)	ON indication (LED)	0	
External connection		38-point terminal block connector (M3×6 screws)	40-pin connector (included)		
Applicable wire size		0.75 to 2mm <sup>2</sup>	0.3mm <sup>2</sup>	×	Wiring must be changed. <sup>*3</sup>
Applicable solderless terminal		R1.25-3,R2-3, RAV1.25-3,RAV2-3		×	- 0
Current con	sumption	0.11A (TYP. all points ON)	0.08A (TYP. all points ON)	0	
External dim	nensions	250(H)×37.5(W)×131(D)mm	130(H)×34.5(W)×93.6(D)mm	Δ	
Weight		0.49kg	0.21kg	0	

\*1 Check the specifications of sensor or switch to connect to the A1SX41.

\*2 The figure on the right shows derating.



## (9) Specifications comparisons between the AX40 and the A1SX40

Specification		AX40	A1SX40	Compati- bility	Precautions for replacement
Number of input points		16 points	16 points	0	
Isolation me	thod	Photocoupler	Photocoupler	0	
Rated input	voltage	12/24VDC	12/24VDC	0	
Rated input	current	Approx. 4mA/Approx. 10mA	Approx. 3mA/Approx. 7mA	Δ	Rated input current is smaller.*1
Operating vo	oltage range	10.2 to 26.4VDC (ripple ratio within 5%)	10.2 to 26.4VDC (ripple ratio within 5%)	0	
Maximum si input points	multaneous	100% (8 points/common) simultaneously ON	100% simultaneously ON	0	
ON voltage/	ON current	9.5VDC or more/3mA or more	8VDC or more/2mA or more	0	
OFF voltage/OFF current		6VDC or less/1.5mA or less	4VDC or less/1mA or less	Δ	OFF current is smaller. <sup>*1</sup>
Input resistance		Approx. 2.4k $\Omega$	Approx. 3.3kΩ		Input resistance is greater.*1
Response	OFF→ON	10ms or less	10ms or less (24VDC)	0	
time	ON→OFF	10ms or less	10ms or less (24VDC) O		
Common terminal arrangement		8 points/common (common terminal: TB9,TB18)	16 points/common (common terminal: TB9,TB18)		As 2 commons are reduced to 1,wiring a different voltage for each common is not possible.
Operation in	dicator	ON indication (LED)	ON indication (LED)	0	
External connection		20-point terminal block connector (M3×6 screws)	20-point terminal block connector (M3.5×7 screws)	×	
Applicable wire size		0.75 to 2mm <sup>2</sup>	0.75 to 1.25mm <sup>2</sup>	×	Wiring must be
Applicable solderless terminal		R1.25-3,R2-3, RAV1.25-3,RAV2-3	R1.25-3.5,R2-3.5, RAV1.25-3.5,RAV2-3.5	×	changed.
Current con	sumption	0.055A (TYP. all points ON)	0.050A (TYP. all points ON)	0	
External dim	nensions	250(H)×37.5(W)×121(D)mm	130(H)×34.5(W)×93.6(D)mm	Δ	The dimensions are different.
Weight		0.36kg	0.20kg	0	

\*1 Check the specifications of sensor or switch to connect to the A1SX40.

# (10) Specifications comparisons between the AX40-UL and the A1SX40

O: Compatible, ∆	Partial change required,	×: Incompatible

Specification		AX40-UL	AX40-UL A1SX40		Precautions for replacement
Number of input points		16 points	16 points	0	
Isolation met	thod	Photocoupler	Photocoupler	0	
Rated input	voltage	12/24VDC	12/24VDC	0	
Rated input	current	Approx. 4mA/Approx. 10mA	Approx. 3mA/Approx. 7mA	Δ	Rated input current is smaller.*1
Operating vo range	oltage	10.2 to 26.4VDC (ripple ratio within 5%)	10.2 to 26.4VDC (ripple ratio within 5%)	0	
Maximum simultaneous points	s input	100% (8 points/common) simultaneously ON	100% simultaneously ON	0	
ON voltage/0	ON current	9.5VDC or more/3mA or more	8VDC or more/2mA or more	0	
OFF voltage	/OFF	6VDC or less/1.5mA or less	4VDC or less/1mA or less	Δ	OFF current is smaller. <sup>*1</sup>
Input resistance		Approx. 2.4kΩ	Approx. 3.3kΩ	Δ	Input resistance is greater.*1
Response		10ms or less	10ms or less (24VDC)	0	
time	ON→OFF	10ms or less	10ms or less (24VDC)	0	
Common terminal arrangement		8 points/common (common terminal: TB9,TB18)	16 points/common (common terminal: TB9,TB18)		As 2 commons are reduced to 1,wiring a different voltage for each common is not possible.
Operation in	dicator	ON indication (LED)	ON indication (LED)	0	
External con	nection	20-point terminal block connector (M3.5×7 screws)	20-point terminal block connector (M3.5×7 screws)	×	
Applicable w	vire size	0.75 to 2mm <sup>2</sup>	0.75 to 1.25mm <sup>2</sup>	×	Wiring must be changed.
Applicable solderless terminal		R1.25-3.5,R2-3.5, RAV1.25-3.5,RAV2-3.5	R1.25-3.5,R2-3.5, RAV1.25-3.5,RAV2-3.5	×	0
Current cons	sumption	0.055A (TYP. all points ON)	0.050A (TYP. all points ON)	0	
External dim	ensions	250(H)×37.5(W)×121(D)mm	130(H)×34.5(W)×93.6(D)mm	Δ	The dimensions are different.
Weight		0.36kg	0.20kg	0	

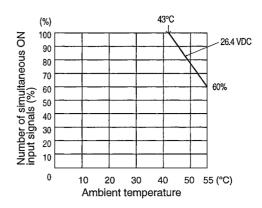
\*1 Check the specifications of sensor or switch to connect to the A1SX40.

#### (11) Specifications comparisons between the AX41 and the A1SX41

			O: Compatible, ∆ ∶Parti	al change re	equired, ×: Incompatibl
Specification		AX41	A1SX41	Compati- bility	Precautions for replacement
Number of i	nput points	32 points	32 points	0	
Isolation me	ethod	Photocoupler	Photocoupler	0	
Rated input	voltage	12/24VDC	12/24VDC	0	
Rated input	current	Approx. 4mA/Approx. 10mA	Approx. 3mA/Approx. 7mA	Δ	Rated input current is smaller.*1
Operating v range	oltage	10.2 to 26.4VDC (ripple ratio within 5%)	10.2 to 26.4VDC (ripple ratio within 5%)	0	
Maximum si input points		60% (5 points/common) simultaneously ON	Refer to the derating chart. *2	0	
ON voltage/	ON current	9.5VDC or more/3mA or more	8VDC or more/2mA or more	0	
OFF voltage current	e/OFF	6VDC or less/1.5mA or less	4VDC or less/1mA or less	Δ	OFF current is smaller. <sup>*1</sup>
Input resistance		Approx. 2.4kΩ	Approx. 3.3kΩ	Δ	Input resistance is greater. <sup>*1</sup>
Response OFF→O	OFF→ON	10ms or less	10ms or less (24VDC)	0	
time	ON→OFF	10ms or less	10ms or less (24VDC)	0	
Common terminal arrangement		8 points/common (common terminal: TB9,TB18,TB27,TB36)	32 points/common (common terminal: B1, B2)	Δ	As 4 commons are reduced to 1,wiring a different voltage for each common is not possible.
Operation ir	ndicator	ON indication (LED)	ON indication (LED)	0	
External connection		38-point terminal block connector (M3×6 screws)	40-pin connector (included)	×	
Applicable wire size		0.75 to 2mm <sup>2</sup>	0.3mm <sup>2</sup>	×	Wiring must be changed. <sup>*3</sup>
Applicable solderless terminal		R1.25-3,R2-3, RAV1.25-3,RAV2-3	_	×	
Current con	sumption	0.11A (TYP. all points ON)	0.08A (TYP. all points ON)	0	
External din	nensions	250(H)×37.5(W)×131(D)mm	130(H)×34.5(W)×93.6(D)mm	Δ	
Weight		0.44kg	0.21kg	0	

\*1 Check the specifications of sensor or switch to connect to the A1SX41.

\*2 The figure on the right shows derating.



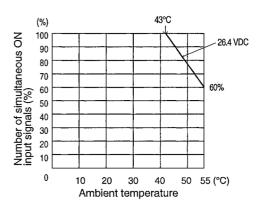
#### (12) Specifications comparisons between the AX41-S1 and the A1SX41-S1

O: Compatible. ∧	Partial change required,	x:	Incompatible
$O$ . Compatible, $\Delta$	. I uruu onunge requireu,	· · ·	moomputble

Specification		AX41-S1	A1SX41-S1	Compati- bility	Precautions for replacement
Number of input points		32 points	32 points	0	
Isolation me	thod	Photocoupler	Photocoupler	0	
Rated input	voltage	12/24VDC	24VDC	Δ	12VDC cannot be used.
Rated input	current	Approx. 4mA/Approx. 10mA	Approx. 7mA	Δ	Rated input current is smaller. <sup>*1</sup>
Operating vo range	oltage	10.2 to 26.4VDC (ripple ratio within 5%)	19.2 to 26.4VDC (ripple ratio within 5%)	Δ	12VDC cannot be used.
Maximum simultaneou points	s input	60% (5 points/common) simultaneously ON	Refer to the derating chart. *2	0	
ON voltage/	ON current	9.5VDC or more/3mA or more	17VDC or more/4.5mA or more	Δ	12VDC cannot be used.
OFF voltage/OFF current		6VDC or less/1.5mA or less	3.5VDC or less/0.8mA or less	Δ	12VDC cannot be used.
Input resistance		Approx. 2.4k $\Omega$	Approx. 3.3k $\Omega$	Δ	Input resistance is greater. <sup>*1</sup>
Response	OFF→ON	0.1ms or less	0.3ms or less (24VDC)	Δ	The response times
time	ON→OFF	0.2ms or less	0.3ms or less (24VDC)	Δ	differ.
Common terminal arrangement		8 points/common (common terminal: TB9,TB18,TB27,TB36)	32points/common (common terminal: B1, B2)	Δ	As 4 commons are reduced to 1,wiring a different voltage for each common is not possible.
Operation in	dicator	ON indication (LED)	ON indication (LED)	0	
External connection		38-point terminal block connector (M3×6 screws)	40-pin connector (included)	×	
Applicable wire size		0.75 to 2mm <sup>2</sup>	0.3mm <sup>2</sup>	×	Wiring must be changed. <sup>*3</sup>
Applicable solderless terminal		R1.25-3,R2-3, RAV1.25-3,RAV2-3	_	×	
Current cons	sumption	0.11A (TYP. all points ON)	0.12A (TYP. all points ON)	Δ	Current capacity must be reviewed.
External dim	ensions	250(H)×37.5(W)×131(D)mm	130(H)×34.5(W)×93.6(D)mm	Δ	
Weight		0.44kg	0.21kg	0	

\*1 Check the specifications of sensor or switch to connect to the A1SX41-S1.

\*2 The figure on the right shows derating.



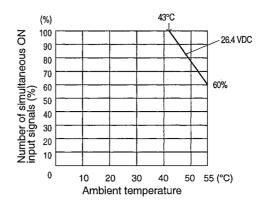
#### (13) Specifications comparisons between the AX41-UL and the A1SX41

			$\bigcirc$ : Compatible, $\triangle$ : Partial change required, $\times$ : Incompatible				
Specification		AX41-UL	A1SX41	Compati- bility	Precautions for replacement		
Number of in	nput points	32 points	32 points	0			
Isolation met	thod	Photocoupler	Photocoupler	0			
Rated input	voltage	12/24VDC	12/24VDC	0			
Rated input of	current	Approx. 4mA/Approx. 10mA	Approx. 3mA/Approx. 7mA	Δ	Rated input current is smaller.*1		
Operating vo range	oltage	10.2 to 26.4VDC (ripple ratio within 5%)	10.2 to 26.4VDC (ripple ratio within 5%)	0			
Maximum simultaneous points	s input	60% (5 points/common) simultaneously ON	Refer to the derating chart. *2	0			
ON voltage/0	ON current	9.5VDC or more/3mA or more	8VDC or more/2mA or more	0			
OFF voltage	/OFF	6VDC or less/1.5mA or less	4VDC or less/1mA or less	Δ	OFF current is smaller.*1		
Input resistance		Approx. 2.4k Ω	Approx. 3.3k $\Omega$	Δ	Input resistance is greater.*1		
Response	OFF→ON	10ms or less	10ms or less (24VDC)				
time	ON→OFF	10ms or less	10ms or less (24VDC)	0			
Common terminal arrangement		8 points/common (common terminal: TB9,TB18,TB27,TB36)	32 points/common (common terminal: B1, B2)	Δ	As 4 commons are reduced to 1,wiring a different voltage for each common is not possible.		
Operation in	dicator	ON indication (LED)	ON indication (LED)	0			
External connection		38-point terminal block connector (M3.5×6 screws)	40-pin connector (included)	×			
Applicable wire size		0.75 to 2mm <sup>2</sup>	0.3mm <sup>2</sup>	×	Wiring must be changed. <sup>*3</sup>		
Applicable solderless terminal		R1.25-3.5,R2-3.5, RAV1.25-3.5,RAV2-3.5	_	×			
Current cons	sumption	0.11A (TYP. all points ON)	0.08A (TYP. all points ON)	0			
External dim	ensions	250(H)×37.5(W)×131(D)mm	130(H)×34.5(W)×93.6(D)mm	Δ			
Weight		0.44kg	0.21kg	0			

 $\bigcirc$ : Compatible,  $\triangle$ : Partial change required, ×: Incompatible

\*1 Check the specifications of sensor or switch to connect to the A1SX41.

\*2 The figure on the right shows derating.

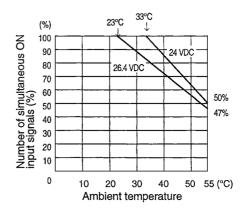


## (14) Specifications comparisons between the AX42 and the A1SX42

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			_ ·	Compati-	Precautions for
Specif	ication	AX42	A1SX42	bility	replacement
Number of input points		64 points	64 points	0	
Isolation me	ethod	Photocoupler	Photocoupler	0	
Rated input	voltage	12/24VDC	12/24VDC	0	
Rated input	current	Approx. 3mA/Approx. 7mA	Approx. 2mA/Approx. 5mA	Δ	Rated input current is smaller. *1
Operating vo range	oltage	10.2 to 26.4VDC (ripple ratio within 5%)	10.2 to 26.4VDC (ripple ratio within 5%)	0	
Maximum simultaneous input points		60% (20 points/common) simultaneously ON	Refer to the derating chart. *2	Δ	Use within the range shown in the derating chart.
ON voltage/	ON current	9.5VDC or more/3mA or more	8VDC or more/2mA or more	0	
OFF voltage current	e/OFF	6VDC or less/1.5mA or less	4VDC or less/0.6mA or less	Δ	OFF current is smaller. <sup>*1</sup>
Input resistance		Approx. 3.4k Ω	Approx. 5kΩ	Δ	Input resistance is greater.*1
Response	OFF→ON	10ms or less	10ms or less (24VDC)	0	
time	ON→OFF	10ms or less	10ms or less (24VDC)	0	
Common ter arrangemen		32 points/common (common terminal: 1B1,1B2,2B1,2B2)	32 points/common (common terminal: 1B1,1B2,2B1,2B2)	0	
Operation in	ndicator	ON indication (LED) 32 point switch-over using a switch	ON indication (LED) 32 point switch-over using a switch	0	
External connection		40-pin connector (with solder) × 2	40-pin connector (with solder) $\times 2$	0	
Applicable wire size		0.3mm <sup>2</sup>	0.3mm <sup>2</sup>	0	
Current consumption		0.12A (TYP. all points ON)	0.09A (TYP. all points ON)	0	
External dim	nensions	250(H)×37.5(W)×106(D)mm	130(H)×34.5(W)×93.6(D)mm	Δ	
Weight		0.51kg	0.28kg	0	

\*1 Check the specifications of sensor or switch to connect to the A1SX42.

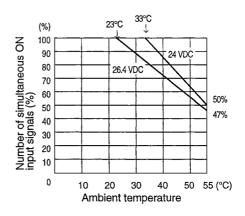


## (15) Specifications comparisons between the AX42-S1 and the A1SX42-S1

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Specifi	cation	AX42-S1	A1SX42-S1	Compati- bility	Precautions for replacement
Number of input points		64 points	64 points	0	•
Isolation met	thod	Photocoupler	Photocoupler	0	
Rated input	voltage	12/24VDC	24VDC	Δ	12VDC cannot be used.
Rated input	current	Approx. 3mA/Approx. 7mA	Approx. 5mA	Δ	Rated input current is smaller. <sup>*1</sup>
Operating vo range	oltage	10.2 to 26.4VDC (ripple ratio within 5%)	19.2 to 26.4VDC (ripple ratio within 5%)	Δ	12VDC cannot be used.
Maximum simultaneous points	s input	60% (20 points/common) simultaneously ON	Refer to the derating chart. *2	Δ	Use within the range shown in the derating chart.
ON voltage/0	ON current	9.5VDC or more/3mA or more	18.5VDC or more/3.5mA or more	Δ	12VDC cannot be used.
OFF voltage/OFF current		6VDC or less/1.5mA or less	3VDC or less/0.45mA or less	Δ	12VDC cannot be used.
Input resistance		Approx. 3.4k $\Omega$	Approx. 4.7kΩ	Δ	Input resistance is greater. <sup>*1</sup>
Response	OFF→ON	0.5ms or less	0.3ms or less (24VDC)	0	
time	ON→OFF	0.5ms or less	0.3ms or less (24VDC)	0	
Common ter arrangemen		32 points/common (common terminal: 1B1,1B2,2B1,2B2)	32 points/common (common terminal: 1B1,1B2,2B1,2B2)	0	
Operation in	dicator	ON indication (LED) 32 point switch-over using a switch	ON indication (LED) 32 point switch-over using a switch	0	
External con	nection	40-pin connector (with solder) $\times 2$	40-pin connector × 2 (included)	0	
Applicable wire size		0.3mm <sup>2</sup>	0.3mm <sup>2</sup>	0	
Accessory		External wiring connectors × 2	External wiring connectors × 2	0	
Current consumption		0.12A (TYP. all points ON)	0.16A (TYP. all points ON)	Δ	Current capacity must be reviewed.
External dim	ensions	250(H)×37.5(W)×106(D)mm	130(H)×34.5(W)×93.6(D)mm	Δ	
Weight		0.51kg	0.28kg	0	

\*1 Check the specifications of sensor or switch to connect to the A1SX42-S1.



## (16) Specifications comparisons between the AX50-S1 and the A1SX40

$\bigcirc$ : Compatible, $\triangle$ : Partial change required, $\times$ : Incom	patible
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Specifi	cation	AX50-S1	A1SX40	Compati- bility	Precautions for replacement
Number of input points		16 points	16 points	0	
Isolation method		Photocoupler	Photocoupler	0	
Rated input	voltage	48VDC	12/24VDC	×	Voltages exceeding 26.4VDC cannot be applied. <sup>*1</sup>
Rated input	current	4mA	Approx. 3mA/Approx. 7mA	0	
Operating vo range	oltage	38.4 to 57.6VDC (ripple ratio within 5%)	10.2 to 26.4VDC (ripple ratio within 5%)	×	Voltages exceeding 26.4VDC cannot be applied. <sup>*1</sup>
Maximum simultaneou input points	S	100% (8 points/common) simultaneously ON	100% simultaneously ON	0	
ON voltage/	ON current	34VDC or more/3mA or more	8VDC or more/2mA or more	×	Voltages exceeding 26.4VDC cannot be applied. <sup>*1</sup>
OFF voltage/OFF current		10VDC or less/1mA or less	4VDC or less/1mA or less	×	Voltages exceeding 26.4VDC cannot be applied. <sup>*1</sup>
Input resista	nce	Approx. 11kΩ	Approx. 3.3kΩ	×	Input resistance is smaller. <sup>*1</sup>
Response	OFF→ON	10ms or less	10ms or less (24VDC)	0	
time	ON→OFF	10ms or less	10ms or less (24VDC)	0	
Common terminal arrangement		8 points/common (common terminal: TB9,TB18)	16 points/common (common terminal: TB9,TB18)	Δ	As 2 commons are reduced to 1,wiring a different voltage for each common is not possible.
Operation in	dicator	ON indication (LED)	ON indication (LED)	0	
External con	nection	20-point terminal block connector (M3×6 screws)	20-point terminal block connector (M3.5×7 screws)	×	
Applicable w	vire size	0.75 to 2mm <sup>2</sup>	0.75 to 1.25mm <sup>2</sup>	×	Wiring must be changed.
Applicable solderless terminal		R1.25-3,R2-3, RAV1.25-3,RAV2-3	R1.25-3.5,R2-3.5, RAV1.25-3.5,RAV2-3.5	×	0
Current cons	sumption	0.055A (TYP. all points ON)	0.050A (TYP. all points ON)	0	
External dim	ensions	250(H)×37.5(W)×121(D)mm	130(H)×34.5(W)×93.6(D)mm	Δ	The dimensions are different.
Weight		0.37kg	0.20kg	0	

\*1 Connect a resistor of 3.3k Ω (1W or more) serially to the external signal line that connects external devices to the A1SX40.

## (17) Specifications comparisons between the AX60-S1 and the A1SX40

			O: Compatible, ∆: Partia	l change re	quired, ×: Incompatibl
Specifi	cation	AX60-S1	A1SX40	Compati- bility	Precautions for replacement
Number of input points		16 points	16 points	0	
Isolation met	thod	Photocoupler	Photocoupler	0	
Rated input	voltage	100/110/125VDC	12/24VDC	×	Voltages exceeding 26.4VDC cannot be applied. <sup>*1</sup>
Rated input	current	2mA	Approx. 3mA/Approx. 7mA	0	
Maximum ini current	rush	65mA (121VDC) 75mA (140VDC)	_	0	
Operating vo range	oltage	85 to 140VDC (ripple ratio within 5%)	10.2 to 26.4VDC (ripple ratio within 5%)	×	Voltages exceeding 26.4VDC cannot be applied. <sup>*1</sup>
Maximum simultaneous points	s input	60% (5 points/common) simultaneously ON	100% simultaneously ON	0	
ON voltage/ON current		80VDC or more/1.4mA or more	8VDC or more/2mA or more	×	Voltages exceeding 26.4VDC cannot be applied. <sup>*1</sup>
OFF voltage/OFF current		20VDC or less/0.5mA or less	4VDC or less/1mA or less	×	Voltages exceeding 26.4VDC cannot be applied. <sup>*1</sup>
Input resistance		Approx. 50k $\Omega$	Approx. 3.3kΩ	×	Input resistance is smaller. <sup>*1</sup>
Response	OFF→ON	10ms or less	10ms or less (24VDC)	0	
time	ON→OFF	20ms or less	10ms or less (24VDC)	0	
Common ter arrangemen		8 points/common	16 points/common (common terminal: TB9,TB18)	Δ	As 2 commons are reduced to 1,wiring a different voltage for each common is not possible.
Operation in	dicator	ON indication (LED)	ON indication (LED)	0	
External con	nection	20-point terminal block connector (M3×6 screws)	20-point terminal block connector (M3.5×7 screws)	×	
Applicable wire size		0.75 to 2mm <sup>2</sup>	0.75 to 1.25mm <sup>2</sup>	×	Wiring must be changed.
Applicable solderless terminal		R1.25-3,R2-3, RAV1.25-3,RAV2-3	R1.25-3.5,R2-3.5, RAV1.25-3.5,RAV2-3.5	×	<b>,</b>
Current cons	sumption	0.055A (TYP. all points ON)	0.050A (TYP. all points ON)	0	
External dim	ensions	250(H)×37.5(W)×121(D)mm	130(H)×34.5(W)×93.6(D)mm	Δ	The dimensions are different.
Weight		0.40kg	0.20kg	0	

\*1 Connect a resistor of  $15k\Omega$  (3W or more) serially to the external signal line that connects external devices to the A1SX40.

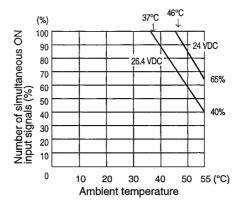
#### (18) Specifications comparisons between the AX70 and the A1SX71

 $\bigcirc$ : Compatible,  $\triangle$ : Partial change required, ×: Incompatible

Specifi	cation	AX70	A1SX71	Compati- bility	Precautions for replacement
Number of input points		16 points	32 points	×	Set sixteen points in the I/O assignment of Parameter.
Isolation me	thod	Photocoupler	Photocoupler	0	
Rated input	voltage	5V/12V/24VDC	5V/12V/24VDC	0	
Rated input	current	3.5mA/2mA/4.5mA(TYP.), 5.5mA/3mA/6mA(MAX.)	5VDC Approx. 1.2mA 12VDC Approx. 3.3mA 24VDC Approx. 7mA		Rated input current is smaller.*2
Operating vo range	oltage	4.5 to 5.5VDC (SW ON), 10.2 to 26.4VDC (SW OFF)	4.5 to 26.4VDC (ripple ratio within 5%)	0	
Maximum simultaneou points	s input	100% (8points/common) simultaneously ON	Refer to the derating chart. <sup>*1</sup>	Δ	Use within the range shown in the derating chart.
ON voltage/ON current		3.5VDC or more/1.0mA or more (SW ON), 5VDC or more/1.0mA or more (SW OFF)	3.5VDC or more/1mA or more	0	
OFF voltage/OFF current		1.1VDC or less/0.2mA or less (SW ON), 2VDC or less/0.2mA or less (SW OFF)	1VDC or less/0.1mA or less		OFF current is smaller. <sup>*2</sup>
Input resista	ince	Approx. 1.4kΩ (SW ON), Approx. 5.5kΩ (SW OFF)	Approx. 3.5k $\Omega$	Δ	Input resistance is greater. <sup>*2</sup>
Response	OFF→ON	1.5ms or less	1.5ms or less	0	
time	ON→OFF	3ms or less	3ms or less	0	
Common ter arrangemen	-	8 points/common (common terminal: TB9, TB18)	32 points/common (common terminal: B1, B2)	Δ	As 2 commons are reduced to 1,wiring a different voltage for each common is not possible.
Operation in	dicator	ON indication (LED)	ON indication (LED)	0	
External con	nection	20-point terminal block connector (M3×6 screws)	40-pin connector (included)	×	
Applicable wire size		0.75 to 2mm <sup>2</sup>	0.3mm <sup>2</sup>	×	Wiring must be changed.
Applicable solderless terminal		R1.25-3,R2-3, RAV1.25-3,RAV2-3	_	×	
Current cons	sumption	0.055A (TYP. all points ON)	0.075A (TYP. all points ON)	Δ	Current capacity must be reviewed.
External dim	nensions	250(H)×37.5(W)×121(D)mm	130(H)×34.5(W)×93.6(D)mm	Δ	
Weight		0.36kg	0.19kg	0	

\*1 The figure on the right shows derating.

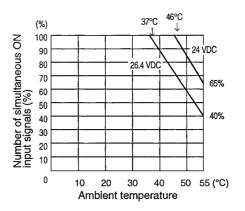
\*2 Check the specifications of sensor or switch to connect to the A1SX71.



## (19) Specifications comparisons between the AX71 and the A1SX71

Specifi	cation	AX71	A1SX71	Compati- bility	Precautions for replacement
Number of in	nput points	32 points	32 points	0	
Isolation method		Photocoupler	Photocoupler	0	
Rated input v	voltage	5V/12V/24VDC	5V/12V/24VDC	0	
Rated input of	current	3.5mA/2mA/4.5mA(TYP.), 5.5mA/3mA/6mA(MAX.)	5VDC Approx. 1.2mA 12VDC Approx. 3.3mA 24VDC Approx. 7mA	Δ	Rated input current is smaller.*1
Operating vo range	oltage	4.5 to 5.5VDC (SW ON), 10.2 to 26.4VDC (SW OFF)	4.5 to 26.4VDC (ripple ratio within 5%)	0	
Maximum sin input points	multaneous	100% (8points/common) simultaneously ON	Refer to the derating chart. *2	Δ	Use within the range shown in the derating chart.
ON voltage/ON current		3.5VDC or more/1.0mA or more (SW ON), 5VDC or more/1.0mA or more (SW OFF)	3.5VDC or more/1mA or more	0	
OFF voltage/OFF current		1.1VDC or less/0.2mA or less (SW ON), 2VDC or less/0.2mA or less (SW OFF)	1VDC or less/0.1mA or less	Δ	OFF current is smaller.*2
Input resistar	nce	Approx. 1.4kΩ (SW ON), Approx. 5.5kΩ (SW OFF)	Approx. 3.5k $\Omega$	Δ	Reduced at SW OFF.*2
Response	OFF→ON	1.5ms or less	1.5ms or less	0	
time	ON→OFF	3ms or less	3ms or less	0	
Common terr arrangement		8 points/common (common terminal: TB9,TB18,TB27,TB36)	32 points/common (common terminal: B1, B2)	Δ	As 4 commons are reduced to 1,wiring a different voltage for each common is not possible.
Operation inc	dicator	ON indication (LED)	ON indication (LED)	0	
External connection		38-point terminal block connector (M3×6 screws)	40-pin connector (included)	×	
Applicable wire size		0.75 to 2mm <sup>2</sup>	0.3mm <sup>2</sup>	×	Wiring must be changed.
Applicable solderless terminal		R1.25-3,R2-3, RAV1.25-3,RAV2-3	_	×	
Current consumption		0.110A (TYP. all points ON)	0.075A (TYP. all points ON)	0	
External dim	ensions	250(H)×37.5(W)×131(D)mm	130(H)×34.5(W)×93.6(D)mm	Δ	
Weight		0.45kg	0.19kg	0	

\*1 Check the specifications of sensor or switch to connect to the A1SX71.



# (20) Specifications comparisons between the AX80 and the A1SX80

Specification		AX80	A1SX80	Compati- bility	Precautions for replacement
Number of input points		16 points	16 points	0	
Isolation me	ethod	Photocoupler	Photocoupler	0	
Rated input	voltage	12/24VDC	12/24VDC	0	
Rated input	current	4mA/10mA	Approx. 3mA/Approx. 7mA	Δ	Rated input current is smaller.*1
Operating v range	voltage	10.2 to 26.4VDC (ripple ratio within 5%)	10.2 to 26.4VDC (ripple ratio within 5%)	0	
Maximum simultaneou points	us input	100% (8points/common) simultaneously ON	100% simultaneously ON (At 26.4VDC)	0	
ON voltage	ON current	9.5VDC or more/3mA or more	8VDC or more/2mA or more	0	
OFF voltage current	e/OFF	6VDC or less/1.5mA or less	4VDC or less/1mA or less	Δ	OFF current is smaller. <sup>*1</sup>
Input resista	ance	Approx. 2.4k $\Omega$	Approx. 3.3kΩ	Δ	Input resistance is greater. <sup>*1</sup>
Response	OFF→ON	10ms or less	10ms or less (24VDC)	0	
time	ON→OFF	10ms or less	10ms or less (24VDC)	0	
Common te arrangemer		8 points/common (common terminal: TB9,TB18)	16 points/common (common terminal: TB9,TB18)	Δ	As 2 commons are reduced to 1,wiring a different voltage for each common is not possible.
Operation in	ndicator	ON indication (LED)	ON indication (LED)	0	
External co	nnection	20-point terminal block connector (M3×6 screws)	20-point terminal block connector (M3.5×7 screws)	×	
Applicable wire size		0.75 to 2mm <sup>2</sup>	0.75 to 1.25mm <sup>2</sup>	×	Wiring must be changed.
Applicable solderless terminal		R1.25-3,R2-3, RAV1.25-3,RAV2-3	R1.25-3.5,R2-3.5, RAV1.25-3.5,RAV2-3.5	×	-
Current cor	sumption	0.055A (TYP. all points ON)	0.050A (TYP. all points ON)	0	
External dir	nensions	250(H)×37.5(W)×121(D)mm	130(H)×34.5(W)×93.6(D)mm	Δ	The dimensions are different.
Weight		0.36kg	0.20kg	0	

\*1 Check the specifications of sensor or switch to connect to the A1SX80.

## (21) Specifications comparisons between the AX80E and the A1SX80-S1

		-	$\bigcirc$ : Compatible, $\triangle$ : Partial	change re	quired, ×: Incompatible
Specific	ation	AX80E	A1SX80-S1	Compati- bility	Precautions for replacement
Number of input points		16 points	16 points	0	
Isolation metho	d	Photocoupler	Photocoupler	0	
Rated input vol	tage	12/24VDC	24VDC	Δ	12VDC cannot be used.
Rated input cur	rrent	4mA/10mA	Approx. 7mA	Δ	Rated input current is smaller.*1
Operating volta	ge range	10.2 to 26.4VDC (ripple ratio within 5%)	19.2 to 26.4VDC (ripple ratio within 5%)		12VDC cannot be used.
Maximum simu input points	Itaneous	100% (8points/common) simultaneously ON	85% simultaneously ON (At 26.4VDC)	Δ	Use within the specification range.
ON voltage/ON	current	9.5VDC or more/2.6mA or more	17VDC or more/5mA or more	Δ	12VDC cannot be used.
OFF voltage/O	FF current	6VDC or less/1.0mA or less	5VDC or less/1.7mA or less	Δ	12VDC cannot be used.
Input resistance	e	Approx. 2.4kΩ	Approx. 3.3k $\Omega$	Δ	Input resistance is greater.*1
Response	OFF→ON	5.5ms (TYP.)	0.4ms (24VDC)	0	
time	ON→OFF	6.0ms (TYP.)	0.5ms (24VDC)	0	
Response time	OFF→ON	0.5ms or less	0.4ms (24VDC)	0	
high-speed mode (upper 8 points only)	ON→OFF	1.0ms or less	0.5ms (24VDC)	0	
Common terminal arrangement		8 points/common (common terminal: TB9,TB18)	16 points/common (common terminal: TB9,TB18)	Δ	As 2 commons are reduced to 1,wiring a different voltage for each common is not possible.
Operation indic	ator	ON indication (LED)	ON indication (LED)	0	
External conne	ction	20-point terminal block connector (M3×6 screws)	20-point terminal block connector (M3.5×7 screws)	×	
Applicable wire size		0.75 to 2mm <sup>2</sup>	0.75 to 1.25mm <sup>2</sup>	×	Wiring must be changed.
Applicable solderless terminal		R1.25-3,R2-3, RAV1.25-3,RAV2-3	R1.25-3.5,R2-3.5, RAV1.25-3.5,RAV2-3.5	×	
Current consur	nption	0.055A (TYP. all points ON)	0.050A (TYP. all points ON)	0	
External dimen	sions	250(H)×37.5(W)×121(D)mm	130(H)×34.5(W)×93.6(D)mm	Δ	The dimensions are different.
Weight		0.36kg	0.2kg	0	

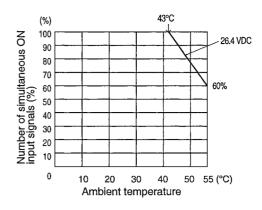
\*1 Check the specifications of sensor or switch to connect to the A1SX80-S1.

#### (22) Specifications comparisons between the AX81 and the A1SX81

			O: Compatible, ∆ : Partia	al change re	quired, ×: Incompatible
Specification		AX81	A1SX81	Compati- bility	Precautions for replacement
Number of in	nput points	32 points	32 points	0	
Isolation me	thod	Photocoupler	Photocoupler	0	
Rated input	voltage	12/24VDC	12/24VDC	0	
Rated input	current	4mA/10mA	Approx. 3mA/Approx. 7mA	Δ	Rated input current is smaller.*1
Operating vo range	oltage	10.2 to 26.4VDC (ripple ratio within 5%)	10.2 to 26.4VDC (ripple ratio within 5%)	0	
Maximum simultaneous points	s input	60% (5points/common) simultaneously ON	Refer to the derating chart. *2	0	
ON voltage/0	ON current	9.5VDC or more/3mA or more	8VDC or more/2mA or more	0	
OFF voltage/OFF current		6VDC or less/1.5mA or less	4VDC or less/1mA or less	Δ	OFF current is smaller.*1
Input resistance		Approx. 2.4k $\Omega$	Approx. 3.3k $\Omega$	Δ	Input resistance is greater. <sup>*1</sup>
Response OFF→ON		10ms or less	10ms or less (24VDC)	0	
time	ON→OFF	10ms or less	10ms or less (24VDC)	0	
Common terminal arrangement		8 points/common (common terminal: TB9,TB18,TB27,TB36)	32 points/common (common terminal: 17, 18, 36)	Δ	As 4 commons are reduced to 1,wiring a different voltage for each common is not possible.
Operation in	dicator	ON indication (LED)	ON indication (LED)	0	
External connection		38-point terminal block connector (M3×6 screws)	37-pin D sub-connector (included)	×	
Applicable wire size		0.75 to 2mm <sup>2</sup>	0.3mm <sup>2</sup>	×	Wiring must be changed.* <sup>3</sup>
Applicable solderless terminal		R1.25-3,R2-3, RAV1.25-3,RAV2-3	_	×	
Current cons	sumption	0.11A (TYP. all points ON)	0.08A (TYP. all points ON)	0	
External dim	ensions	250(H)×37.5(W)×131(D)mm	130(H)×34.5(W)×93.6(D)mm	Δ	
Weight		0.45kg	0.24kg	0	

\*1 Check the specifications of sensor or switch to connect to the A1SX81.

\*2 The figure on the right shows derating.

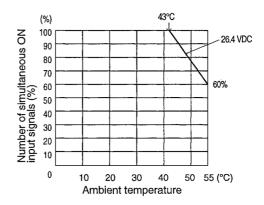


## (23) Specifications comparisons between the AX81B and the A1SX81

Specifi	iaation	AX81B		Compati-	Precautions for	
Specifi	ICation	AX81B	A1SX81	bility	replacement	
Number of i	nput points	32 points (64 points occupied)	32 points	×	Set sixty-four points in the I/O assignment of Parameter.	
Isolation method		Photocoupler	Photocoupler	0		
Rated input	voltage	24VDC	12/24VDC	0		
Rated input	current	7mA (When turning ON an external switch) 1.5mA (When turning OFF an external switch)	Approx. 3mA/Approx. 7mA	×	The wire breakage detection function is not provided.	
Operating verting vertice of the second seco	oltage	21.6 to 30VDC (ripple ratio within 5%)	10.2 to 26.4VDC (ripple ratio within 5%)		Voltages exceeding 26.4VDC cannot be applied.	
Maximum simultaneou points	ıs input	60% (5points/common) simultaneously ON	Refer to the derating chart. <sup>*1</sup>	0		
ON voltage/	ON current	21.0VDC or more/5.4mA or more (Normal input) 1.0VDC or less/0.2mA or less (Wire breakage detection)	8VDC or more/2mA or more	×	The wire breakage detection function is not provided.	
OFF voltage/OFF current		7.0VDC or less/1.9mA or less (Normal input) 6.0VDC or more/1.3mA or more (Wire breakage detection)	4VDC or less/1mA or less	×	The wire breakage detection function is not provided.	
Input resistance		Approx. 3.6kΩ (Normal input) Approx. 4.3kΩ (Wire breakage detection)	Approx. 3.3k $\Omega$	×	The wire breakage detection function is not provided.	
Wire breakage detection		Provided	Not provided	×	The wire breakage detection function is not provided.	
Response OFF→ON		10ms or less	10ms or less (24VDC)	0		
time	ON→OFF	10ms or less	10ms or less (24VDC)	0		
External resistance		0.1kΩ or less (At turning ON) 11.4 to 12.7kΩ or less (At turning OFF) 150kΩ or more (At wire breakage detection)	_	_	Since the wire breakage detection function is not provided, the external resistance is not required.	
Parallel resistance with external switch		$12k\Omega$ (tolerance: $\pm 5\%$ , 1/4W or more)	_	_	Since the wire breakage detection function is not provided, the external resistance is not required.	
Common terminal arrangement		8 points/common (common terminal: TB9,TB18,TB27,TB36)	32 points/common (common terminal: 17, 18, 36)		As 4 commons are reduced to 1,wiring a different voltage for each common is not possible.	
Operation indicator		ON indication (LED) 32 point switch-over using a switch	ON indication (LED)	0		
External connection		38-point terminal block connector (M3× 6 screws)	37-pin D sub-connector (included)	×	Wiring must be	
Applicable wire size		0.75 to 2mm <sup>2</sup>	0.3mm <sup>2</sup>	×	Wiring must be changed. <sup>*2</sup>	
Applicable s terminal	solderless	R1.25-3,R2-3, RAV1.25-3,RAV2-3		×		
Current con	sumption	0.125A (TYP. all points ON)	0.080A (TYP. all points ON)	0		
External din	nensions	250(H)×37.5(W)×131(D)mm	130(H)×34.5(W)×93.6(D)mm	Δ		
Weight		0.45kg	0.24kg	0		

 $\bigcirc$  : Compatible,  $\bigtriangleup$  : Partial change required,  $\times$  : Incompatible

- \*1 The figure on the right shows derating.
- \*2 By using connectors/terminal block converter modules(A6TBX36-E, etc.), conversion to the terminal block is possible.



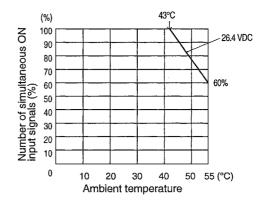
#### (24) Specifications comparisons between the AX81-S1 and the A1SX81

			$\bigcirc$ : Compatible, $\triangle$ : Partia	i change re	quired, x: incompatible
Specific	cation	AX81-S1	A1SX81	Compati- bility	Precautions for replacement
Number of input points		32 points	32 points	0	
Isolation met	thod	Photocoupler	Photocoupler	0	
Rated input	voltage	12/24VDC	12/24VDC	0	
Rated input o	current	2.5mA/5mA	Approx. 3mA/Approx. 7mA	Δ	Rated input current is smaller.*1
Operating vo range	oltage	10.2 to 26.4VDC (ripple ratio within 5%)	10.2 to 26.4VDC (ripple ratio within 5%)	0	
Maximum simultaneous points	s input	60% (5points/common) simultaneously ON	Refer to the derating chart. $^{*2}$	0	
ON voltage/0	ON current	5.6VDC or more/1.1mA or more	8VDC or more/2mA or more	Δ	The ON voltage has been increased.*1
OFF voltage/OFF current		2.4VDC or less/0.39mA or less	4VDC or less/1mA or less	0	
Input resistance		Approx. 4.8kΩ	Approx. 3.3k $\Omega$	Δ	Input resistance is smaller. <sup>*1</sup>
Response OFF→ON		10ms or less	10ms or less (24VDC)	0	
time	ON→OFF	10ms or less	10ms or less (24VDC)	0	
Common terminal arrangement		8 points/common (common terminal: TB9,TB18,TB27,TB36)	32 points/common (common terminal: 17, 18, 36)	Δ	As 4 commons are reduced to 1,wiring a different voltage for each common is not possible.
Operation in	dicator	ON indication (LED)	ON indication (LED)	0	
External connection		38-point terminal block connector (M3×6 screws)	37-pin D sub-connector (included)	×	
Applicable wire size		0.75 to 2mm <sup>2</sup>	0.3mm <sup>2</sup>	×	Wiring must be changed. <sup>*3</sup>
Applicable solderless terminal		R1.25-3,R2-3, RAV1.25-3,RAV2-3	_	×	Shangoa.
Current cons	sumption	0.105A (TYP. all points ON)	0.080A (TYP. all points ON)	0	
External dim	ensions	250(H)×37.5(W)×131(D)mm	130(H)×34.5(W)×93.6(D)mm	Δ	
Weight		0.45kg	0.24kg	0	

 $\bigcirc$ : Compatible,  $\triangle$ : Partial change required, ×: Incompatible

\*1 Check the specifications of sensor or switch to connect to the A1SX81.

\*2 The figure on the right shows derating.



Specification

Number of input pol

Rated input voltage

Rated input current

Operating voltage

range Maximum simultaneous input

points

#### (25) Specifications comparisons between the AX81-S2 and the A1SX81

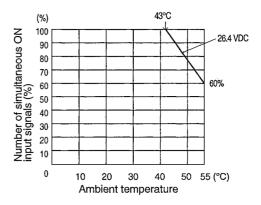
	$\bigcirc$ : Compatible, $\triangle$ : Partial change required, ×: Incompatible							
	AX81-S2	A1SX81	Compati- bility	Precautions for replacement				
oints	32 points	32 points	0					
	Photocoupler	Photocoupler	0					
е	48/60VDC	12/24VDC	×	Voltages exceeding 26.4VDC cannot be applied.*1				
t	3mA/4mA	Approx. 3mA/Approx. 7mA	0					
	41 to 66VDC (ripple ratio within 5%)	10.2 to 26.4VDC (ripple ratio within 5%)	×	Voltages exceeding 26.4VDC cannot be applied. <sup>*1</sup>				
t	60% (5points/common) simultaneously ON	Refer to the derating chart. *2	0					
				Voltages exceeding				

ON voltage/ON current		31VDC or more/1.7mA or more	8VDC or more/2mA or more	×	Voltages exceeding 26.4VDC cannot be applied. <sup>*1</sup>
OFF voltage/ current	/OFF	10VDC or less/0.5mA or less	4VDC or less/1mA or less	×	Voltages exceeding 26.4VDC cannot be applied. <sup>*1</sup>
Input resistance		Approx. $18k\Omega$	Approx. 3.3k $\Omega$	×	Input resistance is smaller.
Response	Sponse OFF→ON 20ms or less (60VDC)		10ms or less (24VDC)	0	
time	ON→OFF	20ms or less (60VDC)	10ms or less (24VDC)	0	
Common terminal arrangement		8 points/common (common terminal: TB9,TB18,TB27,TB36)	32 points/common (common terminal: 17, 18, 36)	Δ	As 4 commons are reduced to 1,wiring a different voltage for each common is not possible.
Operation indicator		ON indication (LED)	ON indication (LED)	0	
External connection		38-point terminal block connector (M3×6 screws)	37-pin D sub-connector (included)	×	
Applicable wire size		0.75 to 2mm <sup>2</sup>	0.3mm <sup>2</sup>	×	Wiring must be changed. <sup>*3</sup>
Applicable solderless terminal		R1.25-3,R2-3, RAV1.25-3,RAV2-3	×		
Current cons	sumption	0.110A (TYP. all points ON)	0.080A (TYP. all points ON)	0	
External dim	ensions	250(H)×37.5(W)×131(D)mm	130(H)×34.5(W)×93.6(D)mm	Δ	
Weight		0.45kg	0.24kg	0	

\*1 For use of 48VDC, connect a resistor of 3.3kΩ (1W or more) in series with the external signal line connected between the A1SX81 and an external device.

For use of 60VDC, connect a resistor of  $5.6k\,\Omega$  (2W or more) in series with the external signal line connected between the A1SX81 and an external device.

- \*2 The figure on the right shows derating.
- \*3 By using connectors/terminal block converter modules (A6TBX36-E, etc.), conversion to the terminal block is possible.



## (26) Specifications comparisons between the AX81-S3 and the A1SX80-S1

			O: Compatible, ∆: Partia	al change re	quired, ×: Incompatible
Specifi	cation	AX81-S3	A1SX80-S1	Compati- bility	Precautions for replacement
Number of input points		32 points	16 points	×	When seventeen or more points are used, use two of the A1SX80-S1 modules.
Isolation met	thod	Photocoupler	Photocoupler	0	
Rated input	voltage	12/24VDC	24VDC	Δ	12VDC cannot be used.
Rated input of	current	4mA/10mA	Approx. 7mA	Δ	Rated input current is smaller.*1
Operating vo range	oltage	10.2 to 26.4VDC (ripple ratio within 5%)	19.2 to 26.4VDC (ripple ratio within 5%)	Δ	12VDC cannot be used.
Maximum simultaneous points	s input	60% (5points/common) simultaneously ON	85% simultaneously ON (26.4VDC)		Use within the specification range.
ON voltage/ON current		9.5VDC or more/3mA or more	17VDC or more/5mA or more	Δ	12VDC cannot be used.
OFF voltage/OFF current		6VDC or less/1.5mA or less	5VDC or less/1.7mA or less	Δ	12VDC cannot be used.
Input resistance		Approx. 2.4k $\Omega$	Approx. 3.3kΩ	Δ	Input resistance is greater.*1
Response OFF→ON		0.1ms or less	0.4ms (24VDC)	Δ	The response times
time	ON→OFF	0.2ms or less	0.5ms (24VDC)	Δ	differ.
Common terminal arrangement		8 points/common (common terminal: TB9,TB18,TB27,TB36)	16 points/common (common terminal: TB9,TB18)		As 2 commons are reduced to 1,wiring a different voltage for each common is not possible.
Operation in	dicator	ON indication (LED)	ON indication (LED)	0	
External connection		38-point terminal block connector (M3×6 screws)	20-point terminal block connector (M3.5×7 screws)	×	
Applicable wire size		0.75 to 2mm <sup>2</sup>	0.75 to 1.25mm <sup>2</sup>	×	Wiring must be changed.
Applicable solderless terminal		R1.25-3,R2-3, RAV1.25-3,RAV2-3	R1.25-3.5,R2-3.5, RAV1.25-3.5,RAV2-3.5	×	
Current cons	sumption	0.110A (TYP. all points ON)	0.050A (TYP. all points ON)	0	
External dim	ensions	250(H)×37.5(W)×121(D)mm	130(H)×34.5(W)×93.6(D)mm	Δ	The dimensions are different.
Weight		0.45kg	0.2kg	0	

\*1 Check the specifications of sensor or switch to connect to the A1SX80-S1.

# (27) Specifications comparisons between the AX82 and the A1SX82-S1

O: Cor	mpatible. /	< :	Partial	change	reauired.	X:	Incompatible

Specification		AX82	A1SX82-S1	Compati- bility	Precautions for replacement
Number of input points		64 points	64 points	0	
Isolation met	thod	Photocoupler	Photocoupler	0	
Rated input	voltage	12/24VDC	24VDC	Δ	12VDC cannot be used.
Rated input	current	Approx. 3mA/Approx. 7mA	Approx. 5mA	Δ	Rated input current is smaller.*1
Operating vo range	oltage	10.2 to 26.4VDC (ripple ratio within 5%)	19.2 to 26.4VDC (ripple ratio within 5%)	Δ	12VDC cannot be used.
Maximum simultaneous points	s input	40 points (When located next to the power supply module : 26 points)	50% (16points/common) simultaneously ON (24VDC)	Δ	Use within the specification range.
ON voltage/0	ON current	9.5VDC or more/2.6mA or more	18.5VDC or more/3.5mA or more	Δ	12VDC cannot be used.
OFF voltage current	/OFF	6VDC or less/1.0mA or less	3VDC or less/0.45mA or less	Δ	12VDC cannot be used.
Input resistance		Approx. 3.4kΩ	Approx. 4.7k $\Omega$	Δ	Input impedance is larger. <sup>*1</sup>
Response	OFF→ON	10ms or less	0.3ms (24VDC)	0	
time	ON→OFF	10ms or less	0.3ms (24VDC)	0	
Common terminal arrangement		32 points/common (common terminal: 1-17,1-18,1-36,2-17,2-18,2-36)	32 points/common (common terminal: 1B1,1B2,2B1,2B2)	0	
Operation in	dicator	ON indication (LED) 32 point switch-over using a switch	ON indication (LED) 32 point switch-over using a switch	0	
External con	nection	37-pin D sub-connector (with solder) × 2	40-pin connector × 2 (included)	×	Connector must be changed.
Applicable wire size		0.3mm <sup>2</sup>	0.3mm <sup>2</sup>	0	
Accessory		External wiring D sub connectors × 2	External wiring connectors × 2	Δ	The shapes of the accessory connectors are different.
Current cons	sumption	0.12A (TYP. all points ON)	0.16A (TYP. all points ON)	0	
External dim	ensions	250(H)×37.5(W)×106(D)mm	130(H)×34.5(W)×93.6(D)mm	Δ	
Weight		0.60kg	0.28kg	0	

\*1 Check the specifications of sensor or switch to connect to the A1SX82-S1.

## 3.2.2 Specifications comparisons between output modules

## (1) Specifications comparisons between the AY10 and the A1SY10

 $\bigcirc$ : Compatible,  $\triangle$ : Partial change required, ×: Incompatible

Specification		AY10	A1SY10	Compati- bility	Precautions for replacement
Number of output points		16 points	16 points	0	
Isolation me	ethod	Photocoupler	Photocoupler	0	
Rated switching voltage/current		24VDC 2A (resistive load)/point 240VAC 2A (COS $\phi$ =1)/point 8A/common	24VDC 2A (resistive load)/point 240VAC 2A (COS $\phi$ =1)/point 8A/common	0	
Minimum sv load	witching	5VDC 1mA	5VDC 1mA	0	
Maximum s voltage	witching	264VAC 125VDC	264VAC 125VDC	0	
Response	OFF→ON	10ms or less	10ms or less	0	
time	ON→OFF	12ms or less	12ms or less	0	
Mechanical	life	20 million times or more	20 million times or more	0	
		Rated switching voltage/current load 200 thousand times or more	Rated switching voltage/current load 100 thousand times or more	Δ	
Electrical life		200VAC 1.5A, 240VAC 1A (COS $\phi$ =0.7) 200 thousand times or more 200VAC 0.75A, 240VAC 0.5A (COS $\phi$ =0.35) 200 thousand times or more 24VDC 1A, 100VDC 0.1A (L/R=7ms) 200 thousand times or more	200VAC 1.5A, 240VAC 1A (COS $\phi$ =0.7) 100 thousand times or more 200VAC 1A, 240VAC 0.5A (COS $\phi$ =0.35) 100 thousand times or more 24VDC 1A, 100VDC 0.1A (L/R=7ms) 100 thousand times or more	Δ	Reduce the exchange intervals of the modules as Mechanical/Electrical Life is cut to about half.
Maximum s frequency	witching	3600 times/hour	3600 times/hour	0	
Common te arrangemer		8 points/common (common terminal: TB9,TB18)	8 points/common (common terminal: TB9,TB18)	0	
Operation in	ndicator	ON indication (LED)	ON indication (LED)	0	
External supply	Voltage	24VDC±10% Ripple voltage 4Vp-p or less	$24$ VDC $\pm$ 10% Ripple voltage 4Vp-p or less	0	
power	Current	150mA (24VDC TYP. all points ON)	90mA (24VDC TYP. all points ON)	0	
External connection		20-point terminal block connector (M3×6 screws)	20-point terminal block connector (M3.5×7 screws)	×	
Applicable wire size		0.75 to 2mm <sup>2</sup>	0.75 to 1.25mm <sup>2</sup>	×	Wiring must be changed.
Applicable solderless terminal		R1.25-3,R2-3, RAV1.25-3,RAV2-3	R1.25-3.5,R2-3.5, RAV1.25-3.5,RAV2-3.5	×	
Current cor	sumption	0.115A (TYP. all points ON)	0.120A (TYP. all points ON)	Δ	Current capacity must be reviewed.
External dir	mensions	250(H)×37.5(W)×121(D)mm	130(H)×34.5(W)×93.6(D)mm	Δ	The dimensions are different.
Weight		0.44kg	0.25kg	0	

#### (2) Specifications comparisons between the AY10A and the A1SY18A

 $\bigcirc$ : Compatible,  $\triangle$ : Partial change required, ×: Incompatible

Specif	ication	AY10A	A1SY18A	Compati- bility	Precautions for replacement
Number of opoints	output	16 points	8 points (16 points occupied)	×	When nine or more points are used, use two of the A1SY18A modules.
Isolation me	ethod	Photocoupler	Photocoupler	0	
Rated switc voltage/curr	-	24VDC 2A (resistive load)/point 240VAC 2A (COS $\phi$ =1)/point 16A/all points	24VDC 2A (resistive load)/point 240VAC 2A (COS $\phi$ =1)/point 8A/module	0	
Minimum sv load	witching	5VDC 1mA	5VDC 1mA	0	
Maximum s voltage	witching	264VAC 125VDC	264VAC 125VDC	0	
Response	OFF→ON	10ms or less	10ms or less	0	
time	ON→OFF	12ms or less	12ms or less	0	
Mechanical	life	20 million times or more	20 million times or more	0	
		Rated switching voltage/current load 200 thousand times or more	Rated switching voltage/current load 200 thousand times or more	0	
Electrical lif	e	200VAC 1.5A, 240VAC 1A (COS $\phi$ =0.7) 200 thousand times or more 200VAC 0.75A, 240VAC 0.5A (COS $\phi$ =0.35) 200 thousand times or more 24VDC 1A, 100VDC 0.1A (L/R=7ms) 200 thousand times or more	200VAC 1.5A, 240VAC 1A (COS $\phi$ =0.7) 200 thousand times or more 200VAC 0.75A, 240VAC 0.5A (COS $\phi$ =0.35) 200 thousand times or more 24VDC 1A, 100VDC 0.1A (L/R=7ms) 200 thousand times or more	0	
Maximum s frequency	witching	3600 times/hour	3600 times/hour	0	
Common te arrangemer		Not provided (all points independent)	Not provided (all points independent)	0	
Operation in	ndicator	ON indication (LED)	ON indication (LED)	0	
External supply	Voltage	$24$ VDC $\pm$ 10% Ripple voltage 4Vp-p or less	$24VDC \pm 10\%$ Ripple voltage 4Vp-p or less	0	
power	Current	150mA (24VDC TYP. all points ON)	75mA (24VDC TYP. all points ON)	0	
External co	nnection	38-point terminal block connector (M3×6 screws)	20-point terminal block connector (M3.5×7 screws)	×	
Applicable	wire size	0.75 to 2mm <sup>2</sup>	0.75 to 1.25mm <sup>2</sup>	×	Wiring must be changed.
Applicable s terminal	solderless	R1.25-3,R2-3, RAV1.25-3,RAV2-3	R1.25-3.5,R2-3.5, RAV1.25-3.5,RAV2-3.5	×	
Current cor	sumption	0.115A (TYP. all points ON)	0.240A (TYP. all points ON)	Δ	Current capacity must be reviewed.
External dir	nensions	250(H)×37.5(W)×131(D)mm	130(H)×34.5(W)×93.6(D)mm	Δ	The dimensions are different.
Weight		0.50kg	0.25kg	0	

# (3) Specifications comparisons between the AY11 and the A1SY10

$\bigcirc$ Compatible $\land$	Partial change required	×۰	Incompatible
$\bigcirc$ . $\bigcirc$ . $\bigcirc$ . $\square$ .	i unun onunge requireu	,	moomputible

Specif	cation	AY11	A1SY10	Compati- bility	Precautions for replacement
Number of points	output	16 points	16 points	0	
Isolation method		Photocoupler	Photocoupler	0	
Rated switc voltage/curr		24VDC 2A (resistive load)/point 240VAC 2A (COS $\phi$ =1)/point 8A/common	24VDC 2A (resistive load)/point 240VAC 2A (COS $\phi$ =1)/point 8A/common	0	
Minimum sv load	witching	5VDC 1mA	5VDC 1mA	0	
Maximum s voltage	witching	264VAC 125VDC	264VAC 125VDC	0	
Leakage cu OFF	rrent at	0.1mA (200VAC, 60Hz)	_	0	
Response	OFF→ON	10ms or less	10ms or less	0	
time	ON→OFF	12ms or less	12ms or less	0	
Mechanical	life	20 million times or more	20 million times or more	0	
		Rated switching voltage/current load 200 thousand times or more	Rated switching voltage/current load 100 thousand times or more		
Electrical life		200VAC 1.5A, 240VAC 1A (COS $\phi$ =0.7) 200 thousand times or more 200VAC 0.75A, 240VAC 0.5A (COS $\phi$ =0.35) 200 thousand times or more 24VDC 1A, 100VDC 0.1A (L/R=7ms) 200 thousand times or more	200VAC 1.5A, 240VAC 1A (COS $\phi$ =0.7) 100 thousand times or more 200VAC 1A, 240VAC 0.5A (COS $\phi$ =0.35) 100 thousand times or more 24VDC 1A, 100VDC 0.1A (L/R=7ms) 100 thousand times or more	Δ	Reduce the exchange intervals of the modules as Mechanical/Electrical Life is cut to about half.
Maximum s frequency	witching	3600 times/hour	3600 times/hour	0	
Surge supp	ressor	Varistor (387 to 473V)	None	×	The varistor is not built in. <sup>*1</sup>
Relay sock	et	Yes	None	×	Replace the module itself when its relay has a failure.
Common te arrangemer		8 points/common (common terminal: TB9,TB18)	8 points/common (common terminal: TB9,TB18)	0	
Operation in	ndicator	ON indication (LED)	ON indication (LED)	0	
External supply	Voltage	24VDC±10% Ripple voltage 4Vp-p or less	24VDC±10% Ripple voltage 4Vp-p or less	0	
power	Current	150mA (24VDC TYP. all points ON)	90mA (24VDC TYP. all points ON)	0	
External co	nnection	20-point terminal block connector (M3×6 screws)	20-point terminal block connector (M3.5×7 screws)	×	
Applicable wire size		0.75 to 2mm <sup>2</sup>	0.75 to 1.25mm <sup>2</sup>	×	Wiring must be changed.
Applicable sterminal	solderless	R1.25-3,R2-3, RAV1.25-3,RAV2-3	R1.25-3.5,R2-3.5, RAV1.25-3.5,RAV2-3.5	×	
Current cor	sumption	0.115A (TYP. all points ON)	0.120A (TYP. all points ON)		Current capacity must be reviewed.
External dir	nensions	250(H)×37.5(W)×121(D)mm	130(H)×34.5(W)×93.6(D)mm		The dimensions are different.
Weight		0.50kg	0.25kg	0	

### (4) Specifications comparisons between the AY11A and the A1SY18A

Specific	cation	AY11A	A1SY18A	Compati- bility	Precautions for replacement
Number of c points	output	16 points	8 points (16 points occupied)	×	When nine or more points are used, use two of the A1SY18A modules.
Isolation me	ethod	Photocoupler	Photocoupler	0	
Rated switc voltage/curr	-	24VDC 2A (resistive load) /point 240VAC 2A (COS $\phi$ =1)/point 16A/all points	24VDC 2A (resistive load)/point 240VAC 2A (COS $\phi$ =1)/point 8A/module	0	
Minimum sv Ioad	vitching	5VDC 1mA	5VDC 1mA	0	
Maximum sv voltage	witching	264VAC 125VDC	264VAC 125VDC	0	
Leakage cu OFF	rrent at	0.1mA (200VAC, 60Hz)	—	0	
Response	OFF→ON	10ms or less	10ms or less	0	
time	ON→OFF	12ms or less	12ms or less	0	
Mechanical	life	20 million times or more	20 million times or more	0	
		Rated switching voltage/current load 200 thousand times or more	Rated switching voltage/current load 200 thousand times or more	0	
Electrical life	e	200VAC 1.5A, 240VAC 1A (COS $\phi$ =0.7) 200 thousand times or more 200VAC 0.75A, 240VAC 0.5A (COS $\phi$ =0.35) 200 thousand times or more 24VDC 1A, 100VDC 0.1A (L/R=7ms) 200 thousand times or more	200VAC 1.5A, 240VAC 1A (COS $\phi$ =0.7) 200 thousand times or more 200VAC 0.75A, 240VAC 0.5A (COS $\phi$ =0.35) 200 thousand times or more 24VDC 1A, 100VDC 0.1A (L/R=7ms) 200 thousand times or more	0	
Maximum sy frequency	witching	3600 times/hour	3600 times/hour	0	
Surge supp	ressor	Varistor (387 to 473V)	None	×	The varistor is not built in.*
Common te arrangemen	-	Not provided (all points independent)	Not provided (all points independent)	0	
Operation ir	ndicator	ON indication (LED)	ON indication (LED)	0	
External supply	Voltage	$24VDC \pm 10\%$ Ripple voltage 4Vp-p or less	$24VDC \pm 10\%$ Ripple voltage 4Vp-p or less	0	
power	Current	150mA (24VDC TYP. all points ON)	75mA (24VDC TYP. all points ON)	0	
External cor	nnection	38-point terminal block connector (M3×6 screws)	20-point terminal block connector (M3.5×7 screws)	×	
Applicable v	vire size	0.75 to 2mm <sup>2</sup>	0.75 to 1.25 mm <sup>2</sup>	×	Wiring must be changed.
Applicable s terminal	olderless	R1.25-3,R2-3, RAV1.25-3,RAV2-3	R1.25-3.5,R2-3.5, RAV1.25-3.5,RAV2-3.5	×	
Current con	sumption	0.115A (TYP. all points ON)	0.240A (TYP. all points ON)	Δ	Current capacity must be reviewed.
External din	nensions	250(H)×37.5(W)×131(D)mm	130(H)×34.5(W)×93.6(D)mm	Δ	The dimensions are different.
Weight		0.47kg	0.25kg	Δ	When calculating the weight, note that the weigh becomes heavier if the two or more A1SY18A modules are used.

#### (5) Specifications comparisons between the AY11AEU and the A1SY18AEU

$\bigcirc$ : Compatible. $\land$ :	Partial change required	×: Incompatible
$O$ . Compatible, $\Delta$ .	i urtiur onunge requireu	$,  \alpha$ . moomputible

Specifi	cation	AY11AEU	A1SY18AEU	Compati- bility	Precautions for replacement
Number of opoints	output	16 points	8 points (16 points occupied)	×	When nine or more points are used, use two of the A1SY18AEU modules.
Isolation me	ethod	Photocoupler	Photocoupler	0	
Rated switc voltage/curr		24VDC 2A (resistive load)/point 24VAC 2A (COS $\phi$ =1)/point 16A/all points	24VDC 2A (resistive load)/point 240VAC 2A (COS $\phi$ =1)/point	0	
Minimum sv load	witching	5VDC 1mA	5VDC 1mA	0	
Maximum s voltage	witching	49.9VAC 74.9VDC	264VAC 125VDC	0	
Leakage cu OFF	rrent at	0.1mA (49.9VAC, 60Hz)	_	0	
Response	OFF→ON	10ms or less	10ms or less	0	
time	ON→OFF	12ms or less	12ms or less	0	
Mechanical	life	20 million times or more	20 million times or more	0	
		Rated switching voltage/current load 200 thousand times or more	Rated switching voltage/current load 200 thousand times or more	0	
Electrical life		24VAC 1.5A (COS $\phi$ =0.7) 200 thousand times or more 24VAC 0.75A (COS $\phi$ =0.35) 200 thousand times or more 24VDC 1A, 48VDC 0.1A (L/R=7ms) 200 thousand times or more	200VAC 1.5A, 240VAC 1A (COS $\phi$ =0.7) 200 thousand times or more 200VAC 0.75A, 240VAC 0.5A (COS $\phi$ =0.35) 200 thousand times or more 24VDC 1A, 100VDC 0.1A (L/R=7ms) 200 thousand times or more	0	
Maximum s frequency	witching	3600 times/hour	3600 times/hour	0	
Surge supp	ressor	Varistor (387 to 473V)	None	×	The varistor is not built in.*1
Common te arrangemer		Not provided (all points independent)	Not provided (all points independent)	0	
Operation ir	ndicator	ON indication (LED)	ON indication (LED)	0	
External supply	Voltage	$24VDC \pm 10\%$ Ripple voltage 4Vp-p or less	$24VDC \pm 10\%$ Ripple voltage 4Vp-p or less	0	
power	Current	150mA (24VDC TYP. all points ON)	75mA (24VDC TYP. all points ON)	0	
External co	nnection	38-point terminal block connector (M3×6 screws)	20-point terminal block connector (M3.5×7 screws)	×	
Applicable v	wire size	0.75 to 2mm <sup>2</sup>	0.75 to 1.25mm <sup>2</sup>	×	Wiring must be changed.
Applicable s terminal	solderless	R1.25-3,R2-3, RAV1.25-3,RAV2-3	RAV1.25-3.5	×	
Current con	sumption	0.115A (TYP. all points ON)	0.240A (TYP. all points ON)	Δ	Current capacity must be reviewed.
External dir	nensions	250(H)×37.5(W)×131(D)mm	130(H)×34.5(W)×93.6(D)mm	Δ	The dimensions are different.
Weight		0.47kg	0.25kg	Δ	When calculating the weight, note that the weight becomes heavier if the two or more A1SY18AEU modules are used.

#### (6) Specifications comparisons between the AY11E and the A1SY10

O: Compatible, ∆ :	Partial change	required x.	Incompatible
$O$ . Compatible, $\Delta$ .	i artial change	required, A.	meempauble

Specif	ication	AY11E	A1SY10	Compati- bility	Precautions for replacement
Number of points	output	16 points	16 points	0	
Isolation m	ethod	Photocoupler	Photocoupler	0	
Rated swite voltage/cur		24VDC 2A (resistive load)/point 240VAC 2A (COS $\phi$ =1)/point 8A/common	24VDC 2A (resistive load)/point 240VAC 2A (COS $\phi$ =1)/point 8A/common	0	
Minimum s load	witching	5VDC 1mA	5VDC 1mA	0	
Maximum s voltage	witching	250VAC 125VDC	264VAC 125VDC	0	
Leakage cu OFF	irrent at	0.1mA (200VAC, 60Hz)	_	0	
Response	OFF→ON	10ms or less	10ms or less	0	
time	ON→OFF	12ms or less	12ms or less	0	
Mechanica	life	20 million times or more	20 million times or more	0	
		Rated switching voltage/current load 200 thousand times or more	Rated switching voltage/current load 100 thousand times or more	Δ	Reduce the
Electrical lit	ē	200VAC 1.5A, 240VAC 1A (COS $\phi$ =0.7) 200 thousand times or more 200VAC 0.75A, 240VAC 0.5A (COS $\phi$ =0.35) 200 thousand times or more 24VDC 1A, 100VDC 0.1A (L/R=7ms) 200 thousand times or more	200VAC 1.5A, 240VAC 1A (COS $\phi$ =0.7) 100 thousand times or more 200VAC 1A, 240VAC 0.5A (COS $\phi$ =0.35) 100 thousand times or more 24VDC 1A, 100VDC 0.1A (L/R=7ms) 100 thousand times or more	Δ	exchange intervals of the modules as Mechanical/ Electrical Life is cut to about half.
Maximum s frequency	witching	3600 times/hour	3600 times/hour	0	
Surge supp	ressor	Varistor (387 to 473V)	None	×	The varistor is not built in. <sup>*1</sup>
Common te arrangeme		8 points/common (common terminal: TB9,TB18)	8 points/common (common terminal: TB9,TB18)	0	
Operation i	ndicator	ON indication (LED)	ON indication (LED)	0	
Fuse		8A MF51NM8 or FGMA250V8A	None	×	The fuse is not built in. <sup>*2</sup>
Fuse blow	ndicator	None	None	0	
External supply	Voltage	24VDC±10% Ripple voltage 4Vp-p or less	24VDC±10% Ripple voltage 4Vp-p or less	0	
power	Current	150mA (24VDC TYP. all points ON)	90mA (24VDC TYP. all points ON)	0	
External co	nnection	20-point terminal block connector (M3×6 screws)	20-point terminal block connector (M3.5×7 screws)	×	
Applicable	wire size	0.75 to 2mm <sup>2</sup>	0.75 to 1.25mm <sup>2</sup>	×	Wiring must be changed.
Applicable solderless terminal		R1.25-3,R2-3, RAV1.25-3,RAV2-3	R1.25-3.5,R2-3.5, RAV1.25-3.5,RAV2-3.5	×	
Current cor	nsumption	0.115A (TYP. all points ON)	0.120A (TYP. all points ON)	Δ	Current capacity must be reviewed.
External dir	mensions	250(H)×37.5(W)×121(D)mm	130(H)×34.5(W)×93.6(D)mm	Δ	The dimensions are different.
Weight		0.47kg	0.25kg	0	

\*1 Connect a varistor to reduce external noise.

\*2 Mount a fuse on every external terminal to prevent external devices and modules from burning out upon load short circuit.

#### (7) Specifications comparisons between the AY11EEU and the A1SY10EU

Specifi	ication	AY11EEU	A1SY10EU	Compati- bility	Precautions for replacement
Number of opoints	output	16 points	16 points	0	
Isolation me	ethod	Photocoupler	Photocoupler	0	
Rated switc voltage/curr		24VDC 2A (resistive load)/point 24VAC 2A (COS $\phi$ =1)/point 8A/common	24VDC 2A (resistive load)/point 240VAC 2A (COS $\phi$ =1)/point 8A/common	0	
Minimum sv load	witching	5VDC 1mA	5VDC 1mA	0	
Maximum s voltage	witching	49.9VAC 74.9VDC	132VAC 125VDC	0	
Leakage cu OFF	rrent at	0.1mA (49.9VAC, 60Hz)	_	0	
Response	OFF→ON	10ms or less	10ms or less	0	
time	ON→OFF	12ms or less	12ms or less	0	
Mechanical	life	20 million times or more	20 million times or more	0	
		Rated switching voltage/current load 200 thousand times or more	Rated switching voltage/current load 100 thousand times or more	Δ	
Electrical life		24VAC 1.5A (COS $\phi$ =0.7) 200 thousand times or more 24VAC 0.75A (COS $\phi$ =0.35) 200 thousand times or more 24VDC 1A, 48VDC 0.1A (L/R=7ms) 200 thousand times or more	100VAC 2A, 120VAC 2A (COS $\phi$ =0.7) 200 thousand times or more 100VAC 2A, 120VAC 2A (COS $\phi$ =0.35) 100 thousand times or more 24VDC 1.5A, 100VDC 0.1A (L/R=7ms) 100 thousand times or more	Δ	Reduce the exchange intervals of the modules as Mechanical/Electrical Life is cut to about half.
Maximum s frequency	witching	3600 times/hour	3600 times/hour	0	
Surge supp	ressor	Varistor (387 to 473V)	None	×	The varistor is not built in. <sup>*1</sup>
Common te arrangemer		8 points/common (common terminal: TB9,TB18)	8 points/common (common terminal: TB9,TB18)	0	
Operation in	ndicator	ON indication (LED)	ON indication (LED)	0	
Fuse		8A MF51NM8 or FGMA250V8A	None	×	The fuse is not built in. <sup>*2</sup>
Fuse blow i	ndicator	None	None	0	
External supply	Voltage	$24$ VDC $\pm$ 10% Ripple voltage 4Vp-p or less	$24VDC \pm 10\%$ Ripple voltage 4Vp-p or less	0	
power	Current	150mA (24VDC TYP. all points ON)	90mA (24VDC TYP. all points ON)	0	
External co	nnection	20-point terminal block connector (M3×6 screws)	20-point terminal block connector (M3.5×7 screws)	×	
Applicable wire size		0.75 to 2mm <sup>2</sup>	0.75 to 1.25mm <sup>2</sup>	×	Wiring must be changed.
Applicable s terminal	solderless	R1.25-3,R2-3, RAV1.25-3,RAV2-3	RAV1.25-3.5	×	
Current con	sumption	0.115A (TYP. all points ON)	0.120A (TYP. all points ON)	Δ	Current capacity must be reviewed.
External dir	nensions	250(H)×37.5(W)×121(D)mm	130(H)×34.5(W)×93.6(D)mm	Δ	The dimensions are different.
Weight		0.47kg	0.25kg	0	

\*1 Connect a varistor to reduce external noise.

\*2 Mount a fuse on every external terminal to prevent external devices and modules from burning out upon load short circuit.

#### (8) Specifications comparisons between the AY11-UL and the A1SY10

Specification		AY11-UL	A1SY10	Compati- bility	Precautions for replacement
Number of opoints	output	16 points	16 points	0	
Isolation me	ethod	Photocoupler	Photocoupler	0	
Rated switc voltage/curr		24VDC 2A (resistive load)/point 240VAC 2A (COS $\phi$ =1)/point 8A/common	24VDC 2A (resistive load)/point 240VAC 2A (COS $\phi$ =1)/point 8A/common	0	
Minimum sv load	vitching	5VDC 1mA	5VDC 1mA	0	
Maximum s voltage	witching	264VAC 125VDC	264VAC 125VDC	0	
Leakage cu OFF	rrent at	0.1mA (200VAC, 60Hz)		0	
Response	OFF→ON	10ms or less	10ms or less	0	
time	ON→OFF	12ms or less	12ms or less	0	
Mechanical	life	20 million times or more	20 million times or more	0	
		Rated switching voltage/current load 200 thousand times or more	Rated switching voltage/current load 100 thousand times or more	Δ	
Electrical life		200VAC 1.5A, 240VAC 1A (COS $\phi$ =0.7) 200 thousand times or more 200VAC 0.75A, 240VAC 0.5A (COS $\phi$ =0.35) 200 thousand times or more 24VDC 1A, 100VDC 0.1A (L/R=7ms) 200 thousand times or more	200VAC 1.5A, 240VAC 1A (COS $\phi$ =0.7) 100 thousand times or more 200VAC 1A, 240VAC 0.5A (COS $\phi$ =0.35) 100 thousand times or more 24VDC 1A, 100VDC 0.1A (L/R=7ms) 100 thousand times or more	Δ	Reduce the exchange intervals of the modules as Mechanical/Electrical Life is cut to about half.
Maximum s frequency	witching	3600 times/hour	3600 times/hour	0	
Surge supp	ressor	Varistor (387 to 473V)	None	×	The varistor is not built in. <sup>*1</sup>
Relay socke	et	Yes	None	×	Replace the module itself when its relay has a failure.
Common te arrangemer		8 points/common (common terminal: TB9,TB18)	8 points/common (common terminal: TB9,TB18)	0	
Operation ir	ndicator	ON indication (LED)	ON indication (LED)	0	
External supply	Voltage	$24VDC \pm 10\%$ Ripple voltage 4Vp-p or less	$24VDC \pm 10\%$ Ripple voltage 4Vp-p or less	0	
power	Current	150mA (24VDC TYP. all points ON)	90mA (24VDC TYP. all points ON)	0	
External connection		20-point terminal block connector (M3.5×6 screws)	20-point terminal block connector (M3.5×7 screws)	×	
Applicable wire size		0.75 to 2mm <sup>2</sup>	0.75 to 1.25mm <sup>2</sup>	×	Wiring must be changed.
Applicable solderless terminal		R1.25-3.5,R2-3.5, RAV1.25-3.5,RAV2-3.5	R1.25-3.5,R2-3.5, RAV1.25-3.5,RAV2-3.5	0	J
Current consumption		0.12A (TYP. all points ON)	0.12A (TYP. all points ON)	0	
External din	nensions	250(H)×37.5(W)×121(D)mm	130(H)×34.5(W)×93.6(D)mm	Δ	The dimensions are different.
Weight		0.50kg	0.25kg	0	

# (9) Specifications comparisons between the AY13 and the A1SY10

$\bigcirc$ : Compatible. $\land$ :	Partial change required,	×:	Incompatible
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Specifi	ication	AY13	A1SY10	Compati- bility	Precautions for replacement
Number of o points	output	32 points	16 points	×	When seventeen or more points are used, use two of the A1SY10 modules.
Isolation me	ethod	Photocoupler	Photocoupler	0	
Rated switc voltage/curr		24VDC 2A (resistive load)/point 240VAC 2A (COS $\phi$ =1)/point 5A/common	24VDC 2A (resistive load)/point 240VAC 2A (COS $\phi$ =1)/point 8A/common	0	
Minimum sv load	vitching	5VDC 1mA	5VDC 1mA	0	
Maximum svoltage	witching	264VAC 125VDC	264VAC 125VDC	0	
Response	OFF→ON	10ms or less	10ms or less	0	
time	ON→OFF	12ms or less	12ms or less	0	
Mechanical	life	20 million times or more	20 million times or more	0	
		Rated switching voltage/current load 200 thousand times or more	Rated switching voltage/current load 100 thousand times or more	Δ	
Electrical life		200VAC 1.5A, 240VAC 1A (COS $\phi$ =0.7) 200 thousand times or more 200VAC 0.75A, 240VAC 0.5A (COS $\phi$ =0.35) 200 thousand times or more 24VDC 1A, 100VDC 0.1A (L/R=7ms) 200 thousand times or more	200VAC 1.5A, 240VAC 1A (COS $\phi$ =0.7) 100 thousand times or more 200VAC 1A, 240VAC 0.5A (COS $\phi$ =0.35) 100 thousand times or more 24VDC 1A, 100VDC 0.1A (L/R=7ms) 100 thousand times or more	Δ	Reduce the exchange intervals of the modules as Mechanical/Electrical Life is cut to about half.
Maximum so frequency	witching	3600 times/hour	3600 times/hour	0	
Common te arrangemer		8 points/common (common terminal: TB9,TB18,TB27,TB36)	8 points/common (common terminal: TB9,TB18)	0	
Operation in	ndicator	ON indication (LED)	ON indication (LED)	0	
External supply	Voltage	24VDC±10% Ripple voltage 4Vp-p or less	24VDC±10% Ripple voltage 4Vp-p or less	0	
power	Current	290mA (24VDC TYP. all points ON)	90mA (24VDC TYP. all points ON)	0	
External connection		38-point terminal block connector (M3×6 screws)	20-point terminal block connector (M3.5×7 screws)	×	
Applicable wire size		0.75 to 2mm <sup>2</sup>	0.75 to 1.25mm <sup>2</sup>	×	Wiring must be changed.
Applicable solderless terminal		R1.25-3,R2-3, RAV1.25-3,RAV2-3	R1.25-3.5,R2-3.5, RAV1.25-3.5,RAV2-3.5	×	changeu.
Current consumption		0.23A (TYP. all points ON)	0.12A (TYP. all points ON)	Δ	Review current capacity hen using the two A1SY10 modules.
External din	nensions	250(H)×37.5(W)×131(D)mm	130(H)×34.5(W)×93.6(D)mm	Δ	The dimensions are different.
Weight		0.59kg	0.25kg	0	

#### (10) Specifications comparisons between the AY13E and the A1SY10

 $\bigcirc$ : Compatible,  $\triangle$ : Partial change required,  $\times$ : Incompatible

Specifi	cation	AY13E	A1SY10	Compati- bility	Precautions for replacement	
Number of o	utput points	32 points	16 points	×	When seventeen or more points are used, use two of the A1SY10 modules.	
Isolation met	thod	Photocoupler	Photocoupler	0		
Rated switch current	ning voltage/	24VDC 2A (resistive load)/point 240VAC 2A (COS $\phi$ =1)/point 5A/common	24VDC 2A (resistive load)/point 240VAC 2A (COS $\phi$ =1)/point 8A/common	0		
Minimum sw	itching load	5VDC 1mA	5VDC 1mA	0		
Maximum sv voltage	vitching	250VAC 125VDC	264VAC 125VDC	0		
	OFF→ON	10ms or less	10ms or less	0		
time	ON→OFF	12ms or less	12ms or less	0		
Mechanical I	ife	20 million times or more	20 million times or more	0		
		Rated switching voltage/current load 200 thousand times or more	Rated switching voltage/current load 100 thousand times or more			
Electrical life		200VAC 1.5A, 240VAC 1A (COS $\phi$ =0.7) 200 thousand times or more 200VAC 0.75A, 240VAC 0.5A (COS $\phi$ =0.35) 200 thousand times or more 24VDC 1A, 100VDC 0.1A (L/R=7ms) 200 thousand times or more	200VAC 1.5A, 240VAC 1A (COS $\phi$ =0.7) 100 thousand times or more 200VAC 1A, 240VAC 0.5A (COS $\phi$ =0.35) 100 thousand times or more 24VDC 1A, 100VDC 0.1A (L/R=7ms) 100 thousand times or more	Δ	Reduce the exchange intervals of the modules as Mechanical/Electrical Life is cut to about half.	
Maximum sw frequency	vitching	3600 times/hour	3600 times/hour	0		
Surge suppr	essor	None	None	0		
Common ter arrangement	-	8 points/common (common terminal: TB9,TB18,TB27,TB36)	8 points/common (common terminal: TB9,TB18)	0		
Operation in	dicator	ON indication (LED)	ON indication (LED)	0		
Fuse		8A MF51NM8 or FGMA250V8A	None	×	The fuse is not built in. <sup>*1</sup>	
Fuse blow in	dicator	None	None	0		
External supply	Voltage	$24$ VDC $\pm$ 10% Ripple voltage 4Vp-p or less	$24VDC \pm 10\%$ Ripple voltage 4Vp-p or less	0		
power	Current	290mA (24VDC TYP. all points ON)	90mA (24VDC TYP. all points ON)	0		
External connection		38-point terminal block connector (M3×6 screws)	20-point terminal block connector (M3.5×7 screws)	×		
Applicable wire size		0.75 to 2mm <sup>2</sup>	0.75 to 1.25mm <sup>2</sup>	×	Wiring must be changed.	
Applicable solderless terminal		R1.25-3,R2-3, RAV1.25-3,RAV2-3	R1.25-3.5,R2-3.5, RAV1.25-3.5,RAV2-3.5	×		
Current cons	sumption	0.23A (TYP. all points ON)	0.12A (TYP. all points ON)		Review current capacity when using the two A1SY10 modules.	
External dim	ensions	250(H)×37.5(W)×131(D)mm	130(H)×34.5(W)×93.6(D)mm	Δ	The dimensions are different.	
Weight		0.60kg	0.25kg	0		

\*1 Mount a fuse on every external terminal to prevent external devices and modules from burning out upon load short circuit.

### (11) Specifications comparisons between the AY13EU and the A1SY10EU

Specifi	ication	AY13EU	A1SY10EU	Compati- bility	Precautions for replacement
Number of output points		32 points	16 points	×	When seventeen or more points are used, use two of the A1SY10EU modules.
Isolation me	ethod	Photocoupler	Photocoupler	0	
Rated switc voltage/curr	-	24VDC 2A (resistive load)/point 24VAC 2A (COS $\phi$ =1)/point 5A/common	24VDC 2A (resistive load)/point 240VAC 2A (COS $\phi$ =1)/point 8A/common	0	
Minimum sv load	vitching	5VDC 1mA	5VDC 1mA	0	
Maximum s voltage	witching	49.9VAC 74.9VDC	132VAC 125VDC	0	
Response	OFF→ON	10ms or less	10ms or less	0	
time	ON→OFF	12ms or less	12ms or less	0	
Mechanical	l life	20 million times or more	20 million times or more	0	
		Rated switching voltage/current load 200 thousand times or more	Rated switching voltage/current load 200 thousand times or more	0	
Electrical life		24VAC 1.5A (COS $\phi$ =0.7) 200 thousand times or more 24VAC 0.75A (COS $\phi$ =0.35) 200 thousand times or more 24VDC 1A, 48VDC 0.1A (L/R=7ms) 200 thousand times or more	100VAC 2A, 120VAC 2A (COS $\phi$ =0.7) 200 thousand times or more 100VAC 2A, 120VAC 2A (COS $\phi$ =0.35) 100 thousand times or more 24VDC 1.5A, 100VDC 0.1A (L/R=7ms) 100 thousand times or more	Δ	Reduce the exchange intervals of the modules as Mechanical/ Electrical Life is cut to about half.
Maximum s frequency	witching	3600 times/hour	3600 times/hour	0	
Common te arrangemer		8 points/common (common terminal: TB9,TB18,TB27,TB36)	8 points/common (common terminal: TB9,TB18)	0	
Operation in	ndicator	ON indication (LED)	ON indication (LED)	0	
External supply	Voltage	24VDC±10% Ripple voltage 4Vp-p or less	$24VDC \pm 10\%$ Ripple voltage 4Vp-p or less	0	
power	Current	290mA (24VDC TYP. all points ON)	90mA (24VDC TYP. all points ON)	0	
External connection		38-point terminal block connector (M3×6 screws)	20-point terminal block connector (M3.5×7 screws)	×	
Applicable wire size		0.75 to 2mm <sup>2</sup>	0.75 to 1.25mm <sup>2</sup>	×	Wiring must be changed.
Applicable solderless terminal		R1.25-3,R2-3, RAV1.25-3,RAV2-3	RAV1.25-3.5	×	
Current consumption		0.23A (TYP. all points ON)	0.12A (TYP. all points ON)	Δ	Review current capacity when using the two A1SY10EU modules.
External din	nensions	250(H)×37.5(W)×131(D)mm	130(H)×34.5(W)×93.6(D)mm	Δ	The dimensions are different.
Weight		0.59kg	0.25kg	0	

### (12) Specifications comparisons between the AY15EU and the A1SY14EU

 $\bigcirc$  : Compatible,  $\bigtriangleup$  : Partial change required, ×: Incompatible

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Specifi	ication	AY15EU	A1SY14EU	Compati- bility	Precautions for replacement
Number of o points	putput	24 points (32 points occupied)	12 points (16 points occupied)	×	When thirteen or more points are used, use two of the A1SY14EU modules.
Isolation me	thod	Photocoupler	Photocoupler	0	
Rated switch voltage/curre		24VDC 2A (resistive load)/point 240VAC 2A (COS $\phi$ =1)/point 8A/common	24VDC 2A (resistive load)/point 240VAC 2A (COS $\phi$ =1)/point 8A/common	0	
Minimum sw	itching load	5VDC 10mA	5VDC 10mA	0	
Maximum sv voltage	witching	264VAC 125VDC	264VAC 125VDC	0	
Response	OFF→ON	10ms or less	10ms or less	0	
time	ON→OFF	12ms or less	12ms or less	0	
Mechanical I	ife	20 million times or more	20 million times or more	0	
		Rated switching voltage/current load 100 thousand times or more	Rated switching voltage/current load 200 thousand times or more	0	
Electrical life		200VAC 2A, 240VAC 1.8A (COS $\phi$ =0.7) 200 thousand times or more 200VAC 1.1A, 240VAC 0.9A (COS $\phi$ =0.35) 200 thousand times or more 24VDC 1.1A, 100VDC 0.1A (L/R=7ms) 200 thousand times or more	200VAC 2A, 240VAC 1.8A (COS $\phi$ =0.7) 200 thousand times or more 200VAC 1.1A, 240VAC 0.9A (COS $\phi$ =0.35) 200 thousand times or more 24VDC 1.1A, 100VDC 0.1A (L/R=7ms) 200 thousand times or more	0	
Maximum sw frequency	witching	3600 times/hour	3600 times/hour	0	
Surge suppr	ressor	None	None	0	
Common ter arrangemen		8 points/common (common terminal: TB9,TB20,TB31)	4 points/common (common terminal: TB5,TB10,TB15)	0	
Operation in	dicator	ON indication (LED)	ON indication (LED)	0	
External supply	Voltage	$24VDC \pm 10\%$ Ripple voltage 4Vp-p or less (Must be SELV power supply)	24VDC±10% Ripple voltage 4Vp-p or less	0	
power	Current	220mA (24VDC TYP. all points ON) (Must be SELV power supply)	100mA (24VDC TYP. all points ON) (Must be SELV power supply)	0	
External connection		38-point terminal block connector (M3.5×6 screws)	20-point terminal block connector (M3.5×7 screws)	×	
Applicable wire size		0.75 to 2mm <sup>2</sup> (AWG14 to AWG19)	0.75 to 1.25mm <sup>2</sup> (AWG16 to AWG19)	×	Wiring must be changed.
Applicable solderless terminal		RAV1.25-3.5,RAV2-3.5	RAV1.25-3.5	×	
Dielectric withstand voltage		(AC external batch relay drive power supply. 5V internal circuit) 2830VAC rms/3cycles (altitude 2000m) (Relay-drive power supply, 5V internal circuit) 500VAC rms/3cycles (altitude 2000m)	(AC external batch relay drive power supply. 5V internal circuit) 2830VAC rms/3cycles (altitude 2000m) (Relay-drive power supply, 5V internal circuit) 500VAC rms/3cycles (altitude 2000m)	0	
Insulation re	esistance	10MΩ or more by insulation resistance tester	10MΩ or more by insulation resistance tester	0	

 $\bigcirc$  : Compatible,  $\bigtriangleup$  : Partial change required,  $\times$  : Incompatible

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Specification	AY15EU	A1SY14EU	Compati- bility	Precautions for replacement
Current consumption	0.15A (TYP. all points ON)	0.12A (TYP. all points ON)	Δ	Review current capacity when using two of the A1SY14EU modules.
External dimensions	250(H)×37.5(W)×131(D)mm	130(H)×34.5(W)×93.6(D)mm	Δ	The dimensions are different.
Weight	0.50kg	0.25kg	0	

# (13) Specifications comparisons between the AY22 and the A1SY22

Specifi	cation	AY22	A1SY22	Compati- bility	Precautions for replacement
Number of points	output	16 points	16 points	0	
Isolation me	ethod	Photocoupler	Photocoupler	0	
Rated load	voltage	100-240VAC 50/60Hz±5%	100-240VAC 50/60Hz±3Hz	0	
Maximum lo voltage	bad	264VAC	264VAC	0	
Maximum lo	oad current	2A/point, 3.3A/common	0.6A/point, 2.4A/common	×	Carefully select load for use since the maximum load current per point is lowered.
Minimum Ic current	ad voltage	24VAC 100mA 100VAC 10mA 240VAC 20mA	24VAC 100mA 100VAC 10mA 240VAC 20mA	0	
Maximum in current	nrush	40A 10ms or less 15A 100ms or less	20A 10ms or less 8A 100ms or less	Δ	The inrush current value differs.Use caution on selecting the load to use.
Leakage cu OFF	rrent at	1.5mA (At 120VAC, 60Hz) 3mA (At 240VAC, 60Hz)	1.5mA or less (At 120VAC, 60Hz) 3mA or less (At 240VAC, 60Hz)	0	
Maximum v drop at ON	oltage	1.5VAC or less (1 to 2A) 1.8VAC or less (0.2 to 1A) 5VAC or less (0.2A or less)	1.5VAC or less (0.1 to 0.6A) 1.8VAC or less (50 to 100mA) 2VAC or less (10 to 50mA)	0	
Response	OFF→ON	1ms or less	1ms or less	0	
time	ON→OFF	0.5 cycles + 1ms or less	0.5 cycles + 1ms or less	0	
Surge supp	ressor	CR absorber (0.022 μF+47Ω ) Varistor (387 to 473V)	CR absorber (0.01 $\mu$ F+47 $\Omega$ )	Δ	The varistor is not built in. <sup>*1</sup>
Common te arrangemer	nt	8 points/common (common terminal: TB9,TB18)	8 points/common (common terminal: TB9,TB18)	0	
Operation in	ndicator	ON indication (LED)	ON indication (LED)	0	
Fuse		7A fast blow fuse (1 fuse/common) HP-70k	5A fuse (1 fuse/common) Not replaceable	Δ	If a fast blow fuse is necessary, connect it outside.
Fuse blow i	ndicator	Yes (LED is turned ON when a fuse is blown. Signal is output to a PLC CPU.)	Yes (LED is turned ON when a fuse is blown. Signal is output to a PLC CPU.)	Δ	Error LED is also turned ON when the external supply power is OFF.
External	Voltage	—	100-240VAC (85 to 264VAC)	×	External supply
supply power	Current	_	2mA (TYP. 200VAC/common)	×	power is required.
External connection		20-point terminal block connector (M3×6 screws)	20-point terminal block connector (M3.5×7 screws)	×	Wiring must be
Applicable wire size		0.75 to 2mm <sup>2</sup>	0.75 to 1.25mm <sup>2</sup>	×	Wiring must be changed.
Applicable solderless terminal		R1.25-3,R2-3, RAV1.25-3,RAV2-3	R1.25-3.5,R2-3.5, RAV1.25-3.5,RAV2-3.5	×	
Current cor	sumption	0.305A (TYP. all points ON)	0.270A (TYP. all points ON)	0	
External dir	nensions	250(H)×37.5(W)×121(D)mm	130(H)×34.5(W)×93.6(D)mm	Δ	The dimensions are different.
Weight		0.71kg	0.24kg	0	

#### (14) Specifications comparisons between the AY23 and the A1SY22

 $\bigcirc$  : Compatible,  $\bigtriangleup$  : Partial change required,  $\times$  : Incompatible

Specifi	cation	AY23	A1SY22	Compati- bility	Precautions for replacement
Number of c points	putput	32 points	16 points	×	When seventeen or more points are used, use two of the A1SY22 modules.
Isolation me	ethod	Photocoupler	Photocoupler	0	
Rated load	voltage	100-240VAC 40 to 70Hz	100-240VAC 50/60Hz±3Hz	0	
Maximum lo	ad voltage	264VAC	264VAC	0	
Maximum Ic	ad current	0.6A/point, 2.4A/common (When placing next to the power supply module: 1.05A/common)	0.6A/point, 2.4A/common	0	
Minimum lo current	ad voltage	24VAC 100mA 100VAC 10mA 240VAC 10mA	24VAC 100mA 100VAC 10mA 240VAC 20mA	Δ	Carefully select load for use since the minimum load current is increased.
Maximum in current	ırush	20A 10ms or less 8A 100ms or less	20A 10ms or less 8A 100ms or less	0	
Leakage cu OFF	rrent at	1.5mA (At 120VAC 60Hz) 3mA (At 240VAC 60Hz)	1.5mA or less (At 120VAC 60Hz) 3mA or less (At 240VAC 60Hz)	0	
Maximum vo drop at ON	oltage	1.5VAC or less (100 to 600mA) 1.8VAC or less (50 to 100mA) 2VAC or less (10 to 50mA)	1.5VAC or less (0.1 to 0.6A) 1.8VAC or less (50 to 100mA) 2VAC or less (10 to 50mA)	0	
Response	OFF→ON	1ms or less	1ms or less	0	
time	ON→OFF	0.5 cycles + 1ms or less	0.5 cycles + 1ms or less	0	
Surge supp	ressor	CR absorber( $0.022 \mu\text{F+47}\Omega$ )	CR absorber(0.01 $\mu$ F+47 $\Omega$ )	0	
Common te arrangemen		8 points/common (common terminal: TB9,TB18,TB27,TB36)	8 points/common (common terminal: TB9,TB18)	0	
Operation in	ndicator	ON indication (LED)	ON indication (LED)	0	
Fuse		3.2A fast blow fuse (1 fuse/common) HP-32	5A fuse (1 fuse/common) Not replaceable	Δ	Connect the fast blow fuse to the external if necessary.
Fuse blow in	ndicator	Yes (LED is turned ON when a fuse is blown. Signal is output to a PLC CPU.)	Yes (LED is turned ON when a fuse is blown. Signal is output to a PLC CPU.)	Δ	Fuse blow error also occurs when the external supply power is OFF.
External supply	Voltage	_	100-240VAC (85 to 264VAC)	×	External supply
power	Current	_	2mA (TYP. 200VAC/common)	×	power is required.
External connection		38-point terminal block connector (M3×6 screws)	20-point terminal block connector (M3.5×7 screws)	×	
Applicable wire size		0.75 to 2mm <sup>2</sup>	0.75 to 1.25mm <sup>2</sup>	×	Wiring must be changed.
Applicable solderless terminal		R1.25-3,R2-3, RAV1.25-3,RAV2-3	R1.25-3.5,R2-3.5, RAV1.25-3.5,RAV2-3.5	×	
Current con	sumption	0.59A (TYP. all points ON)	0.27A (TYP. all points ON)	0	
External din	nensions	250(H)×37.5(W)×131(D)mm	130(H)×34.5(W)×93.6(D)mm	Δ	The dimensions are different.
Weight		0.55kg	0.24kg	0	

#### (15) Specifications comparisons between the AY40 and the A1SY40

			⊖: Compatible, ∆ ∶Partia	I change ree	quired, ×: Incompatible
Specifi	cation	AY40	A1SY40	Compati- bility	Precautions for replacement
Number of or points	output	16 points	16 points	0	
Isolation me	ethod	Photocoupler	Photocoupler	0	
Rated load	voltage	12/24VDC	12/24VDC	0	
Operating lo voltage rang		10.2 to 40VDC	10.2 to 30VDC	Δ	Voltages exceeding 30VDC can not be applied.
Maximum lo current	ad	0.1A/point, 0.8A/common	0.1A/point, 0.8A/common	0	
Maximum ir current	rush	0.4A	0.4A 10ms or less	0	
Leakage cu OFF	rrent at	0.1mA or less	0.1mA or less	0	
Maximum v drop at ON	oltage	2.5VDC (0.1A) 1.75VDC (5mA) 1.7VDC (1mA)	1.0VDC (TYP.) 0.1A 2.5VDC (MAX.) 0.1A	0	
Response	OFF→ON	2ms or less	2ms or less	0	
time	ON→OFF	2ms or less (resistive load)	2ms or less (resistive load)	0	
Surge supp	ressor	Clamp diode	Zener diode	0	
Common te arrangemer		8 points/common (common terminal: TB10,TB20)	8 points/common (common terminal: TB10,TB20)	0	
Operation ir	ndicator	ON indication (LED)	ON indication (LED)	0	
External supply	Voltage	12/24VDC (10.2 to 40VDC)	12/24VDC (10.2 to 30VDC)	Δ	Voltages exceeding 30VDC cannot be applied.
power	Current	8mA (TYP. 24VDC/common)	8mA (TYP. 24VDC/common)	0	
External co	nnection	20-point terminal block connector (M3×6 screws)	20-point terminal block connector (M3.5×7 screws)	×	
Applicable wire size		0.75 to 2mm <sup>2</sup>	0.75 to 1.25mm <sup>2</sup>	×	Wiring must be changed.
Applicable solderless terminal		R1.25-3,R2-3, RAV1.25-3,RAV2-3	R1.25-3.5,R2-3.5, RAV1.25-3.5,RAV2-3.5	×	
Current con	sumption	0.115A (TYP. all points ON)	0.270A (TYP. all points ON)	Δ	Current capacity must be reviewed.
External din	nensions	250(H)×37.5(W)×121(D)mm	130(H)×34.5(W)×93.6(D)mm	Δ	The dimensions are different.
Weight		0.36kg	0.19kg	0	

#### (16) Specifications comparisons between the AY40A and the A1SY68A

		$\bigcirc$ : Compatible, $\triangle$ : Partial change required, ×: Incompatible				
Specifi	cation	AY40A	A1SY68A	Compati- bility	Precautions for replacement	
Number of c points	output	16 points	8 points (16 points occupied)	×	When nine or more points are used, use two of the A1SY68A modules.	
Isolation me	ethod	Photocoupler	Photocoupler	0		
Rated load	voltage	12/24VDC	5/12/24/48VDC	0		
Operating lo		10.2 to 30VDC (Max. applied voltage)	4.5 to 52.8VDC	0		
Maximum lo current	bad	0.3A/point	2A/point	0		
Maximum in current		1A 100ms or less	8A 10ms or less	0		
Leakage cu OFF	rrent at	0.1mA or less	0.1mA or less	0		
Maximum vo drop at ON	oltage	1.5VDC (50mA to 0.3A) 1.0VDC (50mA or less)	0.4VDC (MAX.) 2A	0		
Response	OFF→ON	2ms or less	3ms or less	Δ	The response times differ.	
time	ON→OFF	2ms or less (resistive load)	10ms or less (resistive load)	Δ		
Surge supp	ressor	Surge suppression diode	Zener diode	0		
Common te arrangemen		Not provided (all points independent)	Not provided (all points independent)	0		
Operation ir	ndicator	ON indication (LED)	ON indication (LED)	0		
External cor	nnection	38-point terminal block connector (M3×6 screws)	20-point terminal block connector (M3.5×7 screws)	×		
Applicable wire size		0.75 to 2mm <sup>2</sup>	0.75 to 1.25mm <sup>2</sup>	×	Wiring must be changed.	
Applicable solderless terminal		R1.25-3,R2-3, RAV1.25-3,RAV2-3	R1.25-3.5,R2-3.5, RAV1.25-3.5,RAV2-3.5	×		
Current con	sumption	0.19A (TYP. all points ON)	0.11A (TYP. all points ON)	Δ	Review current capacity when using the two A1SY68 modules.	
External din	nensions	250(H)×37.5(W)×131(D)mm	130(H)×34.5(W)×93.6(D)mm	Δ	The dimensions are different.	
Weight		0.42kg	0.20kg	0		

#### (17) Specifications comparisons between the AY41 and the A1SY41

 $\bigcirc$ : Compatible,  $\triangle$ : Partial change required, ×: Incompatible

Specifi	cation	AY41	A1SY41	Compati- bility	Precautions for replacement
Number of o points	output	32 points	32 points	0	
Isolation me	ethod	Photocoupler	Photocoupler	0	
Rated load	voltage	12/24VDC	12/24VDC	0	
Operating lo		10.2 to 40VDC	10.2 to 30VDC	Δ	Voltages exceeding 30VDC cannot be applied.
Maximum Ic	oad current	0.1A/point, 1.6A/common	0.1A/point, 2A/common	0	
Maximum ir current	nrush	0.4A	0.4A 10ms or less	0	
Leakage cu OFF	rrent at	0.1mA or less	0.1mA or less	0	
Maximum v drop at ON	oltage	2.5VDC (0.1A) 1.75VDC (5mA) 1.7VDC (1mA)	1.0VDC (TYP.) 0.1A 2.5VDC (MAX.) 0.1A	0	
Response	OFF→ON	2ms or less	2ms or less	0	
time	ON→OFF	2ms or less (resistive load)	2ms or less (resistive load)	0	
Surge supp	ressor	Clamp diode	Zener diode	0	
Common te arrangemer		16 points/common (common terminal: TB18,TB36)	32 points/common (common terminal: A1,A2)	Δ	As 2 commons are reduced to 1,wiring a different voltage for each common is not possible.
Operation in	ndicator	ON indication (LED)	ON indication (LED)	0	
External supply	Voltage	12/24VDC (10.2 to 40VDC)	12/24VDC (10.2 to 30VDC)	Δ	Voltages exceeding 30VDC cannot be applied.
power	Current	20mA (TYP. 24VDC/common)	8mA (TYP. 24VDC/common)	0	
External connection		38-point terminal block connector (M3×6 screws)	40-pin connector (accessory)	×	
Applicable wire size		0.75 to 2mm <sup>2</sup>	0.3mm <sup>2</sup>	×	Wiring must be changed. <sup>*1</sup>
Applicable solderless terminal		R1.25-3,R2-3, RAV1.25-3,RAV2-3	_	×	
Current con	sumption	0.23A (TYP. all points ON)	0.50A (TYP. all points ON)	Δ	Current capacity must be reviewed.
External din	nensions	250(H)×37.5(W)×131(D)mm	130(H)×34.5(W)×93.6(D)mm	Δ	
Weight		0.44kg	0.21kg	0	

\*1 By using connectors/terminal block converter modules(A6TBXY36, etc.), conversion to the terminal block is possible.

#### (18) Specifications comparisons between the AY41-UL and the A1SY41

O: Compatible. ∧	: Partial change required,	×:	Incompatible
	andai onango roquirou,		moompaasio

Specifi	cation	AY41-UL	A1SY41	Compati- bility	Precautions for replacement
Number of output points		32 points	32 points	0	
Isolation me	ethod	Photocoupler	Photocoupler	0	
Rated load	voltage	12/24VDC	12/24VDC	0	
Operating lo		10.2 to 40VDC	10.2 to 30VDC	Δ	Voltages exceeding 30VDC cannot be applied.
Maximum lo	bad current	0.1A/point, 1.6A/common	0.1A/point, 2A/common	0	
Maximum ir current	nrush	0.4A	0.4A 10ms or less	0	
Leakage cu OFF	rrent at	0.1mA or less	0.1mA or less	0	
Maximum v drop at ON	oltage	2.5VDC (0.1A) 1.75VDC (5mA) 1.7VDC (1mA)	1.0VDC (TYP.) 0.1A 2.5VDC (MAX.) 0.1A	0	
Response	OFF→ON	2ms or less	2ms or less	0	
time	ON→OFF	2ms or less (resistive load)	2ms or less (resistive load)	0	
Surge supp	ressor	Clamp diode	Zener diode	0	
Common te arrangemer		16 points/common (common terminal: TB18,TB36)	32 points/common (common terminal: A1,A2)	Δ	As 2 commons are reduced to 1,wiring a different voltage for each common is not possible.
Operation ir	ndicator	ON indication (LED)	ON indication (LED)	0	
External supply	Voltage	12/24VDC (10.2 to 40VDC)	12/24VDC (10.2 to 30VDC)	Δ	Voltages exceeding 30VDC cannot be applied.
power	Current	20mA (TYP. 24VDC/common)	8mA (TYP. 24VDC/common)	0	
External connection		38-point terminal block connector (M3.5×6 screws)	40-pin connector (accessory)	×	
Applicable wire size		0.75 to 2mm <sup>2</sup>	0.3mm <sup>2</sup>	×	Wiring must be changed. <sup>*1</sup>
Applicable solderless terminal		R1.25-3.5, R2-3, RAV1.25-3.5,RAV2-3.5	_	×	
Current con	sumption	0.23A (TYP. all points ON)	0.50A (TYP. all points ON)	Δ	Current capacity must be reviewed.
External dir	nensions	250(H)×37.5(W)×131(D)mm	130(H)×34.5(W)×93.6(D)mm	Δ	
Weight		0.44kg	0.21kg	0	

\*1 By using connectors/terminal block converter modules(A6TBXY36, etc.), conversion to the terminal block is possible.

#### (19) Specifications comparisons between the AY42 and the A1SY42P

			$\bigcirc$ : Compatible, $\triangle$ : Partial	change re	quired, ×: Incompatible
Specific	cation	AY42	A1SY42P	Compati- bility	Precautions for replacement
Number of or points	output	64 points	64 points	0	
Isolation me	ethod	Photocoupler	Photocoupler	0	
Rated load	voltage	12/24VDC	12/24VDC	0	
Operating lo		10.2 to 40VDC	10.2 to 30VDC	Δ	Voltages exceeding 30VDC cannot be applied.
Maximum Ic current	bad	0.1A/point, 2A/common (When placing next to the power supply module: 1.6A/common)	0.1A/point, 2A/common	0	
Maximum ir current	nrush	0.4A	0.7A 10ms or less	0	
Leakage cu OFF	rrent at	0.1mA or less	0.1mA or less	0	
Maximum v drop at ON	oltage	2.5VDC (0.1A) 1.75VDC (5mA) 1.7VDC (1mA)	0.1VDC (TYP.) 0.1A 0.2VDC (MAX.) 0.1A	0	
Response	OFF→ON	2ms or less	1ms or less	0	
time	ON→OFF	2ms or less (resistive load)	1ms or less (rated load, resistance load)	0	
Surge supp	ressor	Clamp diode	Zener diode	0	
Common te arrangemer	-	32 points/common (common terminal: 1A1,1A2,2A1,2A2)	32 points/common (common terminal: 1A1,1A2,2A1,2A2)	0	
Operation ir	ndicator	ON indication (LED) 32 point switch-over using a switch	ON indication (LED) 32 point switch-over using a switch	0	
External supply	Voltage	12/24VDC (10.2 to 40VDC)	12/24VDC (10.2 to 30VDC)	Δ	Voltages exceeding 30VDC cannot be applied.
power	Current	40mA (TYP. 24VDC/common)	14mA (At 24VDC/common)	0	
External cor	nnection	40-pin connector (with solder) $\times 2$	40-pin connector (accessory) × 2	0	
Applicable wire size		0.3mm <sup>2</sup>	0.3mm <sup>2</sup>	0	
Accessory		External wiring connectors × 2	External wiring connectors × 2	0	
Current con	sumption	0.34A (TYP. all points ON)	0.17A (TYP. all points ON)	0	
External din	nensions	250(H)×37.5(W)×106(D)mm	130(H)×34.5(W)×93.6(D)mm	Δ	
Weight		0.50kg	0.17kg	0	

#### (20) Specifications comparisons between the AY42-S1 and the A1SY42P

		$\bigcirc$ : Compatible, $\triangle$ : Partial change required, ×: Incompatible					
Specific	cation	AY42-S1	A1SY42P	Compati- bility	Precautions for replacement		
Number of o	output	64 points	64 points	0			
Isolation me	ethod	Photocoupler	Photocoupler	0			
Rated load	voltage	12/24VDC	12/24VDC	0			
Operating lo voltage rang		10.2 to 40VDC	10.2 to 30VDC		Voltages exceeding 30VDC cannot be applied.		
Maximum Ic current	bad	0.1A/point, 2A/common (When placing next to the power supply module: 1.6A/common)	0.1A/point, 2A/common	0			
Maximum ir current	nrush	0.4A	0.7A 10ms or less	0			
Leakage cu OFF	rrent at	0.1mA or less	0.1mA or less	0			
Maximum v drop at ON	oltage	2.5VDC (0.1A) 1.75VDC (5mA) 1.7VDC (1mA)	0.1VDC (TYP.) 0.1A 0.2VDC (MAX.) 0.1A	0			
Response	OFF→ON	0.1ms or less	1ms or less	Δ	The response times		
41.000	ON→OFF	0.3ms or less (resistive load)	1ms or less (rated load, resistance load)	Δ	differ.		
Surge supp	ressor	Clamp diode	Zener diode	0			
Common te arrangemer		32 points/common (common terminal: 1A1,1A2,2A1,2A2)	32 points/common (common terminal: 1A1,1A2,2A1,2A2)	0			
Operation ir	ndicator	ON indication (LED) 32 point switch-over using a switch	ON indication (LED) 32 point switch-over using a switch	0			
External supply	Voltage	12/24VDC (10.2 to 40VDC)	12/24VDC (10.2 to 30VDC)		Voltages exceeding 30VDC cannot be applied.		
power	Current	40mA (TYP. 24VDC/common)	14mA (At 24VDC/common)	0			
External cor	nnection	40-pin connector (with solder) $\times 2$	40-pin connector (accessory) × 2	0			
Applicable wire size		0.3mm <sup>2</sup>	0.3mm <sup>2</sup>	0			
Accessory		External wiring connectors × 2	External wiring connectors × 2	0			
Current con	sumption	0.29A (TYP. all points ON)	0.17A (TYP. all points ON)	0			
External din	nensions	250(H)×37.5(W)×106(D)mm	130(H)×34.5(W)×93.6(D)mm	Δ			
Weight		0.50kg	0.17kg	0			

#### (21) Specifications comparisons between the AY42-S3 and the A1SY42P

			$\bigcirc$ : Compatible, $\triangle$ : Partial	l change re	quired, ×: Incompatible
Specifi	cation	AY42-S3	A1SY42P	Compati- bility	Precautions for replacement
Number of opoints	output	64 points	64 points	0	
Isolation me	ethod	Photocoupler	Photocoupler	0	
Rated load	voltage	12/24VDC	12/24VDC	0	
Operating lover the second sec		10.2 to 40VDC	10.2 to 30VDC	Δ	Voltages exceeding 30VDC cannot be applied.
Maximum lo current	bad	0.1A/point, 2A/common (When placing next to the power supply module: 1.6A/common)	0.1A/point, 2A/common	0	
Maximum ir current	nrush	0.4A/point 3.5A/fuse	0.7A 10ms or less	0	
Leakage cu OFF	rrent at	0.1mA or less	0.1mA or less	0	
Maximum v drop at ON	oltage	2.5VDC (0.1A) 1.75VDC (5mA) 1.7VDC (1mA)	0.1VDC (TYP.) 0.1A 0.2VDC (MAX.) 0.1A	0	
Pesnonse	OFF→ON	2ms or less	1ms or less	0	
Response time	ON→OFF	2ms or less (resistive load)	1ms or less (rated load, resistance load)	0	
Surge supp	ressor	Clamp diode	Zener diode	0	
Common te arrangemer		32 points/common (common terminal: 1A1,1A2,2A1,2A2)	32 points/common (common terminal: 1A1,1A2,2A1,2A2)	0	
Operation in	ndicator	ON indication (LED) 32 point switch-over using a switch	ON indication (LED) 32 point switch-over using a switch	0	
Fuse		1.6A normal fuse (2 fuses/common)	Not equipped	Δ	The fuse-equivalent short circuit protection function is incorporated.
Fuse blow i	ndicator	Yes	_	×	Fuse blown is not displayed since the A1SY42P does not have fuses.
External supply	Voltage	12/24VDC (10.2 to 40VDC)	12/24VDC (10.2 to 30VDC)	Δ	Voltages exceeding 30VDC cannot be applied.
power	Current	40mA (TYP. 24VDC/common)	14mA (At 24VDC/common)	0	
External connection		40-pin connector (with solder) $\times 2$	40-pin connector (included) × 2	0	
Applicable wire size		0.3mm <sup>2</sup>	0.3mm <sup>2</sup>	0	
Accessory		External wiring connectors × 2	External wiring connectors × 2	0	
Current cor	sumption	0.29A (TYP. all points ON)	0.17A (TYP. all points ON)	0	
External dir	nensions	250(H)×37.5(W)×106(D)mm	130(H)×34.5(W)×93.6(D)mm	Δ	
Weight		0.50kg	0.17kg	0	

#### (22) Specifications comparisons between the AY42-S4 and the A1SY42P

			tial change required, ×: Incompatible		
Specifi	cation	AY42-S4	A1SY42P	Compati- bility	Precautions for replacement
Number of opoints	output	64 points	64 points	0	
Isolation me	ethod	Photocoupler	Photocoupler	0	
Rated load	voltage	12/24VDC	12/24VDC	0	
Operating lo		10.2 to 30VDC	10.2 to 30VDC	0	
Maximum Io current	bad	0.1A/point, 1.92A/common	0.1A/point, 2A/common	0	
Maximum ir current	nrush	0.4A 10ms or less	0.7A 10ms or less	0	
Leakage cu OFF	rrent at	0.1mA or less	0.1mA or less	0	
Maximum v drop at ON	oltage	2.5VDC (MAX.) 0.1A 1.0VDC (TYP.) 0.1A	0.1VDC (TYP.) 0.1A 0.2VDC (MAX.) 0.1A	0	
Response	OFF→ON	2ms or less	1ms or less	0	
time	ON→OFF	2ms or less (resistive load)	1ms or less (rated load, resistance load)	0	
Surge supp	ressor	Photocoupler build-in zener diode	Zener diode	0	
Common te arrangemer	-	32 points/common (common terminal: 1A1,1A2,2A1,2A2)	32 points/common (common terminal: 1A1,1A2,2A1,2A2)	0	
Operation ir	ndicator	ON indication (LED) 32 point switch-over using a switch	ON indication (LED) 32 point switch-over using a switch	0	
External	Voltage	_	12/24VDC (10.2 to 30VDC)	×	External supply power
supply power	Current	—	14mA (At 24VDC/common)	×	is required.
External co	nnection	40-pin connector (with solder) × 2	40-pin connector × 2 (included)	0	
Applicable wire size		0.3mm <sup>2</sup>	0.3mm <sup>2</sup>	0	
Accessory		External wiring connectors × 2	External wiring connectors × 2	0	
Current consumption		0.50A (TYP. 60% or less simultaneously ON)	0.17A (TYP. all points ON)	0	
External dir	nensions	250(H)×37.5(W)×106(D)mm	130(H)×34.5(W)×93.6(D)mm	Δ	
Weight		0.44kg	0.17kg	0	

#### (23) Specifications comparisons between the AY50 and the A1SY50

	-		$\bigcirc$ : Compatible, $\triangle$ : Partial	change re	quired, ×: Incompatible
Specifi	cation	AY50	A1SY50	Compati- bility	Precautions for replacement
Number of opoints	output	16 points	16 points	0	
Isolation me	ethod	Photocoupler	Photocoupler	0	
Rated load	voltage	12/24VDC	12/24VDC	0	
Operating lo voltage rang		10.2 to 30VDC	10.2 to 30VDC	0	
Maximum lo current	bad	0.5A/point, 2A/common	0.5A/point, 2A/common	0	
Maximum ir current	nrush	7A 10ms or less 3.5A 100ms or less	4A 10ms or less	Δ	The inrush current value differs.Use caution on selecting the load to use.
Leakage cu OFF	rrent at	0.1mA or less	0.1mA or less	0	
Maximum v drop at ON	oltage	0.9VDC (TYP.) 0.5A 1.5VDC (MAX.) 0.5A	0.9VDC (TYP.) 0.5A 1.5VDC (MAX.) 0.5A	0	
Response	OFF→ON	2ms or less	2ms or less	0	
time	ON→OFF	2ms or less (resistive load)	2ms or less (resistive load)	0	
Surge supp	ressor	Varistor (52 to 62V)	Zener diode	0	
Common te arrangemer		8 points/common (common terminal: TB10,TB20)	8 points/common (common terminal: TB10,TB20)	0	
Operation in	ndicator	ON indication (LED)	ON indication (LED)	0	
Fuse		2A fast blow fuse(1 fuse/common)	3.2A (not replaceable) (fuse blow capacity: 50A)		Connect the fast blow fuse to the external if necessary.
Fuse blow i	ndicator	Yes (LED is turned ON when a fuse is blown. Signal is output to a PLC CPU.)	Yes (When a fuse is blown, LED indicates and signal is output to CPU.)	Δ	Fuse blow error also occurs when the external supply power is OFF.
External supply	Voltage	12/24VDC (10.2 to 30VDC)	12/24VDC (10.2 to 30VDC)	0	
power	Current	65mA (TYP. 24VDC/common)	60mA (TYP. 24VDC/common)	0	
External connection		20-point terminal block connector (M3×6 screws)	20-point terminal block connector (M3.5×7 screws)	×	
Applicable wire size		0.75 to 2mm <sup>2</sup>	0.75 to 1.25mm <sup>2</sup>	×	Wiring must be changed.
Applicable solderless terminal		R1.25-3,R2-3, RAV1.25-3,RAV2-3	R1.25-3.5,R2-3.5, RAV1.25-3.5,RAV2-3.5	×	changeu.
Current con	sumption	0.115A (TYP. all points ON)	0.120A (TYP. all points ON)	Δ	Current capacity must be reviewed.
External din	nensions	250(H)×37.5(W)×121(D)mm	130(H)×34.5(W)×93.6(D)mm		The dimensions are different.
Weight		0.42kg	0.20kg	0	

### (24) Specifications comparisons between the AY51 and the A1SY50

Specifi	cation	AY51	A1SY50	Compati- bility	Precautions for replacement
Number of opoints	output	32 points	16 points	×	When seventeen or more points are used, use two of the A1SY50 modules.
Isolation me	ethod	Photocoupler	Photocoupler	0	
Rated load	voltage	12/24VDC	12/24VDC	0	
Operating lo voltage rang		10.2 to 30VDC	10.2 to 30VDC	0	
Maximum lo current	bad	0.5A/point, 4A/common (When placing next to the power supply module: 3.3A/common)	0.5A/point, 2A/common	0	
Maximum ir current	nrush	4A 10ms or less	4A 10ms or less	0	
Leakage cu OFF	rrent at	0.1mA or less	0.1mA or less	0	
Maximum v drop at ON	oltage	0.9VDC (TYP.) 0.5A 1.5VDC (MAX.) 0.5A	0.9VDC (TYP.) 0.5A 1.5VDC (MAX.) 0.5A	0	
Response	OFF→ON	2ms or less	2ms or less	0	
time	ON→OFF	2ms or less (resistive load)	2ms or less (resistive load)	0	
Surge supp	ressor	Varistor (52 to 62V)	Zener diode	0	
Common te arrangemer		16 points/common (common terminal: TB18,TB36)	8 points/common (common terminal: TB10,TB20)	0	
Operation in	ndicator	ON indication (LED)	ON indication (LED)	0	
Fuse		None	3.2A (not replaceable) (fuse blow capacity: 50A)	0	
Fuse blow i	ndicator	—	Yes (When the fuse is blown, LED indicates and signal is output to CPU.)	Δ	Fuse blow error also occurs when the external supply power is OFF.
External	Voltage	12/24VDC (10.2 to 30VDC)	12/24VDC (10.2 to 30VDC)	0	
supply power	Current	50mA (TYP. 24VDC/common)	60mA (TYP. 24VDC/common)	0	
External co	nnection	38-point terminal block connector (M3×6 screws)	20-point terminal block connector (M3.5×7 screws)	×	
Applicable wire size		0.75 to 2mm <sup>2</sup>	0.75 to 1.25mm <sup>2</sup>	×	Wiring must be changed.
Applicable solderless terminal		R1.25-3,R2-3, RAV1.25-3,RAV2-3	R1.25-3.5,R2-3.5, RAV1.25-3.5,RAV2-3.5	×	
Current consumption		0.230A (TYP. all points ON)	0.120A (TYP. all points ON)	Δ	Review current capacity when using the two A1SY50 modules.
External dir	nensions	250(H)×37.5(W)×131(D)mm	130(H)×34.5(W)×93.6(D)mm	Δ	The dimensions are different.
Weight		0.53kg	0.20kg	0	

### (25) Specifications comparisons between the AY51-S1 and the A1SY50

Specifi	cation	AY51-S1	A1SY50	Compati- bility	Precautions for replacement
Number of opoints	output	32 points	16 points	×	When seventeen or more points are used, use two of the A1SY50 modules.
Isolation me	ethod	Photocoupler	Photocoupler	0	
Rated load	voltage	12/24VDC	12/24VDC	0	
Operating lo		10.2 to 30VDC	10.2 to 30VDC	0	
Maximum lo current	bad	0.3A/point, 2A/common (1A/fuse common)	0.5A/point, 2A/common	0	
Maximum ir current	nrush	3A 10ms or less	4A 10ms or less	0	
Leakage cu OFF	rrent at	0.1mA or less	0.1mA or less	0	
Maximum v drop at ON	oltage	1VDC (TYP.) 0.3A 1.5VDC (MAX.) 0.3A	0.9VDC (TYP.) 0.5A 1.5VDC (MAX.) 0.5A	0	
Response	OFF→ON	2ms or less	2ms or less	0	
time ON→OFF		2ms or less (resistive load)	2ms or less (resistive load)	0	
Surge supp	ressor	Transistor built-in zener diode	Zener diode	0	
Common terminal arrangement		16 points/common (common terminal: TB18,TB36) 8 points/fuse common	8 points/common (common terminal: TB10,TB20)	0	
Operation in	ndicator	ON indication (LED)	ON indication (LED)	0	
Fuse		1A fast blow fuse (2 fuses/common in 8 point units) MP-10	3.2A (not replaceable) (fuse blow capacity: 50A)	Δ	Connect the fast blow fuse to the external if necessary.
Fuse blow i	ndicator	Yes (LED is turned ON when a fuse is blown. Signal is output to a PLC CPU.)	Yes (When a fuse is blown, LED indicates and signal is output to CPU.)	Δ	Fuse blow error also occurs when the external supply power is OFF.
External	Voltage	12/24VDC (10.2 to 30VDC)	12/24VDC (10.2 to 30VDC)	0	
supply power	Current	100mA (TYP. 24VDC/common)	60mA (TYP. 24VDC/common)	0	
External connection		38-point terminal block connector (M3×6 screws)	20-point terminal block connector (M3.5×7 screws)	×	
Applicable wire size		0.75 to 2mm <sup>2</sup>	0.75 to 1.25mm <sup>2</sup>	×	Wiring must be changed.
Applicable solderless terminal		R1.25-3,R2-3, RAV1.25-3,RAV2-3	R1.25-3.5,R2-3.5, RAV1.25-3.5,RAV2-3.5	×	
Current cor	sumption	0.310A (TYP. all points ON)	0.120A (TYP. all points ON)	0	
External dir	nensions	250(H)×37.5(W)×131(D)mm	130(H)×34.5(W)×93.6(D)mm	Δ	The dimensions are different.
Weight		0.55kg	0.20kg	0	

### (26) Specifications comparisons between the AY51-UL and the A1SY50

O: Compatible A :	Partial change required	$\sim$ .	Incompatible
$\bigcirc$ . Companye, $\triangle$ .	Partial change required,	<u>^.</u>	Incompatible

Specific	cation	AY51-UL	A1SY50	Compati- bility	Precautions for replacement
Number of o points	output	32 points	16 points	×	When seventeen or more points are used, use two of the A1SY50 modules.
Isolation me	ethod	Photocoupler	Photocoupler	0	
Rated load	voltage	12/24VDC	12/24VDC	0	
Operating lo voltage rang		10.2 to 30VDC	10.2 to 30VDC	0	
Maximum Ic current	bad	0.5A/point, 4A/common (When placing next to the power supply module: 3.3A/common)	0.5A/point, 2A/common	0	
Maximum ir current	nrush	0.4A 10ms or less	4A 10ms or less	0	
Leakage cu OFF	rrent at	0.1mA or less	0.1mA or less	0	
Maximum v drop at ON	oltage	0.9VDC (TYP.) 0.5A 1.5VDC (MAX.) 0.5A	0.9VDC (TYP.) 0.5A 1.5VDC (MAX.) 0.5A	0	
Response	OFF→ON	2ms or less	2ms or less	0	
time ON→OFF		2ms or less (resistive load)	2ms or less (resistive load)	0	
Surge suppressor		Varistor (52 to 62V)	Zener diode	0	
Common terminal arrangement		16 points/common (common terminal: TB18,TB36)	8 points/common (common terminal: TB10,TB20)	0	
Operation ir	ndicator	ON indication (LED)	ON indication (LED)	0	
Fuse		None	3.2A (not replaceable) (fuse blow capacity: 50A)	0	
Fuse blow i	ndicator	None	Yes (When a fuse is blown, LED indicates and signal is output to CPU.)	0	Fuse blow error also occurs when the external supply power is OFF.
External supply	Voltage	12/24VDC (10.2 to 30VDC)	12/24VDC (10.2 to 30VDC)	0	
power	Current	50mA (TYP. 24VDC/common)	60mA (TYP. 24VDC/common)	0	
External cor	nnection	38-point terminal block connector (M3.5×6 screws)	20-point terminal block connector (M3.5×7 screws)	×	
Applicable v	vire size	0.75 to 2mm <sup>2</sup>	0.75 to 1.25mm <sup>2</sup>	×	Wiring must be changed.
Applicable solderless terminal		R1.25-3.5,R2-3.5, RAV1.25-3.5,RAV2-3.5	R1.25-3.5,R2-3.5, RAV1.25-3.5,RAV2-3.5	0	
Current con	sumption	0.230A (TYP. all points ON)	0.120A (TYP. all points ON)		Review current capacity hen using the two A1SY50 modules.
External din	nensions	250(H)×37.5(W)×131(D)mm	130(H)×34.5(W)×93.6(D)mm		The dimensions are different.
Weight		0.53kg	0.20kg	0	

#### (27) Specifications comparisons between the AY60 and the A1SY60

			⊖: Compatible, △ : Partia	I change re	equired, ×: Incompatible
Specific	cation	AY60	A1SY60	Compati- bility	Precautions for replacement
Number of c points	output	16 points	16 points	0	
solation me	ethod	Photocoupler	Photocoupler	0	
Rated load	voltage	24VDC (12/48V)	24VDC	Δ	Voltages exceeding 26.4VDC cannot be applied.
Operating lo		21.6 to 26.4VDC (10.2 to 56VDC)	21.6 to 26.4VDC	Δ	Voltages exceeding 26.4VDC cannot be applied.
Maximum Ic current	bad	2A/point, 5A/common (3A/fuse) (When placing next to the power supply module: 3A/common)	2A/point, 4A/common (25℃ ), 1.8A/point, 3.6A/common (45℃ ), 1.6A/point, 3.2A/common (55℃ )	Δ	Since the maximum load current per common is different, pay attention to the current used in the entire module.
Maximum in current	nrush	4A 100ms or less, 8A 10ms or less	8A 10ms or less	0	
Leakage cu OFF	rrent at	0.1mA or less	0.1mA or less	0	
Maximum voltage drop at ON		1.5VDC (2A)	0.9VDC (TYP.) 2A, 1.5VDC (MAX.) 2A	0	
Response	OFF→ON	2ms or less	2ms or less	0	
time	ON→OFF	2ms or less (resistive load)	2ms or less (resistive load)	0	
Surge suppressor		Varistor (108 to 132V)	Zener diode	0	
Common terminal arrangement		8 points/common (common terminal: TB10,TB20)	8 points/common (common terminal: TB10,TB20)	0	
Operation ir	ndicator	ON indication (LED)	ON indication (LED)	0	
Fuse		3.2A fast blow fuse (2 fuse/common) MP-32	5A fuse (1 fuse/common) Not replaceable	Δ	Connect the fast blow fuse to the external if necessary.
Fuse blow indicator		Yes (LED is turned ON when a fuse is blown. Signal is output to a PLC CPU.)	Yes (LED is turned ON when a fuse is blown. Signal is output to a PLC CPU.)	Δ	Fuse blow error also occurs when the external supply power is OFF.
External supply	Voltage	24VDC (21.6 to 26.4VDC)	24VDC (21.6 to 26.4VDC)	0	
power	Current	65mA (TYP. 24VDC/common)	15mA (TYP. 24VDC/common)	0	
External cor	nnection	20-point terminal block connector (M3×6 screws)	20-point terminal block connector (M3.5×7 screws)	×	
Applicable v	vire size	0.75 to 2mm <sup>2</sup>	0.75 to 1.25mm <sup>2</sup>	×	Wiring must be changed.
Applicable s terminal	solderless	R1.25-3,R2-3, RAV1.25-3,RAV2-3	R1.25-3.5,R2-3.5, RAV1.25-3.5,RAV2-3.5	×	
Current con	sumption	0.115A (TYP. all points ON)	0.120A (TYP. all points ON)	Δ	Current capacity must be reviewed.
External din	nensions	250(H)×37.5(W)×121(D)mm	130(H)×34.5(W)×93.6(D)mm	Δ	The dimensions are different.
Weight		0.64kg	0.20kg	0	

### (28) Specifications comparisons between the AY60E and the A1SY60E

O: Compatible, ∆ :	Partial change required,	×:	Incompatible

Specific	cation	AY60E	A1SY60E	Compati- bility	Precautions for replacement
Number of o points	output	16 points	16 points	0	
Isolation me	ethod	Photocoupler	Photocoupler	0	
Rated load	voltage	24VDC (12/48V)	5/12/24VDC		Voltages exceeding 26.4VDC cannot be applied.
Operating lo		21.6 to 26.4VDC (10.2 to 56VDC)	4.5 to 26.4VDC	Δ	Voltages exceeding 26.4VDC cannot be applied.
Maximum lo current	$5A/common$ [ (condition: $T = I/R \le 2.5ms$ )		Δ	Since the maximum load current per common is different, pay attention to the current used in the entire module.	
Maximum in current	nrush	4A 100ms or less 8A 10ms or less	8A 10ms or less	0	
Leakage cu OFF	rrent at	0.1mA or less	0.1mA or less	0	
Maximum v drop at ON	oltage	1.5VDC (2A)	0.2VDC (MAX.) 1A 0.4VDC (MAX.) 2A	0	
Response	OFF→ON	2ms or less	3ms or less	$\triangle$	The response times
time	ON→OFF	2ms or less (resistive load)	10ms or less (resistive load)	Δ	differ.
Surge suppressor		Surge suppression diode	Zener diode	0	
Common te arrangemer		8 points/common (common terminal: TB10,TB20)	8 points/common (common terminal: TB10,TB20)	0	
Operation indicator		ON indication (LED)	ON indication (LED)	0	
Fuse		5A fast blow fuse (2 fuses/common)	7A fuse (1 fuse/common) Not replaceable	Δ	Connect the fast blow fuse to the external if necessary.
Fuse blow in	ndicator	Yes (LED is turned ON when a fuse is blown. The signal is output to a PLC CPU.)	Yes (LED is turned ON when a fuse is blown. The signal is output to a PLC CPU.)	Δ	Fuse blow error also occurs when the external supply power is OFF.
External	Voltage	24VDC (21.6 to 26.4VDC)	12/24VDC (10.2 to 30VDC)	0	
supply power	Current	65mA (TYP. 24VDC/common)	10mA (TYP. 24VDC/common)	0	
External connection		20-point terminal block connector (M3×6 screws)	20-point terminal block connector (M3.5×7 screws)	×	
Applicable v	wire size	0.75 to 2mm <sup>2</sup>	0.75 to 1.25mm <sup>2</sup>	×	Wiring must be changed.
Applicable s terminal	solderless	R1.25-3,R2-3, RAV1.25-3,RAV2-3	R1.25-3.5,R2-3.5, RAV1.25-3.5,RAV2-3.5	×	
Current con	sumption	0.115A (TYP. all points ON)	0.200A (TYP. all points ON)	Δ	Current capacity must be reviewed.
External din	nensions	250(H)×37.5(W)×121(D)mm	130(H)×34.5(W)×93.6(D)mm	Δ	The dimensions are different.
Weight		0.63kg	0.20kg	0	

### (29) Specifications comparisons between the AY60S and the A1SY60

$\bigcirc$ : Compatible, $\triangle$ : Partia	al change required, ×:	Incompatible
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Specifi	cation	AY60S	A1SY60	Compati- bility	Precautions for replacement
Number of points	output	16 points	16 points	0	
Isolation me	ethod	Photocoupler	Photocoupler	0	
Rated load	voltage	24/48VDC (12V)	24VDC	Δ	Voltages exceeding 26.4VDC cannot be applied.
Operating lo		21.6 to 52.8VDC (10.2 to 52.8VDC)	21.6 to 26.4VDC	Δ	Voltages exceeding 26.4VDC cannot be applied.
Maximum load current		2A/point, 6.4A/common (5A/ fuse) (When placing next to the power supply module: 5A/common)	2A/point, 4A/common ( $25^{\circ}$ C), 1.8A/point, 3.6A/common ( $45^{\circ}$ C), 1.6A/point, 3.2A/common ( $55^{\circ}$ C)	Δ	Since the maximum load current per common is different, pay attention to the current used in the entire module.
Maximum ir current	nrush	4A 100ms or less, 8A 10ms or less	8A 10ms or less	0	
Leakage cu OFF	irrent at	0.1mA or less	0.1mA or less	0	
Maximum v drop at ON	oltage	1VDC (2A)	0.9VDC (TYP.) 2A, 1.5VDC (MAX.) 2A	0	
Response	OFF→ON	1ms or less	2ms or less	Δ	The response times differ.
time ON→OFF		3ms or less (resistive load)	2ms or less (resistive load)	0	
Surge suppressor		Varistor (90 to 110V)	Zener diode	0	
Common te arrangemer		8 points/common (common terminal: TB10,TB20)	8 points/common (common terminal: TB10,TB20)	0	
Operation in	ndicator	ON indication (LED)	ON indication (LED)	0	
Fuse		5A fast blow fuse (2 fuse/common) MP-50	5A fuse (1 fuse/common) Not replaceable	Δ	Connect the fast blow fuse to the external if necessary.
Fuse blow i	ndicator	Yes (LED is turned ON when a fuse is blown. The signal is output to a PLC CPU.)	Yes (LED is turned ON when a fuse is blown. The signal is output to a PLC CPU.)	Δ	Fuse blow error also occurs when the external supply power is OFF.
External supply	Voltage	24/48VDC (21.6 to 52.8VDC)	24VDC (21.6 to 26.4VDC)	Δ	Voltages exceeding 26.4VDC cannot be applied.
power	Current	3mA (TYP. 24VDC/common)	15mA (TYP. 24VDC/common)	Δ	Current capacity must be reviewed.
External co	nnection	20-point terminal block connector (M3×6 screws)	20-point terminal block connector (M3.5×7 screws)	×	
Applicable		0.75 to 2mm <sup>2</sup>	0.75 to 1.25mm <sup>2</sup>	×	Wiring must be changed.
Applicable terminal	solderless	R1.25-3,R2-3, RAV1.25-3,RAV2-3	R1.25-3.5,R2-3.5, RAV1.25-3.5,RAV2-3.5	×	
Current cor	sumption	0.75A (TYP. all points ON)	0.12A (TYP. all points ON)		Current capacity must be reviewed.
External dir	nensions	250(H)×37.5(W)×121(D)mm	130(H)×34.5(W)×93.6(D)mm		The dimensions are different.
Weight		0.66kg	0.20kg	0	

### (30) Specifications comparisons between the AY70 and the A1SY71

$\bigcirc$ Compatible $\land$ .	Partial change required,	×٠	Incompatible
$O$ . compatible, $\Delta$ .	i ultur onunge requirea,	· · ·	moomputible

Specifie	cation	AY70	Compati- bility	Precautions for replacement	
Number of o points	output	16 points	32 points	×	Set sixteen points in the I/O assignment of Parameter.
Isolation me	ethod	Photocoupler	Photocoupler	0	
Rated load	voltage	5/12VDC	5/12VDC	0	
Operating lo		4.5 to 15VDC	4.5 to 15VDC	0	
Maximum Ic current	bad	16mA/point, 128mA/common	16mA/point, 256mA/common	0	
Maximum ir current	ırush	50mA 10ms	40mA 10ms or less	Δ	The inrush current value differs.Use caution on selecting the load to use.
Output volta OFF	ige at	V <sub>OH</sub> : 3.5VDC (Vcc=5VDC, I <sub>OH</sub> =0.4mA)	V <sub>OH</sub> : 3.5VDC (Vcc=5VDC, I <sub>OH</sub> =0.4mA)	0	
Maximum v drop at ON	oltage	V <sub>OL</sub> : 0.2VDC (I <sub>OL</sub> =16mA)	V <sub>OL</sub> : 0.3VDC	Δ	Check the input specifications of external equipment to be connected since the maximum voltage drop is bigger when turning ON.
Response	OFF→ON	1ms or less	1ms or less	0	
time ON→OFF		1ms or less	1ms or less (resistive load)	0	
Common te arrangemer	nmon terminal 8 points/common ingement (common terminal: TB10,TB20)		32 points/common (common terminal: A1,A2)	Δ	As 2 commons are reduced to 1,wiring a different voltage for each common is not possible.
Operation ir	ndicator	ON indication (LED)	ON indication (LED)	0	
Fuse		None	1.6A (not replaceable) (fuse blow capacity: 50A)	0	
Fuse blow in	ndicator	_	Yes (LED is turned ON when a fuse is blown. The signal is output to a PLC CPU)	0	Fuse blow error also occurs when the external supply power is OFF.
External	Voltage	5/12VDC (4.5 to 15VDC)	5/12VDC (4.5 to 15VDC)	0	
supply power	Current	55mA (TYP.12VDC/common)	150mA (12VDC/common) (MAX. all points ON)	Δ	Current capacity must be reviewed.
External cor	nnection	20-point terminal block connector (M3×6 screws)	40-pin connector (included)	×	Wiring must be
Applicable v	vire size	0.75 to 2mm <sup>2</sup>	0.3mm <sup>2</sup>	×	changed.
Current con	sumption	0.10A (TYP. all points ON)	0.40A (TYP. all points ON)		Current capacity must be reviewed.
External din	nensions	250(H)×37.5(W)×121(D)mm	130(H)×34.5(W)×93.6(D)mm	Δ	
Weight		0.36kg	0.19kg	0	

### (31) Specifications comparisons between the AY71 and the A1SY71

O: Compatible, △: Partial change required, ×: Incompatible	O: Compatible, ∧	:	Partial change required, ×	Incompatible
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Specific	cation	AY71	A1SY71	Compati- bility	Precautions for replacement
Number of output points		32 points 32 points		0	
Isolation me	ethod	Photocoupler	Photocoupler	0	
Rated load	voltage	5/12VDC	5/12VDC	0	
Operating lo voltage rang		4.5 to 15VDC	4.5 to 15VDC	0	
Maximum Ic current	bad	16mA/point, 256mA/common (Sink loading)	16mA/point, 256mA/common	0	
Maximum ir current	nrush	50mA 10ms	40mA 10ms or less	Δ	The inrush current value differs.Use caution on selecting the load to use.
Output volta OFF	age at	V <sub>OH</sub> : 3.5VDC (Vcc=5VDC, I <sub>OH</sub> =0.4mA)	V <sub>OH</sub> : 3.5VDC (Vcc=5VDC, I <sub>OH</sub> =0.4mA)	0	
Maximum v drop at ON	oltage	V <sub>OL</sub> : 0.2VDC (I <sub>OL</sub> =16mA)	V <sub>OL</sub> : 0.3VDC	Δ	Check the input specifications of external equipment to be connected since the maximum voltage drop is bigger when turning ON.
Response	OFF→ON	1ms or less	1ms or less	0	
time	ON→OFF	1ms or less	1ms or less (resistive load)	0	
Common terminal arrangement		16 points/common (common terminal: TB18,TB36)	32 points/common (common terminal: A1,A2)	Δ	As 2 commons are reduced to 1,wiring a different voltage for each common is not possible.
Operation ir	ndicator	ON indication (LED)	ON indication (LED)	0	
Fuse		None	1.6A (not replaceable) (fuse blow capacity: 50A)	0	
Fuse blow in	ndicator	None	Yes (LED is turned ON when a fuse is blown. The signal is output to a PLC CPU.)	Δ	Since the fuse blown error detection is executed, the parameter or sequence program must be reviewed.
External	Voltage	5/12VDC (4.5 to 15VDC)	5/12VDC (4.5 to 15VDC)	0	
supply power	Current	100mA (TYP.12VDC/common)	150mA (12VDC/common) (MAX. all points ON)	Δ	Current capacity must be reviewed.
External cor	nnection	38-point terminal block connector (M3×6 screws)	40-pin connector (included)	×	Wiring must be
Applicable v	vire size	0.75 to 2mm <sup>2</sup>	0.3mm <sup>2</sup>	×	changed.
Current con	sumption	0.20A (TYP. all points ON)	0.40A (TYP. all points ON)	Δ	Current capacity must be reviewed.
External din	nensions	250(H)×37.5(W)×131(D)mm	130(H)×34.5(W)×93.6(D)mm	Δ	
Weight		0.44kg	0.19kg	0	

#### (32) Specifications comparisons between the AY72 and the A1SY71

 $\bigcirc$  : Compatible,  $\bigtriangleup$  : Partial change required,  $\times$  : Incompatible

Specifi	cation	AY72	A1SY71	Compati- bility	Precautions for replacement
Number of output points		64 points	32 points	×	When thirty-two or more points are used, use two of the A1SY71 modules.
Isolation me	ethod	Photocoupler	Photocoupler	0	
Rated load	voltage	5/12VDC	5/12VDC	0	
Operating lo voltage rang		4.5 to 15VDC	4.5 to 15VDC	0	
Maximum lo current	bad	16mA/point, 512mA/common (Sink loading)	16mA/point, 256mA/common	0	
Maximum ir current	nrush	50mA 10ms	40mA 10ms or less	Δ	The inrush current value differs.Use caution on selecting the load to use.
Output volta OFF	age at	V <sub>OH</sub> : 3.5VDC (Vcc=5VDC, I <sub>OH</sub> =0.4mA)	V <sub>OH</sub> : 3.5VDC (Vcc=5VDC, I <sub>OH</sub> =0.4mA)	0	
Maximum v drop at ON	oltage	V <sub>OL</sub> : 0.2VDC (I <sub>OL</sub> =16mA)	V <sub>OL</sub> : 0.3VDC	۵	Check the input specifications of external equipment to be connected since the maximum voltage drop is bigger when turning ON.
Response	OFF→ON	1ms or less	1ms or less	0	
time	ON→OFF	1ms or less	1ms or less (resistive load)	0	
Common terminal arrangement		32 points/common (common terminal: 1A1,1A2,2A1,2A2)	32 points/common (common terminal: A1,A2)	0	
Operation in	ndicator	ON indication (LED)	ON indication (LED)	0	
Fuse		None	1.6A (not replaceable) (fuse blow capacity: 50A)	0	
Fuse blow indicator		None	Yes (LED is turned ON when a fuse is blown. The signal is output to a PLC CPU.)	Δ	Since the fuse blow error detection is executed, the parameter or sequence program must be reviewed.
External	Voltage	5/12VDC (4.5 to 15VDC)	5/12VDC (4.5 to 15VDC)	0	
supply power	Current	300mA (TYP.12VDC 1-common ON)	150mA (12VDC/common) (MAX. all points ON)	0	
External connection		40-pin connector (with solder) × 2	40-pin connector (included)	0	
Applicable wire size		0.3mm <sup>2</sup>	0.3mm <sup>2</sup>	0	
Accessory		External wiring connectors × 2	External wiring connectors × 1	0	
Current consumption		0.30A (TYP. all points ON)	0.40A (TYP. all points ON)	Δ	Current capacity must be reviewed.
External dimensions		250(H)×37.5(W)×106(D)mm	130(H)×34.5(W)×93.6(D)mm	Δ	
Weight		0.47kg	0.19kg	0	

#### (33) Specifications comparisons between the AY80 and the A1SY80

		$\bigcirc$ : Compatible, $\triangle$ : Partial change required, ×: Incompatible				
Specific	cation	AY80	A1SY80	Compati- bility	Precautions for replacement	
Number of output points		16 points	16 points	0		
Isolation me	thod	Photocoupler	Photocoupler	0		
Rated load v	voltage	12/24VDC	12/24VDC	0		
Operating lo voltage rang		10.2 to 30VDC	10.2 to 30VDC	0		
Maximum lo	ad current	0.5A/point, 2A/common	0.8A/point, 3.2A/common	0		
Maximum in current	rush	7A 10ms or less 3.5A 100ms or less	8A 10ms or less	0		
Leakage cui OFF	rrent at	0.1mA or less	0.1mA or less	0		
Maximum vo drop at ON	oltage	1.5VDC (MAX.) 0.5A	1.5VDC (MAX.) 0.8A	0		
Response	OFF→ON	2ms or less	2ms or less	0		
time	ON→OFF	2ms or less (resistive load)	2ms or less (resistive load)	0		
Surge suppressor		Varistor (52 to 62V)	Zener diode	0		
Common terminal arrangement		8 points/common (common terminal: TB9,TB19)	8 points/common (common terminal: TB9,TB19)	0		
Operation in	dicator	ON indication (LED)	ON indication (LED)	0		
Fuse		2A fast blow fuse (1 fuse/common) MP-20	5A fuse (1 fuse/common) Not replaceable (fuse blow capacity: 50A)	Δ	Connect the fast blow fuse to the external if necessary.	
Fuse blow indicator		Yes (LED is turned ON when a fuse is blown. The signal is output to a PLC CPU.)	Yes (LED is turned ON when a fuse is blown. The signal is output to a PLC CPU.)	Δ	Fuse blow error also occurs when the external supply power is OFF.	
External supply	Voltage	12/24VDC (10.2 to 30VDC)	12/24VDC (10.2 to 30VDC)	0		
power	Current	60mA (TYP. 24VDC/common)	20mA (TYP. 24VDC/common)	0		
External connection		20-point terminal block connector (M3×6 screws)	20-point terminal block connector (M3.5×7 screws)	×		
Applicable wire size		0.75 to 2mm <sup>2</sup>	0.75 to 1.25mm <sup>2</sup>	×	Wiring must be changed.	
Applicable solderless terminal		R1.25-3,R2-3, RAV1.25-3,RAV2-3	R1.25-3.5,R2-3.5, RAV1.25-3.5,RAV2-3.5	×		
Current consumption		0.115A (TYP. all points ON)	0.120A (TYP. all points ON)	Δ	Current capacity must be reviewed.	
External dimensions		250(H)×37.5(W)×121(D)mm	130(H)×34.5(W)×93.6(D)mm	Δ	The dimensions are different.	
Weight		0.42kg	0.20kg	0		

#### (34) Specifications comparisons between the AY81 and the A1SY81

Specific	cation	AY81	A1SY81	Compati- bility	Precautions for replacement
Number of output points		32 points	32 points	0	
Isolation method		Photocoupler	Photocoupler	0	
Rated load	voltage	12/24VDC	12/24VDC	0	
Operating lo voltage rang		10.2 to 30VDC	10.2 to 30VDC	0	
Maximum load current		0.5A/point, 4A/common (When placing next to the power supply module: 3A/common)	0.1A/point, 2A/common	Δ	Carefully select load for use since the maximum load current per point is lowered.*1
Maximum in current	nrush	4A 10ms or less	0.4A 10ms or less	Δ	The inrush current value differs.Use caution on selecting the load to use.
Leakage cu OFF	rrent at	0.1mA or less	0.1mA or less	0	
Maximum voltage drop at ON		1.5VDC (MAX.) 0.5A	1.0VDC (TYP.) 0.1A 2.5VDC (MAX.) 0.1A	Δ	Because different values for maximum voltage drop are given when turning ON, care should be taken to select loads to be used.
Response	OFF→ON	2ms or less	2ms or less	0	
time	ON→OFF	2ms or less (resistive load)	2ms or less (resistive load)	0	
Surge supp	ressor	Varistor (52 to 62V)	Zener diode	0	
Common terminal arrangement		16 points/common (common terminal: TB17,TB35)	32 points/common (common terminal: 17,18,36)	Δ	As 2 commons are reduced to 1, wiring a different voltage for each common is not possible.
Operation in	ndicator	ON indication (LED)	ON indication (LED)	0	
External	Voltage	12/24VDC (10.2 to 30VDC)	12/24VDC (10.2 to 30VDC)	0	
supply power	Current	50mA (TYP. 24VDC/common)	8mA (24VDC/common)	0	
External connection		38-point terminal block connector (M3× 6 screws)	37-pin D sub connector (included)	×	Wiring must be
Applicable wire size		0.75 to 2mm <sup>2</sup>	0.3mm <sup>2</sup>	×	changed. *2
Current consumption		0.23A (TYP. all points ON)	0.50A (TYP. all points ON)	Δ	Current capacity must be reviewed.
External dimensions		250(H)×37.5(W)×131(D)mm	130(H)×34.5(W)×93.6(D)mm	Δ	
Weight		0.53kg	0.23kg	0	

\*1 Replacement with the interface terminal module (FA-TH16YTH11S) allows 1.0A/point, 8A/common outputs.

\*2 By using connectors/terminal block converter modules (A6TBY36-E, etc.), conversion to the terminal block is possible.

#### (35) Specifications comparisons between the AY82-EP and the A1SY82

 $\bigcirc$ : Compatible,  $\triangle$ : Partial change required, ×: Incompatible

Specification		AY82-EP	A1SY82	Compati- bility	Precautions for replacement
Number of output points		64 points	64 points	0	
Isolation method		Photocoupler	Photocoupler	0	
Rated load	voltage	12/24VDC	12/24VDC	0	
Operating lo		10.2 to 26.4VDC	10.2 to 30VDC	0	
Maximum lo current	bad	0.1A/point 0.04A/point (60% ON, 55℃ )	0.1A/point, 2A/common	0	
Maximum ir current	nrush	No limit (Short protect)	0.4A 10ms or less	Δ	The inrush current value differs.Use caution on selecting the load to use.
Leakage cu OFF	rrent at	0.1mA or less	0.1mA or less	0	
Maximum v drop at ON	oltage	3.5VDC (0.1A) 2.5VDC (0.1A TYP.)	1.0VDC (TYP.) 0.1A 2.5VDC (MAX.) 0.1A	0	
Response	OFF→ON	0.5ms or less	2ms or less	Δ	The response times
time	ON→OFF	1.5ms or less	2ms or less (resistive load)	Δ	differ.
Surge supp	ressor	Surge suppression diode	Zener diode	0	
Common terminal arrangement		32 points/common (common terminal: 1-17,1-18,1-36,2-17,2-18,2-36)	32 points/common (common terminal: 1B1,1B2,2B1,2B2)	0	
Operation in	ndicator	ON indication (LED)	ON indication (LED) 32-point switch-over using a switch	0	
Protection		Yes (Overheat protection function and short-circuit protection function) • Overheat protection function is detected in 1 common units. When Overheat protection function occurs at a 1 point of 1 common, output of all points for the corresponded common terminal is turned OFF.	None	×	No protection function
Protection of display	letection	None (No signal output to a PLC CPU)	None	×	No protection function
Protection function reset		Automatic reset (reset by canceling overheat protection function)	None	×	No protection function
External	Voltage	12/24VDC (10.2 to 30VDC)	12/24VDC (10.2 to 30VDC)	0	
power supply	Current	50mA (TYP. 24VDC/common)	8mA (24VDC/common)	0	
External connection		37-pin connector (with solder) $\times 2$	40-pin connector	×	Wiring must be changed.
Applicable wire size		0.3mm <sup>2</sup>	0.3mm <sup>2</sup>	0	
Accessory		External wiring connectors × 2	External wiring connectors × 1	0	
Current consumption		0.29A (TYP. all points ON)	0.93A (TYP. all points ON)	Δ	Current capacity must be reviewed.
External dimensions		250(H)×37.5(W)×106(D)mm	130(H)×34.5(W)×93.6(D)mm	Δ	
Weight		0.58kg	0.27kg	0	

# **3.3 Specifications Comparisons between I/O Modules**

#### (1) Specifications comparisons between the AH42 and the A1SH42

 $\bigcirc$ : Compatible,  $\bigtriangleup$ : Partial change required, ×: Incompatible

	Specifica	ition	AH42	A1SH42	Compati- bility	Precautions for replacement
	Number of input points		32 points	32 points	O	replacement
	Isolation n	nethod	Photocoupler	Photocoupler	0	
	Input type		Sink type	Sink type	0	
Input specification	Rated input voltage		12/24VDC	12/24VDC	0	
	Rated input current		Approx. 3mA/Approx. 7mA	Approx. 2mA/Approx. 5mA	Δ	Rated input current is smaller.*1
	Operating voltage range		10.2 to 26.4VDC (ripple ratio within 5%)	10.2 to 26.4VDC (ripple ratio within 5%)	0	
	Maximum simultaneous input points		60% simultaneously ON	Refer to the derating chart. *2	0	
	ON voltag current	e/ON	9.5VDC or more/3mA or more	8VDC or more/2mA or more	0	
	OFF volta	ge/OFF	6VDC or less/1.5mA or less	4VDC or less/0.6mA or less	Δ	OFF current is smaller. <sup>*1</sup>
	Input resis	stance	Approx. 3.3kΩ	Approx. 5kΩ	Δ	Input resistance is greater.*1
	Response	OFF→ON	10ms or less (24VDC)	10ms or less (24VDC)	0	
	time	ON→OFF	10ms or less (24VDC)	10ms or less (24VDC)	0	
	Common terminal arrangement		32 points/common (common terminal: 1B1,1B2)	32 points/common (common terminal: 1B1,1B2)	0	
	Number of output points		32 points	32 points	0	
	Isolation method		Photocoupler	Photocoupler	0	
	Output type		Sink type	Sink type	0	
	Rated load voltage		12/24VDC	12/24VDC	0	
	Operating load voltage range		10.2 to 40VDC	10.2 to 30VDC	Δ	Voltages exceeding 30VDC cannot be applied.
			0.1A/point, 1A/common	0.1A/point, 1.6A/common	0	
tion	Maximum inrush current		0.4A 10ms or less	0.4A 10ms or less	0	
specification	Leakage o OFF	current at	0.1mA or less	0.1mA or less	0	
Output spe	Maximum voltage drop at ON		2.5VDC (0.1A) 1.75VDC (5mA) 1.7VDC (1mA)	1.0VDC (TYP.) 0.1A 2.5VDC (MAX.) 0.1A	0	
Ū	Response	OFF→ON	2ms or less	2ms or less	0	
	time	ON→OFF	2ms or less (resistive load)	2ms or less (resistive load)	0	
	Surge sup	pressor	Clamp diode	Zener diode	0	
	Common terminal arrangement		32 points/common (common terminal: 2A1,2A2)	32 points/common (common terminal: 2A1,2A2)	0	
	External supply power	Voltage	12/24VDC (10.2 to 40VDC)	12/24VDC (10.2 to 30VDC)	Δ	Voltages exceeding 30VDC cannot be applied.
		Current	40mA (24VDC TYP.)	8mA (At 24VDC)/common (MAX. all points ON)	0	
Ор	eration indic	cator	ON indication (LED) 32-point switch-over using a switch	ON indication (LED) 32-point switch-over using a switch	0	
	External connection		40-pin connector $\times 2$	40-pin connector (included) $\times 2$	0	

# 3 I/O MODULE REPLACEMENT

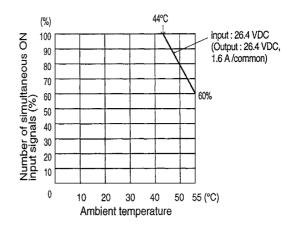
# MELSEC

Specification	AH42	A1SH42	Compati- bility	Precautions for replacement
Applicable wire size	0.3mm <sup>2</sup>	0.3mm <sup>2</sup>	0	
Accessory	40-pin connector ×2 (with solder)	40-pin connector × 2 (with solder)	0	
Occupied points	64 points (output 64 points)	32 points (I/O assignment: input/ output composite)	×	Output number (Y□)differs. <sup>*3</sup>
Current consumption	0.25A (TYP. all points ON)	0.50A (TYP. all points ON)	Δ	Current capacity must be reviewed.
External dimensions	250(H)×37.5(W)×121(D)mm	130(H)×34.5(W)×93.6(D)mm	Δ	
Weight	0.70kg	0.27kg	0	

\*1 Check the specifications of sensor or switch to connect to the A1SH42.

\*2 The figure on the right shows derating.

\*3 Modify the output number used in the program. (For the A1SH42, the same number of X0 to X1F and Y0 to Y1F)



#### (2) Specifications comparisons between the A42XY and the A1S42X/A1S42Y

#### (a) Specifications comparisons between the A42XY (input part) and the A1S42X

 $\bigcirc$ : Compatible,  $\triangle$ : Partial change required, ×: Incompatible

Specifi	cation	A42XY (input specification)	A1S42X	Compati- bility	Precautions for replacement	
Number of input points		64 points	16/32/48/64 points (switch setting)	0		
Isolation me	thod	Photocoupler	Photocoupler	0		
Input type		Dynamic scan of 8 inputs×8	Dynamic scan of 8 inputs×8	0		
Rated input	voltage	12/24VDC (10.2 to 26.4VDC ripple ratio within 5%)	12/24VDC (10.2 to 26.4VDC ripple ratio within 5%)	0		
Maximum si input points	multaneous	60% simultaneously ON	100% simultaneously ON	0		
ON voltage/	ON current	7VDC or more	8VDC or more/2mA or more	Δ	ON current is greater.*1	
OFF voltage current	/OFF	3VDC or less	4VDC or less/1mA or less	0		
Input resista	nce	Approx. 2.4kΩ	Approx. 2.4k Ω	0		
Response	OFF→ON	16ms or less	0.4ms or less (24VDC)+13.3ms	0		
time ON→OFF		16ms or less	0.4ms or less (24VDC)+13.3ms	0		
Operation indicator		ON indication (LED) 8-point switch-over using a rotary switch	ON indication (LED) 32-point switch-over using a switch	0		
External supply	Voltage	12/24VDC (10.2 to 26.4VDC ripple ratio within 5%)	_	×	External supply power is not	
power	Current	55mA TYP.	_	×	required.	
External cor	inection	Input: 16-pin connector	24-pin connector	×	Wiring must be changed.	
Applicable w	/ire size	0.3mm <sup>2</sup>	0.3mm <sup>2</sup>	0		
Occupied points		64 points (output 64 points)	64 points (I/O assignment: input)	Δ	The number of occupied points is 128 points (64points × 2 = 128 points) when using both modules of the A1S42X and A1S42'	
Current consumption		0.11A (TYP.)	0.08A (TYP. all points ON)		Review current capacity when using with the A1SY42Y.	
External dim	ensions	250(H)×37.5(W)×119(D)mm	130(H)×34.5(W)×93.6(D)mm			
Weight		0.60kg	0.18kg	0		

\*1 Check the specifications of sensor or switch to connect to the A1S42X.

#### (b) Specifications comparisons between the A42XY (output part) and the A1S42Y

 $\bigcirc$ : Compatible,  $\triangle$ : Partial change required, ×: Incompatible

Specif	fication	A42XY (output specification)	A1S42Y	Compati- bility	Precautions for replacement
Number of output points		64 points	16/32/48/64 points (switch setting)	0	
Isolation method		Photocoupler	Photocoupler	0	
Output type		Dynamic scan of 8 outputs × 8	Dynamic scan of 8 outputs×8	0	
Rated load	voltage	12/24VDC (10.2 to 26.4VDC ripple ratio within 5%)	12/24VDC (10.2 to 26.4VDC ripple ratio within 5%)	0	
Maximum o	utput current	50mA/point (built in limiting resistor (1kΩ ) not used)	0.1A/point	0	
Maximum v at ON	voltage drop	1.5V on the source side (built in limiting resistor not used) 1V on the sink side	1.1VDC on the source side 1.5VDC on the sink side	Δ	Voltage dorp is greater.
Maximum soutput point	simultaneous s	60% simultaneously ON (built in limiting resistor (1kΩ ) not used)	100% simultaneously ON	0	
Dynamic		16ms or less	13.3ms	0	
scan synchr	ronization	16ms or less	13.3ms	0	
Operation indicator		ON indication (LED) 8-point switch-over using a rotary switch	ON indication (LED) 32-point switch-over using a switch	0	
External supply	Voltage	12/24VDC (10.2 to 26.4VDC ripple ratio within 5%)	12/24VDC (10.2 to 26.4VDC ripple ratio within 5%)	0	
power	Current	180mA TYP.	80mA (At 24VDC)/common	0	
External cor	nnection	Output: 32-pin connector	24-pin connector	×	Wiring must be changed.
Applicable v	vire size	0.3mm <sup>2</sup>	0.3mm <sup>2</sup>	0	
Occupied points		64 points (output 64 points)	64 points (I/O assignment: output)	۵	The number of occupied points is 128 points (64points $\times$ 2 = 128 points) when using both modules of the A1S42X and A1S42Y.
Current consumption		0.11A (TYP.)	0.10A (TYP. all points ON)		Review current capacity when using with the A1S42X.
External din	nensions	250(H)×37.5(W)×119(D)mm	130(H)×34.5(W)×93.6(D)mm	Δ	The dimensions are different.
Weight		0.60kg	0.19kg	0	

# 3.4 Specifications Comparisons between Interrupt Modules

#### (1) Specifications comparisons between the Al61 and the A1SI61

			O: Compatible, ∆: Partial	l change re	quired, ×: Incompatible
Specif	ication	AI61	A1SI61	Compati- bility	Precautions for replacement
Number of in points	nterrupt input	16 points	16 points	0	
Isolation me	thod	Photocoupler	Photocoupler	0	
Rated input	voltage	12/24VDC	12/24VDC	0	
Rated input	current	6mA (12VDC) 14mA (24VDC)	4mA (12VDC) 8mA (24VDC)	Δ	Rated input current is smaller.*1
Operating ve	oltage range	10.2 to 26.4VDC	10.2 to 26.4VDC	0	
Maximum s input points	simultaneous	100% (16/common) simultaneously ON	100% (16/common) simultaneously ON	0	
ON voltage		9V or more	9V or more/3mA or more	0	
OFF voltage	•	4V or less	4V or less/1mA or less	0	
Input resista	ince	Approx. 2.4k $\Omega$	Approx. 2.7kΩ	Δ	Input resistance is greater.*1
Response	OFF→ON	0.2ms or less	0.2ms or less	0	
time	ON→OFF	0.2ms or less	0.2ms or less	0	
Interrupt cor setting	ndition	1-point unit	4-point unit	Δ	The point unit for setting whether the interrupt is processed at the rising/falling of input signals has been changed.
Common ter arrangemen		16 points/common (common terminal: TB9,TB18)	16 points/common (common terminal: TB9,TB18)	0	
Operation in	dicator	ON indication (LED)	ON indication (LED)	0	
External cor	nection	20-point terminal block connector (M3×6 screws)	20-point terminal block connector (M3.5×7 screws)	×	
Applicable v	vire size	0.75 to 1.5mm <sup>2</sup>	0.75 to 1.25mm <sup>2</sup>	×	Wiring must be
Applicable solderless terminal		1.25-3,1.25-YS3A, 2-S3,2-YS3A V1.25-3,V1.25-YS3A, V2-S3,V2-YS3A	R1.25-3.5,R2-3.5, RAV1.25-3.5,RAV2-3.5	×	changed.
Occupied po	pints	32 points (special 32 points)	32 points (special 32 points)	0	
Current consumption		0.140A (TYP. all points ON)	0.057A (TYP. all points ON)	0	
External dim	nensions	250(H)×37.5(W)×121(D)mm	130(H)×34.5(W)×93.6(D)mm	Δ	The dimensions are different.
Weight		0.40kg	0.20kg	0	

\*1 Check the specifications of sensor or switch to connect to the A1SI61.

#### (2) Specifications comparisons between the AI61-SI and the A1SI61

O: Compatible, ∆: Partial change required, ×: In
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Specification		AI61-S1	A1SI61	Compati- bility	Precautions for replacement
Number of in points	nterrupt input	16 points	16 points	0	
Isolation method		Photocoupler	Photocoupler	0	
Rated input	voltage	24VDC	12/24VDC	0	
Rated input	current	14mA	4mA (12VDC) 8mA (24VDC)	Δ	Rated input current is smaller.*1
Operating v	oltage range	21.6 to 26.4VDC	10.2 to 26.4VDC	0	
Maximum sinput points	simultaneous	100% (16/common) simultaneously ON	100% (16/common) simultaneously ON	0	
ON voltage		16V or more	9V or more/3mA or more	0	
OFF voltage	9	9V or less	4V or less/1mA or less	Δ	The OFF voltage has been reduced.*1
Input resista	ance	Approx. 2.4k $\Omega$	Approx. 2.7kΩ	Δ	Input resistance is greater. <sup>*1</sup>
Response	OFF→ON	2ms or less, 8ms or less	0.2ms or less	0	
time	ON→OFF	2ms or less, 8ms or less	0.2ms or less	0	
Interrupt condition setting		1-point unit	4-point unit	Δ	The point unit for setting whether the interrupt is processed at the rising/falling of input signals has been changed.
Common ter arrangemen		16 points/common (common terminal: TB9,TB18)	16 points/common (common terminal: TB9,TB18)	0	
Operation in	ndicator	ON indication (LED)	ON indication (LED)	0	
External cor	nnection	20-point terminal block connector (M3×6 screws)	20-point terminal block connector (M3.5×7 screws)	×	
Applicable v	vire size	0.75 to 1.5mm <sup>2</sup>	0.75 to 1.25 mm <sup>2</sup>	×	Wiring must be
Applicable solderless terminal		1.25-3,1.25-YS3A, 2-S3,2-YS3A V1.25-3,V1.25-YS3A, V2-S3,V2-YS3A	R1.25-3.5,R2-3.5, RAV1.25-3.5,RAV2-3.5	×	changed.
Occupied points		32 points (special 32 points)	32 points (special 32 points)	0	
Current consumption		0.14A (TYP. all points ON)	0.057A (TYP. all points ON)	0	
External dim	nensions	250(H)×37.5(W)×121(D)mm	130(H)×34.5(W)×93.6(D)mm	Δ	The dimensions are different.
Weight		0.40kg	0.20kg	0	

\*1 Check the specifications of sensor or switch to connect to the A1SI61.

### **3.5 Precautions for I/O Module Replacement**

#### (1) Wiring

#### (a) Size of wire and solderless terminal

The module and terminal block of the small-sized AnS/Q2AS series are smaller than the large-sized A/QnA series, therefore the applicable size of wire and solderless terminal for terminal blocks differ between the two series.

For this reason, use the wire and solderless terminal compatible with the specifications of the small-sized AnS/Q2AS series I/O module when replacing with the small-sized AnS/Q2AS series.

#### (b) Change from terminal block to connecter

The 32-point I/O modules of the large-sized A/QnA series uses terminal blocks while that of the AnS/ Q2AS series uses connecters.

When using a 32-point I/O module of the small-sized AnS/Q2AS series, shift to the wiring using connecters or convert the connecters to terminal blocks with the following method.

 $\cdot$  Use the conversion module for the connecters and terminal block.

#### (2) Connecter for external wiring

When replacing the large-sized A/QnA series  $A \square 82$  (D sub-connector) with the small-sized AnS/Q2AS series  $A1S \square 82$ , the wiring must be changed since the external wiring connecters included in the package are different.

When directly using a D sub-connector, replace  $A \square 82$  with  $A1S \square 81$  (two modules). However, confirm the wiring condition of the entire system since the number of modules will be increased.

#### (3) Precautions for input modules

#### (a) Specifications change of rated input current

Check the specifications of sensors and switches since some of the small-sized AnS/Q2AS series input modules support lower rated input current than those of the large-sized A/QnA series.

#### (b) Specifications change of OFF current

Check the specifications of sensors and switches since some of the small-sized AnS/Q2AS series input modules support lower OFF current than those of the large-sized A/QnA series.

#### (c) Specifications change of maximum simultaneous input points

Check the specifications of sensors and switches since some of the small-sized Ans/Q2AS series input modules have less maximum simultaneous input points than those of the large-sized A/QnA series. Refer to the derating diagram and use within the range shown in the diagram when replacing with the small-sized AnS/Q2AS series.

#### (d) Specifications change of rated voltage value

The A1SX  $\square$   $\square$ -S1 type DC input module of the small-sized AnS/Q2AS series is dedicated to 24VDC and cannot be used at 12VDC.

#### (e) Specifications change of response time

Pay attention that the response time may differ between the large-sized A/QnA series and the small-sized AnS/Q2AS series.

#### (f) Specifications change of common terminal arrangement

The common terminal arrangement may differ between the large-sized A/QnA series and the smallsized AnS/Q2AS series. Pay attention when applying a different voltage to each common.

#### (4) Precautions for output module

#### (a) Specifications change of output current value

Some of the small-sized AnS/Q2AS series output modules support lower output current than those of the large-sized A/QnA series. Check the specification of the load side when using the small-sized AnS/Q2AS series output module with smaller output current.

#### (b) Specifications change of common terminal arrangement

The common terminal arrangement may differ between the large-sized A/QnA series and the smallsized AnS/Q2AS series. Pay attention when applying a different voltage to each common.

#### (c) Specifications change of common maximum load current

Check the maximum load current for one common before use, since the current for one common may differ between the large-sized A/QnA series and the small-sized AnS/Q2AS series.

#### (d) Specifications change of fuse blow error

On the small-sized AnS/Q2AS series, note that a fuse blow error is detected if the external supply power is not supplied to the output module with a fuse.

# POWER SUPPLY MODULE REPLACEMENTS

# 4.1 List of Power Supply Module Alternative Models

A/QnA series model to	be discontinued	AnS series alternative models			
Product	Model	Model	Remarks (restrictions)		
	A61P *	A1S61PN	<ol> <li>1) External wiring change: Required</li> <li>2) Change in number of slots: Not required</li> <li>3) Change in specifications: Current capacity is smaller.</li> </ol>		
	A62P	A1S62PN	<ol> <li>1) External wiring change: Required</li> <li>2) Change in number of slots: Not required</li> <li>3) Change in specifications: Current capacity is smaller.</li> </ol>		
	A63P *	A1S63P	<ol> <li>1) External wiring change: Required</li> <li>2) Change in number of slots: Not required</li> <li>3) Change in specifications: Current capacity is smaller.</li> </ol>		
Power supply module	A61PEU	A1S61PN	<ol> <li>1) External wiring change: Required</li> <li>2) Change in number of slots: Not required</li> <li>3) Change in specifications: Current capacity is smaller.</li> </ol>		
	A62PEU	A1S62PN	<ol> <li>1) External wiring change: Required</li> <li>2) Change in number of slots: Not required</li> <li>3) Change in specifications: Current capacity is smaller.</li> </ol>		
	A68P	None	General-purpose switching power supply (For $\pm 15$ VDC)		
	A61P-UL	A1S61PN	<ol> <li>1) External wiring change: Required</li> <li>2) Change in number of slots: Not required</li> <li>3) Change in specifications: Current capacity is smaller.</li> </ol>		

\* This is not a model to be discontinued.

# 4.2 Power Supply Module Specifications Comparisons

#### (1) Specifications comparisons between the A61P(-UL) and the A1S61PN

 $\bigcirc$ : Compatible,  $\triangle$ : Partial change required,  $\times$ : Incompatible

Specification		A61P(-UL)	A1S61PN	Compati- bility	Precautions for replacement
		100-120VAC+10%-15% (85 to 132VAC)	100-240VAC+10%-15%	0	
Input power supply		200-240VAC+10%-15% (170 to 264VAC)	(85 to 264VAC)	0	
Input frequenc	у	50/60Hz±5%	50/60Hz±5%	0	
Input voltage d	listortion	5% within	5% within	0	
Max. input app power	parent	130VA	105VA	0	
Inrush current		20A within 8ms	20A within 8ms	0	
Rated output current	5VDC	8A	5A	Δ	Confirm the current consumption of entire system.
ourront	24VDC	-	_		
Overcurrent	5VDC	8.8A or more	5.5A or more	0	
protection	24VDC	—	—		
Overvoltage	5VDC	5.5 to 6.5V	5.5 to 6.5V	0	
protection	24VDC	_			
Efficiency		65% or more	65% or more	0	
Power indicato		Power LED indication	LED indication (5VDC output: ON)	0	
Terminal screw	v size	M4 × 0.7 × 6	M3.5×7	×	Wiring must be changed.
Applicable wire	e size	0.75 to 2mm <sup>2</sup>	0.75 to 2mm <sup>2</sup>	0	
Applicable solo terminal	derless	R1.25-4,R2-4, RAV1.25-4, RAV2-4	RAV1.25-3.5, RAV2-3.5	×	Wiring must be changed.
Applicable tigh torque	Itening	98 to 137N₊cm	59 to 88N.cm	×	Tighten within the applicable tightening torque.
External dimer	nsion	250(H)×55(W)×121(D) mm	130(H)×55(W)×93.6(D) mm	Δ	
Weight		0.98kg	0.6kg	0	
Allowable mon power failure p		Within 20ms	Within 20ms	0	
Noise durabilit	у	Noise voltage 1500Vp-p	<ul> <li>By noise simulator of 1500Vp- p noise voltage, 1 µs noise width and 25 to 60Hz noise frequency</li> <li>Noise voltage IEC801-4, 2kV</li> </ul>	0	
Dielectric withs voltage	stand	Across external AC terminal batch and ground: 1500VAC for 1minute Across external DC terminal batch and ground: 500VAC for 1minute	Across inputs/LG and outputs/ FG 2830VAC rms/3 cycles (2000m)	0	
Insulation resis	stance	Across external AC terminal batch and ground: 5MΩ or more by 500VDC insulation resistance tester	Across inputs and outputs (LG and FG separated), across inputs and LG/FG, across outputs and FG/LG $10M_{\Omega}$ or more by 500VDC insulation resistance tester	0	
Accessory		Spare fuse: 1 Short chip for applied voltage select terminal: 1	None	×	Fuses are not included in accessories since they are not replaceable, nor are short chip since it is unnecessary to switch operating voltage.

#### (2) Specifications comparisons between the A62P and the A1S62PN

Creatifica	tion	A62P	A1S62PN	Compati-	Processitions for real-comment
Specifica	tion	A62P	A1562PN	bility	Precautions for replacement
Input power su	innly	100-120VAC+10%-15% (85 to 132VAC)	100-240VAC+10%-15%	0	
		200-240VAC+10%-15% (170 to 264VAC)	(85 to 264VAC)	0	
Input frequenc	у	50/60Hz±5%	50/60Hz±5%	0	
Input voltage d	istortion	5% within	5% within	0	
Max. input app power	arent	155VA	105VA	0	
Inrush current		20A within 8ms	20A within 8ms	0	
Rated output	5VDC	5A	3A	Δ	Confirm the current consumption
current	24VDC	0.8A	0.6A	Δ	of entire system.
Overcurrent	5VDC	5.5A or more	3.3A or more	0	
protection	24VDC	1.2A or more	0.66A or more	0	
Overvoltage	5VDC	5.5 to 6.5V	5.5 to 6.5V	0	
protection	24VDC	_	—	_	
Efficiency		65% or more	65% or more	0	
Power indicato	r	Power LED indication	LED indication (5VDC output: ON)	0	
Terminal screw	/ size	M4 × 0.7 × 6	M3.5×7	×	Wiring must be changed.
Applicable wire size		0.75 to 2mm <sup>2</sup>	0.75 to 2mm <sup>2</sup>	0	
Applicable solderless terminal		R1.25-4,R2-4 RAV1.25-4, RAV2-4	RAV1.25-3.5, RAV2-3.5	×	Wiring must be changed.
Applicable tightening torque		48 to 137N⋅cm	59 to 88N₊cm	×	Tighten within the applicable tightening torque.
External dimer	isions	250(H) × 55(W) × 121(D) mm	130(H) × 55(W) × 93.6(D) mm	Δ	
Weight		0.94kg	0.6kg	0	
Allowable mon power failure p		Within 20ms	Within 20ms	0	
Noise durabilit	y	Noise voltage 1500Vp-p	<ul> <li>By noise simulator of 1500Vp- p noise voltage, 1 µ s noise width and 25 to 60Hz noise frequency</li> <li>Noise voltage IEC801-4, 2kV</li> </ul>	0	
Dielectric withstand voltage		Across external AC terminal batch and ground: 1500VAC for 1minute Across external DC terminal batch and ground: 500VAC for 1minute	Across inputs/LG and outputs/ FG 2830VAC rms/3 cycles (2000m)	0	
Insulation resistance		Across external AC terminal batch and ground: $5M_{\Omega}$ or more by 500VDC insulation resistance tester	Across inputs and outputs (LG and FG separated), across inputs and LG/FG, across outputs and FG/LG 10M $\Omega$ or more by 500VDC insulation resistance tester	0	
Accessory		Spare fuse: 1 Short chip for applied voltage select terminal: 1	None	×	Fuses are not included in accessories since they are not replaceable, nor are short chip since it is unnecessary to switch operating voltage.

#### (3) Specifications comparisons between the A63P and the A1S63P

			$\bigcirc$ : Compatible, $\triangle$ : Partial change required, $\times$ : Incompatible				
Specifica	ition	A63P	A1S63P	Compati- bility	Precautions for replacement		
Input power supply		24VDC+30%-35% (15.6 to 31.2VDC)	24VDC+30%-35% (15.6 to 31.2VDC)	0			
Input frequenc	у	_	_	_			
Input voltage d	istortion	_	5% within	0			
Max. input app power	arent	65W	41W	0			
Inrush current		100A within 1ms	81A within 1ms	0			
Rated output current	5VDC	8A	5A	Δ	Confirm the current consumption of entire system.		
current	24VDC	—		-			
Overcurrent	5VDC	8.5A or more	5.5A or more	0			
protection	24VDC	_		_			
Overvoltage	5VDC	5.5 to 6.5V	5.5 to 6.5V	0			
protection	24VDC	—	_	_			
Efficiency		65% or more	65% or more	0			
Power indicator		Power LED indication	LED indication (5VDC output: ON)	0			
Terminal screw	/ size	M4 × 0.7 × 6	M3.5×7	×	Wiring must be changed.		
Applicable wire	e size	0.75 to 2mm <sup>2</sup>	0.75 to 2mm <sup>2</sup>	0			
Applicable solo terminal	lerless	R1.25-4, R2-4, RAV1.25-4, RAV2-4	RAV1.25-3.5, RAV2-3.5	×	Wiring must be changed.		
Applicable tigh torque	tening	98 to 137N₊cm	59 to 88N⋅cm	×	Tighten within the applicable tightening torque.		
External dimer	isions	250(H)×55(W)×121(D) mm	130(H)×55(W)×93.6(D) mm	Δ			
Weight		0.8kg	0.5kg	0			
Allowable mon power failure p		Within 1ms	Within 1ms	0			
Noise durabilit	y	Noise voltage 500Vp-p	<ul> <li>By noise simulator of 500Vp-p noise voltage, 1 µs noise width and 25 to 60Hz noise frequency</li> </ul>	0			
Dielectric withstand voltage		Across external DC terminal batch and ground: 500VAC for 1 minute	500VAC across primary and 5VDC	0			
Insulation resistance		Across external DC terminal batch and ground: 5MΩ or more by 500VDC insulation resistance tester	$5 M_\Omega$ or more by insulation resistance tester	0			
Accessory		Spare fuse: 1	None	×	Fuses are not included in accessories since they are not replaceable.		

#### (4) Specifications comparisons between the A61PEU and the A1S61PN

O: Compatible.	∆: Partial change	reauired.	x : Incompatible
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Specification		A61PEU	A1S61PN	Compati- bility	Precautions for replacement
	mahi	100-120VAC+10%-15% (85 to 132VAC)	100-240VAC+10%-15%	0	
Input power supply		200-240VAC+10%-15% (170 to 264VAC)	(85 to 264VAC)	0	
Input frequenc	y	50/60Hz±5%	50/60Hz±5%	0	
Input voltage d	listortion	5% within	5% within	0	
Max. input app power	parent	130VA	105VA	0	
Inrush current		20A within 8ms	20A within 8ms	0	
Rated output current	5VDC	8A	5A	Δ	Confirm the current consumption of entire system.
current	24VDC	<u> </u>		—	
Overcurrent	5VDC	8.8A or more	5.5A or more	0	
protection	24VDC	-	_	<u> </u>	
Overvoltage	5VDC	5.5 to 6.5V	5.5 to 6.5V	0	
protection	24VDC			—	
Efficiency		65% or more	65% or more	0	
Power indicato	or	Power LED indication	LED indication (5VDC output: ON)	0	
Terminal screw size		M4 × 0.7 × 6	M3.5×7	×	Wiring must be changed.
Applicable wire	e size	0.75 to 2mm <sup>2</sup>	0.75 to 2mm <sup>2</sup>	0	
Applicable solo terminal	derless	R1.25-4, R2-4, RAV1.25-4, RAV2-4	RAV1.25-3.5, RAV2-3.5	×	Wiring must be changed.
Applicable tigh torque	itening	98 to 137N⋅cm	59 to 88N₊ cm	×	Tighten within the applicable tightening torque.
External dimer	nsions	250(H) × 55(W) × 121(D) mm	130(H) × 55(W) × 93.6(D) mm	Δ	
Weight		0.8kg	0.6kg	0	
Allowable mon power failure p	-	Within 20ms	Within 20ms	0	
Noise durabilit	у	<ul> <li>By noise simulator of 1500Vp-p noise voltage, 1 µs noise width and 25 to 60Hz noise frequency</li> <li>Noise voltage IEC61000-4-4, 2kV</li> </ul>	<ul> <li>By noise simulator of 1500Vp-p noise voltage, 1 μ s noise width and 25 to 60Hz noise frequency</li> <li>Noise voltage IEC801-4, 2kV</li> </ul>	0	
Dielectric withs voltage	stand	Across inputs/LG and outputs/FG 2830VAC rms/3 cycles (2000m)	Across inputs/LG and outputs/FG 2830VAC rms/3 cycles (2000m)	0	
Insulation resistance		Across inputs and outputs (LG and FG separated), across inputs and LG/FG, across outputs and FG/LG $10M_{\Omega}$ or more by 500VDC insulation resistance tester	Across inputs and outputs (LG and FG separated), across inputs and LG/FG, across outputs and FG/LG $10M_{\Omega}$ or more by 500VDC insulation resistance tester	0	
Accessory		Spare fuse: 1 Short chip for applied voltage select terminal: 1	None	×	Fuses are not included in accessories since they are not replaceable, nor are short chip since it is unnecessary to switch operating voltage.

#### (5) Specifications comparisons between the A62PEU and the A1S62PN

				Compati-	
Specific	ation	A62PEU	A1S62PN	bility	Precautions for replacement
Input power supply		100-120VAC+10%-15% (85 to 132VAC)	100-240VAC+10%-15% (85 to 264VAC)	0	
		200-240VAC+10%-15% (170 to 264VAC)		0	
Input frequence	су	50/60Hz±5%	50/60Hz±5%	0	
Input voltage	distortion	5% within	5% within	0	
Max. input app power	parent	110VA	105VA	0	
Inrush current		20A within 8ms	20A within 8ms	0	
Rated output	5VDC	5A	3A	Δ	Confirm the current consumption
current	24VDC	0.8A	0.6A	Δ	of entire system.
Overcurrent	5VDC	5.5A or more	3.3A or more	0	
protection	24VDC	1.2A or more	0.66A or more	0	
Overvoltage	5VDC	5.5 to 6.5V	5.5 to 6.5V	0	
protection	24VDC				
Efficiency		65% or more	65% or more	0	
Power indicate	or	Power LED indication	LED indication (5VDC output: ON)	0	
Terminal screv	w size	M4 × 0.7 × 6	M3.5×7	×	Wiring must be changed.
Applicable wir	e size	0.75 to 2mm <sup>2</sup>	0.75 to 2mm <sup>2</sup>	0	
Applicable solderless terminal		RAV1.25-4, RAV2-4	RAV1.25-3.5, RAV2-3.5	×	Wiring must be changed.
Applicable tightening torque		118N. cm	59 to 88N⊷cm	×	Tighten within the applicable tightening torque.
External dime	nsions	250(H)×55(W)×121(D) mm	130(H) × 55(W) × 93.6(D) mm	Δ	
Weight		0.9kg	0.6kg	0	
Allowable momentary power failure period		Within 20ms	Within 20ms	0	
Noise durability		<ul> <li>By noise simulator of 1500Vp-p noise voltage, 1µs noise width and 25 to 60Hz noise frequency</li> <li>Noise voltage IEC61000-4-4, 2kV</li> </ul>	<ul> <li>By noise simulator of 1500Vp-p noise voltage, 1 µs noise width and 25 to 60Hz noise frequency</li> <li>Noise voltage IEC801-4, 2kV</li> </ul>	0	
Dielectric withstand voltage		Across inputs/LG and outputs/FG 2830VAC rms/3 cycles(2000m)	Across inputs/LG and outputs/FG 2830VAC rms/3 cycles (2000m)	0	
Insulation resistance		Across inputs and outputs (LG and FG separated), across inputs and LG/FG, across outputs and FG/LG $10M_{\Omega}$ or more by 500VDC insulation resistance tester	Across inputs and outputs (LG and FG separated), across inputs and LG/FG, across outputs and FG/LG10M $\Omega$ or more by 500VDC insulation resistance tester	0	
Accessory		Spare fuse: 1 Short chip for applied voltage select terminal: 1	None	×	Fuses are not included in accessories since they are not replaceable, nor are short chip since it is unnecessary to switch operating voltage.

#### (6) Specifications of the A68P

Specific	ation	A68P	
Input power s	vlaqu	100-120VAC+10%-15% (85 to 132VAC)	
		200-240VAC+10%-15% (170 to 264VAC)	
Input frequen	су	50/60Hz±5%	
Input voltage	distortion	_	
Max. input ap power	parent	95VA	
Inrush current	t	20A within 8ms	
Rated	+15VDC	1.2A	
output current	-15VDC	0.7A	
Overcurrent	+15VDC	1.64A or more	
protection	-15VDC	0.94A or more	
Efficiency		65% or more	
Power indicat	or	Power LED indication	
		Contact output	
Power ON inc	licator	Switched on if +15VDC output is +14.25V or higher or -15VDC output is -14.25V or lower.	
		Min. contact switching load: 5VDC, 10mA Max. contact switching load: 264VAC, 2A (R load)	
Terminal scre	w size	$M3 \times 0.5 \times 6$	
Applicable wit	re size	0.75 to 2mm <sup>2</sup>	
Applicable solderless terminal		V1.25-4, V1.25-YS4A, V2-S4, V2-YS4A	
Applicable tightening torque		68N. cm	
External dime	ensions	250(H) × 75.5(W) × 121(D) mm	
Weight		0.9kg	

Substitute the general-purpose switching power supply, whose specifications are shown below, for the A68P. Choose current capacity with the result of calculating the current consumption of entire system to be used.

Specification	General-purpose Switching Power Supply	
Valtage	+15VDC±3%(14.55V to 15.45V)	
Voltage	-15VDC±3%(-14.55V to -15.45V)	
Ripple voltage	50mVp-p or less	
Spike voltage	100mVp-p or less	
Output voltage limit	Within $\pm$ 1V	

## 4.3 Precautions for Power Supply Module Replacement

#### (1) Power supply module selection

Current consumption differs between the AnS series and A series modules. Select the power supply module with the result of calculating the current consumption of entire system.

#### (2) Wiring

Applicable wire and crimping terminals for terminal blocks differ between the AnS series and the A series. Use the wire and crimping terminals compatible with the specifications.

# **5** BASE UNIT AND EXTENSION CABLE REPLACEMENT

# 5.1 List of Alternative Models for Base Unit and Extension Cable

Large-sized A/QnA series model to be discontinued		Small-sized AnS/Q2AS series alternative model		
Product	Model	Model	Remarks (restrictions)	
	A32B	A1S32B		
	A35B	A1S35B		
	A38B	A1S38B		
	A38B-UL	A1S38B		
Main base unit	A32B-E	A1S32B-E		
	A35B-E	A1S35B-E		
	A38B-E	A1S38B-E		
	A32B-S1	A1S32B		
	A38HB	A1S38HB	Cannot be used for the A2USHCPU-S1.	
	A38HBEU	A1S38HBEU	Cannot be used for the A2USHCPU-S1.	
	A52B	A1S52B		
	A55B	A1S55B		
	A58B	A1S58B		
Extension base unit	A62B	A1S65B	Change in number of I/O slots: 2 slots $\rightarrow$ 5 slots	
	A65B	A1S65B		
	A68B	A1S68B		
	A68B-UL	A1S68B		
	AC06B	A1SC07B	Cable length: $0.6m \rightarrow 0.7m$	
	AC12B	A1SC12B		
	AC30B	A1SC30B		
Extension cable	AC50B	A1SC60B	Cable length: $5.0m \rightarrow 6.0m$	
	A1SC05NB	A1SC07B	Cable length: $0.45m \rightarrow 0.7m$	
	A1SC07NB	A1SC07B		
	A1SC30NB	A1SC30B		
	A1SC50NB	A1SC60B	Cable length: 5.0m $\rightarrow$ 6.0m	

## 5.2 Base Unit and Extension Cable Specifications Comparisons

#### 5.2.1 Base unit specifications comparisons

#### (1) Main base unit

#### (a) Comparisons between the A32B(-E) and the A1S32B(-E)

	Ту		
Item	Large-sized A/QnA series	Small-sized AnS/Q2AS series	Precautions for replacement
	A32B(-E)	A1S32B(-E)	
Loaded I/O modules	2 can be loaded.		
Extension availability	Cannot connect extension modules.	Extendable	
Mounting hole size	$\phi$ 6 mm dia. pear-shap	bed hole (for M5 screw)	Refer to Section 5.3.1 for replacement precautions.
External dimension	250(H) × 247(W) × 29(D)mm	130(H) × 220(W) × 28(D)mm	
Dimension for mounting to the panel	227 × 200mm	200 × 110mm	

#### (b) Comparisons between the A32B-S1 and the A1S32B

	Ту		
Item	Large-sized A/QnA series	Small-sized AnS/Q2AS series	Precautions for replacement
	A32B-S1	A1S32B	
Loaded I/O modules	2 can be		
Extension availability	Exter	1	
Mounting hole size	$\phi$ 6 mm dia. pear-shaped hole (for M5 screw)		Refer to Section 5.3.1 for
External dimensions	250(H) × 268(W) × 29(D)mm	130(H) × 220(W) × 28(D)mm	replacement precautions.
Dimensions for mounting to the panel	248 × 200mm	200 × 110mm	

#### (c) Comparisons between the A35B(-E) and the A1S35B(-E)

	Ту		
Item	Large-sized A/QnA series	Small-sized AnS/Q2AS series	Precautions for replacement
	A35B(-E)	A1S35B(-E)	
Loaded I/O modules	5 can be		
Extension availability	Exter		
Mounting hole size	$\phi$ 6 mm dia. pear-shaped hole (for M5 screw)		Refer to Section 5.3.1 for
External dimensions	250(H) × 382(W) × 29(D)mm	130(H) × 325(W) × 28(D)mm	replacement precautions.
Dimensions for mounting to the panel	362 × 200mm	305 × 110mm	

#### (d) Comparisons between the A38(-E/-UL)/A38HB/A38HBEU and the A1S38B(-E)/A1S38HB/ A1S38HBEU

	Ту		
Item	Large-sized A/QnA series	Small-sized AnS/Q2AS series	Precautions for replacement
nem	A38B(-E/-UL)/A38HB/ A38HBEU	A1S38B(-E)/A1S38HB/ A1S38HBEU	Precautions for replacement
Loaded I/O modules	ed I/O modules 8 can be loaded.		
Extension availability	Extendable		
Mounting hole size	$\phi$ 6 mm dia. pear-shaped hole (for M5 screw)		Refer to Section 5.3.1 for
External dimensions	250(H) × 480(W) × 29(D)mm	130(H) × 430(W) × 28(D)mm	replacement precautions.
Dimensions for mounting to the panel	460 × 200mm	410 × 110mm	

#### (2) Extension base unit (No power supply module required)

#### (a) Comparisons between the A52B and the A1S52B

	Ту		
Item	Large-sized A/QnA series	Small-sized AnS/Q2AS series	Precautions for replacement
	A52B	A1S52B	
Loaded I/O modules	2 can be loaded.		
Extension availability	Extendable	Cannot connect extension modules.	
Mounting hole size	size $\phi$ 6 mm dia. pear-shaped hole (for M5 screw)		Refer to Section 5.3.1 for
External dimensions	250(H) × 183(W) × 29(D)mm	130(H) × 155(W) × 28(D)mm	replacement precautions.
Dimensions for mounting to the panel	163 × 200mm	135×110mm	

#### (b) Comparisons between the A55B and the A1S55B

	Т		
Item	Large-sized A/QnA series	Small-sized AnS/Q2AS series	Precautions for replacement
	A55B	A1S55B	
Loaded I/O modules	5 can be loaded.		
Extension availability	Extendable	Cannot connect extension modules.	
Mounting hole size	$\phi$ 6 mm dia. pear-shaped hole (for M5 screw)		Refer to Section 5.3.1 for
External dimensions	250(H) × 297(W) × 29(D)mm	130(H) × 260(W) × 28(D)mm	replacement precautions.
Dimensions for mounting to the panel	277 × 200mm	240 × 110mm	

#### (c) Comparisons between the A58B and the A1S58B

	Т		
Item	Large-sized A/QnA series	Small-sized AnS/Q2AS series	Precautions for replacement
	A58B	A1S58B	
Loaded I/O modules	8 can be loaded.		
Extension availability	Extendable	Cannot connect extension modules.	
Mounting hole size	$\phi$ 6 mm dia. pear-shaped hole (for M5 screw)		Refer to Section 5.3.1 for
External dimensions	250(H)×411(W)×29(D)mm	130(H) × 365(W) × 28(D)mm	replacement precautions.
Dimensions for mounting to the panel	391 × 200mm	345 × 110mm	

#### (3) Extension base unit (Power supply module loaded)

#### (a) Comparisons between the A62B and the A1S65B

	т	уре	
Item	Large-sized A/QnA series	Small-sized AnS/Q2AS series	Precautions for replacement
	A62B	A1S65B	
Loaded I/O modules	2 can be loaded.	5 can be loaded.	
Extension availability	Extendable	Cannot connect extension modules.	]
Mounting hole size	$\phi$ 6 mm dia. pear-sha	ped hole (for M5 screw)	Refer to Section 5.3.1 for
External dimensions	250(H) × 283(W) × 29(D)mm	130(H) × 315(W) × 28(D)mm	replacement precautions.
Dimensions for mounting to the panel	218 × 200mm	295×110mm	

#### (b) Comparisons between the A65B and the A1S65B

		уре	
Item	Large-sized A/QnA series	Small-sized AnS/Q2AS series	Precautions for replacement
	A65B	A1S65B	
Loaded I/O modules	5 can b	e loaded.	
Extension availability	Extendable	Cannot connect extension modules.	
Mounting hole size	$\phi$ 6 mm dia. pear-sha	ped hole (for M5 screw)	Refer to Section 5.3.1 for
External dimensions	250(H) × 352(W) × 29(D)mm	130(H) × 315(W) × 28(D)mm	replacement precautions.
Dimensions for mounting to the panel	332 × 200mm	295 x 110mm	

#### (c) Comparisons between the A68B (-UL) and the A1S68B

	Ту	уре	
Item	Large-sized A/QnA series	Small-sized AnS/Q2AS series	Precautions for replacement
	A68B(-UL)	A1S68B	
Loaded I/O modules	8 can b	e loaded.	
Extension availability	Extendable	Cannot connect extension modules.	
Mounting hole size	$\phi$ 6 mm dia. pear-sha	ped hole (for M5 screw)	Refer to Section 5.3.1 for
External dimensions	250(H) × 466(W) × 29(D)mm	130(H) × 420(W) × 28(D)mm	replacement precautions.
Dimensions for mounting to the panel	446 × 200mm	400 × 110mm	

#### 5.2.2 Extension cables specifications comparisons

			Ту	vpe		
lten	Item		A/QnA series	Small-sized AnS/Q2AS series	Precautions for replacement	
		A Main-A Extension	AnS Main-A Extension	AnS Main-AnS Extension		
	0.45m	—	A1SC05NB	A1SC07B		
	0.6m	AC06B	—	A1SC07B		
	0.7m	—	A1SC07NB	A1SC07B	]	
Cable length	1.2m	AC12B	—	A1SC12B	Refer to Section 5.3.2 for replacement precautions.	
	3.0m	AC30B	A1SC30NB	A1SC30B		
	5.0m	AC50B	—	A1SC60B		
	5.0m	_	A1SC50NB	A1SC60B		

## 5.3 Precautions for Base Unit and Extension Cable Replacement

#### 5.3.1 Precautions for base unit replacement

#### (1) Installation dimensions

When replacing the large-sized A/QnA series base unit with the small-sized AnS/Q2AS series, it is necessary to rework the mounting holes to fix the unit to a control panel, since the two series have different mounting hole size.

#### (2) Number of extension stages

For the small-sized AnS/Q2AS series, up to one stage of the extension base unit is applicable. If the extension stages are two stages or more, consider replacing by the Q series.

#### 5.3.2 Precautions for extension cable replacement

#### (1) Total extension distance of extension cable

The total extension distance of the small-sized AnS/Q2AS series extension cable is up to 6.0m while that of the large-sized A/QnA series is 6.6m. Select a suitable cable according to your system.

# 6 MEMORY AND BATTERY REPLACEMENT

# 6.1 List of Alternative Models for Memory

#### (1) Large-sized A series

Large-sized A series m	nodel to be discontinued	Sma	all-sized AnS series alternative model
Product	Model	Model	Remarks (restrictions)
	A3NMCA-0	Unnecessary	
	A3NMCA-2	Unnecessary	
	A3NMCA-4	Unnecessary	
	A3NMCA-8	Unnecessary	
	A3NMCA-16	Unnecessary	
	A3NMCA-24	Unnecessary	Built-in RAM is the alternative.
Memory cassette	A3NMCA-40	Unnecessary	For ROM operation, the A2SNMCA-30KE is
	A3NMCA-56	Unnecessary	required.
	A3AMCA-96	Unnecessary	
	A4UMCA-128	Unnecessary	
	A4UMCA-8E	Unnecessary	
	A4UMCA-32E	Unnecessary	
	A4UMCA-128E	Unnecessary	
IC-RAM memory	4KRAM	Unnecessary	Built-in RAM is the alternative.
E <sup>2</sup> PROM memory	4KEROM	A2SNMCA-30KE	Use the memory cassette.
	4KROM *1		
	8KROM *1	]	
EPROM memory	16KROM *1	A2SNMCA-30KE	Use the memory cassette.
	32KROM	1	
	64KROM		

\*1 Will be discontinued in the end of September, 2008.

#### (2) Large-sized QnA series

Large-sized QnA series model to be discontinued		Small-sized Q2AS series alternative model		
Product	Model	Model	Remarks (restrictions)	
	Q1MEM-64S	Q1MEM-64S		
	Q1MEM-128S	Q1MEM-128S		
	Q1MEM-256S	Q1MEM-256S		
	Q1MEM-512S	Q1MEM-512S		
	Q1MEM-1MS	Q1MEM-1MS		
IC memory card	Q1MEM-2MS	Q1MEM-2MS	No. of IC memory cards is changed from two to one.	
	Q1MEM-64SE	Q1MEM-64SE		
	Q1MEM-128SE	Q1MEM-128SE		
	Q1MEM-256SE	Q1MEM-256SE		
	Q1MEM-512SE	Q1MEM-512SE		
	Q1MEM-1MSE	Q1MEM-1MSE		

### 6.2 Precautions for Memory and Battery Replacement

#### (1) Precaution for memory replacement

#### (a) Necessity of memory cassette

The A2USHCPU-S1 does not need memory cassettes for the RAM operation, since its CPU module incorporates the built-in RAM. For the ROM operation, the memory cassette of the A2SNMCA-30KE is needed.

#### (b) Memory capacity

If the memory capacities are insufficient depending on types of CPU module or memory cassette after the replacement, consider replacing by the Q series. For the memory capacity and configurations used on CPU module, refer to Section 2.4.1.

#### (2) Precaution for battery replacement

All the batteries (A6BAT) for the large-sized A/QnA series and small-sized AnS/Q2AS series are common.

Refer to the users manual of each CPU module for battery life, since it varies depending on the type of CPU module and memory cassette.

7

# **PROGRAM REPLACEMENT**

This chapter explains how to replace (reuse) the programs and comments of the large-sized A/QnA series CPU with the small-sized AnS/Q2AS series, and precautions for the replacement.

#### (1) Comparisons between the large-sized A series CPU and the A2USHCPU-S1

			O: Compatible, ∆: Partial change requ	uired, ×:In	compatible
Item		Large-sized A series CPU specification	The A2USHCPU-S1 specification and precautions for replacement	Compati- bility	Reference section
	Main	. Main program is required	[Specification] <ul> <li>Only main and SFC programs</li> </ul>		
	Sub 1	<ul> <li>Main program is required.</li> <li>Sub programs, if included, are switched with the CLIC</li> </ul>	[Measure]		
Sequence program	Sub 2	switched with the CHG instructions.	The sequence program of Sub 1 to 3 must be added to the main	Δ	_
	Sub 3	The SFC is dealt as the microcomputer program of main	program. If the program exceeds 30k steps		
	SFC	program.	after the addition, consider replacing by the QCPU.		
Microcompute program	er	<ul> <li>A user-created microcomputer program and the microcomputer program of the utility package are available for the AnNCPU.</li> </ul>	<ul> <li>[Specification]</li> <li>Creating microcomputer program is not possible.</li> <li>[Measure]</li> <li>Since the AnNCPU user-created microcomputer program cannot be performed, consider replacing the microcomputer program with sequence program or the A2SHCPU in which the execution is possible.</li> <li>For the utility packages instructions, correct them equivalent to the corresponding instructions of the A2USHCPU- S1.</li> </ul>	Δ	_
Instruction		<ul> <li>For the A3N, A3A, A3U, A4UCPU, the display instructions (LED instruction, etc.) are available.</li> </ul>	<ul> <li>[Specification]</li> <li>The display instructions (LED, LEDA, LEDB, LEDC) cannot be used.</li> <li>[Measure]</li> <li>The display instructions (LED, LEDA, LEDB, LEDC) must be deleted.</li> </ul>	Δ	_
File register		<ul> <li>Storage area is reserved in a memory cassette.</li> <li>One block is set in 4k or 8k-point units.</li> </ul>	<ul><li>[Specification]</li><li>Data is stored in the built-in RAM.</li><li>One block is set in 8k-point units.</li></ul>	Δ	Section 7.3.4

		$\bigcirc$ : Compatible, $\triangle$ : Partial change requ	uired, ×∶Ir	ncompatible
Item	Large-sized A series CPU specification	The A2USHCPU-S1 specification and precautions for replacement	Compati- bility	Reference section
Timer, Counter	Timer and counter are processed with the END processing.	<ul><li>[Specification]</li><li>Same specifications</li></ul>	0	
Parameter	<ul> <li>Parameters are dedicated for each CPU.</li> </ul>	<ul> <li>[Specification]</li> <li>Each CPU has the dedicated parameters.</li> <li>[Measure]</li> <li>When replacing by the A2USHCPU-S1, check and reset the parameters since specifications and functions differ between the two CPUs.</li> </ul>	Δ	Section 7.2.1
Special relay	<ul> <li>256 points of M9000 to M9255 are provided.</li> </ul>	<ul><li>[Specification]</li><li>Same specifications</li></ul>	0	_
Special register	256 points of D9000 to D9255 are provided.	<ul><li>[Specification]</li><li>Same specifications</li></ul>	0	_
Comment	<ul> <li>Comments are managed as a common comment or comment by program.</li> <li>The comment capacity of the ACPU is up to 127k (64k + 63k) bytes.</li> </ul>	[Specification] • Same specifications	0	_
Writing programs to ROM	<ul> <li>The ROM operation is executed with the EPROM.</li> </ul>	<ul> <li>[Specification]</li> <li>The E<sup>2</sup>PROM cassette is installed to the CPU, and the ROM operation is executed.</li> <li>[Measure]</li> <li>By replacing by E<sup>2</sup>PROM, writing to PLC can be made by the operation equivalent to that of RAM.</li> </ul>	Δ	Section 7.3.5

#### (2) Comparisons between the large-sized QnA series CPU and the small-sized Q2AS series CPU

Item	Large-sized QnA series CPU specification	Small-sized Q2AS series CPU specifications and precautions for replacement	Compati- bility	Reference section
Sequence program SFC program	Each program is dealt as one file.	<ul><li>[Specification]</li><li>Same specifications</li></ul>	0	_
Instruction	Each instruction described in the QCPU (Q mode)/QnACPU Programming Manual (Common Instructions/PID Control Instructions/SFC, etc.) is usable.	[Specification] • Same specifications	0	
File register	<ul> <li>Data is stored in a memory card.</li> <li>One block is set in 32K-point units.</li> <li>Up to two memory cards can be installed.</li> </ul>	<ul> <li>[Specification]</li> <li>Same specifications, and one memory card can be installed.</li> <li>[Measure]</li> <li>Review the setting since the number of memory cards differs between the two CPUs.</li> </ul>	Δ	Section 7.3.4
Parameter	<ul> <li>Each CPU has the dedicated parameters.</li> </ul>	[Specification] <ul> <li>Same specifications</li> </ul>	0	_
Special relay	<ul> <li>1800 points of SM0 to SM1799 are provided.</li> </ul>	<ul><li>[Specification]</li><li>Same specifications</li></ul>	0	_
Special register	<ul> <li>1800 points of SD0 to SD1799 are provided.</li> </ul>	[Specification] <ul> <li>Same specifications</li> </ul>	0	_
Comment	<ul> <li>Comments are managed as a common comment or comment by program.</li> </ul>	[Specification] <ul> <li>Same specifications</li> </ul>	0	_
Writing programs to ROM	<ul> <li>The boot run is executed with storing a program and parameter in a memory card.</li> <li>Up to two memory cards can be installed.</li> </ul>	<ul> <li>[Specification]</li> <li>Same specifications, and one memory card can be installed.</li> <li>[Measure]</li> <li>Review the setting since the number of memory cards differs between the two CPUs.</li> </ul>	Δ	Section 7.3.5

# 7.1 Program Replacement Procedure

The programs and comments of the large-sized A/QnA series CPU can be replaced with the small-sized AnS/Q2AS series by using "Change PLC type" of the GX Developer.

#### 7.1.1 Change PLC type operation

The Change PLC type is a function that changes the target PLC type of the data read to the GX Developer.

For special function modules and network modules, review programs and parameters.

#### (1) Applicable range of conversion from the A/QnACPU by the GX Developer

The following table shows the applicable range of conversion from the A/QnACPU to other PLCs. As it shows, converting to all PLC CPUs is applicable.

Product	Change source	Change destination PLC			
Product	PLC	ACPU	QnACPU	QCPU	
GX Developer	ACPU	0	0	0	
GA Developei	QnACPU	0	0	0	

#### (2) Operation of GX Developer

(a) Select the "Change PLC type" of the "Project" menu.

•	MELSOFT series GX Developer (Unset project	st) - [LD[Edit mode) MAIN 37 Step]	_ 8 ×
	Project Edit Eind/Replace Convert View Or New project Ctrl+N		@_×
[	New project Ctrl+N Open project Ctrl+O	E Carlos Correction Co	
H	Close project Save Ctrl+S	11- 41- 41- 41- 45- 45- 45- 45- 45- 45- 45- 45- 45- 45	
	Save Chl+S Save_gs		
	Delete project	MO	×
E	Vejity Copy	(M1 )	_
II.	Edit Data		
L	Change PLC type	FIND 7	
L	Import file   Export file	Levan 1	
L	Macro Function Block		
	Printer setyp Print Ctrl+P		
	1 C:\My Documents\\program		
II.	2 C:\MELSEC\LLT\Sample\program 3 C:\WINDOWS\Desktop\Sample1		
II.	4 C:\WINDOWS\Desktop\qcpu_fb		
L	Start new <u>G</u> X Developer session Egit GX Developer		
E	Project		•

(b) Specify the PLC type after conversion in the "Change PLC type" dialog box.

PLC series	
	ок 📘
ACPU 🔹 🗖	
PLC type	Cancel
A2USH-S1	

Click the [OK] button after setting the PLC type.

PLC series setting	
Change PLC type	×
PLC series	ок
ACPU PLC QCPU(Qmode) QCPU(Amode) QnACPU ACPU MOTION(SCPU) FXCPU CNC(M6/M7)	Cancel
PLC type setting	
Change PLC type	×
PLC series	ОК
ACPU	
PLC type	Cancel
A2USH-S1	
A3A A2U(S1) A2US(S1) A2AS(S1) A2AS-S30 A2AS-S60 A2USH-S1 A3U	
A4U 🔽	

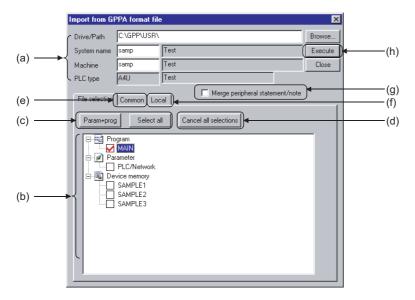
#### 7.1.2 Reading (Reusing) other format files

The following explains how to read (reuse) GPPQ/GPPA format files other than that of the GX Developer. Follow this procedure to convert them to the file format of the GX Developer.

#### (1) Operation procedure

Select [Project] → [Import file] → [Import from GPPQ format file] [Import from GPPA format file]

#### (2) Setting screen



#### (a) Drive/Path, System name, Machine

Designates the location of data created in GPPQ or GPPA format. Enter the system name and machine name of the data specified in the Drive/Path. Clicking the [Browse] button shows the dialog box for choosing the system name and machine name. Double-click the file to be read to specify it.

#### (b) Source data list

Displays data created in GPPQ or GPPA format.

Check the checkbox of data name.

For the selected comments, the device comment range to be read can be set in the Common tab or Local tab.

#### (c) [Param+prog] button/[Select all] button

[Param+prog] button

Select only the parameter data and program data of the source data.

[Select all] button
 Select all data in the source data list.
 Comment2 is selected for the A series, and the device memories of the number of data are
 displayed.
 The first data name is selected for comments and file registers in the QnA series.

#### (d) [Cancel all selections] button

Cancels all the selected data.

#### (e) <<Common>> tab screen (A series)

Set this when specifying the range for common comments and reading data.

Import from GF	PPA forma	t file					
Drive/Path C:\GPP\USR							Browse
System name	, 						Execute
Machine		-					Close
PLC type							
File selection	Common	Local	Γ				
Comment ty	[Read/Write the comments of the set range.]       [f closed, the changed range scilication[Excluding Extended Comments]         Comment/tipe specification       Comment2         Range specification       Comment2         PLC/SEPA Format       GX Developer Format						
Device	Start	End		Start	J End		
XX							
Special M Special D							
	Comment1/2 memory capacity K Bytes						
Device	Start	End		Start	End 🧖		
						-	
						-	
	Extended comment memory capacity 0 K Bytes						

#### (f) <<Local>> tab screen (A series)

Set this when specifying the range for comments by program and reading data.

Import from GPP	A format fi	le					×		
Drive/Path	:\GPP\USR	-	-	-	-	E	Irowse		
System name	ame								
Machine		- ír					Close		
PLC type									
[Read/Write the If closed, the cl	File selection         Common         Local         Merge peripheral istatement/note           [Read/Write the comments of the set range.]         If closed, the changed range specifications will be deleted.         PL/SEPPA Format         PA								
Device	Start	End		Start	End				
М									
S									
B									
F									
T									
С									
D									
W									
R									
P									
<u> </u>									
	Comment 1/2 memory capacity 0 K Bytes								

#### (g) Merge peripheral statement/note

#### (h) [Execute] button

Click this button after making the setting.

#### (3) Setting procedure

#### (a) Data selection

- 1) Set a drive/path for reading in GPPQ or GPPA format.
- 2) Click the [Browse] button to set the system name and machine name of the project to be read.
- 3) Check the checkbox of the data to be selected with the [Param+prog] button, [Select all] button, or the mouse.
- 4) Click the [Execute] button after making necessary settings.

#### (b) Canceling data selection

- When canceling the selected data arbitrarily: Clear the checkmark (√) in the checkbox with the mouse or space key.
- 2) When canceling all the selected data: Click the [Cancel all selections] button.

#### (4) Precautions for reading the other format files

	For A series
A6GPP, SW0S-GPPA format data	Read data with the GX Developer after performing the corresponding format conversion with GPPA. For the operating methods, refer to the Type SW4IVD-GPPA(GPP) Operating Manual.
For data selection	For device comment selection, you can only choose either comment 2 or comment 1.
GPPA format file reading	Deletes the project data on the GX Developer and reads the other format file. The area in excess of the program capacity is deleted when read. For the PLC type which cannot use subprograms, subprograms are deleted when read. When the file includes microcomputer programs edited with other than the SFC program (e.g. SW0SRX-FNUP), they are lost.

	For QnA series
Ladder return positions	Returning places are different between GPPQ and GX Developer. Because of this, if the total of return sources and return destinations exceeds 24 lines in a single ladder block, the program is not displayed properly. Corrective action: Add SM400 (normally ON contact) to adjust the return positions.
For data selection	For the device memory and file register, you can select only one data name for each item.

## 7.2 Precautions for Parameter Replacement

#### 7.2.1 Conversion from large-sized A series CPU to small-sized AnS series CPU

This section explains the parameter conversion upon replacement of the large-sized A series CPU programs with the small-sized AnS series CPU.

<Compatibility>

- ○:Common item between the large-sized A series CPU and the small-sized AnS series CPU, that can be converted directly.
- $\bigtriangleup:$  ltem that requires re-setting after the conversion, since the functions/specifications are partially different
- × :Items to be deleted, since there is no common item between the large-sized A series and the smallsized AnS series CPU

Confirm the parameters after the conversion, and correct/reset as required.

#### (1) When replacing the AnNCPU by the A2USHCPU-S1

Name		Compati-	Remarks	
		Name	bility	When replacing the AnNCPU by the A2USHCPU-S1
	Memory capacity	Program capacity	Δ	Main microcomputer program capacity is dedicated to the SFC. Sub microcomputer program capacity is not converted.
	' cal	Comment	0	
PLC parameter	lory	Expanded comment	0	Can be converted directly.
	Men	File register	0	
	~	Capacity for debugging	×	Not converted since it is the online setting.
	RAS	WDT (watchdog timer) setting	Δ	Not converted since fixed to 200ms.
	PLC R	Operation mode when these is an error	Δ	Since the setting is converted to default, resetting is required when the setting has been changed.
pan		Annunciator display mode	×	No compatible function is available.
PLC		RUN - PAUSE contacts	Δ	Resetting is required since the setting is converted to default.
	system	System interrupt settings	Δ	The caution is required since the specifications differ.
	PLC s	Output mode at STOP to RUN	Δ	Resetting is required since the setting is converted to default.
	н	Data communications request batch processing	-	The new AnUCPU function
	I/O as	ssignment	Δ	Resetting is required since the setting is not converted.
	Device			Resetting is required since the setting is converted to default.

# 7 PROGRAM REPLACEMENT

Network parameter

				Compati-	Remarks
			Name	bility	When replacing the AnNCPU by the A2USHCPU-S1
		Ъ	Network type	0	Can be converted directly.
	ameter station parameter	ramete	Start I/O No.	Δ	The I/O No. of module installed must be changed since the start I/O No. is set to the default value of "00".
		n pa	Total stations	0	
		statior	Network range assignment	0	Can be converted directly.
twork par Master	Master	Refresh parameters	Δ	New setting is required. For 3-tier master station, the setting is required including the link range of 2-tier local station.	
MELSECNET (II) network parameter	parameter	Network type	Δ		
	MELSECN	Local station para	Start I/O No.	Δ	New settings for the network type and start I/O No. are required. If there is no 3-tier master station, the refresh parameter setting is also required.
		Local	Refresh parameters		
М	MELSECNET/MINI parameter		Δ	The AnU and A2USHCPU-S1 can be set in the program and the parameter. The AnNCPU program can be diverted and does not require modification. For changing to the parameter setting, a new setting is required.	

**MELSEC** 

#### (2) When replacing the AnACPU by the A2USHCPU-S1

Name		Compati-	Remarks		
		Name	bility	When replacing the AnACPU by the A2USHCPU-S1	
	ity	Program capacity	0		
	oaci	Comment	0	Can be converted directly.	
	cal	Expanded comment	0		
	ory	File register	0		
	Memory capacity	Capacity for debugging	0	Since it is the online setting, parameter setting is not required.	
parameter	Ŋ	WDT (watchdog timer) setting	0	Fixed to 200ms.	
	PLC RAS	Operation mode when these is an error	0	Can be converted directly.	
C para	Ē	Annunciator display mode	×	No compatible function is available.	
PLC	_	RUN - PAUSE contacts	0		
	'stem	System interrupt settings	0	Can be converted directly.	
	PLC system	Output mode at STOP to RUN	0		
	ш.	Data communications request batch processing		The new AnUCPU function	
	I/O as	signment	0	Can be converted directly.	
	Dev. P	Point	0		

			Name	Compati- bility	Remarks When replacing the AnACPU by the A2USHCPU-S1
		L	Network type	0	Can be converted directly.
		Master station parameter	Start I/O No.		The I/O No. of module installed must be changed since the start I/O No. is set to the default value of "00".
	ameter	ation pa	Total stations	0	Can be converted directly
eter	ork par	ister sta	Network range assignment	0	Can be converted directly.
Network parameter	. (II) netw	Ma	Refresh parameters	Δ	New setting is required. For 3-tier master station, the setting is required including the link range of 2-tier local station.
Netwo	Network parameter MELSECNET (II) network parameter	ameter	Network type	Δ	
		-ocal station parameter	Start I/O No.	Δ	New settings for the network type and start I/O No. are required. If there is no 3-tier master station, the refresh parameter setting is also required.
		Local s	Refresh parameters	Δ	
	MELS	SECN	ET/MINI parameter	0	Can be converted directly.

#### (3) When replacing the AnUCPU by the A2USHCPU-S1

	Name		Comati-	Remarks
			bility	When replacing the AnUCPU by the A2USHCPU-S1
	N	Program capacity	0	
	oacit	Comment	0	Can be converted directly
	r cap	Expanded comment	0	Can be converted directly.
	Memory capacity	File register	0	
	Mer	Capacity for debugging	0	Since this is the online setting, parameter setting is not required.
Ŀ	RAS	WDT (watchdog timer) setting	0	Fixed to 200ms.
PLC parameter	PLC R.	Operation mode when these is an error	0	Can be converted directly.
ă U		Annunciator display mode	×	No compatible function is available.
ЫС		RUN - PAUSE contacts	0	
	stem	System interrupt settings	0	
	PLC system	Output mode at STOP to RUN	0	Can be converted directly.
	Ц	Data communications request batch processing	0	
	I/O as	signment	0	
	Dev. F	Point	0	

# 7 PROGRAM REPLACEMENT

			Name	Compati- bility	Remarks When replacing the AnUCPU by the A2USHCPU-S1
		eter	Network type	0	
	L	arame	Start I/O No.	0	
	ramete	Master station parameter	Total stations	0	
	ork pai	ster st	Network range assignment	0	
	) netw	Ма	Refresh parameters	0	
	MELSECNET (II) network parameter	ameter	Network type	0	
	MELSE	Local station parameter	Start I/O No.	0	
Network parameter		Local st	Refresh parameters	0	
rk para		Start I/O No.		0	Can be converted directly.
Netwo	,	Network No. Total of link device (slave) stations Refresh parameters Network range assignment Station inherent parameters I/O assignment	vork No.	0	
	Irametei			0	
	/ork pa		0		
	0 netv	Netv	Network range assignment		
	NET/1	Stati	ion inherent parameters	0	
	ELSEC	I/O a	assignment	0	
	Σ	Transfer between link devices		0	
	Routing parameters		ting parameters	0	
	MELSE	ECNE	T/MINI parameter	0	

#### 7.2.2 Conversion from large-sized QnA series CPU to small-sized Q2AS series CPU

This section explains the parameter conversion upon replacement of the large-sized QnA series CPU programs with the small-sized Q2AS series CPU. The symbols in the table indicate the followings.

<Compatibility>

- ○:Common item between the large-sized QnA series CPU and the small-sized Q2AS series CPU, that can be converted directly.
- ∆:Item that requires re-setting after the conversion, since the functions/specifications are partially different
- $\times$  :Items to be deleted, since there is no common item between the large-sized QnA series CPU and the small-sized Q2AS series CPU

Confirm the parameters after the conversion, and correct/re-set as required.

Name		Compati- bility	Remarks	
	PLC name		0	
PLC parameter	PLC system	Timer limit setup	0	
		RUN - PAUSE contacts	0	
		Remote reset	0	
		Output mode at STOP to RUN	0	
		Common pointer	0	
		General data processing	0	
		Number of empty slots	0	
		System interrupt settings	0	
	PLC file	File register	0	
		Comment file used in a command	0	
		Device initial value	0	
		File for local device	0	
	Device		0	
	PLC RAS	WDT (watchdog timer) setting	0	Can be converted directly.
		Error check	0	
		Operation mode when these is an error	0	
		Constant scanning	0	
		Annunciator display mode	0	
		Brakedown history	0	
		Low speed program execution time	0	
	I/O assignment		0	
	Program		0	
	Boot file		0	
	SFC	SFC program start mode	0	
		Start conditions	0	
		Operation mode when the block is stopped	0	
	Acknowledge X/Y assignment		0	

	Name	Compati- ibility	Remarks
parameter	MELSECNET/Ethernet	0	
Network para	MELSECNET/MINI	0	Can be converted directly.
Ne	CC-Link	0	

## 7.3 Precautions for Program Replacement

#### 7.3.1 List of applicable devices

De	vice name		QnACPU		Q2AS(H)CPU-(S1)		
	Number of I/O points <sup>*1</sup>		Q2A: 512 points Q2A-S1: 1024 points Q3A: 2048 points Q4A: 4096 points	Q2AS-S Q2ASH	512 points 51: 1024 points : 512 points -S1: 1024 points		
Number of I/	/O device p	oints*2		8192 points			
Internal relay	у			3192 points <sup>*3</sup>			
Latch relay				8192 points <sup>*3</sup>			
Step relay	Sequenc program	e		—			
	SFC		8192 po	nts (512 points/l	block)		
Annunciator				2048 points <sup>*3</sup>			
Edge relay				2048 points <sup>*3</sup>			
Link relay				8192 points <sup>*3</sup>			
Special relay	ys for link			2048 points			
Timer				2048 points <sup>*3</sup>			
Retentive tin	ner			0 points <sup>*3</sup>			
Counter				1024 points <sup>*3</sup>			
Data registe	Data register		12288 points*3				
Link register	Link register		8192 points <sup>*3</sup>				
Link special	Link special register		2048 points				
Function inp	Function input		16 points (FX0 to FXF)*4				
Function out	Function output		16 points (FX0 to FXF)*4				
Special relay	Special relay		2048 points				
Function reg	gister		5 points (FD0 to FD4)				
Special regis	ster		2048 points				
Link direct d	evice		Specified from J□\G□				
Special direct	ct device		Specified from U□\G□				
Index registe	~~	Z	16	oints (Z0 to Z15	5)		
Index registe	er	V*5		_			
File register	File register		32768 points/block (R0 to R32767)				
Accumulator			_				
Nesting	Nesting		15 points				
Pointer	Pointer		4096 points				
Interrupt poi	Interrupt pointer		48 points				
SFC block	SFC block		320 points				
SFC transition	SFC transition device		512 points/block				
Decimal constant			K-2147483648 to K2147483647				
Hexadecima	al constant		HC	to HFFFFFFFF			
Real consta	nt		E±1.1755	0-38 to E±3.40	282+38		
Character st	ring		"QI	ACPU", "ABCD	"		

\*1 The number of accessible points to actual I/O modules.

\*2 The number of points that can be used on the programs.

\*3 The number of points used can be changed with parameters.

\*4 Each 5 points of FX0 to FX4 and FY0 to FY4 can be used on the programs.

\*5 "V" is used for edge relays for the QnACPU.

A2USH-S1: 1024 points A2USH-S1: 1024 points A2USH-S1: 1024 points A2U : 512 points A2U : 2048 points A2U: 4096 points A2U: 4096 points A2U: 4096 points A3U: 2048 points A3V: 2048 points Total 2048 points'3A1N: 256 points A2V.S1: 1024 points A3V: 2048 points A3V: 2048 pointsCC-CC-2048 points256 points1024 points'3CC56 points1024 pointsCTotal 2048 points1024 points256 pointsCC56 points1024 pointsCTotal 2048 points4096 points1024 pointsA1D: 256 points1024 points256 points1024 pointsCTotal 2048 points6144 points1024 pointsCCCCCCCCCCCCCCCCCCCCCCCCCCCCCC
Total 8192 points'3         Total 2048 points'3           Control
Image: constraint of the sector of the se
Image: constraint of the strength of the strengt of the strength of the strength of the strength of th
56 pointsTotal 2048 pointsTotal 256 pointsTotal 2048 pointsTotal 256 points1024 points1024 points6144 points1024 points1025 points
56 pointsTotal 2048 pointsTotal 256 pointsTotal 2048 pointsTotal 256 points1024 points1024 points6144 points1024 points1025 points
Total 2048 pointsTotal 256 points1024 points256 points1024 points6144 points1024 points1024 points4096 points1024 points1024 points56 points1024 points1024 points256 points1024 points1024 points
$\begin{tabular}{ c c c c } \hline \begin{tabular}{ c c c } \hline \begin{tabular}{ c c c } \hline \begin{tabular}{ c c } \hline \hline \begin{tabular}{ c c } \hline \hline \begin{tabular}{ c c } \hline t$
8192 points         6144 points         1024 points           8192 points         4096 points         1024 points           56 points             256 points             256 points             256 points             256 points             7 points (Z,Z1 to Z6)         1 point (Z)
8192 points         4096 points         1024 points           56 points
56 points
8192 points/block (R0 to R8191)
2 points
8 points
256 points
32 points
_
K-2147483648 to K2147483647
H0 to HFFFFFFF
_

#### 7.3.2 I/O Control method

I/O	control method	QnACPU Q2AS(H)CPU-S1	AnUCPU A2USHCPU-S1	AnACPU	AnNCPU
Refresh mode		0	0	0	O*2
	Partial refresh instruction	0	0	0	0
Direct I/O method	Dedicated instruction*1	—	0	0	—
Direct i/O method	Direct access input	0		—	—
	Direct access output	0	_	_	_
Direct mode		_	_	_	O*2

\*1 The direct output dedicated instructions include the DOUT, DSET and SRST instruction and do not include the direct input dedicated instructions.

\*2 The DIP switch on the AnNCPU enables to switch between the refresh mode and direct mode.

#### 7.3.3 Usable data format for instructions

Setting data		QnACPU Q2AS(H)CPU-S1	AnUCPU A2USHCPU-S1	AnACPU	AnNCPU
	Bit device	0	0	0	0
Bit data	Word device	O (Bit designation required)	_		_
Word data	Bit device	O (Digit designation required)	O (Digit designation required)	O (Digit designation required)	O (Digit designation required)
	Word device	0	0	0	0
Double-word data	Bit device	O (Digit designation required)	O (Digit designation required)	O (Digit designation required)	O (Digit designation required)
	Word device	0	0	0	0
Real number data		0	0	0	Δ
Character string data		0	0		

#### 7.3.4 Precautions for file register replacement

This section describes precautions for replacing the large-sized A/QnA series CPU using file registers with the small-sized AnS/Q2AS CPU .

(1) Precautions for replacing the large-sized A series CPU with the small-sized AnS series CPU

Item	Large-sized A series CPU	Small-sized AnS series CPU	
Storage destination	Memory cassette	Built-in RAM	
Maximum number of points	Depends on the memory cassette used	Depends on the built-in RAM capacity for each CPU	
Number of points for 1 block	8k points		

The storage destination is different, however, the capacity setting of the parameter can be converted. Since the file register area may not be secured when the built-in RAM capacity of CPU type is lower than the memory capacity before converted, calculate the memory capacity in advance. The built-in RAM capacity of the A2USHCPU-S1 is 256k bytes (equivalent to the memory cassette, the A3NMCA-32).

(2) Precautions for replacing the large-sized QnA series CPU with the small-sized Q2AS series CPU

Item	Large-sized QnA series CPU	Small-sized Q2AS series CPU		
Storage destination	Memory card (Up to 2 cards, 4 drives)	Memory card (1 card, 2 drives)		
Maximum number of points	1018k points × 2 (When using two 2M memory cards)	1018k points (When using a 2M memory card)		
Number of points for 1 block	32k points			

#### (a) Changing storage destination

The applicable memory card is reduced from 2 to 1, however, the file register setting of the parameter can be converted.

When "Memory card B" (second card) is used in the large-sized QnA series CPU, the parameter storage destination must be changed.

#### (b) Maximum number of points

When two memory cards are installed and used with switching files, the maximum number of points may not be secured when replacing with the small-sized Q2AS series CPU.

## 

Check the storage destination for the following items to be set in "PLC file" of the PLC parameter.

- Comment file used in a command
- Initial device value
- File for local device 0

#### 7.3.5 Writing programs to ROM

# (1) Precautions for replacing the large-sized A series CPU with the small-sized AnS series CPU

EP-ROM used in the large-sized A series CPU cannot be reused for the A2USHCPU-S1. The E<sup>2</sup>PROM memory cassette is included in the small-sized AnS series CPU. This memory cassette is installed to the CPU, and the write of the parameter and main program can be made using the same operation with that of RAM by the GX Developer. Consider replacing EP-ROM on the large-sized A series CPU with E<sup>2</sup>PROM.

# (2) Precautions for replacing the large-sized QnA series CPU with the small-sized Q2AS series CPU

The applicable memory card is reduced from 2 to 1, however, the boot file of the parameter can be converted.

When "Memory card B" (second card) is used in the large-sized QnA series CPU, the parameter storage destination must be changed.

# 8 COMMUNICATION AND INFORMATION MODULE REPLACEMENT

## 8.1 List of Alternative Communication and Information Module Models

Large-sized A/QnA series models to be discontinued			Alternative AnS/Q2AS series models
Product	Model	Model	Remarks (restrictions)
Intelligent communication module	AD51H-S3	A1SD51S	<ol> <li>Change in number of tasks: 8 → 2</li> <li>Memory: 300k bytes → 60k bytes</li> <li>Parallel: Yes → None</li> <li>RS-232 connector: 25 pins → 9 pins</li> <li>Change in number of modules: 2 → 1 (1 slot vacant)</li> <li>Memory card I/F: 2 → None (file creation disabled)</li> <li>No LED indicators</li> <li>Program storage media: Memory card, EP-ROM → Built-in E<sup>2</sup>PROM</li> </ol>
	AD51-S3	A1SD51S	<ol> <li>The BASIC program of AD51-S3 is incompatible with that of A1SD51S. Analyze the existing program to create a program with AD51H-BASIC.</li> </ol>
Computer link module	AJ71UC24	A1SJ71UC24-R2 A1SJ71UC24-R4 A1SJ71UC24-PRF	<ol> <li>When 2ch are used for RS-232 and RS-422, two modules are required.</li> <li>AnS series do not have an interlock function between RS-232 and RS-422.</li> <li>RS-232 connector: 25 pins → 9 pins</li> </ol>
Multidrop link module	AJ71C22-S1	A1SJ71UC24-R4	<ul> <li>The following functions differ.</li> <li>1) Buffer memory Work area 61h to 07FFh → 71h to 0DFFh</li> <li>2) LED With slave station I/O monitor display LED → None</li> <li>3) Setting switch Baud rate setting: Fixed to 38400bps → 19200/38400 setting allowed Master/local: Fixed to master → Setting allowed</li> <li>4) Terminal block screw M4 screw → M3.5</li> <li>5) Terminating resistor Built in → External</li> </ul>
	AJ71E71N-B2	A1SJ71E71N-B2	No special restrictions
	AJ71E71N-B5	A1SJ71E71N-B5	No special restrictions
	AJ71E71N-T	A1SJ71E71N3-T	No special restrictions
Ethernet module	AJ71E71N3-T	A1SJ71E71N3-T	No special restrictions
	AJ71QE71N-B2	A1SJ71QE71N-B2	No special restrictions
	AJ71QE71N-B5	A1SJ71QE71N-B5	No special restrictions
	AJ71QE71N-T	A1SJ71QE71N3-T	No special restrictions
	AJ71QE71N3-T	A1SJ71QE71N3-T	No special restrictions
Host controller high-speed link module	AJ71C23-S3	None	No alternative

## 8.2 Communication/Information Modules Comparison

#### 8.2.1 Intelligent communication modules comparison

#### (1) Comparisons between the AD51H-S3 and A1SD51S

#### (a) Performance specifications comparison

 $\bigcirc$  : Compatible,  $\bigtriangleup$  : Partial change required,  $\times$  : Incompatible

Specification		AD51H-S3	A1SD51S	Compati- bility	Precautions for replacement
Programming language		AD51H-BASIC		0	
Number of tasks		(Max.) 8 tasks	(Max.) 2 tasks	×	Review the BASIC program, and reduce the number of tasks to two or less.
Task start	condition	Start at power on.     Start by interrupt from     (Impossible when co     Start by start request)	mpiled BASIC is run.)	0	
		Program memory: Max. 384k bytes/8 tasks (16/32/48/64k bytes selectable for one task.)	Program memory: Max. 64k bytes/2 tasks (16/32/48/64k bytes selectable for one task.)	×	Review the BASIC program, and reduce the memory size to 64k bytes or less.
Internal m	emory	Common mer	mory: 8k bytes	0	
		Extension register: 1	024 points (2k bytes)	0	
		Extension rela	ay: 1024 points	0	
General-p	urpose I/O	Input: 27 points Output: 23 points		0	
Buffer mer	nory	3k words (6k bytes)		0	
	RS422I/F		el 3, Connector connection, ance: Within 500m	0	
	RS232I/F	Transmission dis	el 1,2, Connector connection, tance: Within 15m	Δ	Use a suitable connector on the other side.
Built-in interface		D sub 25-pin (female), screw type	D sub 9-pin (female), screw type		
Intenace	Centronics I/F	Centronics compliant, Channel 4, Connector connection, Transmission distance: Within 3m	_	×	Change the external devices to the I/F devices of RS-232C/RS-422.
	Memory card I/F	Applicable memory card: A6MEM-  AW	_	×	Use the device memory of the CPU module.
Power failure protection		None (The data stored in a memory card can be protected by battery)	Protected (Common memory, extension relay, extension register)	_	
Storage to ROM		Possible (For execution program only)	Not allowed (although E <sup>2</sup> PROM is built in the module.)	Δ	Write user program to ROM with the MSAVE command.
Console		A7PHP     A7HGP     A7LMS     IBM-PC/AT-compatible PC     VG-620·····Manufactured by Victor Data Systems     VT-382·····Manufactured by Digital Equipment Corporation Japan		0	
Number of occupied I/O points		48 points (I/O assignment: 16 empty points + 32 special-purpose points)	32 points (I/O assignment: 32 special-purpose points)	Δ	Change the start I/O No. by PLC parameter.
Internal cu consumpti	irrent on (5VDC)	1.0A	0.4A	0	
External d		250(H) × 76(W) × 120(D) mm	130(H) × 34(W) × 93.6(D) mm	Δ	The dimensions are different.
Weight		0.9kg	0.3kg	0	

#### (b) Function comparison

 $_{O}$  : Compatible,  $_{\bigtriangleup}$  : Partial change required,  $\times$  : Incompatible

Function		AD51H-S3	A1SD51S	Compati- bility	Precautions for replacement
	Sub CPU function	Uses a BASIC program to perform comp that lengthen the scan time of a PLC CP • Collection, analysis and compensation • Functional operations such as sines, lo	0		
	Monitor display function	Displays the operating states (production information) on a console or terminal con		0	
	Key operation function	Allows entry of production schedules, the setting data from the keyboard of a cons module.		0	
	Printer function	Prints the production plans, achievements, daily reports, error details, plan data, inspection results, and test results from a printer connected to the module.		0	
BASIC program functions	Data input function			0	
	External device connection function	Transmits and receives data from/to a BASIC program by connecting a computer to the RS-232C or RS-422 interface.			
	File manage- ment function Reads/write card installe interface of		_	×	CPU module devices must be used as substitutes.
	Clock Reads and writes clock data (year, month, day, hour, minute, second and day of the week) from/to a PLC CPU which has a clock function.		0		
Offline programming function		Creates, corrects, and stores a BASIC program to a user's FD and prints it out independently by A7PHP/A7HGP/A7LMS/IBM-PC/AT-compatible PC.		0	
Online programming function		Creates, executes, and corrects a BASIC program by using system commands when a console is connected to the module. Stores/reads an execution program to/from E <sup>2</sup> PROM using system commands.		0	
Multitask debugging	function	Debugs a BASIC program while executin debugger.	ng it by connecting a console and a	0	

#### (c) Comparison of I/O signals for PLC CPU

Modifying sequence programs is required as the I/O signals differ. For details of the I/O signals and sequence programs, refer to the user's manual.

Input (Sphall         A19513         Compati- Signal name         Procautions for replacement           X0         X1         X2         A           X1         X         A         A           X2         A         A         A           X3         A         A         A           X4         A         A         A           X5         A         A         A           X4         A         A         A           X4         A         A         A           X4         A         A         A           X4         A         A         A           X5         General-purpose input         A         A           X6         A         A         A           X11         X12         A         A           X14		⊖ : Compatible, △ : Partial change requir				
X0         X1           X1         A           X2         A           X3         A           X4         A           X5         A           X6         A           X7         A           X8         A           X9         A           XA         A           X8         Stor of multitasking         A           X0         A           X10         A         A           X11         A         A           X12         A         A           X14         A         A           X15         A         A           X10         A         A           X11         A         A           X12         A         A           X14         A         A	Input signal			Compati- bility	Precautions for replacement	
X1         Δ           X2         Δ           X3         Δ           X4         Δ           X5         Δ           X6         Δ           X7         Δ           X8         Δ           X9         Δ           X4         Δ           X8         Δ           X9         Δ           X4         Δ           X8         Δ           X0         Δ           X1         Δ           X10         Δ           X11         Δ           X12         Δ           X13         Δ           X14         Δ           X15         Δ           X16         Δ           X17         Δ           X18         Start of multitasking         Δ           X11         Δ         Δ           X12         Δ         Δ           X13         Δ         Δ           X14         Δ         Δ           X15         Start of multitasking         Δ           X16         X17         Δ           X18         Start of mult						
X2       X3         X4       A         X4       A         X5       A         X6       A         X7       A         X8       A         X9       A         XA       A         X8       A         X9       A         XA       A         X8       A         X0       A         X10       A         X11       A         X12       A         X11       A         X12       A         X11       A         X12       A         X11       A         X12       A         X14       A         X15       General-purpose input       A         X16       Start of multitasking       A         X14       A       A         X15       General-purpose input       A         X16       Start of multitasking       A         X17       A       A         X18       Start of multitasking       A         X16       AD511+S3 system down       A         X17						
X3       X4         X4       C         X5       C         X6       C         X7       C         X8       C         X9       C         X4       C         X9       C         X0       C         X0       Start of multitasking       C         X0       Stop of multitasking       C         X10       XF       C       C         X11       C       C       C         X12       C       C       C         X14       C       C       C         X16       C       C       C         X17       C       C	X2					
X4     Δ       X5     Δ       X6     Δ       X7     Δ       X8     Δ       X0     Δ       X8     Δ       X0     Δ       X0     Δ       X0     Δ       X0     Δ       X10     Δ       X11     Δ       X12     Δ       X13     Δ       X14     Δ       X18     Sart of multitasking       X18     Sart of multitasking       X18     Δ       X19     Δ       X18     Sart of multitasking       X18     Δ       X19     Δ       X10     Δ       X11     Δ       X12     Δ       X14     Δ       X16     Δ       X17     Δ       X18     Sart of multitasking       X19     Δ <t< td=""><td></td><td></td><td></td><td></td><td></td></t<>						
X5         General-purpose input         Δ           X7         Δ         Δ           X8         Δ         Δ           X9         Δ         Δ           X4         Δ         Δ           X8         Δ         Δ           X9         Δ         Δ           X4         Δ         Δ           X6         Δ         Δ           X8         Δ         Δ           X0         Δ         Δ           X10         Δ         Δ           X11         Δ         Δ           X16         Δ         Δ <t< td=""><td></td><td></td><td></td><td></td><td></td></t<>						
X6     Х       X7     Unusable       X8     А       X9     XA       X9     Start of multitasking       XA     Start of multitasking       XB     Start of multitasking       XD     A1SDS1S system down       XE     Δ       XF     Δ       X10     XF       X11     Δ       X12     Δ       X13     Δ       X14     Δ       X15     Δ       X16     Δ       X17     Δ       X18     Start of multitasking       X10     ADS1H-S3 system down       X11     Δ       X12     Unusable       X18     Start of multitasking       X19     Δ       X10     ADS1H-S3 system down       X11     Δ       X12     General-purpose input       X16     Δ       X17     Δ       X18     Start of multitasking       X11     Δ       X12     General-purpose input       Δ     Δ <t< td=""><td></td><td></td><td>General-purpose input</td><td></td><td></td></t<>			General-purpose input			
X8     Unusable     A       X9     A       X0     Start of multitasking     A       X0     Start of multitasking     A       X0     AtSD51S system down     A       X10     AtSD51S system down     A       X11     A     A       X11     A     A       X11     A     A       X12     A     A       X16     Ceneral-purpose input     A       X11     AtsD51H-S3 system down     A       X12     AtsD51H-S3 system down     A       X11     AtsD51H-S3 system down     A       X12     AtsD51H-S3 system down     A       X14     AtsD51H-S3 system down     A       X15     General-purpose input     A       X16     AtsD51H-S3 system down     A       X17     AtsD51H-S3 system down     A       X16     AtsD51H-S3 system down     A       X17     AtsD51H-S3 s	X6					
X8     X9     A       X9     A       XA     A       XB     Start of multitasking     A       XC     Stop of multitasking     A       XD     A1SD51S system down     A       XE     Unusable     A       X10     A     A       X11     A     A       X12     A     A       X13     A     A       X14     A     A       X15     General-purpose input     A       X16     A     A       X17     A     A       X18     Start of multitasking     A       X11     A     A       X12     Start of multitasking     A       X14     A     A       X15     Start of multitasking     A       X16     A     A       X17     X18     A       X18     Start of multitasking     A       X10     AD51H-S3 system down     A       X11     AD51H-S3 system down     A       X16     AD51H-S3 system down     A       X17     AD51H-S3 system down     A       X18     General-purpose input     A       X19     AD51H-S3 system down     A	X7			Δ		
XA     XA       XB     Start of multitasking     A       XC     Stop of multitasking     A       XD     A1SD51S system down     A       XE     A1SD51S system down     A       X10     A1SD51S system down     A       X10     A     A       X11     A     A       X12     A     A       X13     A     A       X14     A     A       X16     A     A       X17     General-purpose input     A       X18     Start of multitasking     A       X10     AD511+S3 system down     A       X11     AD511+S3 system down     A       X16     AD511+S3 system down     A       X17     AD511+S3 system down     A       X18     General-purpose input     A       X19     O     A       X10     Dusable     A       X11     AD511+S3 system down     A       X16     A	X8	Unusable		Δ		
X8     Start of multitasking     △       X0     Stop of multitasking     △       X1     AtSD51S system down     △       X10     Unusable     △       X11     △     △       X12     △     △       X13     △     △       X14     △     △       X13     △     △       X14     △     △       X13     △     △       X14     △     △       X15     General-purpose input     △       X16     △     △       X17     △     △       X18     Start of multitasking     △       X10     AD511+S3 system down     △       X11     △     △       X12     Unusable     △       X13     △     △       X14     △     △       X17     △     △       X18     Start of multitasking     △       X10     AD511+S3 system down     △       X11     △     △       X16     Unusable     △       X17     △     △       X18     Start of multitasking     △       X19     O     △       X10     AD511+S3 system down<	X9			Δ		
XC     Stop of multitasking     A       XD     A1SD51S system down     A       XF     A       X10     A       X11     A       X12     A       X11     A       X12     A       X13     A       X14     A       X13     A       X14     A       X15     General-purpose input       X16     A       X17     A       X18     Start of multitasking       X10     AD51H-S3 system down       X11     A       X12     Unusable       X18     Start of multitasking       X10     AD51H-S3 system down       X11     A       X12     Unusable       X14     A       X15     Change "X10 to X2F" to "X0 to X1F".	XA			Δ		
XD     A1SD51S system down     A       XE     A       XF     A       Unusable     A       X10     A       X11     A       X12     A       X13     A       X14     A       X15     General-purpose input     A       X16     A       X17     A       X18     Start of multitasking     A       X10     AD51H-S3 system down     A       X11     A       X12     A       X13     A       X14     A       X15     General-purpose input     A       A1     A       X17     A       X18     Start of multitasking     A       X10     AD51H-S3 system down     A       X11     A       X12     A       X13     A       X14     A       X15     Change "X10 to X2F" to "X0 to X1F".       X16     A       X17     A       X18     Start of multitasking       X10     AD51H-S3 system down       X11     A       X12     A       X14     A       X15     D       X16     Chan<	XB		Start of multitasking	Δ		
XE     Unusable     Δ       X10     Δ       X11     Δ       X12     Δ       X13     Δ       X14     Δ       X15     General-purpose input       X16     Δ       X17     Δ       X18     Δ       X19     Δ       X10     AD51H-S3 system down       X11     Δ       X12     Unusable       X13     Δ       X14     Δ       X17     Δ       X18     Start of multitasking       X10     AD51H-S3 system down       X11     Δ       X12     Unusable       X11     Δ       X12     Unusable       X14     Δ       X15     Δ       X16     Δ       X17     Δ       X18     Start of multitasking       X10     AD51H-S3 system down       X11     Δ       X12     Unusable       X14     Δ       X15     Δ       X16     Δ       X17     Δ       X18     Δ       X19     Δ       X10     Δ       X11     Δ       X12     Δ <td>XC</td> <td></td> <td>Stop of multitasking</td> <td>Δ</td> <td></td>	XC		Stop of multitasking	Δ		
XF     Unusable       X10     A       X11     A       X11     A       X12     A       X13     A       X14     A       X15     A       Seneral-purpose input     A       X16     A       X17     A       X18     A       X19     A       X11     A       X12     A       X13     A       X14     A       X15     General-purpose input       X16     A       X17     A       X18     Start of multitasking       X10     AD51H-S3 system down       X1E     Unusable       X1F     A       X20     General-purpose input	XD		A1SD51S system down	Δ		
XF     Δ       X10     Δ       X11     Δ       X11     Δ       X12     Δ       X13     Δ       X14     Δ       X15     General-purpose input       X16     Δ       X17     Δ       X18     Δ       X19     Δ       X11     Δ       X12     Δ       X13     Δ       X14     Δ       X15     General-purpose input       X16     Δ       X17     Δ       X18     Start of multitasking       X10     AD51H-S3 system down       X11     Δ       X11     Δ       X12     Unusable       X14     Δ       X15     General-purpose input	XE		Unusable	Δ		
X11       A         X12       A         X13       A         X14       A         X15       A         X16       A         X17       A         X18       A         X19       A         X18       A         X19       A         X11       A         X12       A         X13       A         X16       A         X17       A         X18       Start of multitasking         X10       AD51H-S3 system down         X11       A         X12       Unusable         X14       A         X20       General-purpose input         to       General-purpose input	XF			Δ		
X11       X12         X12       X13         X13       X14         X15       General-purpose input         X16       A         X17       A         X18       A         X19       A         X1A       A         X1B       Start of multitasking         X1C       Stop of multitasking         X1D       AD51H-S3 system down         X1E       Unusable         X1F       A         X20       General-purpose input	X10			Δ		
X13       Image: Constraint of the second seco	X11			Δ	X1F".	
X14       Δ         X15       Δ         X16       Δ         X17       Δ         X18       Δ         X19       Δ         X1A       Δ         X1B       Start of multitasking         X1C       Stop of multitasking         X1D       AD51H-S3 system down         X1E       Δ         X1F       Δ         X20       Ceneral-purpose input         X20       Ceneral-purpose input	X12			Δ		
X15     General-purpose input     Δ       X16     Δ       X17     Δ       X18     Δ       X19     Δ       X1A     Δ       X1A     Δ       X1B     Start of multitasking       X1C     Stop of multitasking       X1D     AD51H-S3 system down       X1E     Unusable       X1F     Δ       X20     General-purpose input				Δ		
X16     X       X17     A       X18     A       X19     A       X1A     A       X1B     Start of multitasking       X1C     Stop of multitasking       X1D     AD51H-S3 system down       X1E     Unusable       X1F     A       X20     General-purpose input				Δ		
X17       X17         X18       X19         X1A       △         X1A       △         X1B       Start of multitasking       △         X1C       Stop of multitasking       △         X1D       AD51H-S3 system down       △         X1E       Unusable       △         X1F       △       △         X20       General-purpose input       Outside I/O point range       △		General-purpose input		Δ		
X18     A       X19     A       X1A     A       X1A     A       X1B     Start of multitasking       X1C     Stop of multitasking       X1D     AD51H-S3 system down       X1E     Unusable       X1F     A       X20     General-purpose input       to     General-purpose input				Δ		
X19       Δ         X1A       Δ         X1B       Start of multitasking       Δ         X1C       Stop of multitasking       Δ         X1D       AD51H-S3 system down       Δ         X1E       Unusable       Δ         X1F       Δ       Δ         X20       General-purpose input       Outside I/O point range       Δ			General-purpose input	-		
X1A       Δ         X1B       Start of multitasking       Δ         X1C       Stop of multitasking       Δ         X1D       AD51H-S3 system down       Δ         X1E       Unusable       Δ         X1F       Δ       Δ         X20       General-purpose input       Outside I/O point range       Δ						
X1B       Start of multitasking       Δ         X1C       Stop of multitasking       Δ         X1D       AD51H-S3 system down       Δ         X1E       Unusable       Δ         X1F       Δ       Δ         X20       General-purpose input       Outside I/O point range       Δ		Stop of multitasking				
X1C       Stop of multitasking       △         X1D       AD51H-S3 system down       △         X1E       Unusable       △         X1F       △       △         X20       General-purpose input       Outside I/O point range       △						
X1D     AD51H-S3 system down       X1E     △       X1F     △       X20     General-purpose input     Outside I/O point range						
X1E     Unusable       X1F     △       X20     △       to     General-purpose input       Outside I/O point range     △						
X1F     Unusable       X20     Control of the second seco						
X20     General-purpose input     Outside I/O point range     A		Unusable		-		
to General-purpose input Outside I/O point range						
		General-purpose input	Outside I/O point range			
	X2F					

$\bigcirc$ : Compatible, $\triangle$ :	Partial change required,	×: Incompatible
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<b>•</b> • • •	AD51H-S3			ange required, ×: Incompatible
Output signal	Signal name	Signal name	Compati- bility	Precautions for replacement
Y0			Δ	
Y1			Δ	
Y2			Δ	
Y3			Δ	
Y4			$\bigtriangleup$	
Y5			Δ	
Y6			Δ	
Y7	Unusable		Δ	
Y8		General-purpose output	$\bigtriangleup$	
Y9				
YA				
YB			Δ	
YC				
YD				
YE				
YF		-		
Y10		Designation of a general-purpose output/start	Δ	
Y11		BASIC task No. 1 Designation of a general-purpose output/start	Δ	
Y12		BASIC task No. 2	Δ	
Y13 Y14				
Y15				
Y16		General-purpose output		
Y17				Change "Y10 to Y2F" to "Y0 to
Y18	General-purpose output			Y1F".
Y19		Program start signal (unusable for a general-purpose output)		
Y1A			Δ	
Y1B			Δ	
Y1C			Δ	
Y1D		Unusable		
Y1E				
Y1F			Δ	
Y20				
Y21	Designation of a general-purpose output/start BASIC task No. 1		Δ	
Y22	Designation of a general-purpose output/start BASIC task No. 2		Δ	
Y23	Designation of a general-purpose output/start BASIC task No. 3		Δ	
Y24	Designation of a general-purpose output/start BASIC task No. 4		Δ	
Y25	Designation of a general-purpose output/start BASIC task No. 5	Outside I/O point sacra	Δ	
Y26	Designation of a general-purpose output/start BASIC task No. 6	Outside I/O point range	Δ	
Y27	Designation of a general-purpose output/start BASIC task No. 7		Δ	
Y28	Designation of a general-purpose output/start BASIC task No. 8 Program start signal		Δ	
Y29	(unusable for a general-purpose output)		Δ	
Y2A				
to	General-purpose output		Δ	
Y2F				

#### (d) Buffer memory address comparison No special differences are identified.

#### (2) Comparisons between the AD51-S3 and A1SD51S

The BASIC program of AD51-S3 is incompatible with that of A1SD51S. Analyze the existing program to create a program with AD51H-BASIC.

#### (a) Performance specifications comparison

 $\bigcirc$ : Compatible,  $\triangle$ : Partial change required, ×: Incompatible

Speci	fication	AD51-S3	A1SD51S	Compati- bility	Precautions for replacement
Programmi	ng language	GPC-BASIC	AD51H-BASIC	×	Change the BASIC program to AD51H- BASIC.
Number of tasks		(Max.) 8 tasks	(Max.) 2 tasks	×	Review the BASIC program, and reduce the number of tasks to two or less.
Task start o	ondition	<ul> <li>Start at power on.</li> <li>Start by interrupt from the PLC CPU (Impossible when compiled BASIC is run).</li> <li>Start by real time interrupt.</li> </ul>	<ul> <li>Start at power on.</li> <li>Start by interrupt from the PLC CPU (Impossible when compiled BASIC is run).</li> <li>Start by start request from another task.</li> </ul>	Δ	When using a real time interrupt, the program must be changed.
		Program memory: Max. 64k bytes+48k bytes (Writing programs to ROM)	Program memory: Max. 64k bytes/2 tasks	×	Review the BASIC program, and reduce the memory size to 64k bytes or less.
Internal me	mory	Common memory: 2k bytes	Common memory: 8k bytes	0	
		_	Extension register: 1024 points (2k bytes)	-	
		—	Extension relay: 1024 points	_	
General-purpose I/O		Input: 13 points Output: 10 points	Input: 27 points Output: 23 points	Δ	Change the start I/O No. in the PLC parameter I/O assignment setting.
Buffer merr	iory	3k words	(6k bytes)	0	
		RS-422 compliant, Connector connection	on, Transmission distance: Within 500m		Change into one channel
Built-in	RS422I/F	Channel 1, 2Channel 3D sub 25-pin (female), screw typeD sub 25-pin (female), screw type		Δ	when using two channels.Also, change the channel number.
interface		RS-232C compliant, Connector connection	tion, Transmission distance: Within 15m		Also, change the channel
	RS232I/F	Channel 3, 4 D sub 25-pin (female), screw type	Channel 1, 2 D sub 9-pin (female), screw type	Δ	number and the number of connector pins.
Power failu	re protection	None	Protected (Common memory, extension relay, extension register)	0	
Storage to	ROM	Possible (8k ROM/16k ROM)	Not allowed (although E <sup>2</sup> PROM is built in the module.)	Δ	Write user program to ROM with the MSAVE command.
Console		<ul> <li>A6GPP</li> <li>VG-620/670···</li> <li>Manufactured by Victor Data Systems</li> </ul>	<ul> <li>A7PHP</li> <li>A7HGP</li> <li>A7HGP</li> <li>A7LMS</li> <li>IBM-PC/AT-compatible PC</li> <li>VG-620</li> <li>Manufactured by Victor Data Systems</li> <li>VT-382</li> <li>Manufactured by Digital Equipment Corporation Japan</li> </ul>	Δ	Use compatible consoles.
Number of occupied I/O points		48 points (I/O assignment: 16 empty points + 32 special-purpose points)	32 points (I/O assignment: 32 special-purpose points)		Change the start I/O No. in the PLC parameter I/O assignment setting.
Internal pov consumptic		1.3A	0.4A	0	
External dir	<u> </u>	250(H) × 76(W) × 120(D) mm	130(H) × 34(W) × 93.6(D) mm	Δ	The dimensions are different.
Weight		1.1kg 0.3kg		0	

#### (b) Function comparison

 $_{O}$  : Compatible,  $_{\bigtriangleup}$  : Partial change required,  $\times$  : Incompatible

Functio	n	AD51-S3	A1SD51S	Compati- bility	Precautions for replacement	
	Sub CPU function	Uses a BASIC program to perform comp which may result in a prolonged PLC CF • Collection, analysis and correction of r • Functional operations such as sines, lo	PU scan time. neasurement data	0		
	Monitor display function	Displays working states (progress of pro the console or terminal connected to the	0			
	Key operation function	Allows entry of production schedules, pr data from the keyboard of a console or t		0		
BASIC program	Printer function	Prints production plans, production reco inspection results, test records, etc. from	0			
functions	Data input function	<ul> <li>Allows data entry through a bar code reat to the module.</li> <li>(Since a BASIC program enables the modified format, it can perform data communicating device uses.)</li> <li>Input of production lot numbers, prodution of measurement values and</li> </ul>	0	The BASIC program of AD51-S3 is incompatible with that of A1SD51S. Analyze the existing program to create a program with AD51H- BASIC.		
	External device connection function	Establishes data communications by usi computer to the RS-232C or RS-422/48	0			
	Clock function	Reads and writes clock data (year, mont the week) from/to a PLC CPU which has		0		
Offline programming	function	_	Allows the A7PHP/A7HGP/A7LMS/ IBM-PC/AT-compatible PC to create, correct and store BASIC programs on a user's FD and to print them independently.			
Online programming	function	Creates, executes and corrects BASIC p module using system commands. Stores and reads execution programs to commands.	0			
Multitask debugging	function	_	Debugs a BASIC program on a console and a debugger connected to the module while executing it by multitasking.			

#### (c) Comparison of I/O signals for PLC CPU

Modifying sequence programs is required as the I/O signals differ. For details of the I/O signals and sequence programs, refer to the user's manual.

O: Compatible, △: Partial change required, ×: Incompatible, △: Partial change required, ×: Partial change required, ×: Partial change required, ×: Parti										
Input signal	AD51-S3 Signal name	A1SD51S Signal name	Compati- bility	Precautions for replacement						
X0			Δ							
X1			Δ							
X2			Δ							
X3			Δ							
X4			Δ							
X5		General-purpose input	Δ							
X6			Δ							
X7	Unusable		Δ							
X8			Δ							
X9			Δ							
XA			Δ							
XB		Start of multitasking	Δ							
XC		Stop of multitasking	Δ							
XD		A1SD51S system down	Δ							
XE		Unusable	Δ							
XF		Ullusable	Δ							
X10			Δ	Change "X10 to X1C" to "X0 to						
X11			Δ	XA" and "X10 to X1F". Also, change "X1D" to "XD".						
X12			Δ	Also, change ATD to AD.						
X13			Δ							
X14			Δ							
X15			Δ							
X16	General-purpose input		Δ							
X17		General-purpose input	Δ							
X18		· · · · · · · · · · · · · · · · · · ·	Δ							
X19			Δ							
X1A			Δ							
X1B			Δ							
X1C			Δ							
X1D	AD51-S3 system down		Δ							
X1E			Δ							
X1F			Δ							
X20	Unusable									
to		Outside I/O point range	Δ							
X2F										

#### $\bigcirc$ : Compatible, $\triangle$ : Partial change required, ×: Incompatible

		$\bigcirc$ : Compatible, $\triangle$ :	Partial cha	ange required, ×: Incompatible
Output	AD51-S3	A1SD51S	Compati-	Precautions for replacement
signal	Signal name	Signal name	bility	
Y0 Y1				
Y2 Y3				
Y4 Y5				
Y6				
Y7				
Y8		General-purpose output		
Y9		General-pulpose output		
YA				
YB				
YC				
YD				
YE				
YF				
Y10	Unused			
	(May be used for internal relay (M).)	Designation of a general-purpose output/start		
Y11		BASIC task No. 1	Δ	
Y12		Designation of a general-purpose output/start	Δ	
		BASIC task No. 2		
Y13				
Y14 Y15				
Y16				Change "Y20 to Y29" to "Y0 to Y1F". However, when Y29 is
Y17 Y18				used as an interrupt signal, the
Y19		General-purpose output		program must be reviewed.
Y1A		General-purpose output		
Y1B				
Y1C				
Y1D				
Y1E				
Y1F				
Y20				
Y21				
Y22				
Y23				
Y24	General-purpose output			
Y25				
Y26				
Y27				
Y28		Outside I/O point range		
Y29	When this turns on in the sequence program, an interrupt is triggered in the AD51.			
Y2A			Δ	
Y2B			Δ	
Y2C				
Y2D	Unusable			
Y2E			Δ	
Y2F			Δ	
			•	

# (d) Buffer memory address comparison

No special differences are identified.

#### 8.2.2 Serial communication modules comparison

#### (1) Comparisons between the AJ71UC24 and A1SJ71UC24-R2/R4/PRF

#### (a) Performance specifications comparison

 $\bigcirc$  : Compatible,  $\bigtriangleup$  : Partial change required,  $\times$  : Incompatible

Sp	ecification	AJ71UC24	A1SJ71U	C24-R2/R4/PRF	Compati- bility	Precautions for replacement
		RS-232 compliant (D-Sub 25P)	RS-232 con	pliant (D-Sub 9P)	Δ	The connector on the
Interface*		RS-422/485 compliant	RS-422/	485 compliant	Δ	connection cable must be changed. Use A1SJ71UC24-□□ when using two channels or more.
Communication	Communication using dedicated protocol	Half-duplex c	0			
Communication method	Non-procedural/ bidirectional communication	Full duplex communic /Half-duplex communicat	0			
Synchronization	method	Start stop synchronization	n (asynchronous	s method)	0	
Transmission spe	eed	300 to 19	9200 bps		0	
	Start bit		1		0	
	Data bit	7 c	or 8		0	
Data format	Parity bit	1 (vertical p	parity)/none		0	
	Stop bit	1 c		0		
	Communication using dedicated protocol	One request is processed when the processing.	mounted PLC C	PU executes END	0	
Access cycle	Non-procedural/ bidirectional communication	I reception is	0			
Error	Parity check	Performed (or	dd/even)/none		0	
detection	Sum check	Performed (Dedicated pr	otocol/Bidirectio	nal)/none	0	
Transmission cor	ntrol	DTR/DSR (ER/DR) control CD signal control DC1/DC3 (Xon/Xoff) control DC2/DC4 control	0			
Line	RS-232	1	:1	11	0	
configuration (connection)	RS-422/485	1:1, 1:n, m:n (n: ma	ix.32, m+n: max	.32)	0	
Line	Communication using dedicated protocol	1:1, 1:n, m:n (n: ma	ıx.32, m+n: max	.32)	0	For details on linked
configuration ( data	Non-procedural communication	1:1, 1:n (r	n: max.32)		0	operation between interfaces, refer to the
communication)	Bidirectional communication		:1		0	manual.
Transmission	RS-232		15m		0	
distance	RS-422/485	Max. 500m (ov	verall distance)		0	
Current consump		0.3A		0.1A	0	
No. of E <sup>2</sup> PROM No. of flash ROM		Max. 100,000 times on	same area in E	<sup>2</sup> PROM	0	
Number of occup		32 points (I/O assignm	nent: special 32	points)	0	
External dimensi	ons	250(H) × 37.5(W) × 131(D) mm	130(H) × 34.	5(W)×93.6(D) mm	Δ	The dimensions are different.
Weight		0.63kg	0.25kg (A18	SJ71UC24-R2) SJ71UC24-R4) SJ71UC24-PRF)	0	

\*The following table shows the AJ71UC24, A1SJ71UC24-R2, A1SJ71UC24-R4, A1SJ71UC24-PRF interface configurations.

Model	RS-232	RS-422/485
AJ71UC24	1ch	1ch
A1SJ71UC24-R2	1ch	—
A1SJ71UC24-R4	—	1ch
A1SJ71UC24-PRF	1ch	—

#### (b) Function comparison

 $_{O}$  : Compatible,  $_{\bigtriangleup}$  : Partial change required,  $\times$  : Incompatible

	Function	AJ71UC24	A1SJ71UC24-R2/R4/PRF	Compati- bility	Precautions for replacement		
col	Device memory read/write	PLC CPU data are read from and writte	en to the external device.	0			
n Communication using dedicated protocol	On-demand	Data are transmitted to the external de	vice from the PLC CPU.	0			
ation	Data transmission PLC $\rightarrow$ external device	vice from the PLC CPU.	0				
Non-procedural communication	Data reception PLC ← external device	The PLC recieves data transmitted fror	0				
ication	Data transmission PLC $\rightarrow$ external device	Data are transmitted to the external de	vice from the PLC CPU.	0			
Bidirectional communication	Data reception PLC ← external device	The PLC recieves data transmitted fror	n the external device.	0			
Tran func	ismission using printer	Transmits messages (character strings	) to the printer from the PLC CPU.	0			
	DTR/DSR control	Data exchange with the external device	e is controlled by RS-232 control	0			
contr	CD signal control	signals.		0			
Transmission control	DC code control	DC codes (including Xon/Xoff) are sent with the external device.	0				
	idrop connection with 232C and RS-422 mixed	Multidrop connection with RS-232C and RS-422 mixed is available.					

#### (c) Switch settings comparisons

1) Mode setting switch

#### The difference between the AJ71UC24 and A1SJ71UC24-R2/R4/PRF is as follows.

Mode setting switch number	RS-232C side setting	RS-422 side setting	AJ71UC24	A1SJ71UC24-R2/ PRF	A1SJ71UC24-R4
0	Use pro	hibited	—	—	_
1 to 3	Type n protocol	Nonprocedural mode	0	0	_
4	Type 4 protocol	Nonprocedural mode	0	0	0
5	Nonprocedural mode	Type 1 protocol	0	0	0
6 to 8	Nonprocedural mode	Type n protocol	0	—	0
9	Nonproced	ural mode	0	—	_
A to D	Туре п р	protocol	0	—	_
E	Use pro	hibited	—	—	_
F	Self-loopl	back test	0	0	0

#### 2) Transmission setting switch

AJ71UC24

Switch se	otting	Setting Setting item					Setting	switch s	status			Remarks	
Switch se	etting	switch	Se	OFF					Remarks				
		SW11	Main c	hannel setting	RS-232C					RS	Valid when mode setting switches 9 to D are set.		
		SW12	Data		7	bits				8 bits		Excluding parity bit	
				Transmission speed (BPS)	300	600	1200	2400	4800	9600	19200	Use prohibited	
SW11	► ON	SW13	Transmission speed setting		OFF	ON	OFF	ON	OFF	ON	OFF	ON	_
SW12 SW13		SW14			OFF	OFF	ON	ON	OFF	OFF	ON	ON	
SW14		SW15			OFF	OFF	OFF	OFF	ON	ON	ON	ON	
SW16		SW16	Parit	None					_				
SW18		SW17	Even/oc	ld parity setting	Odd							Valid when parity bit is used.	
	► ON	SW18	Stop	bit setting		1	bit				2 bits		—
		SW21	Si	um check		Ν	one				Yes		For dedicated
		SW22	Write	during RUN		Dis	abled			E	nabled		protocol
	3₩24	SW23		nputer link/ p link selection		Multidrop link				Con	Always set to computer link (ON).		
		SW24		ation/local station setting			drop link station				tidrop link ter station		Setting ignored for computer link.

#### A1SJ71UC24-R2/PRF

0	h 44 <sup>2</sup>	Setting	0	tting item				Setting	g switch s	status			Remarks
Switc	h setting	switch	50	ON						Remarks			
		SW03	l	Jnused			_				_		—
		SW04	Write	during RUN	Enabled					C	For dedicated protocol		
SW 03	ON ←			Transmission speed (BPS)	300	600	1200	2400	4800	9600	19200	Use prohibited	
04		SW05			OFF	ON	OFF	ON	OFF	ON	OFF	ON	_
05	ON 🗲	SW06			OFF	OFF	ON	ON	OFF	OFF	ON	ON	
05 06		SW07		OFF	OFF	OFF	OFF	ON	ON	ON	ON		
07		SW08	Data	a bit setting	8 bits					Excluding parity bit			
08 09		SW09	Parit	y bit setting		Ň	Yes					—	
10 11 12	10 <b>—</b> 11 <b>—</b>	SW10	Even/od	d parity setting	Even			Odd				Valid when parity bit is used.	
12		SW11	Stop	bit setting		2	bits					—	
		SW12	Sı	ım check		Ň	Yes				None		For dedicated protocol

#### A1SJ71UC24-R4

o	Setting						Setting	g switch s	status			Domorko
Switch setti	switch		etting item	ON						Remarks		
	SW01	Master sta setting	ation/local station	Multidrop link master station					Mu loc		Setting ignored for computer link.	
	SW02	Computer selection	link/multidrop link	Computer link					Mu		Always set to computer link (ON).	
sw ON 🗲	SW03		Unused			_				_		—
01 02 03	SW04	Write	Enabled					۵	For dedicated protocol			
03 04 ON <del>&lt;</del>			Transmission speed (BPS)	300	600	1200	2400	4800	9600	19200	Use prohibited	
05	SW05				ON	OFF	ON	OFF	ON	OFF	ON	_
06 🗖 07 🗖	SW06	Transmis	sion speed setting	OFF	OFF	ON	ON	OFF	OFF	ON	ON	
08	SW07			OFF	OFF	OFF	OFF	ON	ON	ON	ON	
09 🗖 10 🗖	SW08	Dat	ta bit setting		8	bits				7 bits		Excluding parity bit
11 🗖	SW09	Par	ity bit setting			Yes				None		—
12	SW10	Even/o	dd parity setting		E	ven		Odd				Valid when parity bit is used.
	SW11	Sto	p bit setting		2	bits		1 bit				—
	SW12	S	um check		,	Yes				None		For dedicated protocol

3) Station number setting switch

The difference between the AJ71UC24 and A1SJ71UC24-R2/R4/PRF is as follows.

Item	AJ71UC24	A1SJ71UC24-R2/R4/PRF	Compati- bility	Precautions for replacement
Station number setting switch	Equipped with station number setting switch	No station number setting switch	×	Specify 00 <sub>H</sub> for the station number to access a computer with a dedicated protocol from an external device.

#### (d) Comparison of I/O signals for PLC CPU

There are no differences in I/O signals between the AJ71UC24 and A1SJ71UC24-R2/R4/PRF.  $_{\bigcirc}$ : Compatible,  $_{\bigtriangleup}$ : Partial change required,  $\times$ : Incompatible

Input	AJ71UC24	A1SJ71UC24-R2/R4/PRF	Compati-	Precautions for replacement	
signal	Signal name	Signal name	bility	recautions for replacement	
X0	Transmissio		0		
X1	Receive data		0		
X2	Global	signal	0		
X3	On-demand	in execution	0		
X4			0		
X5	Computer link module tran	smission sequence status	0		
X6			0		
X7	Computer link	module ready	0		
X8	Use pro	phibited	0		
X9	Mode switch	ing complete	0		
XA			0		
XB	Use pro	phibited	0		
XC					
XD	WDT	error	0		
XE			0		
XF			0		
X10			0		
X11			0		
X12			0		
X13			0		
X14			0		
X15			0		
X16		shibitod	0		
X17	Use pro	אוואוכע	0		
X18			0		
X19			0		
X1A			0		
X1B			0		
X1C			0		
X1D					
X1E			0		
X1F			0		

	⊖: Compatible, △: Partial change required, ×: Incompatible						
Output signal	AJ71UC24 Signal name	A1SJ71UC24-R2/R4/PRF Signal name	Compati- bility	Precautions for replacement			
Y0			0				
Y1			0				
Y2			0				
Y3			0				
Y4			0				
Y5			0				
Y6			0				
Y7			0				
Y8	Use pr	phibited	0				
Y9			0				
YA			0				
YB			0				
YC			0				
YD			0				
YE			0				
YF			0				
Y10	Send	equest	0				
Y11	Receive data	read complete	0				
Y12			0				
Y13			0				
Y14			0				
Y15	Use pr	bhibited	0				
Y16			0				
Y17			0				
Y18			0				
Y19	Mode switc	hing request	0				
Y1A			0				
Y1B			0				
Y1C	Use or	phibited	0				
Y1D			0				
Y1E			0				
Y1F			0				

#### (e) Buffer memory address comparison

No special differences are identified in buffer memory assignment.

 $\bigcirc$  : Compatible,  $\bigtriangleup$ : Partial change required,  $\times$ : Incompatible

Add	ress	AJ71UC24	A1SJ71UC24-R2/R4/PRF	Compati-	
HEX	DEC	Buffer memory name	Buffer memory name	bility	Precautions for replacement
0H	0	Non-procedural ser	Non-procedural send data count storage area		
1H to 7FH	1 to 127	Send da	Send data storage area		
80H	128	Non-procedural rece	ive data count storage area	0	
81H to FFH	129 to 255	Receive d	Receive data storage area		
100H	256	Non-procedural receiv	e end code specification area	0	
to	to		to	-	
103H	259	Non-procedural word/byte specification area		0	
104H	260	Non-procedural send buffer memory head address specification area		0	
105H	261	Non-procedural send buffer memory length specification area		0	
106H	262		receive buffer memory s specification area	0	
107H	263		eive buffer memory length fication area	0	
108H	264	Non-procedural receive end data count specification area		0	
109H	265	On-demand buffer memory head address specification area		0	
10AH	266	On-demand data length specification area		0	
10BH	267	RS-232 CD terminal check setting area		0	
to	to		to		
DFFH	3583		_	0	

#### (2) Comparisons between AJ71C22-S1 and A1SJ71UC24-R4

#### (a) Performance specifications comparison

 $_{O}$  : Compatible,  $_{\bigtriangleup}$  : Partial change required,  $\times$  : Incompatible

Specification		AJ71C22-S1	A1SJ71UC24-R4	Compati- bility	Precautions for replacement
Interfaces	3	RS-422 compliant	RS-422/485 compliant	0	
Communi	cation method	Half-duplex com	nmunication	0	
Synchron	ization method	Start stop synchronization (	asynchronous method)	0	
Transmission speed 38400bps 19200, 38400bps		19200, 38400bps	0		
Start bit 1		0			
Data	Data bit	7		0	
format	Parity bit	1 (vertical	0		
Stop bit		1	0		
Error detection		Parity check	0		
	cuon	BCC ch	eck	0	
Line configuration (connection)		8 local stations for 1 master station		0	
Transmission distance Max. 500m (overall distance)		rall distance)	0		
Current consumption		1.4A	0.1A		
Number of occupied I/O points		32 points (I/O assignment: 32 special-purpose points)		0	
External dimensions		250(H) × 37.5(W) × 120(D) mm	mm 130(H) × 34.5(W) × 93.6(D) mm		The dimensions are different.
Weight		0.6kg	0.25kg	0	

#### (b) Function comparison

 $_{O}$  : Compatible,  $_{\bigtriangleup}$  : Partial change required,  $\times$  : Incompatible

Function	AJ71C22-S1	A1SJ71UC24-R4	Compati- bility	Precautions for replacement
Data communication	<ol> <li>ON/OFF data is sent to and received from a 422 interface.</li> <li>Maximum number of ON/OFF data transferr Total of inputs/outputs for all slave stations Inputs/outputs per station ····· Outputs ≤ Inputs</li> <li>For communication with a slave station, the (a) Slave stations to communicate with (b) Priority order of slave stations (c) Number of communication data points</li> </ol>	0		
Communication data monitoring	Data communicated with a slave station can be monitored in units of 8 points.	_	×	A1SJ71UC24-R4 does not have the LED for slave station communication data monitoring. Directly monitor the buffer memory addresses 20H to 3FH and 40H to 5FH.
Self-loopback test	Communication check is conducted for the RS4	422 interface.	0	

#### (c) Comparison of I/O signals for PLC CPU

There are no differences in I/O signals between the AJ71C22-S1 and A1SJ71UC24-R4.  $_{\odot}$ : Compatible,  $_{\Delta}$ : Partial change required,  $\times$ : Incompatible

Input	AJ71C22-S1	A1SJ71UC24-R4	Compati-	Precautions for replacement
signal	Signal name	Signal name	bility	
X0	Data transmission see	quence in execution	0	
X1	Pre-transmission	sequence error	0	
X2	Data transmission	sequence error	0	
X3		0		
X4			0	
X5			0	
X6				
X7			0	
X8	Use pro	nidited	0	
X9			0	
XA			0	
ХВ			0	
XC				
XD	WDT	WDT error		
XE		- <b>1</b> - 1	0	
XF	Use pro	ποπα	0	

 $\bigcirc$ : Compatible,  $\triangle$ : Partial change required, ×: Incompatible

Output signal	AJ71C22-S1	A1SJ71UC24-R4	Compati- bility	Precautions for replacement	
Y0	Signal name Link st	Signal name art	O		
Y1	Error re		0		
Y2			0		
Y3			0		
Y4			0		
Y5			0		
Y6			0		
Y7			0		
Y8	Use prohi	ibited	0		
Y9			0		
YA YB			0		
YC			0		
YD			0		
YE					
YF			0		

#### (d) Buffer memory address comparison

(a)		mory address comparison ifferences are identified in buffer	memory assignment.	Partial cha	nge required, ×: Incompatible
Add	Iress	Compati-			
HEX	DEC	Buffer memory name	Buffer memory name	bility	Precautions for replacement
0H	0	Number of acce	ss slave stations	0	
1H to 8H	1 to 8	Transmiss	sion priority	0	
9H to 10H	9 to 16	Number of I	bits received	0	
11H to 18H	17 to 24	Number of I	bits received	0	
19H to 1DH	25 to 29		Use prohibited	0	
1EH	30	Use prohibited	Max. number of link points (256/512 points)	-	
1FH	31		Off-link station	_	
20H to 3FH	32 to 63	Receive data	Receive data storing area		
40H to 5FH	64 to 95	Send data	storing area	0	
60H	96	Error	code	0	
61H	97	Error slave s	tation display	0	
62H	98	Link time storing a	area (current value)	0	
63H	99	Link time storing an	ea (maximum value)	0	
64H to 6FH	100 to 111	Use pr	Use prohibited		
70H	112	Error return request area		0	
71H	113				
to	to	Work area		0	
7FFH	2047		Work area		
to	to				
DFFH	3583	-			

# 9 NETWORK SYSTEM REPLACEMENT

## 9.1 List of Alternative Network Module Models

Large-sized A/QnA series models to be discontinued			Alternative small-sized AnS/Q2AS series models		
Product name	Model name	Model name	Remarks (restrictions)		
	A1NCPUP21	A2USHCPU-S1			
	AINCFUPZI	A1SJ71AP21			
	A1NCPUR21	A2USHCPU-S1 A1SJ71AR21			
	A2NCPUP21	A2USHCPU-S1 A1SJ71AP21			
	A2NCPUR21	A2USHCPU-S1 A1SJ71AR21			
	A2NCPUP21-S1	A2USHCPU-S1 A1SJ71AP21			
	A2NCPUR21-S1	A2USHCPU-S1 A1SJ71AR21			
	A3NCPUP21	A2USHCPU-S1 A1SJ71AP21			
	A3NCPUR21	A2USHCPU-S1 A1SJ71AR21			
	A2ACPUP21	A2USHCPU-S1 A1SJ71AP21			
	A2ACPUR21	A2USHCPU-S1 A1SJ71AR21			
CPU module with link	A2ACPUP21-S1	A2USHCPU-S1 A1SJ71AP21	Refer to Section 2.1 for the details.		
	A2ACPUR21-S1	A2USHCPU-S1 A1SJ71AR21			
	A3ACPUP21	A2USHCPU-S1 A1SJ71AP21			
	A3ACPUR21	A2USHCPU-S1 A1SJ71AR21			
	A1NCPUP21-S3	A2USHCPU-S1 A1SJ71AP21-S3			
	A2NCPUP21-S3	A2USHCPU-S1 A1SJ71AP21-S3			
	A2NCPUP21-S4	A2USHCPU-S1 A1SJ71AP21-S3			
	A3NCPUP21-S3	A2USHCPU-S1 A1SJ71AP21-S3			
	A2ACPUP21-S3	A2USHCPU-S1 A1SJ71AP21-S3			
	A2ACPUP21-S4	A2USHCPU-S1 A1SJ71AP21-S3			
	A3ACPUP21-S3	A2USHCPU-S1 A1SJ71AP21-S3			
MELSECNET/MINI-S3	AJ71PT32-S3	A1SJ71PT32-S3	No monitor station function		
master module	AJ71T32-S3	A1SJ71PT32-S3	No monitor station function		
MELSEC-I/OLINK master module	AJ51T64	A1SJ51T64	No special restrictions		

Large-sized A/QnA series models to be discontinued		Alternative small-sized AnS/Q2AS series models			
Product name	Model name	Model name	Remarks (restrictions)		
MELSECNET data	AJ71AP21	A1SJ71AP21	No special restrictions		
link module	AJ71AR21	A1SJ71AR21	No special restrictions		
	AJ71LP21	A1SJ71LP21	No special restrictions		
	AJ71LP21G	None	No alternative model		
	AJ71LR21	A1SJ71LR21			
	AJ71BR11	A1SJ71BR11	No special restrictions		
MELSECNET/10 * data link module	AJ71QLP21	A1SJ71QLP21	No special restrictions		
	AJ71QLP21S	A1SJ71QLP21S			
	AJ71QLP21G	None	No alternative model		
	AJ71QLR21	A1SJ71QLR21	No anapial rastrictions		
	AJ71QBR11	A1SJ71QBR11	No special restrictions		
B/NET interface module	AJ71B62-S3	A1SJ71B62-S3	No special restrictions		
JEMANET(OPCN-1) interface module	AJ71J92-S3	A1SJ71J92-S3	No special restrictions		
CC-Link master/local	AJ61BT11	A1SJ61BT11	No special restrictions		
module	AJ61QBT11	A1SJ61QBT11			

\* This is not a model to be discontinued.

## 9.2 Network Module Comparison

#### 9.2.1 Replacement of CPU module with link

#### (1) Empty slot (32 occupied points) required

The small-sized A series include no CPU module with link function. For this reason, the existing CPU module with link function must be replaced with a CPU + a link module so that extra 1 slot (32 occupied points) is required.

When large-sized A CPU (with link) is used

#### (2) Network parameter settings are required

Network parameter settings are required. Refer to Section 7.2.

#### 9.2.2 MELSECNET/MINI-S3 master module comparison

# (1) Comparison between AJ71PT32-S3 (AJ71T32-S3) and A1SJ71PT32-S3 (A1SJ71T32-S3)

(a) Performance specifications comparison

 $\bigcirc$ : Compatible,  $\triangle$ : Partial change required, ×: Incompatible

Specification	AJ71PT32-S3 (AJ71T32-S3)	A1SJ71PT32-S3 (A1SJ71T32-S3)	Compati- bilty	Precautions for replacement
Max. number of link stations	6	64		
Input	512 p	points	0	
Output	512 p	points	0	
I/O refresh time	3.2 to	3.2 to 18ms		
Communication speed	1.5N	Nbps	0	
Optical transmission level (Optical cable)	-12.5 to	-11.6dB	0	
Optical receive level (Optical cable)	-34.8 to -14.0dB			
Optical wave length (Optical cable)	660nm (Visil	ble radiation)	0	
Max. inter-station transmission distance	50m (Optical cable)/10	0m (Twisted pair cable)	0	
Number of occupied I/O points	I/O mode: 32 Extension mode: 48		0	
Internal current consumption (5VDC)	0.35A		_	
External dimensions	250(H)×37.5(W)×119(D) mm	130(H) × 34.5(W) × 107.6(D) mm	Δ	The dimensions are different.
Weight	0.6	ŝkg	0	

#### (b) Function comparison

 $\bigcirc$ : Compatible,  $\triangle$ : Partial change required, ×: Incompatible

Function	AJ71PT32-S3 (AJ71T32-S3)	A1SJ71PT32-S3 (A1SJ71T32-S3)	Compati- bility	Precautions for replacement
MINI standard protocol	Standard protocol of remote terminal module			
Nonprocedural protocol	Communication protocol for AJ35PTF-R2			
Dedicated protocol	Communication protocol for AJ35PTF-R2			
Tool box protocol	Communication protocol for AJ35PT-OPB-			
E.C.MODE	Yes			
Monitor station setting	Equipped with monitor station No. setting switch	No monitor station No. setting switch	Δ	Monitor the buffer memory address 10 to 41/110 to 141 by GX Developer.

- (c) Comparison of I/O signals for PLC CPUs
  - No special differences are identified.
- (d) Buffer memory address comparison

No special differences are identified.

### 9.2.3 MELSEC-I/OLINK master module comparison

#### (1) Comparisons between AJ51T64 and A1SJ51T64

#### (a) Performance specifications comparison

		Partial cl	nange required, ×: Incompatible		
	Item	Specification AJ51T64 A1SJ51T64			Precautions for replacement
Max. number of control I/O points		128 points (if the same num	0		
I/O r	efresh time	Approx. 5.4ms (regardless	0		
Communication cable		0.75mm <sup>2</sup> or larger 0.75mm <sup>2</sup> or larg	0		
	Communication speed	3840	0		
cations	Communication method	Register inse	0		
Communications specifications	Synchronization method	Combination of frame synchronization	0		
tions	Error control system	Parity check			
unica	Transmission channel	Bus (T-branch possible, terminal resistors unnecessary)			
numu	Transmission distance	Overall distance: 200m			
ŏ	Max. number of remote I/O modules	16 stations per	0		
Error (RUN) indication/output		Indication The PLC CPU detects External output wi	0		
LED		Communication status and error station station No. display	Communication status display		Error station station No. is not displayed.
Setting switch		On module face	Inside module	Δ	Functions are the same. However, the position of the switches differs.
Number of occupied I/O points		64 points (I/O assignment: 64 output points)*			
External power supply voltage		21.6 to 27.6VDC (for the transmission channel)			
External power supply current consumption		90mA (TYP 24VDC)			
Internal current consumption (5VDC)		115mA			
External dimensions		250(H) × 37.5(W) × 119(D) mm	130(H)×34.5(W)×93.6(D) mm	Δ	The dimensions are different.
Weight		0.35kg	0.3kg	0	

\* If only a few remote I/O modules are used, perform I/O assignment with a peripheral device to decrease the number of occupied I/O points to 16, 32, or 48.

#### (b) Comparison of I/O signals for PLC CPUs

No special differences are identified.

# **10** SPECIAL FUNCTION MODULE REPLACEMENT

# **10.1 List of Alternative Special Function Module Models**

A series models to be discontinued		Alternative AnS series models		
Product	Model	Model	Model Remarks (restrictions)	
	A616AD	A1S68AD	<ol> <li>External wiring change: Required to use a different terminal block</li> <li>Change in number of modules: Required (2 modules necessary)</li> <li>Program changes: Change of I/O signals and buffer memory addresses</li> <li>Performance specifications: Change to 8CH/module and input signal (only positive current for I)</li> <li>Function specifications: No multiplexer function</li> </ol>	
Analog input module	A68AD	A1S68AD	<ol> <li>1) External wiring change: Required to use a different terminal block</li> <li>2) Change in number of modules: Not required</li> <li>3) Program changes: Change of I/O signals and buffer memory addresses</li> <li>4) Performance specifications: Change in I/O characteristics</li> <li>5) Function specifications: Change in A/D conversion enable/disable function settings</li> </ol>	
	A68AD-S2	A1S68AD	<ol> <li>1) External wiring change: Required to use a different terminal block</li> <li>2) Change in number of modules: Not required</li> <li>3) Program changes: Change of I/O signals and buffer memory addresses</li> <li>4) Performance specifications: Change in I/O characteristics</li> <li>5) Function specifications: Not required</li> </ol>	
	A68ADN	A1S68AD	<ol> <li>1) External wiring change: Required to use a different terminal block</li> <li>2) Change in number of modules: Not required</li> <li>3) Program changes: Change of I/O signals and buffer memory addresses</li> <li>4) Performance specifications: Change in I/O characteristics and resolution</li> <li>5) Function specifications: Not required</li> </ol>	
	A60MX	None	Consider using multiple A1S68AD modules.	
Multiplexer module	A60MXRN	None	Consider using the A1S68AD and isolation signal converter.	
	A60MXR	None	Consider using the A1S68AD and isolation signal converter.	
	A60MXTN	None	Consider using multiple A1S68TD modules.	
	A60MXT	None	Consider using multiple A1S68TD modules.	

	be discontinued		Alternative AnS series models	
Product	Model	Model	Remarks (restrictions)	
Analog output module	A616DAI	A1S68DAI	<ol> <li>1) External wiring change: Required to use a different terminal block</li> <li>2) Change in number of modules: Required (2 modules necessary)</li> <li>3) Program changes: Change of I/O signals and buffer memory addresses</li> <li>4) Performance specifications: Change to 8CH/module and in input current range</li> <li>5) Function specifications: Change in relationships between D/A conversion disable channel and conversion time</li> </ol>	
	A616DAV	A1S68DAV	<ol> <li>1) External wiring change: Required to use a different terminal block</li> <li>2) Change in number of modules: Required (2 modules necessary)</li> <li>3) Program changes: Change of I/O signals and buffer memory addresses</li> <li>4) Performance specifications: Change to 8CH/module and in resolution • accuracy</li> <li>5) Function specifications: Change in relationships between D/A conversion disable channel and conversion time</li> </ol>	
	A62DA	A1S62DA	<ol> <li>1) External wiring change: Required to use a different terminal block</li> <li>2) Change in number of modules: Not required</li> <li>3) Program changes: Change of I/O signals and buffer memory addresses</li> <li>4) Performance specifications: Change in I/O characteristics and conversion time</li> <li>5) Function specifications: Not required</li> </ol>	
	A62DA-S1	A1S62DA	<ol> <li>1) External wiring change: Required to use a different terminal block</li> <li>2) Change in number of modules: Not required</li> <li>3) Program changes: Change of I/O signals and buffer memory addresses</li> <li>4) Performance specifications: Change in I/O characteristics and conversion time</li> <li>5) Function specifications: Not required</li> </ol>	
	A68DAI-S1	A1S68DAI	<ol> <li>1) External wiring change: Required to use a different terminal block</li> <li>2) Change in number of modules: Not required</li> <li>3) Program changes: Change of I/O signals and buffer memory addresses</li> <li>4) Performance specifications: Change in output current range and I/O characteristics increase of current consumption</li> <li>5) Function specifications: Not required</li> </ol>	
	A68DAV	A1S68DAV	<ol> <li>1) External wiring change: Required to use a different terminal block</li> <li>2) Change in number of modules: Not required</li> <li>3) Program changes: Change of I/O signals and buffer memory addresses</li> <li>4) Performance specifications: Change in output current range and I/O characteristics, increase of current consumption</li> <li>5) Function specifications: Not required</li> </ol>	
Temperature input module	A616TD	A1S68TD	<ol> <li>1) External wiring change: Required to use a different terminal block</li> <li>2) Change in number of modules: Required (2 modules necessary)</li> <li>3) Program changes: Change of I/O signals and buffer memory addresses</li> <li>4) Performance specifications: Change to 8CH/module, and in input temperature range and conversion accuracy</li> <li>5)Functionspecifications: Change in relationships between conversion disable channel and conversion time</li> </ol>	
	A68RD3N	A1S62RD3N	<ol> <li>1) External wiring change: Required to use a different terminal block</li> <li>2) Change in number of modules: Required (4 modules necessary)</li> <li>3) Program changes: Changed</li> <li>4) Performance specifications: Change to 2CH/module</li> <li>5) Function specifications: Not required</li> </ol>	
	A68RD4N	A1S62RD4N	<ol> <li>External wiring change: Required to use a different terminal block</li> <li>Change in number of modules: Required (4 modules necessary)</li> <li>Program changes: Changed</li> <li>Performance specifications: Change to 2CH/module</li> <li>Function specifications: Not required</li> </ol>	
High-speed counter module	AD61	A1SD62	<ol> <li>1) External wiring change: Required to use a different terminal block</li> <li>2) Change in number of modules: Not required</li> <li>3) Program changes: Change of buffer memory addresses</li> <li>4) Performance specifications: Upward-compatibility</li> <li>5) Function specifications: Upward-compatibility</li> </ol>	
	AD61S1	A1SD62	<ol> <li>1) External wiring change: Required to use a different terminal block</li> <li>2) Change in number of modules: Not required</li> <li>3) Program changes: Change of buffer memory addresses</li> <li>4) Performance specifications: Upward-compatibility</li> <li>5) Function specifications: Upward-compatibility</li> </ol>	
Position detection module	A62LS-S5	A1S62LS	<ol> <li>1) External wiring change: Require of terminal block to connector</li> <li>2) Change in number of modules: Required (2 modules → 1 module)</li> <li>3) Program changes: Not changed. Note that the data setting must be made from a sequence program or the data setting module "VS-T62" manufactured by NSD corporation.</li> <li>4) Performance specifications: Not required</li> <li>5) Function specifications: Not required</li> </ol>	
	A61LS	None		
			No alternative model	

# special function module replacement

A series models to	be discontinued		Alternative AnS series models	
Product	Model	Model	Remarks (restrictions)	
	AD70	A1SD70	<ol> <li>1) External wiring change: Required to use a different terminal block</li> <li>2) Change in number of modules: Required (1 module → 2 modules)</li> <li>3) Program changes: Not changed</li> <li>4) Performance specifications: Not required</li> <li>5) Function specifications: Not required</li> </ol>	
	AD72	None	No alternative model	
	AD75M1	A1SD75M1	No special restrictions However, since the connector for peripheral connection is different, A1SD75-C01HA cable required.	
Positioning module	AD75M2	A1SD75M2	No special restrictions However, since the connector for peripheral connection is different, A1SD75-C01HA cable required.	
Ŭ	AD75M3	A1SD75M3	No special restrictions However, since the connector for peripheral connection is different, A1SD75-C01HA cab required.	
	AD75P1-S3	A1SD75P1-S3	No special restrictions However, since the connector for peripheral connection is different, A1SD75-C01HA cable is required.	
	AD75P2-S3	A1SD75P2-S3	No special restrictions However, since the connector for peripheral connection is different, A1SD75-C01HA cable is required.	
	AD75P3-S3	A1SD75P3-S3	No special restrictions However, since the connector for peripheral connection is different, A1SD75-C01HA cable is required.	

# **10.2 Special Function Modules Comparison**

# 10.2.1 Analog input modules comparison

## (1) Comparisons between A616AD and A1S68AD

#### (a) Performance specifications comparison

 $\bigcirc$ : Compatible,  $\triangle$ : Partial change required, ×: Incompatible

Item		A	616AD			A1S68AD	Compati- bility	Precautions for replacement
Analog input		oltage:-10 to (Input resi urrent: -20 t (Input resis	stance: 1N o 0 to +20	1Ω) mADC		Voltage: -10 to 0 to +10VDC (Input resistance: 1M Ω ) Current: 0 to +20mA (Input resistance: 250 Ω )	Δ	Negative current cannot be converted.
Digital output	(	signed bin 48 to 4047 ng is availal	-2048 to			16-bit, signed binary	0	
	Input	Analog input range	Maximum resolution	Digital output value		I/O characteristics Analog input value Digital output value		
		0 to +10 0 to +5	2.5mV (1/4000) 1.25mV			0 to +10V         0 to +4000           -10 to +10V         -2000 to +2000           0 to 5V or 0 to 20mA         0 to +4000		
I/O characteristics,	Voltage (V)	+1 to +5	(1/4000) 1.0mV (1/4000) 5.0mV	0 to 4000 -2000 to 2000		1 to 5V or 4 to 20mA 0 to +4000 Maximum resolution		Check the I/O conversion
maximum resolution	5 to +5 0 to +20 0 to +20 0 to +20 0 to +20 +4 to +2 (mA)	-10 to +10 -5 to +5	(1/4000) 2.5mV (1/4000)			Analog input value         Digital output value           0 to +10V         2.5mV		characteristics.
		0 to +20 0 to +20	10 μ A (1/2000) 5 μ A (1/4000)	0 to 2000 -2000 to 0 0 to 4000 -2000 to 2000 1000 to 3000 -1000 to 1000		-10 to +10V         5mV           0 to +5V         1.25mV           1 to 5V         1mV		
		+4 to +20 -20 to +20	4 μ A (1/4000) 20 μ A (1/2000)			0 to 20mA         5 μ A           4 to 20mA         4 μ A		
		-20 to +20	10 μ A (1/4000)	0 to 4000 -2000 to 2000				
Overall accuracy (%) (Accuracy to full-scale)	0 to 5V, 1	10 to 10V -20 to 20m to 5V ., 4 to 20m/ combination accuracy o	A A A A A A A A A A A A A A A A A A A		: 12) % : 24) or	Within $\pm$ 1% (Digital output value $\pm$ 40)	×	Overall accuracy differs.
Maximum conversion speed (ms/channel)	When using A6 When using with When using A6 When using A6	16AD only h A60MX 0MXR	1 1 1 (Sam 7.0 (Dir 1 (Sam	pling processir ect access proc pling processing ect access proc	essing) g)	0.5ms/channel (The maximum conversion speed is 1 ms/channel on all channels if averaging processing is set even for only one channel.)	0	
Absolute maximum input		0	e: ± 15V t: ± 30mA			Voltage: $\pm$ 35V Current: $\pm$ 30mA	0	
Analog input points	16 channels/module				8 channels/module	×	As the number of channels is reduced, the number of modules installed may increase. In addition, the number of channels cannot be increased by connecting a multiplexer module.	

Item	A616AD	A1S68AD	Compati- bility	Precautions for replacement
Isolation method	Between the input terminals and PLC : photocoupler isolation Between channels : non-isolated $(1M \ \Omega \ resistor \ isolation)$	Between the input terminals and PLC power : photocoupler isolation Between channels : non-isolated	0	
Occupied I/O points	32 points (I/O assignment: special 32 points)	32 points (I/O assignment: special 32 points)	0	
Connected terminal	38-point terminal block	20-point terminal block	×	
Applicable wire size	0.75 to 2mm <sup>2</sup> (Applicable tightening torque 39 to 59N • cm)	0.75 to 1.5mm <sup>2</sup>	Δ	External wiring must be changed.
Applicable solderless terminal	V1.25-3, V1.25-YS3A, V2-S3, V2-YS3A	R1.25-3, 1.25-YS3, RAV1.25-3, V1.25-YS3A	Δ	
Internal current consumption (5VDC)	1A	0.4A	0	
External dimensions	250(H)×37.5(W)×131(D)mm	130(H)×34.5(W)×93.6(D)mm		The dimensions are different.
Weight	0.85kg	0.27kg	0	

 $\bigcirc$ : Compatible,  $\bigtriangleup$ : Partial change required, x : Incompatible

## (b) Function comparison

O: Compatible, △: Partial change required, ×: Incompatible, —: Additional function

Item	A616AD	A1S68AD	Compati- bility	Precautions for replacement
Sampling processing	Switches channels in order of channel number at intervals of 1 ms to execute A/D conversion, and stores the digital output values to the buffer memory. Maximum conversion speed is 1ms.	Converts an analog input value to a digital value for each channel at any time, and stores it in the buffer memory as a digital output value. The length of time to store the sampled digital output value in the buffer memory differs depending on the number of the A/D conversion enabled channels. (Processing time) = (Number of A/D conversion enabled channels) × 0.5(ms) *0.5(ms) is maximum conversion speed.	0	
Direct access processing	Separately from normal sampling processing, allows direct A/D conversion of the specified channels by specifying channels and outputting direct access request from a sequence program. When channel specifications with sampling and direct access processings are input simultaneously, the direct access request is prioritized. Maximum conversion speed is 1ms.	_	×	No direct access function
Averaging processing	_	Executes the A/D conversion for the channel specified for averaging process by a PLC CPU for the set number of times or set time. The total values except the maximum and minimum values are averaged and stored in the buffer memory. However, when the processing number of times is no more than two, the sampling process is executed. 1) When the averaging process by time is specified (Processing time) = $\frac{\text{Setting time}}{(\text{number of A/D conversion-enabled channels}) \times 1(\text{ms})}$ 2) When the number of times for the averaging process is specified (Processing time) = (the setting number of time) $\times (A/D \text{ conversion-enabled channel}) \times 1(\text{ms})$ 1(ms) is maximum conversion speed.	_	
A/D conversion enable/disable setting	Specifies whether to enable or disable the conversion for each channel and writes it to the buffer memory address FH to 17H. By disabling the conversion for the channels that are not used, the sampling time can be shortened.	Allows the A/D conversion enable/disable setting for each channel by writing "1" (enable) or "0" (disable) to the buffer memory address 0. By disabling the conversion for the channels that are not used, the sampling time can be shortened. (Default: All channels are set to "enable".)	0	
Input range setting	Allows the input range setting for each channel, and change of the I/O conversion characteristics.	Allows the input range setting for each channel, and change of the I/O conversion characteristics.	0	
Offset/gain setting	The I/O characteristics differ slightly between modules according to ambient temperature, A/D converter characteristic variations, etc. The Offset/gain setting allows fine adjustment and corrects this slight difference in characteristics.	_	×	No offset/gain setting function

# (c) Comparison of I/O signals for PLC CPU

	A61	6AD			A1S	68AD		
Device NO.	Signal name	Device NO.	Signal name	Device NO.	Signal name	Device NO.	Signal name	
X0	WDT error (A616AD detection)	Y0		X0	WDT error (A1S68AD detection)	Y0		
X1	A/D conversion READY	Y1		X1	A/D conversion READY	Y1		
X2	Error	Y2		X2	Error	Y2		
X3		Y3		X3		Y3		
X4		Y4		X4		Y4		
X5		Y5		X5		Y5		
X6		Y6		X6		Y6		
X7		Y7		X7		Y7		
X8		Y8		X8		Y8	Not used	
X9		Y9	Not used	X9		Y9		
XA		YA		XA		YA		
XB		YB		XB		YB		
XC		YC		XC		YC		
XD		YD		XD		YD		
XE		YE		XE		YE		
XF		YF		XF	Not used	YF		
X10	Not used	Y10		X10		Y10		
X11		Y11		X11		Y11		
X12		Y12		X12		Y12	Error reset	
X13		Y13		X13		Y13		
X14	Y	Y14		X14		Y14		
X15		Y15		X15		Y15		
X16		Y16		X16		Y16		
X17		Y17		X17		Y17		
X18		Y18	Direct access request signal	X18		Y18		
X19		Y19		X19		Y19	Not used	
X1A		Y1A		X1A		Y1A		
X1B		Y1B		X1B		Y1B		
X1C		Y1C	Not used	X1C		Y1C		
X1D		Y1D		X1D		Y1D		
X1E		Y1E		X1E		Y1E		
X1F		Y1F		X1F		Y1F		

Modifying sequence programs is required as the assignment of buffer memory differs. For details of the buffer memory and sequence programs, refer to the user's manual.

		A616AD				A1S68AD	
Address		Name	Read/Write	Address	Name	Default value	Read/Write
0		INPUT designation	R/W	0	A/D conversion enable/ disable specification	00FFн (All channels are set to "enable".)	R/W
1	For direct access	MX. CH. designation		1	Writing data error code	0 (No error)	R
2		Digital output value	R	2	Average processing specification	0 (Sampling processing specified for all channels)	R/W
3	Sampling cycle design	ation		3			
4	Data format selection		R/W	4			
5	Error code storage		R/W	5			
6	Faulty multiplexer mod	lule CNT. No. storage		6	Not used	—	_
7				7			
8				8			
9				9			
10		lotusod		10	CH1 Average time, count	0	
11	Not used			11	CH2 Average time, count	0	
12				12	CH3 Average time, count	0	
13				13	CH4 Average time, count	0	
14				14	CH5 Average time, count	0	R/W
15		A616AD		15	CH6 Average time, count	0	
16		INPUT 0 A60MX, A60MXR		16	CH7 Average time, count	0	
17	-	INPUT 1 A60MX, A60MXR		17	CH8 Average time, count	0	
18		INPUT 2 A60MX, A60MXR		18	- Not used	_	_
19	Conversion enable/ disable designation	INPUT 3 A60MX, A60MXR	R/W	19			
20		INPUT 4 A60MX, A60MXR	_	20	CH1 Digital output value	0	-
21		INPUT 5 A60MX, A60MXR	_	21	CH2 Digital output value	0	
22		INPUT 6 A60MX, A60MXR	_	22	CH3 Digital output value	0	
23		INPUT 7 A60MX, A60MXR	_	23	CH4 Digital output value	0	R
24	Set data setting reque	st		24	CH5 Digital output value	0	4
25				25	CH6 Digital output value	0	-
26				26	CH7 Digital output value	0	4
27				27	CH8 Digital output value	0	
28	N	lot used	-	28	A/D conversion completed	0	R/W
29 to 47				29	Not used		—
48 to 63	INPUT channel digital output value		R				
64 to 255	Ν	lot used	_				
256 to 383	MX.CH. channel digita	l output value	R				

O: Compatible,  $\wedge$ : Partial change required, x: Incompatible

## (2) Comparisons between A68AD and A1S68AD

#### (a) Performance specifications comparison

Compati bility Precautions for A68AD A1S68AD ltem replacement Voltage: -10 to 0 to +10VDC (Input resistance:hardware version K or later:  $1M \Omega$ , Voltage: -10 to 0 to +10VDC Negative current Analog input hardware version J or earlier:  $30K \Omega$  ) (Input resistance:  $1M \Omega$ ) cannot be Δ Current: +4 to +20mADC (Input resistance: 250  $\Omega$  ) Current: 0 to +20mA converted \*Usable current input: -20 to 0 to +20mA (Input resistance: 250  $\Omega$  ) ACPU 16-bit, signed binary (-2048 to +2047) Digital 16-bit, signed binary 0 K2ACPU signed + 16-bit binary output (±2047) Digital output Analog input Analog input Digital output +10V +2000 Check the I/O I/O 0 to +10\ 0 to +4000 characteristi +5V or +20mA +1000 conversion Δ -10 to 10V -2000 to +2000 0V or +4mA characteristics. CS ±0 0 to +4000 0 to 5V or 0 to 20mA -5V or -12mA -1000 1 to 5V or 4 to 20mA 0 to +4000 -10V -2000 Voltage: 5mV (1/2000) Digital output Analog input 0 to +10V 2.5mV Maximum -10 to 10V 5mV Ο resolution 0 to 5V 1.25mV 1mV 1 to 5V Current: 20 µ A (1/1000) 0 to 20mA 5 u A 4 to 20mA 4μA  $\pm 1\%(\pm 20)$ Overall (Accuracy in respect to maximum digital output value Within + 1% $\cap$ accuracy (+2000)) (Digital output value  $\pm$  40) (The same (+2000) for voltage input and current input.) 0.5ms/channel Maximum (The maximum conversion speed is 1 ms/channel conversion Max. 2.5ms/channel 0 on all channels if averaging processing is set even speed for only one channel.) Absolute Voltage: ± 15V Voltage: ± 35V maximum Ο Current: ± 30mA Current: ± 30mA input Analog input 8 channels/module 8 channels/module 0 points Between the input terminals and PLC power: Between the input terminals and PLC power: Isolation photocoupler isolation photocoupler isolation 0 method Between channels : non-isolated Between channels : non-isolated Occupied I/ 32 points 32 points C O points (I/O assignment: special 32 points) (I/O assignment: special 32 points) Connected 38-point terminal block 20-point terminal block × terminal Applicable 0.75 to 2mm<sup>2</sup> External wiring 0.75 to 1.5mm<sup>2</sup> Δ must be wire size (Applicable tightening torque 7kg - cm) changed. Applicable V1.25-3, V1.25-YS3A, R1.25-3, 1.25-YS3, solderless Δ V2-S3, V2-YS3A RAV1.25-3, V1.25-YS3A terminal Recalculation of Internal internal current current Hardware version K or later: 0.39A. 0.4A consumption Δ Hardware version J or earlier: 0.9A consumption [5VDC] is (5VDC) required. The dimensions External 250(H) × 37.5(W) × 131(D)mm 130(H) × 34.5(W) × 93.6(D)mm Δ dimensions are different. Hardware version K or later: 0.3kg, Weight 0.27kg 0 Hardware version J or earlier: 0.6kg

## (b) Function comparison

O: Compatible,  $\triangle:$  Partial change required, x: Incompatible, —: Additional function

ltem	A68AD	A1S68AD	Compati- bility	Precautions for replacement
Sampling processing	Converts analog input values into digital output values successively, and stores the digital output value in the buffer memory. The length of time to store the sampled digital output value in the buffer memory differs depending on the number of channels used. (Processing time) = (Number of channels used) ×2.5 (ms/channel) * 2.5(ms) is maximum conversion speed.	Converts an analog input value to a digital value for each channel at any time, and stores it in the buffer memory as a digital output value. The length of time to store the sampled digital output value in the buffer memory differs depending on the number of the A/D conversion enabled channels. (Processing time) = (Number of A/D conversion enabled channels) ×0.5 (ms) * 0.5(ms) is maximum conversion speed.	0	
Averaging processing	Executes the A/D conversion for the channel specified for averaging process by a PLC CPU for the set number of times or set time. The total values except the maximum and minimum values are averaged and stored in the buffer memory. However, when the processing number of times is no more than two, the sampling process is executed. The applicable setting range is shown below: Count averaging : 1 to 4000 Time averaging: 20 to 10000ms	Executes the A/D conversion for the channel specified for averaging process by a PLC CPU for the set number of times or set time. The total values except the maximum and minimum values are averaged and stored in the buffer memory. However, when the processing number of times is no more than two, the sampling process is executed. The applicable setting range is shown below: Count averaging :1 to 20000 Time averaging : 4 to 10000ms	0	
A/D conversion enable/ disable setting	Set the number of channels for which conversion is enabled to the buffer memory address 0.	Allows the A/D conversion enable/disable setting for each channel by writing "1" (enable) or "0" (disable) to the buffer memory address 0. By disabling the conversion for the channels that are not used, the sampling time cycle can be shortened. (Default: All channels are set to "enable".)	Δ	Settable for each channel
Input range setting	_	Allows the input range setting for each channel, and change of the I/O conversion characteristics.	_	
Offset/gain setting	The I/O conversion characteristics can be changed.	_	×	No offset/gain setting function

## (c) Comparison I/O signals for PLC CPU

	A68	BAD		A1S68AD				
Device NO.	Signal name	Device NO.	Signal name	Device NO.	Signal name	Device NO.	Signal name	
X0	WDT error (A68AD detection)	Y0		X0	WDT error (A1S68AD detection)	Y0		
X1	A/D conversion READY	Y1		X1	A/D conversion READY	Y1		
X2		Y2		X2	Error	Y2		
X3		Y3		X3		Y3		
X4		Y4		X4		Y4		
X5		Y5		X5		Y5		
X6		Y6		X6		Y6		
X7		Y7		X7		¥7		
X8		Y8		X8		Y8	Not used	
X9		Y9		X9		Y9		
XA		YA		XA		YA		
XB		YB		XB		YB		
XC		YC		XC		YC		
XD		YD		XD		YD		
XE		YE		XE		YE		
XF		YF	Not used	XF		YF		
X10	Not used	Y10		X10		Y10		
X11		Y11		X11	Not used	Y11		
X12		Y12		X12		Y12	Error reset	
X13		Y13		X13		Y13		
X14 X15		Y14 Y15		X14		Y14 Y15		
X15 X16		Y16		X15 X16		Y16		
X10 X17		Y17		X10		Y17		
X17 X18		Y18		X17		Y18		
X10 X19		Y19		X10 X19		Y19	Not used	
X13 X1A		Y1A		X13 X1A		Y1A	Not used	
X1B		Y1B		X1B		Y1B		
X1C		Y1C		X1C		Y1C		
X1D		Y1D		X1D		Y1D		
X1E		Y1E		X1E		Y1E		
X1F		Y1F		X1F		Y1F		

Modifying sequence programs is required as the assignment of buffer memory differs. For details of the buffer memory and sequence programs, refer to the user's manual.

	A68	AD			A	1S68AD	
Address	Name	CPU	Read/Write	Address	Name	Default value	Read/Write
0	Number of channels			0	A/D conversion enable/ disable specification	00FFн (All channels are set to "enable".)	R/W
1	Average processing specification			1	Writing data error code	0 (All channels)	R
2	CH1 Average time, count	Common		2	Average processing specification	0 (Sampling processing specified for all channels)	R/W
3	CH2 Average time, count	to ACPU and	R/W	3		,	
4	CH3 Average time, count	K2ACPU		4			
5	CH4 Average time, count			5			
6	CH5 Average time, count			6	Not used	—	_
7	CH6 Average time, count	]		7			
8	CH7 Average time, count	]		8			
9	CH8 Average time, count			9			
10	CH1 Digital output value			10	CH1 Average time, count	0	
11	CH2 Digital output value			11	CH2 Average time, count	0	
12	CH3 Digital output value			12	CH3 Average time, count	0	R/W
13	CH4 Digital output value	ACPU		13	CH4 Average time, count	0	
14	CH5 Digital output value	ACFU		14	CH5 Average time, count	0	
15	CH6 Digital output value			15CH6 Average time, count0	0		
16	CH7 Digital output value			16	CH7 Average time, count	0	-
17	CH8 Digital output value			17	CH8 Average time, count	0	
18	CH1 Digital output value			18			
19	CH1 Positive or negative sign			19	Not used	_	
20	CH2 Digital output value			20	CH1 Digital output value	0	
21	CH2 Positive or negative sign			21	CH2 Digital output value	0	- - - R
22	CH3 Digital output value		_	22	CH3 Digital output value	0	
23	CH3 Positive or negative sign		R	23	CH4 Digital output value	0	
24	CH4 Digital output value	]		24	CH5 Digital output value	0	ĸ
25	CH4 Positive or negative sign	KAAODU		25	CH6 Digital output value	0	
26	CH5 Digital output value	K2ACPU		26	CH7 Digital output value	0	
27	CH5 Positive or negative sign			27	CH8 Digital output value	0	
28	CH6 Digital output value			28	A/D conversion completed	0	R/W
29	CH6 Positive or negative sign			29	Not used	_	_
30	CH7 Digital output value						
31	CH7 Positive or negative sign						
32	CH8 Digital output value						
33	CH8 Positive or negative sign						
34	Writing data error code	Common to ACPU and K2ACPU	R/W				

# (3) Comparisons between A68AD-S2 and A1S68AD

#### (a) Performance specifications comparison

 $O: \text{Compatible}, \ {\bigtriangleup}: \ \text{Partial change required}, \ {\textbf{x}}: \text{Incompatible}$ 

Item	A68AD-S2	A1S68AD	Compati- bility	Precautions for replacement	
Analog input	Voltage: -10 to 0 to +10VDC (Input resistance:Hardware version K or later: $1M \Omega$ , hardware version J or earlier: $30k \Omega$ ) Current: +4 to +20mADC (Input resistance: $250 \Omega$ ) *Usable current input: -20 to 0 to +20mA	Voltage: -10 to 0 to +10VDC (Input resistance: 1M Ω) Current: 0 to +20mA (Input resistance: 250 Ω)	Δ	Negative current cannot be converted.	
Digital output	ACPU 16-bit, signed binary (-2048 to +2047) K2ACPU sign+16-bit binary (± 2047)	16-bit, signed binary	0		
I/O characteristi cs	Analog input         Digital output           +10V         +2000           +5V or +20mA         +1000           0V or +4mA         ±0           -5V or -20mA         -1000           -10V         -2000	Analog input         Digital output           0 to +10V         0 to +4000           -10 to 10V         -2000 to +2000           0 to 5V or 0 to 20mA         0 to +4000           1 to 5V or 4 to 20mA         0 to +4000	Δ	Check the I/O conversion characteristics.	
Maximum resolution	Voltage: 5mV (1/2000)	Analog input         Digital output           0 to +10V         2.5mV           -10 to 10V         5mV           0 to +5V         1.25mV	0		
	Current: 20 µ A (1/1000)	1 to 5V         1mV           0 to 20mA         5 μ A           4 to 20mA         4 μ A			
Overall accuracy	$\pm$ 1%( $\pm$ 20) (Accuracy in respect to maximum digital output value (+2000)) (The same (+2000) for voltage input and current input.)	Within $\pm$ 1% (Digital output value $\pm$ 40)	0		
Maximum conversion speed	Max. 2.5ms/channel	0.5ms/channel (The maximum conversion speed is 1ms/ channel on all channels if averaging processing is set even for only one channel.)	0		
Absolute maximum input	Voltage: ± 15V Current: ± 30mA	Voltage: ± 35V Current: ± 30mA	0		
Analog input points	8 channels/module	8 channels/module	0		
Isolation method	Between the input terminals and PLC power: photocoupler isolation Between channels : non-isolated	Between the input terminals and PLC power: photocoupler isolation Between channels : non-isolated	0		
Occupied I/ O points	32 points (I/O assignment: special 32 points)	32 points (I/O assignment: special 32 points)	0		
Connected terminal	38-point terminal block	20-point terminal block	×		
Applicable wire size	0.75 to 2mm <sup>2</sup> (Applicable tightening torque 7kg.cm)	0.75 to 1.5mm <sup>2</sup>	Δ	External wiring must be	
Applicable solderless terminal	V1.25-3, V1.25-YS3A, V2-S3, V2-YS3A	R1.25-3, 1.25-YS3, RAV1.25-3, V1.25-YS3A	Δ	changed.	
Internal current consumption (5VDC)	Hardware version K or later: 0.39A, Hardware version J or earlier: 0.9A	0.4A	Δ	Recalculation of internal current consumption [5VDC] is required.	
External dimensions	250(H)×37.5(W)×131(D)mm	130(H)×34.5(W)×93.6(D)mm	Δ	The dimensions are different.	
Weight	Hardware version K or later: 0.3kg, Hardware version J or earlier: 0.6kg	0.27kg	0		

## (b) Function comparison

O: Compatible, <u>∧</u>: Partial change required, ×: Incompatible, —: Additional function

Item	A68AD-S2	A1S68AD	Compati- bility	Precautions for replacement
Sampling processing	Converts analog input values into digital output values successively, and stores the digital output value in the buffer memory. The length of time to store the sampled digital output value in the buffer memory differs depending on the number of channels used. (Processing time) = (Number of channels used) × 2.5 (ms/channel) * 2.5(ms) is maximum conversion speed.	Converts an analog input value to a digital value for each channel at any time, and stores it in the buffer memory as a digital output value. The length of time to store the sampled digital output value in the buffer memory differs depending on the number of the A/D conversion enabled channels. (Processing time) = (Number of A/D conversion enabled channels) × 0.5 (ms) * 0.5(ms) is maximum conversion speed.	0	
Averaging processing	Executes the A/D conversion for the channel specified for averaging process by a PLC CPU for a set number of times or set time. The total values except the maximum and minimum values are averaged and stored in the buffer memory. However, when the processing number of times is no more than two, the sampling process is executed. The applicable setting range is shown below: Count averaging: 1 to 4000 Time averaging : 20 to 10000ms	Executes the A/D conversion for the channel specified for averaging process by a PLC CPU for the set number of times or set time. The total values except the maximum and minimum values are averaged and stored in the buffer memory. However, when the processing number of times is no more than two, the sampling process is executed. The applicable setting range is shown below: Count averaging: 1 to 20000 Time averaging: 4 to 10000ms	0	
A/D conversion enable/disable setting	Set the number of channels for which the A/D conversion is enabled to the buffer memory address 0. The channel can be specified for each channel. (Used channel specification)	Allows the A/D conversion enable/disable setting for each channel by writing "1" (enable) or "0" (disable) to the buffer memory address 0. By disabling the conversion for the channels that are not used, the sampling time cycle can be shortened. (Default: All channels are set to "enable".)	0	
Input range setting	_	Allows input range setting for each channel, and change of the I/O conversion characteristics.	_	
Offset/gain setting	The I/O conversion characteristics can be changed.	_	×	No offset/ gain setting function

## (c) Comparison of I/O signals for PLC CPU

	A68A	D-S2		A1S68AD				
Device NO.	Signal name	Device NO.	Signal name	Device NO.	Signal name	Device NO.	Signal name	
X0	WDT error (A68AD-S2 detection)	YO		X0	WDT error (A1S68AD detection)	Y0		
X1	A/D conversion READY	Y1		X1	A/D conversion READY	Y1		
X2		Y2		X2	Error	Y2		
X3		Y3		X3		Y3		
X4		Y4		X4		Y4		
X5		Y5		X5		Y5		
X6		Y6		X6		Y6		
X7		Y7		X7		Y7	Not used	
X8		Y8		X8		Y8	Not used	
X9		Y9		X9		Y9		
XA		YA		XA	XA XB XC XD XE XF X10	YA		
XB		YB		XB		YB		
XC		YC		XC		YC		
XD		YD				YD		
XE		YE	Not used			YE	-	
XF		YF	Not used			YF		
X10	Not used	Y10				Y10		
X11		Y11		X11	Not used	Y11		
X12		Y12		X12		Y12	Error reset	
X13		Y13		X13		Y13		
X14		Y14		X14		Y14		
X15		Y15		X15		Y15		
X16		Y16		X16		Y16		
X17		Y17		X17		Y17	-	
X18		Y18		X18		Y18	Netword	
X19 X1A	_	Y19 Y1A		X19 X1A		Y19 Y1A	Not used	
X1A X1B		Y1A Y1B		X1A X1B		Y1A Y1B		
X1B X1C		Y1C		X1B X1C		Y1C		
X1C X1D		Y1D		X1C X1D		Y1D	-	
X1D X1E		Y1E		X1D X1E		Y1E	-	
X1E X1F		Y1F		X1E X1F		Y1F	1	
ATE		TIF		ATE		TIF		

Modifying sequence programs is required as the assignment of buffer memory differs. For details of the buffer memory and sequence programs, refer to the user's manual.

Address	A68A1	D-S2		Address		A1S68AD			
Address	Name	CPU	Read/Write	Address	Name	Default value	Read/Write		
0	Used channel specification			0	A/D conversion enable/ disable specification	00FFн (All channels are set to "enable".)	R/W		
1	Average processing specification			1	Writing data error code	0 (All channels)	R		
2	CH1 Average time, count	Common to				2	Average processing specification	0 (Sampling processing specified for all channels)	R/W
3	CH2 Average time, count	ACPU and	R/W	3					
4	CH3 Average time, count	K2ACPU		4					
5	CH4 Average time, count			5					
6	CH5 Average time, count			6	Not used	_	-		
7	CH6 Average time, count			7					
8	CH7 Average time, count			8					
9	CH8 Average time, count			9					
10	CH1 Digital output value	-		10	CH1 Average time, count	0	-		
11	CH2 Digital output value			11	CH2 Average time, count	0	_		
12	CH3 Digital output value		13CH4 Average t14CH5 Average t15CH6 Average t		CH3 Average time, count	0			
13	CH4 Digital output value	ACPU			CH4 Average time, count	0	R/W		
14 15	CH5 Digital output value				CH5 Average time, count	0	_		
15	CH6 Digital output value CH7 Digital output value			CH8 Average time, count	0	-			
10	CH8 Digital output value			10	CH8 Average time, count	0	-		
18	CH1 Digital output value		-	18		0			
19	CH1 Positive or negative sign			19	Not used	_	_		
20	CH2 Digital output value				20	CH1 Digital output value	0		
21	CH2 Positive or negative sign			21	CH2 Digital output value	0			
22	CH3 Digital output value			22	CH3 Digital output value	0			
23	CH3 Positive or negative sign		R	23	CH4 Digital output value	0			
24	CH4 Digital output value			24	CH5 Digital output value	0	R		
25	CH4 Positive or negative sign	K2ACPU		25	CH6 Digital output value	0			
26	CH5 Digital output value	NZACI U		26	CH7 Digital output value	0	]		
27	CH5 Positive or negative sign			27	CH8 Digital output value	0			
28	CH6 Digital output value			28	A/D conversion completed	0	R/W		
29	CH6 Positive or negative sign			29	Not used	—	_		
30	CH7 Digital output value								
31	CH7 Positive or negative sign								
32	CH8 Digital output value								
33	CH8 Positive or negative sign								
34	Writing data error code	Common to	R/W						
35	A/D conversion completed	Common to ACPU and K2ACPU	R						

## (4) Comparisons between A68ADN and A1S68AD

## (a) Performance specifications comparison

(u) 1 01101	mance specifications comparison	$O$ : Compatible, $\triangle$ : Partial chang	e required,	, × : Incompatible
Item	A68ADN	A1S68AD	Compati- bility	Precautions for replacement
Analog input	Voltage: -10 to 0 to +10VDC (Input resistance: 1M Ω ) Current: -20 to 0 to +20mADC (Input resistance: 250 Ω )	Voltage: -10 to 0 to +10VDC (Input resistance: 1M Ω) Current: 0 to +20mA (Input resistance: 250 Ω)		Negative current cannot be converted.
Digital output	16-bit, signed binary When 1/4000 is set: -4096 to +4095 When 1/8000 is set: -8192 to +8191 When 1/12000 is set: -12287 to +12287	16-bit, signed binary	0	
I/O characteristics	Digital output value           Analog input         (For gain 5V/20mA, offset 0V/20mA)           114000         1/8000         1/12000           +10V         +4000         +8000         +12000           +5V	Analog input         Digital output           0 to +10V         0 to +4000           -10 to 10V         -2000 to +2000           0 to 5V or 0 to 20mA         0 to +4000           1 to 5V or 4 to 20mA         0 to +4000		Check the I/O conversion characteristics.
Maximum resolution	1/4000         1/8000         1/12000           Voltage input         2.5mV         1.25mV         0.83mV           Current input         10 μ A         5 μ A         3.33 μ A	Analog input         Digital output           0 to +10V         2.5mV           -10 to 10V         5mV           0 to 5V         1.25mV           1 to 5V         1mV           0 to 20mA         5 μ A           4 to 20mA         4 μ A	×	The resolution decreases.
Overall accuracy (Accuracy in respect to maximum digital output value)	1/4000         1/8000         1/12000           ±1%         ±40         ±80         ±120	Within $\pm$ 1% (Digital output value $\pm$ 40)	0	
Maximum conversion speed	20ms/channel	0.5ms/channel (The maximum conversion speed is 1ms/ channel on all channels if averaging processing is set even for only one channel.)	0	
Absolute maximum input	Voltage: ± 15V Current: ± 30mA	Voltage: ± 35V Current: ± 30mA	0	
Analog input points	8 channels/module	8 channels/module	0	
Isolation method	Isolation position         Isolation method         Dielectric withstand voltage         Insulation resistance           Between the input terminals and PLC power         Photocoupler         500 VAC for 1minute         500 VAC or by insulation resistance tester           Between channels         non-isolated         —         —	Between the input terminals and PLC power: photocoupler isolation Between channels : non-isolated	0	
Occupied I/O points	32 points (I/O assignment: special 32 points)	32 points (I/O assignment: special 32 points)	0	
Connected terminal	38-point terminal block	20-point terminal block	×	
Applicable wire size	0.75 to 2mm <sup>2</sup> (Applicable tightening torque 7kg · cm)	0.75 to 1.5mm <sup>2</sup>	Δ	External wiring must be
Applicable solderless terminal	V1.25-3,V1.25-YS3A, V2-S3, V2-YS3A	R1.25-3, 1.25-YS3, RAV1.25-3, V1.25-YS3A	Δ	changed.
Internal current consumption (5VDC)	0.4A	0.4A	0	

 $\bigcirc$  : Compatible,  $\bigtriangleup$  : Partial change required, x : Incompatible

Item	A68ADN	A1S68AD	Compati- bility	Precautions for replacement
External dimensions	250(H)×37.5(W)×131(D)mm	130(H)×34.5(W)×93.6(D)mm	Δ	The dimensions are different.
Weight	0.51kg	0.27kg	0	

## (b) Function comparison

ltem	A68ADN	A1S68AD	Compati-	Precautions for
A/D conversion enable/disable setting	Sets whether to enable or disable the A/D conversion for each channel. By disabling the conversion for the channels that are not used, the sampling time can be shortened. (Default: All channels are set to "enable".)	Allows the A/D conversion enable/disable setting for each channel by writing "1" (enable) or "0" (disable) to the buffer memory address 0. By disabling the conversion for the channels that are not used, the sampling time can be shortened. (Default: All channels are set to "enable".)	o bility	replacement
Averaging processing	Executes the A/D conversion for the channel specified for averaging process by a PLC CPU for the set number of times or set time. The total values except the maximum and minimum values are averaged and stored in the buffer memory. However, when the processing number of times is no more than two, the sampling process is executed. Data for averaging processing is initialized when the A/D conversion enable/disable is set. The applicable setting range is shown below: Count averaging : 1 to 500 Time averaging : 160 to 10000ms	Executes the A/D conversion for the channel specified for averaging process by a PLC CPU for the set number of times or set time. The total values except the maximum and minimum values are averaged and stored in the buffer memory. However, when the processing number of times is no more than two, the sampling process is executed. The applicable setting range is shown below: Count averaging: 1 to 20000 Time averaging: 4 to 10000ms	0	
Sampling processing	Converts analog input values into digital output values successively, and stores the digital output value in the buffer memory. The length of time to store the sampled digital output value in the buffer memory differs depending on the number of the A/D conversion enabled channels. (Processing time) = (Number of A/D conversion enabled channels) × 20 (ms/channel)	Converts an analog input value is converted to a digital value for each channel at any time, and stores it in the buffer memory as a digital output value. The length of time to store the sampled digital output value in the buffer memory differs depending on the number of the A/D conversion enabled channels. (Processing time) = (Number of A/D conversion enabled channels) $\times 0.5(ms)$ * 0.5(ms) is maximum conversion speed.	0	
Input range setting	_	Allows input range setting for each channel, and change of the I/O conversion characteristics.	_	
Offset/gain setting	The I/O conversion characteristics can be changed.	_	×	No offset/gain setting function

O: Compatible,  $\triangle:$  Partial change required, x: Incompatible, —: Additional function

### (c) Comparison of I/O signals for PLC CPU

	A68	ADN		A1S68AD					
Device NO.	Signal name	Device NO.	Signal name	Device NO.	Signal name	Device NO.	Signal name		
X0	WDT error (A68ADN detection)	Y0		X0	WDT error (A1S68AD detection)	Y0			
X1	A/D conversion READY	Y1		X1	A/D conversion READY	Y1			
X2	Error	Y2		X2	Error	Y2			
X3		Y3		X3		Y3			
X4		Y4		X4		Y4			
X5		Y5		X5		Y5			
X6		Y6		X6		Y6			
X7		Y7		X7		Y7			
X8		Y8	Not used	X8		Y8	Not used		
X9		Y9		X9		Y9			
XA		YA		XA		YA			
XB		YB		XB		YB			
XC		YC		XC		YC			
XD		YD		XD		YD			
XE		YE		XE		YE			
XF		YF		XF		YF			
X10		Y10					X10	Y10	
X11	Not used	Y11		X11	Not used	Y11			
X12		Y12	Error reset	X12		Y12	Error reset		
X13		Y13		X13		Y13			
X14		Y14		X14		Y14			
X15		Y15		X15		Y15			
X16		Y16		X16		Y16			
X17		Y17		X17		Y17			
X18		Y18		X18		Y18			
X19		Y19	Not used	X19		Y19	Not used		
X1A		Y1A		X1A		Y1A			
X1B		Y1B		X1B		Y1B			
X1C		Y1C		X1C		Y1C			
X1D		Y1D		X1D		Y1D			
X1E		Y1E		X1E		Y1E			
X1F		Y1F		X1F		Y1F			

Modifying sequence programs is required as the assignment of buffer memory differs. For details of the buffer memory and sequence programs, refer to the user's manual.

A	A68ADN		A	A1S68AD				
Address	Name	Default value	Address	Name	Default value	Read/Write		
0	A/D conversion enable/ disable specification	00FFн (All channels are set to "enable".)	0	A/D conversion enable/ disable specification	00FFн (All channels are set to "enable".)	R/W		
1	Average processing specification	0 (Sampling processing specified for all channels)	1	Writing data error code	0 (All channels)	R		
2	CH1 Average time, count		2	Average processing specification	0 (Sampling processing specified for all channels)	R/W		
3	CH2 Average time, count		3					
4	CH3 Average time, count		4					
5	CH4 Average time, count	0	5					
6	CH5 Average time, count		6	Not used	_	_		
7	CH6 Average time, count		7					
8	CH7 Average time, count		8					
9	CH8 Average time, count		9					
10	CH1 Digital output value		10	CH1 Average time, count	0			
11	CH2 Digital output value		11	CH2 Average time, count	0	1		
12	CH3 Digital output value	-	12	CH3 Average time, count	0			
13	CH4 Digital output value		13	CH4 Average time, count	0	5		
14	CH5 Digital output value	0	14	CH5 Average time, count	0	R/W		
15	CH6 Digital output value		15	CH6 Average time, count	0			
16	CH7 Digital output value		16	CH7 Average time, count	0			
17	CH8 Digital output value		17	CH8 Average time, count	0			
18	Writing data error code	0 (No error)	18					
19	A/D conversion completed	00FFн (A/D conversion completed on all channels)	19	Not used	_	_		
20	Resolution specification	1(1/4000)	20	CH1 Digital output value	0			
			21	CH2 Digital output value	0			
			22	CH3 Digital output value	0			
			23	CH4 Digital output value	0	R		
			24	CH5 Digital output value	0	К		
			25	CH6 Digital output value	0			
			26	CH7 Digital output value	0			
			27	CH8 Digital output value	0			
			28	A/D conversion completed	0	R/W		
			29	Not used	_	_		

# 10.2.2 Analog output modules comparison

## (1) Comparisons between A616DAI and A1S68DAI

#### (a) Performance specifications comparison

Ite	m	A61	6DAI		A1S68	BDAI	Compati- bility	Precautions for replacement			
Digital inpu	ut	(1) 16-bit, sign (Data unit: (2) Setting ran	12 bits)	<ul><li>(1) 16-bit, signed binary</li><li>(2) Setting range: 0 to 4096</li></ul>		0					
Analog out	tput		mADC stance: 0 to 600 Ω)		4 to 20mADC (External load resistance: 0 to 600 $\Omega$ )			Conversion is limited to 4 to 20mA.			
I/O charac	teristics	Digital input +4000 +2000 0	Analog output +20mA +12mA 4mA		Digital input value 4000 2000 0	Analog output value 20mA 12mA 4mA	Δ	Conversion is limited to 4 to 20mA.			
Digital valu	le	1/4	000		1/40	000	0				
Analog val maximum			ιA		4 μ	Α	0				
Overall acc (Accuracy i to maximu	in respect	$\begin{array}{c} \pm 0.6\% (\pm 120 \; \mu \; \text{A}) \\ (\text{When ambient temperature is } 25^\circ \text{C} : \pm 0.3\%) \\ (\pm 60 \; \mu \; \text{A}) \end{array}$			± 1. (± 200		×	Overall accuracy differs.			
Sampling cycle		1.5+0.5× (Number of D/A conversion enabled channels) (ms)			_						The D/A conversion time is fixed
Conversion	Conversion time 0.5ms (Time required for conversion from 0 to 20mA or 20mA to 0mA)				4 ms or less/8 channels For high frequency of access from the PLC CPU using FROM/TO instructions, this can be extended up to about 6ms.			regardless of the D/A conversion value output disable channel setting.			
Output sho	ort	_			Provi	ded	0				
No. of anal channels	og output	16 channe	els/module		8 channels	s/module	×	As the number of channels is reduced, the number of modules installed may increase.			
Isolation m	nethod		minals and PLC power: er isolation annels : non-isolated		Between the output term photocouple Between output char	er isolation	0				
Occupied I	I/O points	32 p (I/O assignment:	oints special 32 points)		32 pc (I/O assignment: s		0				
Connected	terminal		minal block		20-point terr (M3.5×7		×	External wiring must			
Applicable Applicable		0.75 to V1.25-3, V	1.25-YS3A,		0.75 to 1 R1.25-3, 1	.25-YS3,		be changed.			
				RAV1.25-3, V1.25-YS3A 0.85A						Recalculation of internal current consumption [5VDC] is required.	
External power supply	Voltage Current		/-15VDC -15VDC · · · 0.125V						Δ	External power supply is not required.	
External di	imensions	250(H)×37.5(\	V)×131(D)mm	t	130(H)×34.5(W	/)×93.6(D)mm	Δ	The dimensions are different.			
Weight		0.6	9kg		0.28	ßkg	0				

 $\bigcirc$  : Compatible,  $\bigtriangleup$  : Partial change required,  $\times$  : Incompatible

## (b) Function comparison

 $\bigcirc$ : Compatible,  $\triangle$ : Partial change required,  $\times$ : Incompatible, —: Additional function

Item	A616DAI	A1S68DAI	Compati- bility	Precautions for replacement
Analog output HOLD/CLEAR setting (For all channels)	Using the jumper, specify whether to hold (HOLD) or clear (CLEAR) the analog output before STOP when the ACPU is set to STOP by the RUN key switch, etc. When the ACPU stops operation on detection of an error, the analog output value is 0mA independently of the analog output HOLD/CLEAR setting.	Allows users to select whether to hold the last analog value output at each channel or clear (outputs 0 or 4mA), when the PLC CPU goes into the STOP status, or when digital-to-analog conversion is stopped by an A1S68DAI error: one setting is made for all channels using the HLD/CLR terminal on the front face of the module.	Δ	Check the analog output status combination list.
D/A conversion disable channel setting (For each channel)	<ul> <li>Using the sequence program, specify the channel for which D/A conversion is to be disabled in order to shorten the sampling cycle.</li> <li>(D/A conversion of all 16 channels is enabled when the ACPU is powered up or reset.)</li> <li>(a) D/A conversion enable channel: Performs D/A conversion of the digital value specified from the ACPU.</li> <li>(b) D/A conversion disable channel: Does not perform D/A conversion of the digital value specified from the ACPU.</li> </ul>	Allows users to specify whether to output a D/A conversion value of each channel or 4mA by setting the D/A conversion value output enable flag to ON/OFF in the sequence program. The D/A conversion time (conversion speed) is fixed regardless of setting the D/A conversion value output disable channel setting. (D/A conversion value output enable flag)	Δ	The D/A conversion time (conversion speed) is fixed regardless of the D/A conversion value output disable channel setting.
Analog output disable channel setting (For each channel)	Using the sequence program, specify the channel for which analog value output is to be disabled. (Analog output of all 16 channels is enabled when the ACPU is powered up or reset.) (a) Analog output enable channel: Outputs the offset value or an analog value converted. (b) Analog output disable channel: Outputs 0mA.	Allows users to specify whether to enable or disable the output of analog values to external devices by writing 0/1 to address 0 for each channel in the sequence program. (Analog output enable/disable setting)	0	
Offset/gain setting	The I/O conversion characteristics can be changed.	_	×	No offset/gain setting function. Fixed to 4 to 20mA output. Adjust output with the digital input.

### (c) Comparison of I/O signals for PLC CPU

	A61	6DAI		A1S68DAI				
Device NO.	Signal name	Device NO.	Signal name	Device NO.	Signal name	Device NO.	Signal name	
X0	WDT error	Y0		X0	WDT error (A1S68DAI detection)	Y0		
X1	D/A conversion READY	Y1		X1	D/A conversion READY	Y1		
X2	Error	Y2		X2	Error	Y2		
Х3		Y3		X3		Y3		
X4		Y4		X4		Y4		
X5		Y5		X5		Y5		
X6		Y6		X6		Y6		
X7		Y7		X7		Y7	Not used	
X8		Y8		X8		Y8		
X9		Y9		X9		Y9		
XA		YA		XA		YA		
XB		YB		XB		YB		
XC		YC		XC		YC		
XD		YD	Not used	XD		YD		
XE		YE		XE		YE		
XF		YF		XF		YF		
X10		Y10		X10		Y10		
X11	Not used	Y11		X11	Not used	Y11		
X12		Y12		X12		Y12		
X13		Y13		X13		Y13	D/A conversion value	
X14		Y14		X14		Y14	output enable	
X15	-	Y15		X15		Y15		
X16		Y16		X16		Y16		
X17	ļ	Y17		X17		Y17		
X18		Y18		X18		Y18	Error reset	
X19		Y19		X19		Y19		
X1A		Y1A		X1A		Y1A		
X1B		Y1B	Output batch enable	X1B		Y1B		
X1C		Y1C		X1C		Y1C	Not used	
X1D		Y1D	Not used	X1D		Y1D		
X1E		Y1E		X1E		Y1E		
X1F		Y1F		X1F		Y1F		

Modifying sequence programs is required as the assignment of buffer memory differs. For details of the buffer memory and sequence programs, refer to the user's manual.

	A616DAI			A1S68DA	AI
Address	Name	Read/Write	Address	Name	Default value
0	D/A conversion enable/disable channel	R/W	0	Analog output enable/disable channel	0000н(All channels are set to "enable".)
1	Analog output enable/disable channel		1	CH1 Digital value	
2			2	CH2 Digital value	
3			3	CH3 Digital value	
4			4	CH4 Digital value	
5			5	CH5 Digital value	0
6			6	CH6 Digital value	
7			7	CH7 Digital value	
8			8	CH8 Digital value	
9	Not used	_	9		_
10			10	CH1 Set value check code	
11			11	CH2 Set value check code	1
12			12	CH3 Set value check code	1
13			13	CH4 Set value check code	
14			14	CH5 Set value check code	0
15			15	CH6 Set value check code	
16	CH0 Digital value		16	CH7 Set value check code	
17	CH1 Digital value	-	17	CH8 Set value check code	-
18	CH2 Digital value	-			
19	CH3 Digital value	-			
20	CH4 Digital value	-			
21	CH5 Digital value	-			
22	CH6 Digital value	-			
23	CH7 Digital value	-			
24	CH8 Digital value	R/W			
25	CH9 Digital value	-			
26	CHA Digital value	-			
27	CHB Digital value	-			
28	CHC Digital value	-			
29	CHD Digital value	-			
30	CHE Digital value	-			
31	CHF Digital value	-			
32					
to	Not used	_			
47					
48	CH0 Set value check code		1		
49	CH1 Set value check code	1			
50	CH2 Set value check code	1			
51	CH3 Set value check code	1			
52	CH4 Set value check code	1			
53	CH5 Set value check code	1			
54	CH6 Set value check code	1			
55	CH7 Set value check code	1			
56	CH8 Set value check code	R/W			
57	CH9 Set value check code	1			
58	CHA Set value check code	1			
59	CHB Set value check code	1			
60	CHC Set value check code	1			
61	CHD Set value check code	1			
62	CHE Set value check code	1			
63	CHF Set value check code	1			
00			1		

# (2) Comparisons between A616DAV and A1S68DAV

# (a) Performance specifications comparison

								O. Comp	ballible, $\Delta$ . Partial Ci	· · ·	ired, x: Incompatible
lte	em			A61	6DAV			A1S6	68DAV	Compati- bility	Precautions for replacement
Digital inpu	t			a unit :1	2 bits)	to 4095		(1) 16-bit signed (2) Setting rang	d binary e:- 2048 to 2047		Setting range differs.
Analog out	put		(External loa	ad resist range s	ance: 2k 9 etting is 5	$V \cdots$ -10V to 0V to +10 $\Omega$ to 1M $\Omega$ ) $V \cdots$ -5V to 0V to +5V $\Omega$ to 1M $\Omega$ )		(External load re	to 10 VDC esistance: 2k Ω to 1 Ω)	Δ	No $\pm$ 5V range setting
I/O charact	eristics		Digital input +4000 +2000 0 -2000 -4000	+) +) -2	Analog output           tetting         10V setting           5V         +10V           2:5V         +5V           3V         0V           5V         -5V           0V         -10V		Digital input 2000 1000 0 -1000 -2000	Analog output 10V 5V 0V -5V -10V	Δ	Digital input value differs.	
Digital value resolution	e		1/4000					1/2	2000	×	Resolution is different
Analog valu maximum r			Output vo Output vo			V: 2.5mV, /: 1.25mV		5	mV	×	Resolution is different
Overall acc	uracy		tput voltage set 10V tput voltage set 5V	$\begin{array}{c} \pm 0.6\% (\pm 60 \text{mV}) \text{ (Ambient} \\ \text{temperature 0 to 55°C)} \\ 10\text{V} \qquad \qquad \pm 0.3\% (\pm 30 \text{mV}) \text{ (Ambient} \\ \text{temperature 25°C)} \\ \text{temperature 25°C)} \\ \text{temperature 0 to 55°C)} \end{array}$			nt nt	± 1.0% (± 100 μ A)		×	Overall accuracy differs.
Sampling c	ycle	1.5+0.5× (Number of D/A conversion enabled channels) (ms)				;)				The D/A conversior time is fixed	
Conversion	i time	(Time	required for co	nversio	5ms on from - 10V)	10V to +10V or +10	<i>,</i>	4 ms or less/8 channels For high frequency of access from the PLC CPU using FROM/TO instructions, this can be extended up to about 6ms.		Δ	regardless of the D/A conversion value output disable channel setting.
Absolute m output	aximum			1	5V				_	0	
No. of analochannels	og output		16	chann	els/modu	ıle		8 channe	els/module	×	As the number of channels is reduced the number of modules installed may increase.
Output sho protection	rt			-	_			Pro	vided	0	
Isolation m	Between the output terminals and PLC power supply:				PLC pow photocoup Between outpu	put terminals and /er supply: ler isolation t channels : non- lated	0				
Occupied I/	O points				oints	32 points)			ooints special 32 points)	0	
Connected	terminal				rminal bl	. ,	╡	20-point te	rminal block	×	
Applicable wire size			0.75 to	o 2mm <sup>2</sup>		$\uparrow$	(M3.5×7 screws) 0.75 to 1.5mm <sup>2</sup>		Δ	External wiring must be changed.	
Applicable : terminal	Applicable solderless V1.25-3, V1.25-YS3A, erminal V2-S3,V2-YS3A				1.25-YS3, ,V1.25-YS3A	Δ	must be changed.				
	ernal current nsumption 0.38A				85A		Recalculation of internal current consumption [5VDC] is required				
External power	Voltage Current		++15VDC		C/-15VDC		_			Δ	External power supply is not required.

O: Compatible,  ${\underline{\wedge}}:\,$  Partial change required,  ${\color{black}{\times}}:$  Incompatible

Item	A616DAV	A1S68DAV	Compati- bility	Precautions for replacement
External dimensions	250(H)×37.5(W)×131(D)mm	130(H)×34.5(W)×93.6(D)mm		The dimensions are different.
Weight	0.65kg	0.28kg	0	

## (b) Function comparison

O: Compatible,  $\triangle:$  Partial change required,  $\times:$  Incompatible, —: Additional function

ltem	A616DAV	A1S68DAV	Compati- bility	Precautions for replacement
Analog output HOLD/CLEAR setting (For all channels)	Using the jumper, specify whether to HOLD or CLEAR the analog output before STOP when the ACPU is set to STOP by the RUN key switch, etc. When the ACPU stops operation on detection of an error, the analog output value is 0V independently of the analog output HOLD/CLEAR setting.	Allows users to select whether to hold the last analog value output at each channel or clear (outputs 0 or 4mA), when the PLC CPU goes into the STOP status, or when digital-to-analog conversion is stopped by an A1S68DAV error: one setting is made for all channels using the HLD/CLR terminal on the front face of the module.	Δ	Check the analog output status combination list.
D/A conversion disable channel setting (For each channel)	<ul> <li>Using the sequence program, specify the channel for which D/A conversion is to be disabled in order to shorten the sampling cycle.</li> <li>(D/A conversion of all 16 channels is enabled when the ACPU is powered up or reset.)</li> <li>(a) D/A conversion enable channel: Performs D/A conversion of the digital value specified from the ACPU.</li> <li>(b) D/A conversion disable channel: Does not perform D/A conversion of the digital value specified from the ACPU.</li> </ul>	Allows users to specify whether to output a D/A conversion value of each channel or 0V by setting the D/A conversion value output enable flag to ON/OFF in the sequence program. The D/A conversion time (conversion speed) is fixed regardless of the D/A conversion value output disable channel setting. (D/A conversion value output enable flag)	Δ	The D/A conversion time (conversion speed) is fixed regardless of the D/A conversion value output disable channel setting.
Analog output disable channel setting (For each channel)	Using the sequence program, specify the channel for which analog value output is to be disabled. (Analog output of all 16 channels is enabled when the ACPU is powered up or reset.) (a) Analog output enable channel: Outputs the offset value or an analog value converted. (b) Analog output disable channel: Outputs 0V.	Allows users to specify whether to enable or disable the output of analog values to external devices by writing 0/1 to address 0 for each channel in the sequence program. (Analog output enable/disable setting)	0	
Offset/gain setting	The I/O conversion characteristics can be changed.	_	×	No offset/gain setting function. Fixed to -10 to 10V output. Adjust output with the digital input.

#### (c) Comparison of I/O signals for PLC CPU

	A610	BDAV		A1S68DAV					
Device NO.	Signal name	Device NO.	Signal name	Device NO.	Signal name	Device NO.	Signal name		
X0	WDT error	Y0		X0	WDT error (A1S68DAV detection)	YO			
X1	D/A conversion READY	Y1		X1	D/A conversion READY	Y1			
X2	Error	Y2		X2	Error	Y2			
X3		Y3		X3		Y3			
X4		Y4		X4		Y4			
X5		Y5		X5		Y5			
X6		Y6		X6		Y6			
X7		Y7		X7		Y7	Not used		
X8		Y8		X8		Y8			
X9		Y9		X9		Y9			
XA		YA		XA		YA			
XB		YB	Not used	XB	-	YB			
XC		YC		XC		YC			
XD		YD		XD		YD			
XE		YE		XE		YE			
XF		YF		XF		YF			
X10		Y10		X10		Y10			
X11	Not used	Y11		X11	Not used	Y11			
X12		Y12		X12		Y12			
X13		Y13		X13		Y13	D/A conversion value		
X14		Y14		X14		Y14	output enable		
X15		Y15		X15		Y15			
X16		Y16		X16		Y16			
X17		Y17		X17		Y17			
X18		Y18		X18		Y18	Error reset		
X19		Y19		X19		Y19			
X1A		Y1A		X1A		Y1A			
X1B	Y1B		Output batch enable	X1B		Y1B	Not and		
X1C		Y1C		X1C		Y1C	Not used		
X1D		Y1D	Not used	X1D		Y1D			
X1E		Y1E		X1E		Y1E			
X1F		Y1F		X1F		Y1F			

Modifying sequence programs is required as the assignment of buffer memory differs. For details of the buffer memory and sequence programs, refer to the user's manual.

	A616DAV			A1S68DA	V
Address	Name	Read/Write	Address	Name	Default value
0	D/A conversion enable/disable channel	R/W	0	Analog output enable/disable channel	0000н(All channels are set to "enable".)
1	Analog output enable/disable channel	t	1	CH1 Digital value	0
2			2	CH2 Digital value	0
3			3	CH3 Digital value	0
4			4	CH4 Digital value	0
5			5	CH5 Digital value	0
6			6	CH6 Digital value	0
7			7	CH7 Digital value	0
8			8	CH8 Digital value	0
9	Not used	—	9	Not used	
10			10	CH1 Set value check code	0
11			11	CH2 Set value check code	0
12	-		12	CH3 Set value check code	0
13			13	CH4 Set value check code	0
14			14	CH5 Set value check code	0
15			15	CH6 Set value check code	0
16	CH0 Digital value		16	CH7 Set value check code	0
17	CH1 Digital value	+	17	CH8 Set value check code	0
18	CH2 Digital value	ł			
19	CH3 Digital value	+			
20	CH4 Digital value	+			
21	CH5 Digital value	+			
22	CH6 Digital value	ł			
23	CH7 Digital value	ł			
24	CH8 Digital value	R/W			
25	CH9 Digital value	+			
26	CHA Digital value	ł			
20	CHB Digital value	+			
28	CHC Digital value	ł			
20	CHD Digital value	ł			
30	CHE Digital value	ł			
31	CHF Digital value	ł			
32 to	Not used				
47	Not used	_			
48	CH0 Set value check code		1		
40	CH1 Set value check code	ł			
-49 50	CH2 Set value check code	ł			
50	CH3 Set value check code	ł			
52	CH4 Set value check code	ł			
53	CH5 Set value check code	ł			
54	CH6 Set value check code	ł			
55	CH7 Set value check code	ł			
56	CH8 Set value check code	R/W			
57	CH9 Set value check code	ł			
58	CHA Set value check code	ł			
59	CHB Set value check code	ł			
60	CHC Set value check code	ł			
60	CHD Set value check code	ł			
62	CHE Set value check code	ł			
63		ł			
03	CHF Set value check code		J		

# (3) Comparisons between A62DA and A1S62DA

# (a) Performance specifications comparison

			O: Compatible, ∆: Partial char	Compati	
lte	em	A62DA	A1S62DA	bility	replacement
Digital inp	out	Maximum setting value Voltage: ± 2000 Current: ± 1000	Voltage: -4000 to 4000         Current: 0 to 4000           Voltage: -8000 to 8000         Current: 0 to 8000           Voltage: -12000 to 12000         Current: 0 to 12000	Δ	Check the I/O conversion characteristics to set the digital value.
Analog ot	utput	Voltage : -10 to 0 to +10VDC (external load resistance 500 $\Omega$ to 1M $\Omega$ ) Current : +4 to +20mADC (external load resistance 0 $\Omega$ to 600 $\Omega$ ) Current outputs are usable by -20 to 0 to 20mA.	Voltage:-10 to 0 to +10VDC (external load resistance $2k \Omega$ to1M $\Omega$ ) Current: 0 to 20mADC (external load resistance 0 to 600 $\Omega$ )	Δ	External load resistance must be checked at voltage output. Negative current cannot be output.
I/O chara	cteristics	Digital input         Analog output Voltage         Current           +2000         +10V            +1000         +5V         +20mA           0         0V         +4mA           -1000         -5V         -12mA           -2000         -10V	Resolution         Voltage output value <sup>2</sup> Current output value <sup>2</sup> I/4000         1/8000         1/12000         output value <sup>2</sup> Digital input value         4000         8000         12000         10V         20mA           2000         4000         6000         5V         12mA           0         0         0         0         4mA           -2000         -4000         -6000         -5V           -4000         -8000         -12000         -10V           *1. Offset for voltage output:         0V, Gain: 10V (Factory-set)         *2. Offset for current output: 4mA, Gain: 20mA	Δ	Check the I/O conversion characteristics.
Maximum resolution		Voltage: 5mV (1/2000) Current: 20 μ Α (1/1000)	1/4000 2.5mV(10V) 5 μ A(20mA) 1/8000 1.25mV(10V) 2.5 μ A(20mA) 1/12000 0.83mV(10V) 1.7 μ A(20mA)	0	
Overall ac (Accuracy respect to maximum output va	y in o n analog	$\pm$ 1% (Voltage: $\pm$ 0.1V, current: $\pm$ 0.2mA)	$\pm$ 1% (Voltage: $\pm$ 100mV, current: $\pm$ 200 $\mu$ A)	0	
Maximum conversio		Within 15ms/2 channels (same for 1 channel) Note) Time period from digital input write until specified analog voltage(current) reached.	Within 25ms/2 channels (same for 1 channel)	×	Conversion speed is reduced.
Absolute maximum	n output	Voltage: ± 12V Current: ± 28mA Note) The voltage or current exceeding the above is not output by output protection circuit.	Voltage: ± 12V Current: +28mA	0	
Output sh protectior		Provided	Provided	0	
Number o output po	•	2 channels/module	2 channels/module	0	
Isolation I	method	Between the output terminals and PLC power supply: photocoupler isolation Between channels : non-isolated	Between the output terminals and PLC power supply: photocoupler isolation Between channels : non-isolated	0	
Occupied points		32 points (I/O assignment: special 32 points)	32 points (I/O assignment: special 32 points)	0	
Connecte terminal	ed	20-point terminal block	20-point terminal block	Δ	External wiring
Applicable size		0.75 to 2mm <sup>2</sup> (Applicable tightening torque:39 to 59N.cm)	0.75 to 1.5mm <sup>2</sup>	Δ	must be changed.
Applicable solderless	e s terminal	V1.25-3, V1.25-YS3A, V2-S3, V2-YS3A	1.25-3, 1.25-YS3A, V1.25-3, V1.25-YS3A		-
	nternal current consumption 0.6A 5VDC)		0.8A	Δ	Recalculation of internal current consumption [5VDC] is required
External	Voltage	21.6 to 26.4VDC		İ	External power
power supply	Current	0.35A	Δ	supply is not required.	
Inrush cu	rrent	2.4A			•
External dimensior	ns	250(H)×37.5(W)×121(D)mm	130(H)×34.5(W)×93.6(D)mm		The dimensions are different.

 $\bigcirc$  : Compatible,  $\bigtriangleup$  : Partial change required,  $\times$  : Incompatible

Item	A62DA	A1S62DA	Compati- bility	Precautions for replacement
Weight	0.5kg	0.32kg	0	

## (b) Function comparison

 $\bigcirc$  : Compatible,  $\bigtriangleup$  : Partial change required,  $\times$  : Incompatible, —: Additional function

Item	A62DA	A1S62DA	Compati- bility	Precautions for replacement
Output HOLD/CLEAR setting		Allows users to select whether to hold the last analog value output at each channel or clear (offset value or 0V/0mA output), when the PLC CPU is in STOP, or when the D/A conversion is stopped by this module because of an error. One setting can be set for all channels with the HOLD/CLEAR terminal.	_	_
D/A conversion execute/ non-execute setting function	_	Allows users to specify whether to output a D/A conversion value for each channel or an offset value by setting the D/A conversion value output enable flag to ON/OFF in the sequence program. The D/A conversion time (conversion speed) is fixed regardless of the D/A conversion value output disable channel setting.	_	_
Analog value external output enable/disable setting function		Allows users to specify whether to enable or disable the output of analog values to external devices by writing 0/1 to address 0 for each channel in the sequence program.	_	_

## (c) Comparison of I/O signals for PLC CPU

	A62	2DA		A1S62DA				
Device NO.	Signal name	Device NO.	Signal name	Device NO.	Signal name	Device NO.	Signal name	
X0	WDT error (A62DA detection)	Y0		X0	WDT error (A1S62DA detection)	Y0		
X1	D/A conversion READY	Y1		X1	D/A conversion READY	Y1		
X2		Y2		X2	Error	Y2		
X3		Y3		X3		Y3		
X4		Y4		X4		Y4		
X5		Y5		X5		Y5		
X6		Y6		X6		Y6		
X7		Y7		X7		Y7	Not used	
X8		Y8		X8		Y8		
X9		Y9		X9		Y9		
XA		YA		XA		YA		
XB		YB		XB		YB		
XC		YC	Not used	XC		YC		
XD		YD		XD		YD		
XE		YE		XE		YE		
XF		YF		XF		YF		
X10	Not used	Y10		X10	Not used	Y10	CH1 D/A conversion output enable	
X11	Not used	Y11		X11		Y11	CH2 D/A conversion output enable	
X12		Y12		X12		Y12		
X13		Y13		X13		Y13		
X14		Y14		X14		Y14	Not used	
X15		Y15		X15		Y15	Not used	
X16		Y16		X16		Y16		
X17		Y17		X17		Y17		
X18		Y18	CPU select signal	X18		Y18	Error reset	
X19		Y19	Sign of CH1 digital input	X19		Y19		
X1A		Y1A	Sign of CH2 digital input	X1A		Y1A		
X1B	Y	Y1B	Output enable	X1B		Y1B		
X1C		Y1C		X1C		Y1C	Not used	
X1D		Y1D	Netwood	X1D		Y1D		
X1E		Y1E	Not used	X1E		Y1E		
X1F		Y1F		X1F		Y1F		

Modifying sequence programs is required as the assignment of buffer memory differs. For details of the buffer memory and sequence programs, refer to the user's manual.

Address	A62DA		Address	A1S62DA				
Address	Name	Read/Write	Address	Name	Default value	Read/Write		
0	CH1 Digital value		0	Analog output enable/disable channel	0			
1	CH2 Digital value		1	CH1 Digital value	0	R/W		
2	CH1 Voltage set value check code	R/W	2	CH2 Digital value	0			
3	CH2 Voltage set value check code		3					
4	CH1 Current set value check code		4					
5	CH2 Current set value check code		5	Not used		_		
			6	Not used	_			
			7					
			8					
			9	Resolution of digital value	1(×1)			
			10	CH1 Set value check code	0	R/W		
			11	CH2 Set value check code	0			
			12					
			13					
			14	Not used —				
			15			-		
			16					
			17					

# (4) Comparisons between A62DA-S1 and A1S62DA

# (a) Performance specifications comparison

	( )	chonnance	•						0:	Compati	ble, 🛆 :	Partial of	hange re	quired,	x : Incompatible
	ltem		А	62DA-S	1					A1S	62DA			Compati- bility	Precautions for replacement
Digita	al input		0	to +400	0			Voltage	e: -4000 1 e: -8000 1 e: -12000		С	urrent: 0 urrent: 0 urrent: 0	to 8000	Δ	Check the I/O conversion characteristics to set the digital value.
Analo	Analog output     (external load resistance: 500 Ω to 1M Ω )     (external load resistance)       Current : +4 to +20mADC     Current : +4 to +20mADC     Current : +4 to +20mADC			external Current :	10 to 0 to load resis 0 to +20r load resis	stance 2k nADC	Ω to 1M	,	Δ	External load resistance must be checked at voltage output.					
l/O chara	acteristics	Output range 0 to 10V 0 to 5V 0 to 20m <sup>2</sup> 1 to 5V 4 to 20m <sup>2</sup>	+44 () +44 () +44	out 000 0 000 000	Analog out +10V 0V +5V or +20r 0V or 0m/ +5V or +20r +1V or +4rr	nA A nA				Resolution           1/8000           8000           4000           -4000           -8000           -support           OV, Country			Current output value*2 20mA 12mA 4mA	Δ	Check the I/O conversion characteristics.
solution	Voltage	1 to 5V :1mV (1/4000) 0 to 5V :1.25mV (1/4000) 0 to 10V :2.5mV (1/4000)							1/4000 2.5mV (10V) 1/8000 1.25mV (10V) 1/12000 0.83mV (10V)				0		
Maximum resolution	Current	4 to 20mA: 4 µ A (1/4000) 0 to 20mA: 5 µ A (1/4000)						1/4000 5 μ A (20mA) 1/8000 2.5 μ A (20mA) 1/12000 1.7 μ A (20mA)				0			
respe	racy uracy in ect to mum	Output range           25 °C           (±Within 0.5%)           0 to 55 °C           (±Within 1%)	1 to 5V ±25mV ±50mV	0 to 5V ±25mV ±50mV	0 to 10V ±50mV ±100mV	4 to 20mA ±0.1mA ±0.2mA	0 to 20mA ±0.1mA ±0.2mA	C	/oltage:	± ± 100m\	1% /, current	$\pm 200\mu$	(A)	Δ	Fixed regardless of the output range.
	mum ersion ed	Note)Time perioo analog voltag	d from dig	for 1ch gital inpu	annel) ut write un	til specif	ied	(		/ithin 25m or 1chanr			e)	×	Conversion speed is reduced.
Abso maxi outpu	mum	Note)The voltage output due to	Current e or curre		28mA eding the a	above is	not			•	e: ± 12V t: +28mA			0	
	out short ection		F	Provided	1					Pro	vided			0	
	ber of og output is	of							2 channels/module				0		
lsola meth		Between the o	photoc	oupler is			oply:	Betv	р	output te su photocoup en chann	pply: bler isolat	ion	oower	0	
Occu points	pied I/O s	(I/O a		32 points ent: spec	s cial 32 poir	nts)			(I/O ass	32 ן signment:	ooints special 3	32 points	)	0	

	Item	A62DA-S1	A1S62DA	Compati- bility	Precautions for replacement
Conr term	nected inal	20-point terminal block	20-point terminal block	Δ	
	Applicable         0.75 to 2mm <sup>2</sup> wire size         (Applicable tightening torque 39 to 59N • cm)		0.75 to 1.5mm <sup>2</sup>	Δ	External wiring must be
Applicable v1.25-3, V1.25-YS3A, v2-S3, V2-YS3A			1.25-3, 1.25-YS3A, V1.25-3, V1.25-YS3A	changed.	
Inter curre cons (5VD	ent sumption	0.6A	0.8A	Δ	Recalculation of internal current consumption [5VDC] is required.
l upply	Voltage	21.6 to 26.4VDC		Δ	External power supply is not required.
External power supply	Current	0.35A	_		
Inrus	sh current	2.4A	_		
Exte dime	rnal ensions	250(H)×37.5(W)×121(D)mm	130(H)×34.5(W)×93.6(D)mm	Δ	The dimensions are different.
Weig	Weight 0.5kg		0.32kg	0	

 $\bigcirc$  : Compatible,  $\ \bigtriangleup$  : Partial change required,  $\ \varkappa$  : Incompatible

## (b) Function comparison

 $O: \text{Compatible}, \ \ \underline{\wedge}: \text{Partial change required}, \ \ \underline{\times}: \text{Incompatible}, \ \underline{-}: \text{Additional function}$ 

Item	A62DA-S1	A1S62DA	Compati- bility	Precautions for replacement
Output HOLD/ CLEAR function	<ul> <li>The HOLD/CLEAR of analog output can be set by the setting pin.</li> <li>1) HOLD side: Holds the value before the output enable signal is OFF.</li> <li>2) CLEAR side: Outputs the offset value.</li> </ul>	When the PLC CPU goes into STOP, or when D/A conversion of this module is stopped by an error, whether to hold the last analog value output from each channel or to clear it (offset value or 0V/0mA output) can be selected. This is selected for all channels with the HOLD/CLEAR terminal.	Δ	Check the analog output status combination list.
D/A conversion enable/disable function	_	Allows users to specify whether to output a D/A conversion value for each channel or an offset value by setting the D/A conversion value output enable flag to ON/OFF in the sequence program. The D/A conversion time (conversion speed) is fixed regardless of the D/A conversion value output disable channel setting.	_	_
Analog output enable/disable function	_	Allows users to specify whether to enable or disable the output of analog values to external devices by writing 0/1 to address 0 for each channel in the sequence program.	_	_

## (c) Comparison of I/O signals for PLC CPU

	A62D		A1S62DA						
Device NO.	Signal name	Device NO.	Signal name	Device NO.	Signal name	Device NO.	Signal name		
X0	WDT error (A62DA-S1 detection)	Y0		X0	WDT error (A1S62DA detection)	Y0			
X1	D/A conversion READY	Y1		X1	D/A conversion READY	Y1			
X2		Y2		X2	Error	Y2			
X3		Y3		X3		Y3			
X4		Y4		X4		Y4			
X5		Y5		X5		Y5			
X6		Y6		X6		Y6			
X7		Y7		X7		Y7	Not used		
X8		Y8		X8		Y8			
X9		Y9		X9		Y9			
XA		YA		XA		YA			
ХВ		YB		XB		YB			
XC		YC		XC		YC			
XD		YD	Not used	XD	YD				
XE		YE		XE		YE			
XF		YF		XF		YF			
X10	Not used	Y10		X10		Y10	CH1 D/A conversion output enable		
X11	Not used	Y11		X11	Not used	Y11	CH2 D/A conversion output enable		
X12		Y12		X12		Y12			
X13		Y13		X13		Y13			
X14		Y14		X14		Y14	Not used		
X15		Y15		X15		Y15			
X16		Y16		X16		Y16			
X17		Y17		X17		Y17			
X18		Y18		X18		Y18	Error reset		
X19		Y19		X19		Y19			
X1A		Y1A		X1A		Y1A			
X1B		Y1B	Output enable	X1B		Y1B			
X1C		Y1C		X1C		Y1C	Not used		
X1D		Y1D	Not used	X1D		Y1D			
X1E		Y1E		X1E		Y1E			
X1F		Y1F		X1F		Y1F			

Modifying sequence program is required as the assignment of buffer memory differs. For details of the buffer memory and sequence programs, refer to the user's manual.

Address	A62DA-S1		Address	A1S62DA	A		
Address	Name	Read/Write	Address	Name	Default value	Read/Write	
0	CH1 Digital value		0	Analog output enable/disable channel	0		
1	CH2 Digital value		1	CH1 Digital value	0	R/W	
2	CH1 Upper limit check code	R/W	2	CH2 Digital value	0		
3	CH1 Lower limit check code	R/W	3	-			
4	CH2 Upper limit check code		4				
5	CH2 Lower limit check code		5	Not used	_	_	
			6	Not used			
			7				
			8				
			9	Resolution of digital value	1 (×1)		
			10	CH1 Set value check code	0	R/W	
			11	CH2 Set value check code	0		
			12				
			13				
				Netwood			
			15	Not used			
			16				
			17				

#### (5) Comparisons between A68DAI-S1 and A1S68DAI

( )	normance specifications compa		Partial cha	nge required, ×: Incompatible
Item	A68DAI-S1	A1S68DAI	Compati- bility	Precautions for replacement
	<ul><li>(1) 16-bit, signed binary value</li><li>(2) Setting range:</li></ul>	(1) 16-bit, signed binary value (2) Setting range: 0 to 4096	Unity	
Digital input	Set resolution         Setting range           1/4000         0 to 4000           1/8000         0 to 8000           1/12000         0 to 12000		Δ	Only 0 to 4096 for setting range
Analog output	0 to 20mADC (external load resistance: 0 to 600 $\Omega$ )	4 to 20mADC (external load resistance: 0 to 600 $\Omega$ )	Δ	Output less than 4mA is not allowed.
I/O characteristics	Digital value resolution         *Analog output value           1/4000         1/8000         1/12000         *Analog output value           ag         4000         8000         12000         +20mA           2000         4000         6000         +12mA           ag         0         0         0         +4mV	Digital input valueAnalog output value400020mA200012mA04mA	Δ	I/O conversion characteristics are fixed as in the left.
Maximum resolution of analog value	5.0 μ A (1/4000) 2.5 μ A (1/8000) 1.6 μ A (1/12000)	4 µ A	0	
Overall accuracy (Accuracy in respect to maximum value)	$\pm$ 1.0% ( $\pm$ 200 $\mu$ A)	$\pm$ 1.0% ( $\pm$ 200 $\mu$ A)	0	
Conversion speed	Within 40ms/8 channel (Same for 1 channel) Note) Time period from digital input write until specified analog voltage (current) reached.	Within 4 ms/8 channels For high frequency of access from the PLC CPU using FROM/TO instructions, this can be extended up to about 6ms.	0	
Absolute maximum output	0 to +28mA Note) The current exceeding the above is not output due to output protection circuit.	_	0	
Number of analog output points	8 channels/module	8 channels/module	0	
Output short protection	_	Provided	0	
Crossover wiring for common	Available	N/A	×	
Isolation method	Between the output terminals and PLC power supply: photocoupler isolation Between channels : non-isolated	Between the output terminals and PLC power supply: photocoupler isolation Between output channels : non-isolated	0	
Number of occupied I/O points	32 points (I/O assignment: special 32 points)	32 points (I/O assignment: special 32 points)	0	
Connected terminal	38-point terminal block	20-point terminal block (M3.5×7 screws)	×	
Applicable wire size	0.75 to 2mm <sup>2</sup> (applicable tightening torque 39 to 59N· cm)	0.75 to 1.5mm <sup>2</sup>	Δ	External wiring must be changed.
Applicable solderless terminal	V1.25-3, V1.25-YS3A, V2-S3, V2-YS3A	R1.25-3, 1.25-YS3A, RAV1.25-3, V1.25-YS3A	Δ	energeo.
Internal power consumption (5VDC)	0.15A	0.85A	Δ	Recalculation of internal current consumption [5VDC] is required.

 $\bigcirc$  : Compatible,  $\ \bigtriangleup$  : Partial change required,  $\ \varkappa$  : Incompatible

Item		A68DAI-S1	A1S68DAI	Compati- bility	Precautions for replacement
External power supply	Voltage	21.6 to 26.4VDC			External power supply is not
	Current	0.4A	_		required.
External dimensions		250(H)×37.5(W)×131(D)mm	130(H)×34.5(W)×93.6(D)mm	Δ	The dimensions are different.
Weight		0.65kg	0.28kg	0	

#### (b) Function comparison

 $O: \text{Compatible}, \ \ \bigtriangleup: \text{Partial change required}, \ \ \times: \text{Incompatible}, \ \_: \text{Additional function}$ 

Item	A68DAI-S1	A1S68DAI	Compati- bility	Precautions for replacement	
HOLD/ CLEAR setting	Allows users to select whether to hold the last analog value output from each channel or to clear it (offset value or 0mA output), when the PLC CPU goes into the STOP status, or when digital-to-analog conversion is stopped by an A68DAI-S1 error: one setting is made for all channels using the HLD/CLR terminal on the front face of the module.	Allows users to select whether to hold the last analog value output from each channel or to clear it (0 or 4mA output), when the PLC CPU goes into the STOP status, or when digital-to- analog conversion is stopped by an A1S68DAI error: one setting is made for all channels using the HLD/CLR terminal on the front face of the module.	۵	Since A1S68DAI does not have the offset adjustment, the offset value is not output, and 0 $\mu$ A is output when cleared.	
D/A conversion value output enable flag	Allows users to specify whether to output a D/A conversion value for each channel or an offset value by setting the D/A conversion value output enable flag to ON/OFF in the sequence program. The D/A conversion time (conversion speed) is fixed regardless of the D/A conversion value disable channel setting.	Allows users to specify whether to output a D/A conversion value for each channel or 4mA by setting the D/A conversion value output enable flag to ON/OFF in the sequence program. The D/A conversion time (conversion speed) is fixed regardless of the D/A conversion value disable channel setting .	۵	Check the analog output status combination list.	
Analog output enable/ disable		pecifying whether to enable or disable analog value output to external devices by /1 to address 0 for each channel in the sequence program.			
Offset/gain setting	The I/O conversion characteristics can be changed.	_	×	No offset/gain setting function. Fixed to 4 to 20mA output. Adjust output with the digital input.	

Modifying sequence programs is required as the I/O signals differ. For details of the I/O signals and sequence programs, refer to the user's manual.

A68DAI-S1				A1S68DAI				
Device NO.	Signal name	Device NO.	Signal name	Device NO.	Signal name	Device NO.	Signal name	
X0	WDT error (A68DAI-S1 detection)	Y0		X0	WDT error (A1S68DAI detection)	Y0		
X1	D/A conversion READY	Y1		X1	D/A conversion READY	Y1		
X2		Y2		X2	Error	Y2		
X3		Y3		X3		Y3		
X4		Y4		X4		Y4		
X5		Y5		X5		Y5		
X6		Y6		X6		Y6		
X7		Y7	Not used	X7		Y7	Not used	
X8		Y8		X8		Y8		
X9		Y9		X9		Y9		
XA		YA		XA		YA		
XB		YB		XB		YB		
XC		YC		XC		YC		
XD		YD		XD		YD		
XE		YE		XE		YE		
XF	Not used	YF		XF		YF		
X10		Y10		X10		Y10		
X11		Y11		X11	Not used	Y11		
X12		Y12		X12		Y12		
X13		Y13	D/A conversion output	X13		Y13	D/A conversion output	
X14		Y14	enable	X14		Y14	enable	
X15		Y15		X15		Y15		
X16		Y16		X16		Y16		
X17		Y17		X17		Y17		
X18		Y18	Error reset	X18		Y18	Error reset	
X19		Y19		X19		Y19		
X1A		Y1A		X1A		Y1A		
X1B	]	Y1B		X1B		Y1B		
X1C		Y1C	Not used	X1C		Y1C	Not used	
X1D	]	Y1D		X1D		Y1D		
X1E	]	Y1E		X1E		Y1E		
X1F		Y1F		X1F		Y1F		

# (d) Buffer memory address comparison

Modifying sequence programs is required as the assignment of buffer memory differs. For details of the buffer memory and sequence programs, refer to the user's manual.

A dalara a a	A68DAI-S1		A1S68DAI		
Address	Buffer memory name	Default value	Name	Default value	
0	Analog output enable/disable channel	0000н (All channels are set to "enable".)	Analog output enable/disable channel	0000н (All channels are set to "enable".)	
1	CH1 Digital value		CH1 Digital value		
2	CH2 Digital value		CH2 Digital value		
3	CH3 Digital value		CH3 Digital value		
4	CH4 Digital value	0	CH4 Digital value	0	
5	5     CH5 Digital value       6     CH6 Digital value	0	CH5 Digital value	0	
6			CH6 Digital value		
7	CH7 Digital value		CH7 Digital value		
8	CH8 Digital value		CH8 Digital value		
9	Resolution of digital value	1(1/4000)	Not used	—	
10	CH1 Set value check code		CH1 Set value check code		
11	CH2 Set value check code		CH2 Set value check code		
12	CH3 Set value check code		CH3 Set value check code		
13	CH4 Set value check code	0	CH4 Set value check code	0	
14	CH5 Set value check code	0	CH5 Set value check code	0	
15	CH6 Set value check code		CH6 Set value check code		
16	CH7 Set value check code		CH7 Set value check code	-	
17	CH8 Set value check code		CH8 Set value check code		

# (6) Comparisons between A68DAV and A1S68DAV

	ormance specifications company	O: Compatible, 🛆 : Partial c		-
Item	A68DAV	A1S68DAV	Compati- bility	Precautions for replacement
Digital input	(1) 16-bit, signed binary value (2) Setting range: Setting setting range 1/4000 -4000 to 4000 1/8000 -8000 to 8000 1/12000 -12000 to 12000	(1) 16-bit, signed binary value (2) Setting range: -2048 to 2047	Δ	Only -2048 to 2047 for setting range
Analog output	-10 to 0 to 10 VDC (External load resistance: 2k $\Omega$ to 1M $\Omega$ )	10 to 0 to 10 VDC (External load resistance: 2k $Ω$ to 1M $Ω$ )	0	
I/O characteristics	Digital value resolution         *Analog output value           1/4000         1/8000         1/12000           ag         4000         8000         12000           ag         2000         4000         6000           bg         2000         4000         6000           ag         -2000         -4000         -6000           ag         -2000         -4000         -6000           ag         -4000         -8000         -1200           when offset value 0V, gain value 10V settings         -10V	Digital input value         Analog output value           2000         10V           1000         5V           0         0V           -1000         -5V           -2000         -10V	Δ	I/O conversion characteristics are fixed as in the left.
Maximum resolution of analog value	2.5mV (1/4000) 1.25mV (1/8000) 0.83mV (1/12000)	5mV	×	Resolution is different.
Overall accuracy (Accuracy in respect to maximum value)	± 1.0% (± 100mV)	± 1.0% (± 100mV)	0	
Conversion speed	Within 40ms/8 channel (Same for 1 channel) Note) Time period from digital input write until specified analog voltage (current) reached.	Within 4 ms/8 channels For high frequency of access from the PLC CPU using FROM/TO instructions, this can be extended up to about 6ms.	0	
Absolute maximum output	-12 to +12V Note) The voltage exceeding the above is not output due to output protection circuit.	_	0	
Number of analog output points	8 channels/module	8 channels/module	0	
Output short protection	_	Provided	_	
Isolation method	Between the output terminals and PLC power supply: photocoupler isolation Between channels: non-isolated	Between the output terminals and PLC power supply: photocoupler isolation Between output channels: non-isolated	0	
Occupied I/O points	32 points (I/O assignment: special 32 points)	32 points (I/O assignment: special 32 points)	0	
Connected terminal	38-point terminal block	20-point terminal block (M3.5×7 screws)	×	
Applicable wire size	0.75 to 2mm <sup>2</sup> (Applicable tightening torque 39 to 59N • cm)	0.75 to 1.5mm <sup>2</sup>	Δ	External wiring must be changed.
Applicable solderless terminal	V1.25-3, V1.25-YS3A, V2-S3, V2-YS3A	R1.25-3, 1.25-YS3A, RAV1.25-3, V1.25-YS3A		
Internal power consumption (5VDC)	0.15A	0.65A	Δ	Recalculation of internal current consumption [5VDC] is required.

# 10 SPECIAL FUNCTION MODULE REPLACEMENT

# MELSEC

 $\bigcirc$  : Compatible,  $\ \bigtriangleup$  : Partial change required,  $\ \times$  : Incompatible

			-		-
Item		A68DAV	A1S68DAV	Compati- bility	Precautions for replacement
External power supply	Voltage	21.6 to 26.4VDC			External power
	Current	0.2A	_		supply is not required.
External dimensions		250(H)×37.5(W)×131(D)mm	130(H)×34.5(W)×93.6(D)mm	Δ	The dimensions are different.
Weight		0.6kg	0.28kg	0	

#### (b) Function comparison

O: Compatible, △: Partial change required, ×: Incompatible, —: Additional function

Item	A68DAV	A1S68DAV	Compati- bility	precautions for replacement
HOLD/ CLEAR setting	When the PLC CPU is in STOP, or when D/A conversion is stopped in A68DAV by an error, whether to hold the last analog value output or to clear it (offset value or 0V output) can be selected for all channels with the HOLD/CLEAR terminal on the front face.	When the PLC CPU is in STOP, or when D/A conversion is stopped in A1S68DV by an error, whether to hold the last analog value output or to clear it (0V output) may be selected for all channels with the HOLD/CLEAR terminal on the front face.	Δ	Since A1S68DAI does not have the offset adjustment, the offset value is not output, and 0V is output when cleared.
D/A conversion value output enable flag	Allows users to specify whether to output a D/A conversion value or an offset value by setting the D/A conversion value output enable flag to ON/OFF in the sequence program. The D/A conversion time (conversion speed) is fixed regardless of the D/A conversion value disable channel setting.	Allows users to specify whether to output a D/A conversion value or 0V by setting the D/A conversion value output enable flag to ON/OFF in the sequence program. The D/A conversion time (conversion speed) is fixed regardless of the D/A conversion value disable channel setting.	Δ	Check the analog output status combination list.
Analog output enable/disable setting	Allows specifying whether to enable or disabl writing 0/1 to address 0 for each channel in th	0		
Offset/gain setting	Provided Not provided		×	No offset/gain setting function. Fixed to -10 to 10V output. Adjust output with the digital input.

Modifying sequence programs is required as the I/O signals differ. For details of the I/O signals and sequence programs, refer to the user's manual.

A68DAV				A1S68DAV				
Device NO.	Signal name	Device NO.	Signal name	Device NO.	Signal name	Device NO.	Signal name	
X0	WDT error (A68DAV detection)	Y0		X0	WDT error (A1S68DAV detection)	Y0		
X1	D/A conversion READY	Y1		X1	D/A conversion READY	Y1		
X2	Error	Y2		X2	Error	Y2		
Х3		Y3		X3		Y3		
X4		Y4		X4		Y4		
X5		Y5		X5		Y5		
X6		Y6		X6		Y6		
X7		Y7	Not used	X7		Y7	Not used	
X8		Y8		X8		Y8		
X9		Y9		X9		Y9		
XA		YA		XA		YA		
XB		YB		XB		YB		
XC		YC		XC		YC		
XD		YD		XD		YD		
XE		YE		XE		YE		
XF		YF		XF		YF		
X10		Y10		X10		Y10		
X11	Not used	Y11		X11	Not used	Y11		
X12		Y12		X12		Y12		
X13		Y13	D/A conversion output	X13		Y13	D/A conversion output	
X14		Y14	enable	X14		Y14	enable	
X15		Y15		X15		Y15		
X16		Y16		X16		Y16		
X17		Y17		X17		Y17		
X18		Y18	Error reset	X18		Y18	Error reset	
X19		Y19		X19		Y19		
X1A		Y1A		X1A		Y1A		
X1B		Y1B		X1B		Y1B		
X1C		Y1C	Not used	X1C		Y1C	Not used	
X1D		Y1D		X1D		Y1D		
X1E		Y1E		X1E		Y1E		
X1F		Y1F		X1F		Y1F		

#### (d) Buffer memory address comparison

Modifying sequence programs is required as the assignment of buffer memory differs. For details of the buffer memory and sequence programs, refer to the user's manual.

Address A68DAV			A1S68DAV		
Address	Name	Default value	Name	Default value	
0	Analog output enable/disable channel	0000н (All channels are set to "enable".)	Analog output enable/disable channel	0000н (All channels are set to "enable".)	
1	CH1 Digital value		CH1 Digital value		
2	CH2 Digital value		CH2 Digital value		
3	CH3 Digital value		CH3 Digital value		
4	CH4 Digital value	0	CH4 Digital value	0	
5	5     CH5 Digital value       6     CH6 Digital value	U	CH5 Digital value	0	
6			CH6 Digital value		
7	CH7 Digital value		CH7 Digital value		
8	CH8 Digital value		CH8 Digital value		
9	Resolution of digital value	1(1/4000)	Not used		
10	CH1 Set value check code		CH1 Set value check code		
11	CH2 Set value check code		CH2 Set value check code		
12	CH3 Set value check code		CH3 Set value check code		
13	CH4 Set value check code	0	CH4 Set value check code	0	
14	CH5 Set value check code	U	CH5 Set value check code	0	
15	CH6 Set value check code		CH6 Set value check code		
16	CH7 Set value check code	]	CH7 Set value check code		
17	CH8 Set value check code		CH8 Set value check code	]	

# 10.2.3 Temperature input module comparison

# (1) Comparisons between A616TD and A1S68TD

#### (a) Performance specifications comparison

O: Compatible,  $\ \bigtriangleup$  : Partial change required,  $\ \times$  : Incompatible

		A616TD (A60MXT, A60MXTN when using the combination.)		A1S	68TD		Compati- bility	Precautions for replacement
	perature sor input	-200 to 1800[°C]		0 to 1700[°C]				Input temperature range differs.
out	Digital output value	16-bit, signed binary (0 to 4000) (data unit: 12 bits)	16-	-	inary (0 to 200 g value	00)	×	Changes 0 to 4000 → 0 to 2000.
Output	Detected temperature value	16-bit, signed binary (-2000 to 18000: value to 1 decimal place × 10)	(0 to 170	, 0	ned binary 1 decimal pla	ce× 10)	Δ	Detected temperature range differs.
	licable mocouple	Refer to (e) Applicable thermocouples and measured temperature range accuracies.	Refer to (e) A ter		rmocouples a nge accuracie		Δ	Check the applicable thermocouple.
temp rang	sured perature le uracies	Refer to (e) Applicable thermocouples and measured temperature range accuracies.	Refer to (e) A ter		rmocouples a nge accuracie			
Over accu	rall ıracy [%]	Shown in the list of (e) Applicable thermocouples and measured temperature range accuracies. Measured temperature range accuracies $\pm 0.5^{\circ}$ C	Conversion accuracy + Temperature characteristic × Operating ambient temperature variation + ± 1°C (Cold junction compensation accuracy)				×	Conversion accuracy differs.
com	l junction pensation uracy je	-20 to 80 [°C] (RTD Pt100 included)	_				_	
	imum version ed	50ms/channel	400ms/8 channel					Sampling period is not changed according to No. of channels used.
Isola	ation method	Between the input terminals and PLC: photocoupler isolation Between channels: non-isolated (1M $\Omega$ resistor isolation)	Specific isolation area Between thermocouple input and PLC power supply Between thermocouple input channels	Isolation method Transformer isolation	Dielectric withstand voltage 500VAC for 1 minute	Insulation resistance 5MΩ or higher with a 500VDC insulation resistance tester	0	
			Between the cold junction compensation input (Pt100) and PLC power supply	Not isolated	_	_		
	perature sor input ts	15point/A60MXT, A60MXTN (Up to 7 A60MXT/A60MXTN can be connected to each A616TD.)	8 channels+Pt100 connection channel/module					Since the number of channels is reduced, the number of modules installed may increase. In addition, the number of channels cannot be increased by connecting a multiplexer module.
Occu point	upied I/O ts	32 points (I/O assignment: special 32 points)	32 points (I/O assignment: special 32 points)				0	
Con term	nected iinal	38-point terminal block	20-point terminal block			×		
Appl size	licable wire	0.75 to 2mm <sup>2</sup> (Applicable tightening torque 39 to 59N-cm)		0.75 to	1.5mm <sup>2</sup>			External wiring must be changed.
	licable erless inal	V1.25-3, V1.25-YS3A, V2-S3, V2-YS3A			.25-YS3A, V1.25-YS3A		Δ	

		• • –	•	
Item	A616TD (A60MXT, A60MXTN when using the combination.)	A1S68TD	Compati- bility	Precautions for replacement
Internal current consumption (5VDC)	1.0 A	0.32 A	0	
External dimensions	250(H)×37.5(W)×131(D)mm	130(H)×34.5(W)×93.6(D)mm	Δ	The dimensions are different.
Weight	0.85kg	0.28kg	0	

O: Compatible,  $\ \ \bigtriangleup$  : Partial change required,  $\ \times$  : Incompatible

## (b) Function comparison

Item	A616TD	A1S68TD	Compati- bility	Precautions for replacement
Conversion enable/ disable function	Write "0" or "1", respectively, to the buffer memory addresses $F_H$ to $17_H$ to disable or enable conversion for each channel. A default value is written to address $F_H$ when the power supply is turned on or the PLC CPU is reset to enable conversion for all channels. In addresses 10H to 17H, conversion enable setting for all channels is written to the area of CNT. No. set to an A60MXT, and conversion disable setting to the area of CNT. No. not set to it.	Specifies whether to enable or disable A/D conversion for each channel of A1S68TD. A value, "0000(H)" (All channels disabled) in set as default when the power supply is turned on or the PLC CPU is reset. Only channels to be used are set to enable A/D conversion. (When channels unused are set to enable A/D conversion, a disconnection detection error occurs.) * Conversion speed is not changed by No. of channels used.	Δ	Conversion speed is fixed regardless of No. of channels used.
Disconnection detection function	This function is provided to detect electrical discontinuities in thermocouples and compensating leads connected to the A60MXT. If a break occurs in a thermocouple lead or a compensating lead, the A60MXT internal discontinuity detection circuit outputs a voltage to the A616TD, which is equivalent to an out-of-range detected temperature value. The A616TD detects the discontinuity from the out-of-range voltage and stores the value "1" for the appropriate channel in the discontinuity detection area of the buffer memory and turns on the discontinuity error signal. This function can be enabled or disabled for each channel by setting the disconnection detection switches and buffer memory.	When disconnection occurs in a thermocouple, the disconnection detection flag signal (X3) is turned ON, and "1" is stored in the bit area corresponding to the channel No. in the buffer memory. When the disconnection detection flag is turned ON, the digital value exceeds the measured temperature range specified by the thermocouple used, to the + side. Remove disconnection causes and then turn ON the error reset signal (Y12) using the sequence program. Doing this turns OFF the disconnection detection flags in the buffer memory area.		
Converted temperature value storage	The temperatures detected by the temperature sensors (thermocouples) are input to respective A60MXT channels as thermoelectromotive forces. The A60MXT switches input channels once at sampling cycles and amplifies the voltage input to each channel from its respective thermocouple into a voltage of 0 to 10V, and sends this voltage to the A616TD. The A616TD converts the analog input voltages to digital values. As the relationship between the thermoelectromotive force and the temperature detected by a temperature sensor (thermocouple) is non-linear, converted digital values are linearized before being stored in the buffer memory.	The temperature detected for each channel is linearized and converted to the converted temperature value with cold junction temperature compensation to be stored in the buffer memory. The converted temperature value is "measured temperature × 10".	0	
Scaling value storage	Set a temperature value for each channel so that the A/D conversion digital value is 0 > 4000 for each channel of A616TD.	Converted temperature values are scaled between the lower limit value (0) and upper limit value (2000) set in the buffer memory address 30 to 45 and stored. Scaling value = Converted temperature value - Lower limit value Upper limit value - Lower limit value × 2000		
Lower/Upper limit value setting		Sets for each channel the range of the measured temperature (unit: 0.1°C).As the default, the value in the converted temperature value range of the thermocouple set with the thermocouple selection switch is stored.The lower/upper limit value setting ranges are as follows:Thermocouple TypeDefaultLower/Upper Limit Value Setting RangeK0 to 12000-2000 to 12000E0 to 8000-2000 to 12000J0 to 7500-2000 to 3500T0 to 3500-2000 to 3500B8000 to 170006000 to 17000R3000 to 160000 to 16000S3000 to 160000 to 16000	_	
Error compensation function	This function is provided to compensate for errors between actual and measured temperatures arising from differences in thermocouple accuracy, compensation lead lengths and location conditions of the thermocouples.	_	×	No error compensation function

O: Compatible,  $\triangle:$  Partial change required,  $\times:$  Incompatible, —: Additional function

Modifying sequence programs is required as the I/O signals differ. For details of the I/O signals and sequence programs, refer to the user's manual.

ļ	Signal direction: A616TD → PLC CPU	F	Signal direction: PLC CPU → A616TD		Signal direction: A1S68TD→CPU		Signal direction: CPU → A1S68TD
Device NO.	Signal name	Device NO.	Signal name	Device NO.	Signal name	Device NO.	Signal name
X0	WDT error (A616TD detection)	Y0		X0	WDT error	Y0	
X1	A/D conversion READY	Y1		X1	A/D conversion READY	Y1	
X2	Error	Y2		X2	Error	Y2	
X3	Wire break error detection	Y3		Х3	Disconnection detection	Y3	
X4	Digital output value out- of-range detected	Y4		X4	Exceeding measurement range	Y4	
X5	Temperature output value out-of-range detected	Y5		X5		Y5	
X6		Y6	Not used	X6		Y6	
X7		Y7		X7		Y7	Not used
X8		Y8		X8		Y8	
X9		Y9		X9		Y9	
XA		YA		XA		YA	
XB		YB		XB		YB	
XC		YC		XC		YC	
XD		YD		XD		YD	
XE		YE		XE		YE	
XF		YF		XF		YF	
X10		Y10	Detected temperature value LED display request signal	X10		Y10	
X11		Y11		X11	Not used	Y11	Set lower/upper limit value update instruction
X12	Not used	Y12		X12		Y12	Error reset
X13		Y13		X13		Y13	
X14		Y14		X14		Y14	
X15		Y15		X15		Y15	
X16		Y16		X16		Y16	
X17		Y17		X17		Y17	
X18		Y18	Not used	X18		Y18	
X19		Y19		X19		Y19	Not used
X1A		Y1A		X1A		Y1A	
X1B		Y1B		X1B		Y1B	
X1C		Y1C		X1C		Y1C	
X1D		Y1D		X1D		Y1D	
X1E		Y1E		X1E		Y1E	
X1F		Y1F		X1F		Y1F	

#### (d) Buffer memory address comparison

Modifying sequence programs is required as the assignment of buffer memory differs. For details of the buffer memory and sequence programs, refer to the user's manual.

Address	A616	ſD	Read/Write	Address	A1S68TD	Read/Write	
0	Data format selection			0	A/D conversion enable/disable setting	R/W	
1	Error code storage			1	Error code		
2	Faulty A60MX CONNE	ECT No. storage	R/W	2	Disconnection detection flag	R	
3	Thermocouple-type settin number storage	ng error and channel		3	Exceeding measurement range		
4	Current sampling cycle s	torage		4			
5				5			
6				6	Not used	_	
7				7			
8				8			
9	Not used		R	9			
10				10	CH1 Converted temperature value (0.1°C units)	_	
11				11	CH2 Converted temperature value (0.1°C units)	_	
12				12	CH3 Converted temperature value (0.1°C units)	_	
13				13	CH4 Converted temperature value (0.1°C units)	R	
14		A616TD		14 15	CH5 Converted temperature value (0.1°C units)	_	
15 16	-	AOTOTD		15	CH6 Converted temperature value (0.1°C units) CH7 Converted temperature value (0.1°C units)	_	
10				10	CH8 Converted temperature value (0.1°C units)	_	
18				18	Cho converted temperature value (0.1 C units)		
10	Conversion enable/	Multiplay modula		10	Not used		
20	disable designation	Multiplex module	R/W	20	CH1 Scaling value	-	
21				21	CH2 Scaling value		
22				22	CH3 Scaling value	-	
23				23	CH4 Scaling value	-	
24	Set data setting request			24	CH5 Scaling value	R	
25				25	CH6 Scaling value		
26				26	CH7 Scaling value	]	
27				27	CH8 Scaling value		
28	Not us	ed	R	28	A/D conversion completion		
29				29	Not used	—	
30				30	CH1 Lower limit value (0.1°C units)		
31				31	CH1 Upper limit value (0.1°C units)		
32				32	CH2 Lower limit value (0.1°C units)		
33				33	CH2 Upper limit value (0.1°C units)		
34				34	CH3 Lower limit value (0.1°C units)	_	
35	Disconnection detection	enable/disable	R/W	35	CH3 Upper limit value (0.1°C units)	_	
36	designation			36	CH4 Lower limit value (0.1°C units)	_	
37	4			37	CH4 Upper limit value (0.1°C units)	R/W	
38	4			38	CH5 Lower limit value (0.1°C units)	_	
39				39	CH5 Upper limit value (0.1°C units)	-	
40	4			40	CH6 Lupper limit value (0.1°C units)	-	
41	4			41	CH7 Lower limit value (0.1°C units)	-	
42	1			42 43	CH7 Lower limit value (0.1°C units)	-	
43	Not us	ed	R	43	CH7 Upper limit value (0.1°C units) CH8 Lower limit value (0.1°C units)	-	
44				44	CH8 Upper limit value (0.1°C units)	-	
45	1			45			
40	1			40			
48							
to	Digital output value temp	erature setting		48	Not used	_	
63	· · · · · ·		R/W				
64	Disconnection detection	channel number		49			
to							

Address	A616TD	Read/Write	
72 to 79	Not used	R	
80 to 87	No. of the channel where the digital output value is out of range	R/W	
88 to 95	Not used	R	
96 to 103	No. of the channel where the detected temperature is out of range	R/W	
104 to 111	Not used	R	
112 to 127	Digital output value of INPUT channel		
128 to 255	Error compensation settings	R/W	
256 to 383	Thermocouple type setting	R/W	
384 to 511	Digital output value of MX CH.channel	R	
512 to 639	Detected temperature value of MX CH.channel	IX.	

# (e) Applicable thermocouples and measured temperature range accuracies

				A616	ſD					A1S68TD		
				Measured range no.	1	2	3	4				
JIS	ANSI	DIN	BS	Permitted input voltage range [mV]	-12.5 to 12.5	0 to 25	0 to 50	0 to 100	JIS			
				Temperature input range[°C]	100 to 1500	100 to 1800	100 to 1800	100 to 1800		Temperature input range[°C]	800 to 1700	
В	в	_	PtRh30 -PtRh6	Accuracy at 25°C [%]	_	$\pm 0.5$	_	Ι	В	Conversion accuracy at 25±5℃	±2.5℃	
				Temperature drift [%/°C]		± 0.013				Temperature characteristics	$\pm 0.4$ °C	
				Temperature input range[°C]	0 to 1000	0 to 1700	0 to 1700	0 to 1700		Temperature input range[°C]	300 to 1600	
R	R	_	PtRh13 -Pt	Accuracy at 25°C [%]	_	± 0.4	_		R	Conversion accuracy at $25\pm5^{\circ}$ C	±2°C	
				Temperature drift [%/°C]		± 0.011				Temperature characteristics	$\pm 0.3$ °C	
				Temperature input range[°C]	0 to 1200	0 to 1700	0 to 1700	0 to 1700		Temperature input range[°C]	300 to 1600	
s	s	PtRh -Pt	PtRh10 -Pt	Accuracy at 25°C [%]	_	± 0.4	_	_	s	Conversion accuracy at 25±5°C	± 2°C	
				Temperature drift [%/°C]		± 0.011				Temperature characteristics	$\pm 0.3$ °C	
				Temperature input range[°C]	-200 to 250	0 to 500	0 to 1000	0 to 1300		Temperature input range[°C]	0 to 1200	
к	K I K I	NiCr -Ni	- NiCr -NiAl	Accuracy at 25°C [%]	± 0.4	± 0.3	± 0.3	± 0.5	к	Conversion accuracy at 25±5℃	$\pm$ 0.5°C or $\pm$ 0.25% of measured temperature, whichever is greater.	
				Temperature drift [%/°C]	± 0.011	± 0.01	± 0.01	± 0.013		Temperature characteristics	$\pm$ 0.07°C or $\pm$ 0.02% of measured temperature, whichever is greater.	
				Temperature input range[°C]	-200 to 150	0 to 300	0 to 600	0 to 1000		Temperature input range[°C]	0 to 800	
E	E	_	NiCr -CuNi	Accuracy at 25°C [%]	±0.4	±0.3	± 0.3	± 0.4	E	Conversion accuracy at 25±5°C	$\pm$ 0.5°C or $\pm$ 0.25% of measured temperature, whichever is greater.	
				Temperature drift [%/°C]	± 0.011	± 0.01	± 0.01	± 0.011		Temperature characteristics	$\pm$ 0.07°C or $\pm$ 0.02% of measured temperature, whichever is greater.	
				Temperature input range[°C]	-200 to 200	0 to 400	0 to 800	0 to 1200		Temperature input range[°C]	0 to 750	
J	J	_	Fe -CuNi	Accuracy at 25°C [%]	± 0.4	±0.3	± 0.3	± 0.4	J	Conversion accuracy at 25±5°C	$\pm$ 0.5°C or $\pm$ 0.25% of measured temperature, whichever is greater.	
		-Guivi	Temperature drift [%/°C]	± 0.011	± 0.01	± 0.01	± 0.01 ± 0.011		Temperature characteristics	$\pm$ 0.07°C or $\pm$ 0.02% of measured temperature, whichever is greater.		

# 10 SPECIAL FUNCTION MODULE REPLACEMENT

# MELSEC

	A616TD									A1S68TD			
				Measured range no.	1	2	3	4					
JIS	ANSI	DIN	BS	Permitted input voltage range [mV]	-12.5 to 12.5	0 to 25	0 to 50	0 to 100	JIS	_	_		
				Temperature input range[°C]	-200 to 200	0 to 400	0 to 400	0 to 400		Temperature input range[°C]	0 to 350		
т	т	_	Cu -CuNi	Accuracy at 25°C [%]	± 0.5	± 0.3			т	Conversion accuracy at 25±5℃	$\pm$ 0.5°C or $\pm$ 0.25% of measured temperature, whichever is greater.		
				Temperature drift [%/°C]	± 0.013	± 0.01		_				Temperature characteristics	$\pm$ 0.07°C or $\pm$ 0.02% of measured temperature, whichever is greater.
		Fe		Temperature input range[°C]	-100 to 200	0 to 400	0 to 800	0 to 900					
_	_	-CuNi	_	Accuracy at 25°C [%]		± 0.3	± 0.3	$\pm 0.5$					
				Temperature drift [%/°C]	_	± 0.01	± 0.01	± 0.013					
		Cu		Temperature input range[°C]	-100 to 200	0 to 400	0 to 600	0 to 600					
—	—	-CuNi	-	Accuracy at 25°C [%]		± 0.3	± 0.4						
				Temperature drift [%/°C]	_	± 0.01	± 0.011	] —					

O: Compatible,  $\ \ \bigtriangleup$  : Partial change required,  $\ \times$  : Incompatible

# (2) Comparisons between A68RD3N and A1S62RD3N

Item	ı	A68RD3N	A1S62RD3N	Compati- bility	Precautions for replacement
Measuring met	thod	3-wi	$\bigcirc$		
Output (detected temperature value)		16-bit, si (-1800 to 6000: Value t 32-bit, si	gned binary o one decimal place× 10) gned binary o three decimal places× 1000)	0	
Applicable platinum RTD			C 751-am2, JIS C1604-1989, Pt100 (JIS C1604-1981)	0	
Temperature Pt100		-180 to 600°C(2	27.10 to 313.71 Ω)	~	
input range	JPt100	-180 to 600°C(2	25.80 to 317.28 Ω)	0	
Accuracy		± 1% (accuracy	relative to full-scale)	0	
Resolution		0.0	025°C	0	
Conversion spe	eed	40ms	/channel	0	
Analog input points		8 channels/module	2 channels/module	×	The number of channels has decreased. Using multiple A1S62RD3Ns is recommended.
Output current for temperature detection		1	mA	0	
Isolation metho	bd	Between platinum RTD input and PLC po Between platinum RTD input and channed	0		
Dielectric withs voltage	stand	Between platinum RTD input and PLC p	0		
Disconnection	detection	Detected fo	r each channel	0	
Occupied I/O p	ooints		points : special 32 points)	0	
Connected terr	minal	38-point terminal block	20-point terminal block	×	
Applicable wire	e size	0.75 to 2mm <sup>2</sup>	0.75 to 1.5mm <sup>2</sup>	Δ	External wiring must be
Applicable sold terminal	lerless		/1.25-YS3A, V2-YS3A	Δ	changed.
Cables betwee and platinum R		Conductor resistance between Pt100 an less per wire. All channels have the sam	d A68RD3N/A1S62RD3N must be 10 $\Omega$ or e specifications.	0	
Internal current consumption (5VDC)	t	0.94A	0.49A	0	
External dimen	isions	250(H)×37.5(W)×121(D)mm	130(H)×34.5(W)×93.6(D)mm		The dimensions are different.
Weight		0.43kg	0.27kg	0	

#### (b) Function comparison

O: Compatible,  $\triangle:$  Partial change required,  $\times:$  Incompatible, —: Additional function

Item	A68RD3N	A1S62RD3N	Compati- bility	Precautions for replacement
Conversion enable/ disable setting for each channel	Temperature detection is enabled or disabled for • Conversion enable… Loads external temper • Conversion disable… Does not load external	0		
Sampling/average processing setting	<ol> <li>Sampling processing         For each sampling time, the detected temperature value is stored in the buffer memory.     </li> <li>Time-average processing         For each sampling time, the detected temperature value is loaded for the time of the set value (320 to 32000ms), and the average of the total excluding the maximum and minimum values is stored in the buffer memory.     <li>Count-average processing         For each sampling time, the detected temperature value is loaded for the set value of the total excluding the maximum and minimum values is stored in the buffer memory.     </li> <li>Count-average processing         For each sampling time, the detected temperature value is loaded for the set number of times (1 to 800 times) and the average of the total exculding the maximum and minimum value is stored in the buffer memory.     </li> </li></ol>	<ol> <li>Sampling processing         For each sampling time, the detected temperature value is stored in the buffer memory.     </li> <li>Time-average processing         For each sampling time, the detected temperature value is loaded for the time of the set value (80 to 32000ms), and the average of the total excluding the maximum and minimum values is stored in the buffer memory.     </li> <li>Count-average processing         For each sampling time, the detected temperature value is loaded for the set value (80 to 32000ms), and the average of the total excluding the maximum and minimum values is stored in the buffer memory.     </li> <li>Count-average processing         For each sampling time, the detected temperature value is loaded for the set number of times (1 to 800 times) and the average of the total excluding the maximum and minimum value is stored in the buffer memory.     </li> </ol>	0	
Storage of detected temperature values	<ul> <li>Values rounded to one and three decimal place</li> <li>Value rounded to one decimal place (16-bit s</li> <li>Value rounded to three decimal places (32-b 216.025(°C)→ 216025</li> </ul>	signed binary) Example: 53.8(°C)→ 538	0	
Disconnection detection	Disconnection of Pt100 or cable is detected. Di the disconnection-detected flag that correspond	sconnection can be detected at each channel and ds to that channel turns ON when detected.	0	
Platinum RTD type setting		um RTD to be used is set. kinds of platinum RTDs: Specification JIS C1604-1997, IEC 751-am2 JIS C1604-1989, DIN 43760-1980 JIS C1604-1981	0	

Modifying sequence programs is required as the I/O signals differ. For details of the I/O signals and sequence programs, refer to the user's manual.

	Signal direction: 8RD3N → PLC CPU		Signal direction: C CPU → A68RD3N		Signal direction: 52RD3N → PLC CPU		Signal direction: PLC CPU → A1S62RD3N		
Device NO.	Signal name	Device NO.	Signal name	Device NO.	Signal name	Device NO.	Signal name		
X0	WDT error	Y0		X0	WDT error	Y0			
X1	READY	Y1		X1	READY	Y1			
X2	Write data error	Y2		X2	Write data error	Y2			
X3	CH1: Disconnection detected	Y3		X3	CH1: Disconnection detected	Y3			
X4	CH2: Disconnection detected	Y4		X4	CH2: Disconnection detected	Y4			
X5	CH3: Disconnection detected	Y5		X5		Y5			
X6	CH4: Disconnection detected	Y6		X6		Y6			
X7	CH5: Disconnection detected	Y7		X7		Y7			
X8	CH6: Disconnection detected	Y8	Not used	X8		Y8	Not used		
X9	CH7: Disconnection detected	Y9		Х9		Y9			
XA	CH8: Disconnection detected	YA		XA		YA			
XB		YB		ХВ		YB			
XC		YC		XC		YC			
XD		YD		XD		YD			
XE		YE		XE		YE			
XF		YF		XF		YF			
X10		Y10		X10	Not used	Y10			
X11		Y11		X11		Y11			
X12		Y12	Error code reset	X12		Y12	Error code reset		
X13		Y13		X13		Y13			
X14		Y14		X14		Y14			
X15	Not used	Y15		X15		Y15			
X16		Y16		X16		Y16			
X17		Y17		X17		Y17			
X18		Y18		X18		Y18			
X19		Y19	Not used	X19		Y19	Not used		
X1A		Y1A		X1A		Y1A			
X1B		Y1B		X1B		Y1B			
X1C		Y1C		X1C		Y1C			
X1D		Y1D		X1D		Y1D			
X1E		Y1E		X1E		Y1E			
X1F		Y1F		X1F		Y1F			

#### (d) Buffer memory address comparison

Modifying sequence programs is required as the assignment of buffer memory differs. For details of the buffer memory and sequence programs, refer to the user's manual.

Address	A68RD3N	Read/Write	Address	A1S62RD3N	Read/Write
0	Conversion enable/disable specification		0	Conversion enable/disable specification	
1	Averaging processing specification		1	Averaging processing specification	
2	CH1 Averaging time/count		2	CH1 Averaging time/count	R/W
3	CH2 Averaging time/count		3	CH2 Averaging time/count	
4	CH3 Averaging time/count	DAAK	4		
5	CH4 Averaging time/count	R/W	5	1	
6	CH5 Averaging time/count		6		
7	CH6 Averaging time/count		7	- Not used	_
8	CH7 Averaging time/count		8	1	
9	CH8 Averaging time/count		9	1	
10	CH1 Detected temperature value (16bit)		10	CH1 Detected temperature value (16bit)	
11	CH2 Detected temperature value (16bit)		11	CH2 Detected temperature value (16bit)	R
12	CH3 Detected temperature value (16bit)		12		
13	CH4 Detected temperature value (16bit)		13	1	
14	CH5 Detected temperature value (16bit)		14	Netword	
15	CH6 Detected temperature value (16bit)		15	- Not used	_
16	CH7 Detected temperature value (16bit)		16		
17	CH8 Detected temperature value (16bit)		17	1	
18	CH1 Detected temperature value (L)		18	CH1 Detected temperature value (L)	
19	(32bit) (H)		19	(32bit) (H)	R
20	CH2 Detected temperature value (L)		20	CH2 Detected temperature value (L)	
21	(32bit) (H)	R	21	(32bit) (H)	
22	CH3 Detected temperature value (L)	ĸ	22		
23	(32bit) (H)		23		
24	CH4 Detected temperature value (L)		24		
25	(32bit) (H)		25		
26	CH5 Detected temperature value (L)		26		
27	(32bit) (H)		27	Not used	
28	CH6 Detected temperature value (L)		28	Not used	_
29	(32bit) (H)		29		
30	CH7 Detected temperature value (L)		30		
31	(32bit) (H)		31		
32	CH8 Detected temperature value (L)		32		
33	(32bit) (H)		33		
34	Write data error code	R/W	34	Write data error code	R/W
35	Conversion completed	R	35	Conversion completed	R
36	Type specification of a platinum RTD	R/W	36	Type specification of a platinum RTD	R/W

O: Compatible,  $\ \ \bigtriangleup$  : Partial change required,  $\ \times$  : Incompatible

#### (3) Comparisons between A68RD4N and A1S62RD4N

Item		A68RD4N	A1S62RD4N	Compati- bility	Precautions for replacement
Measuring met	hod	4-w	0		
Output (detected temperature value)		(-1800 to 6000: Value 32-bit, s	igned binary to one decimal place× 10) igned binary to three decimal places× 1000)	0	
Applicable plati	inum RTD		EC 751-am2, JIS C1604-1989, Pt100 (JIS C1604-1981)	0	
Temperature	Pt100	-180 to 600°C (	27.10 to 313.71 Ω)	0	
input range	JPt100	-180 to 600°C (	25.80 to 317.28 Ω)	0	
Accuracy		$\pm$ 1% (accuracy	relative to full-scale)	0	
Resolution		0.	025°C	0	
Conversion spe	eed	40ms	0		
Analog input po	pints	8 channels/module	2 channels/module	×	The number of channels decreases. Using multiple A1S62RD4Ns is recommended.
Output current temperature de			0		
Isolation metho	od	Between platinum RTD input and PLC po Between platinum RTD input and channed	0		
Dielectric withs voltage	tand	Between platinum RTD input and PLC p	0		
Disconnection	detection	Batch-detecte	ed on all channels	0	
Occupied I/O p	oints		points t: special 32 points)	0	
Connected term	ninal	38-point terminal block	20-point terminal block	×	
Applicable wire	e size	0.75 to 2mm <sup>2</sup>	0.75 to 1.5mm <sup>2</sup>	Δ	External wiring must be
Applicable sold terminal	lerless	V1.25-3, V1.25-Y	53A, V2-S3, V2-YS3A	Δ	changed.
Cable across R platinum RTD	RD3N and	Total resistance value of live	conductors must be 70 $\Omega$ or less.	0	
Internal current consumption (5VDC)	t	0.41A	0.39A	0	
External dimen	sions	250(H)×37.5(W)×121(D)mm	130(H)×34.5(W)×93.6(D)m	Δ	The dimensions are different.
Weight		0.43kg	0.27kg	0	

#### (b) Function comparison

O: Compatible, △: Partial change required, ×: Incompatible, —: Additional function

Item	A68RD4N	A1S62RD4N	Compati- bility	Precautions for replacement
Conversion enable/ disable setting for each channel	Temperature detection is enabled or disabled for • Conversion enable…Loads external tempera • Conversion disable…Does not load external		0	
Sampling /average processing setting	<ol> <li>Sampling processing         For each sampling time, the detected temperature value is stored in the buffer memory.     </li> <li>Time-average processing         For each sampling time, the detected temperature value is loaded for the time of the set value (320 to 32000ms) and the average of the total excluding the maximum and minimum values is stored in the buffer memory.     <li>Count-average processing         For each sampling time, the detected temperature value is loaded for the set value (320 to 32000ms) and the average of the total excluding the maximum and minimum values is stored in the buffer memory.     </li> <li>Count-average processing         For each sampling time, the detected temperature value is loaded for the set number of times (1 to 800 times) and the average of the total excluding the maximum and minimum values is stored in the buffer memory.     </li> </li></ol>	<ol> <li>Sampling processing         For each sampling time, the detected temperature value is stored in the buffer memory.     </li> <li>Time-average processing         For each sampling time, the detected temperature value is loaded for the time of the set value (80 to 32000ms) and the average of the total excluding the maximum and minimum values is stored in the buffer memory.     <li>Count-average processing         For each sampling time, the detected temperature value is loaded for the set value (80 to 32000ms) and the average of the total excluding the maximum and minimum values is stored in the buffer memory.     </li> <li>Count-average processing         For each sampling time, the detected temperature value is loaded for the set number of times (1 to 800 times) and the average value of the total excluding the maximum and minimum value is stored in the buffer memory.     </li> </li></ol>	0	
Storage of detected temperature values	<ul> <li>Values rounded to one and three decimal places</li> <li>Value rounded to one decimal place (16-bit sig</li> <li>Value rounded three decimal places (32-bit sig 216.025(°C) → 216025</li> </ul>	gned binary) Example: $53.8(^{\circ}C) \rightarrow 538$	0	
Disconnection detection	Disconnection of Pt100 or cable is detected. If ei $\Sigma$ disconnection-detected flag turns ON.	ther channel disconnection is detected, the	0	
Platinum RTD type setting	, , , , , , , , , , , , , , , , , , ,	n RTD to be used is set. inds of platinum RTDs: Specification JIS C1604-1997, IEC 751-am2 JIS C1604-1989, DIN 43760-1980 JIS C1604-1981	0	

Modifying sequence programs is required as the I/O signals differ. For details of the I/O signals and sequence programs, refer to the user's manual.

	Signal direction: 8RD4N → PLC CPU		Signal direction: C CPU → A68RD4N	A1S	Signal direction: 62RD4N → PLC CPU		Signal direction: CPU → A1S62RD4N			
Device NO.	Signal name	Device NO.	Signal name	Device NO.	Signal name	Device NO.	Signal name			
X0	WDT error	Y0		X0	WDT error	Y0				
X1	READY	Y1		X1	READY	Y1				
X2	Write data error	Y2		X2	Write data error	Y2				
Х3	<ul> <li>Σ disconnection</li> <li>detection</li> <li>(CH1 to CH8)</li> </ul>	Y3		X3	Σ disconnection detection (CH1, CH2)	Y3				
X4		Y4		X4		Y4				
X5		Y5		X5		Y5				
X6		Y6		X6		Y6				
X7		Y7		X7		Y7				
X8		Y8	Not used	X8		Y8	Not used			
X9		Y9		X9		Y9				
XA		YA		XA		YA				
XB		YB		XB		YB				
XC		YC		XC		YC				
XD		YD		XD		YD				
XE		YE		XE		YE				
XF		YF		XF		YF				
X10		Y10		X10		Y10				
X11	Netword	Y11		X11	Natural	Y11				
X12	Not used	Y12	Error code reset	X12	Not used	Y12	Error code reset			
X13		Y13		X13		Y13				
X14		Y14		X14		Y14				
X15		Y15		X15		Y15				
X16		Y16		X16		Y16				
X17		Y17		X17		Y17				
X18		Y18		X18		Y18				
X19		Y19	Not used	X19		Y19	Not used			
X1A		Y1A		X1A		Y1A				
X1B		Y1B		X1B		Y1B				
X1C		Y1C		X1C		Y1C				
X1D		Y1D		X1D		Y1D				
X1E		Y1E		X1E		Y1E				
X1F		Y1F		X1F		Y1F				

#### (d) Buffer memory address comparison

Modifying sequence programs is required as the assignment of buffer memory differs. For details of the buffer memory and sequence programs, refer to the user's manual.

Address	A68RD4N	Read/Write	Address	A1S62RD4N	Read/Write
0	Conversion enable/disable specification		0	Conversion enable/disable specification	
1	Averaging processing specification		1	Averaging processing specification	
2	CH1 Averaging time/count		2	CH1 Averaging time/count	R/W
3	CH2 Averaging time/count		3	CH2 Averaging time/count	
4	CH3 Averaging time/count	R/W	4		
5	CH4 Averaging time/count	K/W	5		
6	CH5 Averaging time/count		6	Not used	
7	CH6 Averaging time/count		7	Not used	_
8	CH7 Averaging time/count		8		
9	CH8 Averaging time/count		9		
10	CH1 Detected temperature value (16bit)		10	CH1 Detected temperature value (16bit)	R
11	CH2 Detected temperature value (16bit)		11	CH2 Detected temperature value (16bit)	ĸ
12	CH3 Detected temperature value (16bit)		12		
13	CH4 Detected temperature value (16bit)		13		
14	CH5 Detected temperature value (16bit)		14	Not used	
15	CH6 Detected temperature value (16bit)		15	Not used	_
16	CH7 Detected temperature value (16bit)		16		
17	CH8 Detected temperature value (16bit)		17		
18	CH1 Detected temperature value (L)		18	CH1 Detected temperature value (L)	
19	(32bit) (H)		19	(32bit) (H)	- R
20	CH2 Detected temperature value (L)		20	CH2 Detected temperature value (L)	n.
21	(32bit) (H)	R	21	(32bit) (H)	
22	CH3 Detected temperature value (L)		22		
23	(32bit) (H)		23		
24	CH4 Detected temperature value (L)		24		
25	(32bit) (H)		25		
26	CH5 Detected temperature value (L)		26		
27	(32bit) (H)	-	27	Not used	_
28	CH6 Detected temperature value (L)		28		
29	(32bit) (H)	-	29		
30	CH7 Detected temperature value (L)		30	1	
31	(32bit) (H)		31		
32	CH8 Detected temperature value (L)		32		
33	(32bit) (H)		33		
34	Write data error code	R/W	36	Write data error code	R/W
35	Conversion completed	R	37	Conversion completed	R
36	Type specification of a platinum RTD	R/W	39	Type specification of a platinum RTD	R/W

# 10.2.4 High-speed counter module comparison

### (1) Comparisons between AD61 and A1SD62

	Iter	n	А	D61		A1SD62	Compati- bility	Precautions for replacemen
CC	upied I/O poir	nts	(I/O assignm	points nent: special 32 pints)	(I/O ass	0		
lum	ber of chann	els	•	2		2	0	
Cou	nting speed s	witch setting		—	100k			
	Count	Phase		nase or ase input	1-բ	phase or 2-phase input	_	
-	input signal	Signal level ( $\phi$ A, $\phi$ B)	5VDC 12VDC 24VDC	} 2 to 5mA	5VDC 12VDC 24VDC	} 2 to 5mA	0	
		Counting speed	1-phase input	50kPPS	100kPPS	10kPPS	0	
		(Max.)*	2-phase input	50kPPS	100kPPS	7kPPS	Ŭ	
		Counting range	Binary format 24-bit binary 0 to 16,777,215 (Dec.)			0		
Performance specifications per channel	Counter	Туре	(1-phase and 2-phase inputs)			/DOWN preset counter Ring counter function	0	
		Minimum count pulse width			10µs 5µs 5µs (1-phase and 2-phase inputs)	100μs         142μs           50μs         71μs           (1-phase input)         (2-phase input)	0	
	Magnitude comparison	Comparison range	Binary format (Bin.) 24 bits					
-	between CPU and AD61	Comparison result	Set value	< Count value = Count value > Count value	Se Se Se	0		
		Preset		DC 3/6mA IC 5mA		5/12/24VDC 2 to 5mA	0	
	External input	Count disable		DC 3/6mA )C 5mA		_	×	Enabled by function start terminal.
		Function start		_		5/12/24VDC 2 to 5mA	—	
	External output	Coincidence output	(open col	nsistor lector) output /DC 0.5A	12/24∨	Transistor (sink type) output DC 0.5A/point 2A/common	0	
	rnal current c DC)	onsumption	(	).3A		0.1A	0	
xte	ernal dimensio	ons	37.5	(H) × (W) × (D) mm	130 (H)	× 34.5 (W) × 93.6 (D) mm	Δ	The dimensions are different.
	ght		0	.5kg		0.25kg	0	

#### (b) Function comparison

O: Compatible,  $\triangle$ : Partial change required,  $\times$ : Incompatible, —: Additional function

Item	AD61	A1SD62	Compati- bility	Precautions for replacement
Preset function	Changes the present counter value to a given value (initial value). The latch function is not provided for the AD61 memory. When the power is turned OFF, or when the CPU is reset, the AD61 memories (counter value, present value, set value and preset value) are initialized. Storing the present counter value into the data register of the CPU in continuous operation allows counting from the stored value in the next operation.	Changes the present counter value to a given value, which is a preset value. This function can be used to start the pulse count from the set value. There are two methods for the function, "preset in sequence program" and "preset by external control signal (applying voltage to external terminal)".	0	
Disable function	Turning ON the count enable signal shown in the PLC I/O signal assignment starts counting on the AD61. (CH1=Y14, CH2=Y1B). When voltage is applied to the DIS (disable) terminal of the external input terminal block, the AD61 stops counting. Therefore, counting can be started and stopped by external input regardless of the scan time.	Stops counting while the count enable command is OFF.	0	
Ring counter function	Automatically presets the value when the counter value and the set value are equal with the ring counter setting pin set to ON on the AD61 board. Used for cyclic control such as constant-rate feeding.	Repeats counting between the preset value and the ring counter value with the ring counter command. Used for control such as constant-rate feeding.	0	
Latch counter function	I	Latches the present value at the time a signal is input.	_	
Sampling counter function	_	Counts the pulse input at the sampling time set. The setting unit of the sampling time is 10ms, and the accuracy is less than 1 count.	_	
Periodic pulse counter function	_	Allows storing the present value and the previous value in the corresponding periodic pulse counter value areas at the specified intervals. The setting unit is 10ms, and the accuracy is less than 1 count.	_	
Coincidence output function	Outputs an ON/OFF signal, comparing the set value with the present value of the counter.	Outputs an signal when the specified counter value is matched with the present value of the counter as a result of comparison. Tow-point setting is available.	0	2 points can be set.

Modifying sequence programs is required as the I/O signals differ. For details of the I/O signals and sequence programs, refer to the user's manual.

	AD61				A1SD				
Device NO.	Signal name	Device NO.	Signal name	Device NO.	Signal name	Device NO.	Signal name		
X0	CH1 Counter value greater	Y0		X0	CH1 Counter value greater (point No.1)	Y0			
X1	CH1 Counter value matched	Y1		X1	CH1 Counter value matched (point No.1)	Y1			
X2	CH1 Counter value smaller	Y2		X2	CH1 Counter value smaller (point No.1)	Y2			
X3	CH1 External preset request detection	Y3		X3	CH1 External preset request detection	Y3			
X4	CH2 Counter value greater	Y4		X4	CH2 Counter value greater (point No.1)	Y4			
X5	CH2 Counter value matched	Y5		X5	CH2 Counter value matched (point No.1)	Y5			
X6	CH2 Counter value smaller	Y6		X6	CH2 Counter value smaller (point No.1)	Y6			
X7	CH2 External preset request detection	Y7	Not used	X7	CH2 External preset request detection	Y7	Not used		
X8		Y8		X8	CH1 Counter value greater (point No.2)	Y8			
X9		Y9		X9	CH1 Counter value matched (point No.2)	Y9			
XA		YA		ХА	CH1 Counter value smaller (point No.2)	YA			
XB		YB		ХВ	CH2 Counter value greater (point No.2)	YB			
XC		YC		XC	CH2 Counter value matched (point No.2)	YC			
XD		YD		XD	CH2 Counter value smaller (point No.2)	YD			
XE		YE		XE	Fuse/External power cutoff detection	YE			
XF		YF		XF		YF			
X10		Y10	CH1 Coincidence signal reset command	X10		Y10	CH1 Coincidence signal reset command CH1 Preset command		
X11		Y11	CH1 Preset command	X11		Y11			
X12		Y12	CH1 Coincidence signal output enable	X12		Y12	CH1 Coincidence signal output enable command		
X13		Y13	CH1 Down count command	X13		Y13	CH1 Down count command		
X14	Not used	Y14	CH1 Count enable	X14		Y14	CH1 Count enable command		
X15		Y15	CH1 Present value read request	X15		Y15	CH1 Count value read request		
X16		Y16	CH1 External preset detection reset command	X16		Y16	CH1 Counter function selection start command		
X17		Y17	CH2 Coincidence signal reset command	X17	Not used	Y17	CH2 Coincidence signal reset command		
X18		Y18	CH2 Preset command	X18		Y18	CH2 Preset command		
X19		Y19	CH2 Coincidence signal output enable	X19		Y19	CH2 Coincidence signal output enable command		
X1A		Y1A	CH2 Down count command	X1A		Y1A	CH2 Down count command		
X1B		Y1B	CH2 Count enable	X1B		Y1B	CH2 Count enable command		
X1C		Y1C	CH2 Present value read request	X1C		Y1C	CH2 Count value read request		
X1D		Y1D	CH2 External preset detection reset command	X1D		Y1D	CH2 Counter function selection start command		
X1E		Y1E	Not used	X1E		Y1E	Not used		
X1F		Y1F		X1F		Y1F			

W

R/W

R

(L)

(H)

(L)

(H)

(L)

(H)

(L)

(H)

(L)

(H)

#### (d) Buffer memory address comparison

Modifying sequence programs is required as the assignment of buffer memory differs. For details of the buffer memory and sequence programs, refer to the user's manual.

Add	ress	AD61		Add	ress	A1SD62		
CH1	CH2	Name	Read/Write	CH1	CH2	Name		Read/Write
1	33	Preset value write (Lower and middle)	w	1	33	Preset value setting	(L)	
(2)	(34)	Preset value write (Upper)	vv	2	34	Treset value setting	(H)	R/W
3	35	Mode register	R/W	3	35	Pulse input mode setting		
4	36	Present value read (Lower and middle)	R	4	36	Present value	(L)	R
(5)	(37)	Present value read (Upper)		5	37		(H)	ĸ
6	38	Set value read/write (Lower and middle)	R/W	6	38	Coincident output point setting	(L)	
(7)	(39)	Set value read/write (Upper)	FV/ VV	7	39	No.1	(H)	
Addrog	e in nar	entheses in the above table indicates that of	the upper	8	40	Counter function selection setting		R/W
	•	-bit data.	the upper	9	41	Sampling/Cycle setting 1 to 65535 [10ms increments]		

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command

Latch count value

Sampling count value

No.2

value

value

External preset detection reset command

Point No.2 coincidence signal reset

Coincident output point setting

Periodic pulse counter previous

Periodic pulse counter present

Sampling/Cycle counter

(for both CH1 and CH2)

# (2) Comparisons between AD61S1 and A1SD62

Iter	m	A	D61S1		A1SD62	Compati- bility	Precautions for replacement			
Occupied I/O po	ints	(I/O assigni	? points ment: special 32 points)	(I/O a	32 points (I/O assignment: special 32 points)					
Number of chan	nels		2		2	0				
Counting speed	switch setting		_	100k	10k	—				
	Phase	1-phase or 2-phase input		1	-phase or 2-phase input					
Count input signal	Signal level ( $\phi$ A, $\phi$ B)	5VDC 12VDC 24VDC	} 2 to 5mA	5VDC 12VDC 24VDC	} 2 to 5mA	0				
	Counting speed	1-phase input	10kPPS	100kPPS	10kPPS	0				
	(Max.)*	2-phase input	7kPPS	100kPPS	7kPPS					
	Counting range	24-t 0 to 1	ry format bit binary 6,777,215 Dec.)		24-bit binary 0 to 16,777,215	0				
	Туре		I preset counter ounter function		P/DOWN preset counter + Ring counter function	0				
	Minimum count pulse width (Input rise and fall time is 2.5µs or less for A1SD62 and 5µs for others. Duty ratio: 50%		50µs ise input) 42µs 71µs ise input) ise input)	10µs 5µs (1-phase and 2-phase inputs)	<u>100µs</u> <u>50µs</u> <u>71µs</u> 71µs (1-phase input) (2-phase input)	] 0				
Magnitude comparison	Comparison range		format (Bin.) 24 bits		- 0					
between CPU and AD61	Comparison result	Set value	< Count value = Count value > Count value	S	Set value < Count value Set value = Count value Set value > Count value					
	Preset		/DC 3/6mA DC 5mA		5/12/24VDC 2 to 5mA	0				
External input	Count disable		/DC 3/6mA DC 5mA		_	×	Enabled by function start terminal.			
	Function start		_		5/12/24VDC 2 to 5mA					
External output	Coincidence output	(open co	ansistor llector) output VDC 0.5A		ansistor (sink type) output VDC 0.5A/point 2A/common	0				
Internal current o (5VDC)	consumption		0.3A		0.1A	0				
External dimens	ions	37.	0 (H) × 5 (W) × (D) mm	130 (H	I) × 34.5 (W) × 93.6 (D) mm	Δ	The dimensions are different.			
Weight		(	0.5kg		0.25kg	0				

O: Compatible, △: Partial change required, ×: Incompatible, —: Additional function

#### (b) Function comparison

Item	AD61S1	A1SD62	Compati- bility	Precautions for replacement
Preset function	Changes the present counter value to a given value (initial value). The latch function is not provided for the AD61 memory. When the power is turned OFF, or when the CPU is reset, the AD61 memories (counter value, present value, set value and preset value) are initialized. Storing the present counter value into the data register of the CPU in continuous operation allows counting from the stored value in the next operation.	Changes the present counter value to a given value, which is a preset value. This function can be used to start the pulse count from the set value. There are two methods for the function, "preset in sequence program" and "preset by external control signal (applying voltage to external terminal)".	0	
Disable function	Turning ON the count enable signal shown in the PLC I/O signal assignment starts counting on the AD61. (CH1 =Y14, CH2=Y1B) When voltage is applied to the DIS (disable) terminal of the external input terminal block, the AD61 stops counting. Therefore, counting can be started and stopped by external input regardless of the scan time.	Stops counting while the count enable command is OFF.	0	
Ring counter function	Automatically presets the value when the counter value and the set value are equal with the ring counter setting pin set to ON on the AD61 board. Used for cyclic control such as constant-rate feeding.	Repeats counting between the preset value and the ring counter value with the ring counter command. Used for control such as constant-rate feeding.	0	
Ring counter function	_	Latches the present value at the time a signal is input.	_	
Sampling counter function	_	Counts the pulse input at the sampling time set. The setting unit of the sampling time is 10ms, and the accuracy is less than 1 count.	_	
Periodicpulse counter function	_	Allows storing the present value and the previous value in the corresponding periodic pulse counter value areas at the specified intervals. The setting unit is 10ms, and the accuracy is less than 1 count.	_	
Coincidence output function	Outputs an ON/OFF signal, comparing the set value with the present value of the counter.	Outputs an signal when the specified counter value is matched with the present value of the counter as a result of comparison. Two-point setting is available.	0	2 points can be set.

Modifying sequence program is required as the I/O signals differ. For details of the I/O signals and sequence programs, refer to the user's manual.

	A	D61S1			A15	SD62			
Device NO.	Signal name	Device NO.	Signal name	Device NO.	Signal name	Device NO.	Signal name		
X0	CH1 Counter value greater	Y0		X0	CH1 Counter value greater (point No.1)	Y0			
X1	CH1 Counter value matched	Y1		X1	CH1 Counter value matched (point No.1)	Y1			
X2	CH1 Counter value smaller	Y2		X2	CH1 Counter value smaller (point No.1)	Y2			
Х3	CH1 External preset request detection	Y3		Х3	CH1 External preset request detection	Y3			
X4	CH2 Counter value greater	Y4		X4	CH2 Counter value greater (point No.1)	Y4			
X5	CH2 Counter value matched	Y5		X5	CH2 Counter value matched (point No.1)	Y5			
X6	CH2 Counter value smaller	Y6		X6	CH2 Counter value smaller (point No.1)	Y6			
X7	CH2 External preset request detection	Y7	Not used	X7	CH2 External preset request detection	Y7	Not used		
X8		Y8		X8	CH1 Counter value greater (point No.2)	Y8			
X9		Y9		X9	CH1 Counter value matched (point No.2)	Y9			
XA		YA		ХА	CH1 Counter value smaller (point No.2)	YA			
ХВ		YB		ХВ	CH2 Counter value greater (point No.2)	YB			
XC		YC		XC	CH2 Counter value matched (point No.2)	YC			
XD		YD		XD	CH2 Counter value smaller (point No.2)	YD			
XE		YE		XE	Fuse/External power cutoff detection flag	YE			
XF		YF		XF		YF			
X10		Y10	CH1 Coincidence signal reset command	X10		Y10	CH1 Coincidence signal reset command		
X11		Y11	CH1 Preset command	X11		Y11	CH1 Preset command		
X12		Y12	CH1 Coincidence signal output enable	X12		Y12	CH1 Coincidence signal output enable command		
X13	Not used	Y13	CH1 Down count command	X13		Y13	CH1 Down count command		
X14		Y14	CH1 Count enable	X14		Y14	CH1 Count enable command		
X15		Y15	CH1 Present value read request	X15		Y15	CH1 Count value read request		
X16		Y16	CH1 External preset detection reset command	X16		Y16	CH1 Counter function selection start command		
X17		Y17	CH2 Coincidence signal reset command	X17	Not used	Y17	CH2 Coincidence signal reset command		
X18		Y18	CH2 Preset command	X18		Y18	CH2 Preset command		
X19		Y19	CH2 Coincidence signal output enable	X19		Y19	CH2 Coincidence signal output enable command		
X1A		Y1A	CH2 Down count command	X1A		Y1A	CH2 Down count command		
X1B		Y1B	CH2 Count enable	X1B		Y1B	CH2 Count enable command		
X1C		Y1C	CH2 Present value read request	X1C		Y1C	CH2 Count value read request		
X1D		Y1D	CH2 External preset detection reset command	X1D		Y1D	CH2 Counter function selection start command		
X1E		Y1E	Not used	X1E		Y1E	Not used		
X1F		Y1F		X1F		Y1F			

(L)

(H)

(L)

(H)

R

Periodic pulse counter previous value

Periodic pulse count present value

Sampling/Cycle counter

(for both CH1 and CH2)

#### (d) Buffer memory address comparison

Modifying sequence programs is required as the assignment of buffer memory differs. For details of the buffer memory and sequence programs, refer to the user's manual.

Add	ress	AD61S1		Add	ress	A1SD62			
CH1	CH2	Name	Read/Write	СН1	CH2	Name		Read/Write	
1	33	Preset value write (Lower and middle)	w	1	33		(L)		
(2)	(34)	Preset value write (Upper)	vv	2	34	Preset value setting	(H)	R/W	
3	35	Mode register	R/W	3	35	Pulse input mode setting		1	
4	36	Present value read (Lower and middle)	R	4	36	Present value	(L)	R	
(5)	(37)	Present value read (Upper)	ĸ	5	37		(H)	ĸ	
6	38	Set value read/write (Lower and middle)	R/W	6	38	Coincident output point setting No.1	(L)		
(7)	(39)	Set value read/write (Upper)	F(/ ) V	7	39	Concident output point setting No. 1	(H)		
Addre	ss in pare	entheses in the above table indicates that of the upp	er 8 bits in	8	40	Counter function selection setting		R/W	
the 24	-bit data.			9	41	Sampling/Cycle setting 1 to 65535 [10ms increments]			
				10	42	External preset detection reset command		w	
				11	43	Point No.2 coincidence signal reset comm	and	vv	
				12	44	Coincident output point patting No 2	(L)	R/W	
				13	45	Coincident output point setting No.2	(H)	R/W	
				14	46	Latch count value	(L)		
				15	47		(H)		
				16	48	Sampling count value	(L)		
				17	49	Sampling count value	(H)		

18 50

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O: Compatible,  $\ \ \bigtriangleup$  : Partial change required,  $\ \times$  : Incompatible

# 10.2.5 Position detection module comparison

#### (1) Comparisons between A62LS-S5 and A1S62LS

		Specifications												Precautions
	Item		ļ	A62LS-S5					ļ	A1S62LS			Compati- bility	for replacement
	nber of ectable axes						1						0	
Pos met	ition detection hod				Ab	solute p	ositi	on detectior	ı				0	
Res	olution	Multiturn type:           [4096 × 32 turns] to [409.6 × 320 turns]           Linear type:           0.0039062mm/256mm to 0.015625mm/1024mm									5]	0		
	Number of programs	9 _	-	Program 1	nen power is			9		Program I	ien power is		0	
-	Number of multi-dogs (dog/CH.)						10					,	0	
utput function	Number of output channels (CH.)	For limit switch output function only: 16/program For limit switch output and positioning functions : 16/program										0		
Limit switch output function	Data setting method	Setting Value Setting Method Sequence program Key input at VS-62	Function Setting Parameter	Multi-Dog O O	Program No. O (Possible only in PROGRAM mode)	Limit SW Output Enabled		Setting Value Setting Method Sequence program Key input at VS-T62	Function Setting Parameter	Multi-Dog O	Program No.	Limit SW Output Enabled	Δ	The setting cannot be made by teaching.
	Control	Teaching (Cosson of any in PROGRAM mode)												
	method				U	nidirectio	onal	positioning					0	
	Target position setting method			e program :		tion		1-point setting prior to positioning operation (sequence program setting, or key input setting at VS-T62)					0	
	Max. number of positioning points					1	poi	nt					0	
Positioning function	Number of registered positioning pattern data						2						0	
Position	Number of positioning signal output channels					8 c	han	nels					0	
	Data setting	Setting Setting Method	Initial Set	ting/Parameter	Target Stop F	Position		Setting Va Setting Method		ıg/Parameter	Target Stop	Position	0	
	method	Sequence program Key input setting at A62LS-S5		0	O (Possible o PROGRAM	nly in mode)		Sequence program Key input at VS-T62						

Item		Specifications			Precautions for
		A62LS-S5	A1S62LS	bility	replacement
	imum position ing unit	0.00001		0	
valu	rent position ue setting ction	Current position value setting, Current position value preset setting		0	
	G operation ction	JOG operation executed by JOG FWD/RVS signal inputs.		0	
San	npling time	1 ms		0	
Response time	Limit SW output signal & positioning output signal	2 ms		0	
Res	Current value output				
me	Speed output	Depends on the parameter setting, 4, 8, 16, 32 or 64		0	
Gate time (ms)	Rotation speed output	117		0	
	nber of upied I/O nts	48 points (I/O assignment: empty 16 + special 32 points)	32 points (I/O assignment: special 32 points)	Δ	Change the start I/O No. by PLC parameter.
	rnal current sumption DC)	1.5A	0.55A	0	
	ernal ensions	250(H)×75(W)×121(D)mm	130(H)×34(W)×93(D)mm	Δ	The dimensions are different.
	Weight	1.1kg	0.5kg	0	

O: Compatible,  $\ \ \bigtriangleup$  : Partial change required,  $\ \times$  : Incompatible

#### (b) External input/output specifications comparison

1) Input specifications

#### O: Compatible, $\ \ \bigtriangleup$ : Partial change required, $\ \times$ : Incompatible

ltem		A62LS-S5	A1S62LS	Compati- ibility	Precautions for replacement	
Number of input points		Current position value preset input: 2 points				
Isolation method		Photo-coupler				
Rated input voltage		12/24VDC				
Rated input current		4/10 mA				
Operating input voltage range		10.2 to 30 VDC		0		
ON voltage		10 VDC or more				
OFF voltage		4VDC or more				
Response	OFF→ ON	0.04 ms (At input voltage of 24V)		0		
time	$ON \rightarrow OFF$	0.2 ms (At input voltage of 24V)		0		
Common connections		1 common for 2 points (common terminal: TB19)	1 common for 2 points (common terminal: B1, B2)		Wiring must be changed.	
External cable connection		20-point terminal block connector (M3×6 screws)	24-pin connector	×		
Applicable wire size		0.75 to 2mm <sup>2</sup>	0.3mm <sup>2</sup>			

#### 2) Output specifications

$O\colon \textsc{Compatible},\ \ \underline{\wedge}$ : Partial change required, $\ \textbf{x} \colon \textsc{Incom}$	npatible
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Item		A62L	.S-S5	A1S	62LS	Compati- bility	Precautions for replacement	
Number of output points		For limit SW output function	16 points	For current value detection function only	Not output	0		
		only For limit SW output and positioning functions Limit SW output: 8 points Positioning signal output: 8 points	Limit SW	For limit SW output function only	16 points	0		
			For limit SW output and positioning functions	Limit SW output: 8 points Positioning signal output: 8 points	0			
Isolation method			Photo-	coupler		0		
Rated load voltage			12/24	4VDC		0		
Operating load voltage range			10.2 to	30VDC		0		
Max. load current			100	)mA		0		
Max. inrush current			0.4 A		0			
Current leakage when OFF			0.1mA	or less		0		
Max. voltage drop when ON		0.5V (at	100 mA)	1.0V (at 50 mA)			Check the external device specifications as the external output specifications differ.	
Response	$OFF \rightarrow ON$	0.04ms (when load current is 100 mA)		1ms (when load current is 50 mA)		Δ		
time	$ON \rightarrow OFF$	0.2ms (when load current is 100 mA)		1ms (when load current is 50 mA)				
Common connections			or 16 points minal: TB20)	1 common for 16 points (common terminal: A1, A2)			Wiring must be	
External cable connection		(M3×6	l block connector screws)	24-pin connector × Wiring must changed.		Wiring must be changed.		
Applicable wire size		0.75 to	2mm <sup>2</sup>	0.3	mm <sup>2</sup>			

#### (c) Comparison of I/O signals for PLC CPU

Modifying sequence programs is required as the I/O signals differ. For details of the I/O signals and sequence programs, refer to the user's manual.

	A62LS	-S5		A1S62LS										
Device NO.	Signal name	Device NO.	Signal name	Device NO.	Signal name			name	Device NO.	Signal name				
				X0	W	DT error								
						X1	A	S62LS opera	tion :	status				
					X2	'U	pper limit over	trave	el' detection					
				X3	'Lo	ower limit over	trave	el' detection	-					
				X4		ensor error det								
				X5		cessive corre	ectior	n amount'						
				X6		ccessive curre	ent po	osition change'						
				X7	Er	ror detection								
	X0 to Not used		to Not used	X8		Channel 0		Channel 8 (FWD or FWD/ low-speed)						
X0 to		Y0		Not used	Х9		Channel 1	functions	Channel 9 (RVS or RVS/ low-speed)	Y0 to	Not used			
XF	Not used	YF			Not used	Not used	Not used	Not used	Not used	XA only	Channel 2	tioning	Channel 10 (High-speed)	YF
				ХВ	output function only	Channel 3	For joint use of limit SW output and positioning functions	Channel 11 (Low- or medium- speed)						
				хс	For limit SW out	Channel 4	nit SW ou	Channel 12 (Brake release)						
				XD		Channel 5	e of lir	Channel 13 (In-position)						
				XE		Channel 6	or joint us	Channel 14 (Positioning in progress)						
						Channel 7	Ľ	Channel 15 (Operation error)						

# 10 special function module replacement

## MELSEC

				A62LS-	S5			A1S62LS		
Device NO.		Siç	gnal	name	Device NO.	Signal name	Device NO.	Signal name	Device NO.	Signal name
X10	W	DT erro	or		Y10				Y10	PLC ready
X11		2LS-S tus	5 ope	eration	Y11				Y11	Positioning START (leading edge detection)
X12		oper lin tection		vertravel"	Y12				Y12	Positioning STOP (leading edge detection)
X13		ower lin tection		vertravel"	Y13				Y13	FWD (forward) JOG (operation occurs during ON)
X14	Se	nsor ei	rror c	detection	Y14				Y14	RVS (reverse) JOG (operation occurs during ON)
X15		cessiv		rrection ction	Y15				Y15	Limit SW output enabled
X16		cessiv		rrent ge" detection	Y16				Y16	Error reset
X17	Err	or dete	ectio	n	Y17				Y17	Current position preset 1 disabled Current position preset command (leading edge detection)
X18		Channel 0		Channel 8 (FWD or FWD/low- speed)	Y18				Y18	Current position preset 2 disabled
X19		Channel 1		Channel 9 (RVS or RVS/low- speed)	Y19	Not used	X10 to X1F	Not used	Y19	Mode lock
X1A	Ŋ	Channel 2	ning functions	Channel 10 (High- speed)	Y1A					
X1B	utput function only	Channel 3	For joint use of limit SW output and positioning functions	Channel 11 (Low- or medium- speed)	Y1B					
X1C	For limit SW out	Channel 4	of limit SW out	Channel 12 (Brake release)	Y1C				Y1A	Not used
X1D	Fo	Channel 5	For joint use c	Channel 13 (In-position)	Y1D				to Y1F	Not used
X1E		Channel 6		Channel 14 (Positioning in progress)	Y1E					
X1F		Channel 7		Channel 15 (Operation error)	Y1F					

	A62LS-S5								
Device NO.	Signal name	Device NO.	Signal name						
		Y20	PLC ready						
		Y21	Positioning START (leading edge detection)						
		Y22	Positioning STOP (leading edge detection)						
		Y23	FWD (forward) JOG (operation occurs during ON)						
X20	Not used	Y24	RVS (reverse) JOG (operation occurs during ON)						
to X2F		Y25	Limit SW output enabled						
		Y26	Error reset						
		Y27	Current position preset 1 disabled Current position preset command (leading edge detection)						
		Y28	Current position preset 2 disabled						
		Y29 to Y2F	Not used						

## (d) Buffer memory address comparison

No special differences are identified.

## 10.2.6 Positioning module comparison

## (1) Comparisons between AD70 and A1SD70

(a) Performance specifications comparison (Compatible for other than items shown in the following list.)

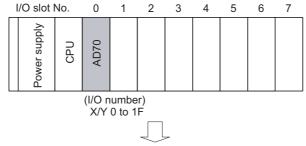
$\bigcirc$ : Compatible, $\triangle$ : Partial change required, $\times$ : Incompatible
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Item		AD70	A1SD70	Compati- bility	Precautions for replacement
Number of I/O points		32 points (I/O assignment: 32 special-purpose points)	48 points (I/O assignment: 16 empty points + 32 special-purpose points)	Δ	Change the start I/O No. by PLC parameter.*1
Number of I/C	) slots	1 slot occupied			Since 1 more slot is occupied, the number of base slots must be reviewed. <sup>*1</sup>
Connector for external		9-pin connector	9-pin connector	0	
connection	For drive module connection	15-pin connector	15-pin connector	0	
	Voltage	+15VDC/-15VDC	+15VDC/-15VDC	0	
External power	Current	+15VDC***0.2A/-15VDC***0.02A	+15VDC0.2A/-15VDC0.2A	0	
supply	Terminal screw size	M4 screw	M3.5 screw	×	Wiring must be changed.
Internal current consumption		5VDC 0.3A	5VDC 0.3A	0	
External dime	entions	250(H) × 37.5(W) × 119(D) (mm)	130(H)×69.5(W)×93.6(D) (mm)	Δ	The dimensions are different.
Weight		0.5kg	0.4kg	0	

\*1 Since 2 slots are occupied for A1SD70, the device NO. differs from that of AD70. Therefore, the existing program must be modified. In the parameter I/O assignment of GX Developer, setting the first slot of A1SD70 to "0 empty point" enables reuse of the existing program. I/O assignment setting is recommended.

I/O assignment for AD70

When AD70 is set to I/O slot No.0 of main base



#### I/O assignment for A1SD70

When A1SD70 is set to I/O slot No.0 and 1 of main base

/O slot I	No.	0	1	2	3	4	5	6	7	_
Power supply	CPU	A1S	D70							

#### (I/O number)

	Slot 0	Slot 1
Without I/O	16 points	32 points
assignment	(X/Y 0 to F)	(X/Y 10 to 2F)
With I/O	0 point	32 points
assignment	(-)	(X/Y 0 to 1F)

### (b) Function comparison

No special differences are identified.

## (c) Comparison of I/O signals for PLC CPU

Compatibility is supported in the setting marked.\*1

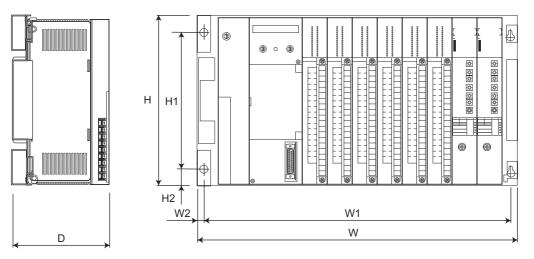
		AD70	oned in the setting mark	1		A1SD70	
Device NO.	Signal	Device NO.	Signal	Device NO.	Signal	Device NO.	Signal
X0	WDT error, H/W error	Y0				Y0	
X1	AD70 ready	Y1				Y1	
X2	Zero return request	Y2				Y2	
X3	Zero return completion	Y3				Y3	
X4	BUSY	Y4				Y4	
X5	Positioning completion	Y5				Y5	
X6	In-position	Y6		X0		Y6	
X7	Servo error excessive	Y7	Not used	to	Not used	Y7	
X8	Error detection	Y8		XF		Y8	
X9	Overflow	Y9				Y9	
XA XB	Underflow Servo ready	YA YB				YA YB	
XC	Near-zero point dog	YC				YC	
XD	Stop (external stop signal)	YD				YD	
XE	Upper limit LS	YE				YE	
XF	Lower limit LS	YF				YF	
		Y10	Zero return start	X10	WDT error, H/W error	Y10	
		Y11	Absolute positioning start	X11	A1SD70 ready	Y11	
		Y12	Forward start (in the incremental mode and velocity/position control switchover mode) Reverse start	X12	Zero return request	Y12	Not used
X10		Y13	(in the incremental mode and velocity/position control switchover mode)	X13	Zero return completion	Y13	
to X1C	Not used	Y14	Forward JOG start	X14	BUSY	Y14	
XIC		Y15	Reverse JOG start	X15	Positioning completion	Y15	
		Y16	Velocity/position mode re-start	X16	In-position	Y16	
		Y17	Stop	X17	Servo error excessive	Y17	
		Y18 Y19	Error reset Overflow reset	X18 X19	Error detection Overflow	Y18 Y19	
		Y1A		X19 X1A	Underflow	Y1A	
		Y1B	Underflow reset Not used	X1A X1B	Servo ready	Y1B	
		Y1C	Velocity/position switchover	X1D X1C		Y1C	
	·Used only when AD70 is	Y1D	enabled	X1D	Near-zero point dog Stop	Y1D	
X1D	used as remote I/O		PLC ready		(external stop signal)		
to X1F	<ul> <li>station.</li> <li>Interlock signals for</li> </ul>	Y1E	Not used	X1E	Upper limit LS	Y1E	
	PFRP, RTOP instructions	Y1F	Not used	X1F	Lower limit LS	Y1F	-
						Y20	Zero return start
						Y21 Y22	Absolute positioning start Forward start (in the incremental mode and velocity/position control switchover mode)
						Y23	Reverse start (in the incremental mode and velocity/position control switchover mode)
				X20		Y24	Forward JOG start
				to	Not used	Y25 Y26	Reverse JOG start
				X2F		Y26 Y27	Velocity/position mode re-start Stop
						Y28	Error reset
						Y29	Overflow reset
						Y2A	Underflow reset
						Y2B	Not used
						Y2C	Velocity/position switchover enabled
						Y2D	PLC ready
						Y2E Y2F	Not used

## (d) Buffer memory address comparison

No special differences are identified.

## **1** EXTERNAL DIMENSIONS

# 11.1 Large-sized A/QnA Series External Dimensions and Mounting Dimensions



Unit: mm

Base		Dimensions			Dimensions for mounting				
unit	Н	w	<b>D</b> <sup>*1</sup>	H1	H2	W1	W2		
A32B		247				227			
A32B-S1		268				248			
A35B		382				362			
A38B		480				460			
A38HB		480				460			
A38HBEU		480				460			
A32RB		494	130*2	200		474			
A33RB	250	570			25	550	10		
A37RHB	250	497			25	477			
A52B		183				163			
A55B		297				277			
A58B		411				391			
A62B		238				218			
A65B		352				332			
A68B		466				446			
A68RB		522				502			

\*1 D (depth) varies depending on the module to be mounted. Therefore, confirm the external dimensions of each module.

\*2 For mounting A series module of which depth is 121mm.

260

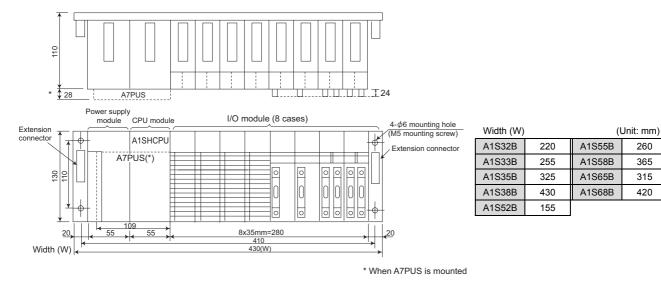
365

315

420

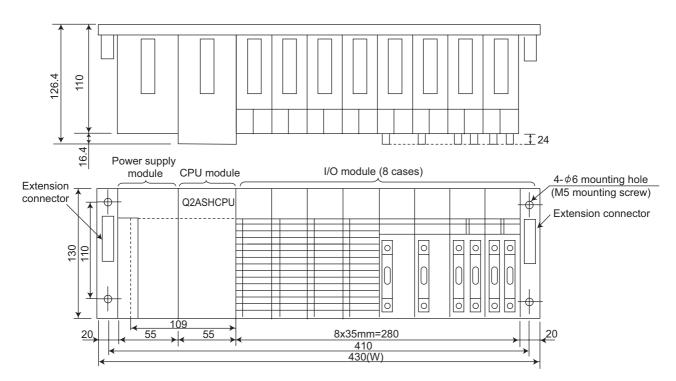
## 11.2 Small-sized AnS/Q2AS Series External Dimensions and **Mounting Dimensions**

## (1) A1SH, A2SH, A2AS, A2AS-S1, A2USH-S1



\* A memory cassette is installed from the front face for A2AS(-S1) and A2USH-S1 and the left face for A1SH and A2SH.

## (2) Q2AS



Width (W)								(Unit: mm)
A1S32B	A1S33B	A1S35B	A1S38B · A1S38HB · A1S38HBEU	A1S52B	A1S55B	A1S58B	A1S65B	A1S68B
220	255	325	430	155	260	365	315	420
					* For the	base unit, A1S	38B is used as	an example.

## **APPENDICES**

## Appendix 1 Spare Parts Storage

(1) The general specifications of PLCs are as follows. Please do not store spare parts under a high temperature or high humidity condition, even within the range guaranteed by the specifications.

Storage ambient temperature	-20 to 75°C
Storage ambient humidity	10 to 90%, no condensation

- (2) Store in a place avoiding direct sunlight.
- (3) Store under a condition with no dust or corrosive gas.
- (4) The battery capacity of a A6BAT battery or a lithium-coin battery (commercially available) for memory card will be decreased by its self-discharging even when it is not used. Replace it with new one in 5 years as a guideline.
- (5) Among power supply modules or CPU modules with built-in power supply that use any aluminum electrolytic capacitor, the characteristics of the modules listed below will be deteriorated if they are left un-energized for a long time. Therefore, take the following measures.

Product	Model
CPU module (Power supply module built-in type)	A1NCPU, A1NCPUP21, A1NCPUR21, A1NCPUP21-S3 A2CCPU, A2CCPUP21, A2CCPUR21, A2CCPUC24 A2CCPUC24-PRF
Power supply module	A61P, A61PEU, A61P-UL, A62P, A62PEU, A63P, A68P A61RP, A67RP, A2CJ66P

[Measures for preventing aluminum electrolytic capacitor characteristics deterioration] Once every 2 or 3 years, increase the voltage gradually from 0V to the rated voltage spending 10 minutes or more and keep the voltage for several hours to activate the aluminum electrolytic capacitor. Or, rotate products at the periodic inspection (in every 1 to 2 years).

[Reference]

The life of an aluminum electrolytic capacitor, even if not used and under a normal temperature, decreases approximately 4 times slowly than the case when it is energized.

## Appendix 2 Related Manuals

## Appendix 2.1 Replacement Handbooks

No.	Manual name	Manual No.	Model name code
1	Transition from MELSEC-A/QnA (Large Type) Series to Q Series Handbook (Fundamentals)	L-08043ENG	_
2	Transition from MELSEC-A/QnA (Large Type) Series to Q Series Handbook (Intelligent Function Modules)	L-08046ENG	_
3	Transition from MELSEC-A/QnA (Large Type) Series to Q Series Handbook (Network Modules)	L-08048ENG	_
4	Transition from MELSEC-A/QnA (Large Type) Series to Q Series Handbook (Communications)	L-08050ENG	_
5	Transition from MELSEC-A0J2H Series to Q Series Handbook	L-08060ENG	_
6	Transition from MELSECNET/MINI-S3,A2C(I/O) to CC-Link Handbook	L-08061ENG	—
7	Transition from MELSEC-I/OLINK to CC-Link/LT Handbook	L-08062ENG	_
8	Transition from MELSEC-A/QnA Large Type Series to AnS/Q2AS Small Type Series Handbook	L-08064ENG	_

## Appendix 2.2 Large-sized A/QnA Series

No.	Manual name	Manual No.	Model name code
1	MELSEC-A/QnA Catalog	L-08033E	—
2	MELSEC-A/QnA Data Book	L-08029E	
3	Type A1N/A2N(S1) / A3NCPU User's Manual	IB-66543	13JE83
4	Type A2A (S1) / A3ACPU User's Manual	IB-66544	13JE84
5	Type A2U (S1) / A3U/A4UCPU User's Manual	IB-66436	13JE25
6	Q2ACPU (S1) / Q3ACPU/Q4ACPU User's Manual	IB-66608	13J821
7	Type A2CCPU (P21/R21), A2CCPU-DC24V, A2CCPUC24 (-PRF), A2CJCPU User's Manual	IB-66545	13JE85
8	Type ACPU/QCPU-A (A Mode) (Fundamentals) Programming Manual	IB-66249	13J740
9	Type ACPU/QCPU-A (A Mode) (Common Instructions) Programming Manual	IB-66250	13J741
10	Type AnSHCPU/AnACPU/AnUCPU/QCPU-A (A Mode) Programming Manual (Dedicated Instructions)	IB-66251	13J742
11	Type AnACPU/AnUCPU/QCPU-A (A Mode) Programming Manual (PID Control Instructions)	IB-66258	13J744
12	Type MELSAP-II(SFC) Programming Manual	IB-66361	13JF40
13	QnACPU Programming Manual (Fundamentals)	IB-66614	13JF46
14	QnACPU Programming Manual (Special Function Module)	SH-4013	13JF56
15	QCPU(Q Mode)/QnACPU Programming Manual (Common Instructions)	SH-080039	13JF58
16	QCPU(Q Mode)/QnACPU Programming Manual (PID Control Instructions)	SH-080040	13JF59
17	QCPU(Q Mode)/QnACPU Programming Manual (SFC)	SH-080041	13JF60
18	I/O module type Building block User's Manual	IB-66140	13J643
19	A/D converter module type A68AD User's Manual	IB-66054	13J607
20	A/D converter module type A68AD-S2 User's Manual	IB-66213	13J647
21	Analog-Digital Converter Module type A68ADN User's Manual	IB-66307	13J668
22	Analog-Digital Converter Module type A616AD User's Manual	IB-66171	13J645
23	D/A converter module type A62DA User's Manual	IB-66053	13J608
24	D/A converter module type A62DA-S1 User's Manual	IB-66177	13J648
25	Digital-Analog Converter Module type A68DAV/DAI(S1) User's Manual	IB-66285	13J667
26	Digital-Analog Converter Module type A616DAV User's Manual	IB-66172	13J650
27	Digital-Analog Converter Module type A616DAI User's Manual	IB-66173	13J651
28	Pt100 input module type A68RD3/4 User's Manual	IB-66308	13J670
29	Type A68RD3N/4N, A1S62RD3N/4N Pt100 Input Module User's Manual	SH-080193	13JR46
30	Temperature-Digital Converter Module type A616TD User's Manual	IB-66174	13J654
31	High speed counter module type AD61(-S1) User's Manual	IB-66052	13J610
32	Positioning module type AD70 User's Manual	IB-66309	13J663
33	Positioning Module Type AD72 User's Manual	IB-66095	13J622
34	A1SD75P1-S3/P2-S3/P3-S3/AD75P1-S3/P2-S3/P3-S3 Positioning Module User's Manual	IB-66716	13J871
35	Positioning module type A1SD75M1/M2/M3, AD75M1/M2/M3 User's Manual	IB-66715	13J870
36	Type MELSECNET, MELSECNET/B Data Link System Reference Manual	IB-66350	13JF70
37	Control & Communication Link System Master/Local Module Type AJ61BT11/A1SJ61BT11 User's Manual	IB-66721	13J872
38	For A Ethernet Interface Module User's Manual	SH-080192	13JR45
39	For QnA Ethernet Interface Module User's Manual	SH-080146	13JR33
40	Computer Link Module (Com.link func./Print. func.) User's Manual	SH-3511	13JF77
41	Serial Communications Module User's Manual (Modem Function Additional Version)	SH-66612	13J825
42	Intelligent Communication Module type AD51-S3 User's Manual	IB-66189	13J655

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No.	Manual name	Manual No.	Model name code
43	Intelligent communication module type AD51H-S3 User's Manual	IB-66401	13JE16
44	MELSECNET/MINI-S3 Master Module Type AJ71PT32-S3, AJ71T32-S3, A1SJ71PT32-S3, A1SJ71T32-S3 User's Manual	SH-66565	13JE64
45	MELSEC-I/O LINK Remote I/O System Master Module type AJ51T64/ A1SJ51T64 User's Manual	SH-66574	13J748
46	Type MELSECNET/10 Network system (PLC to PLC network) Reference Manual	IB-66440	13JE33
47	For QnA/Q4AR MELSECNET/10 Network System Reference Manual	IB-66690	13JF78
48	Control & Communication Link System Master/Local Module type AJ61QBT11/A1SJ61QBT11 User's Manual	IB-66722	13J873
49	Positioning Module Type AD71(S1/S2/S7)/A1SD71-S2(S7) User's Manual	IB-66563	13JE98
50	PC fault detection module type AS91, A1SS91, A0J2-S91 User's Manual	IB-66626	13J828

## Appendix 2.3 Small-sized AnS/Q2AS Series

No.	Manual name	Manual No.	Model name code
1	Type A2USHCPU-S1 User's Manual	IB-66789	13JL30
2	Model Q2AS (H) CPU (S1) User's Manual	SH-3599	13J858
3	Type ACPU/QCPU-A (A Mode) Programming Manual (Fundamentals)	IB-66249	13J740
4	Type ACPU/QCPU-A (A Mode) Programming Manual (Common Instructions)	IB-66250	13J741
5	Type AnSHCPU/AnACPU/AnUCPU/QCPU-A (A Mode) Programming Manual (Dedicated Instructions)	IB-66251	13J742
6	Type AnACPU/AnUCPU/QCPU-A (A Mode) Programming Manual (PID Control Instructions)	IB-66258	13J744
7	Type MELSAP-II (SFC) Programming Manual	IB-66361	13JF40
8	QnACPU Programming Manual (Fundamentals)	IB-66614	13JF46
9	QnACPU Programming Manual (Special Function Module)	SH-4013	13JF56
10	QCPU (Q Mode)/QnACPU Programming Manual (Common Instructions)	SH-080039	13JF58
11	QCPU (Q Mode)/QnACPU Programming Manual (PID Control Instructions)	SH-080040	13JF59
12	QCPU (Q Mode)/QnACPU Programming Manual (SFC)	SH-080041	13JF60
13	AnS Module type I/O User's Manual	IB-66541	13JE81
14	Analog-Digital Converter Module type A1S68AD User's Manual	IB-66576	13J757
15	D/A converter module type A1S62DA User's Manual	IB-66335	13J673
16	Thermocouple input module type A1S68TD User's Manual	IB-66571	13J781
17	Digital-Analog Converter Module type A1S68DAV/DAI User's Manual	IB-66587	13J810
18	Pt100 input module type A1S62RD3/4 User's Manual	IB-66338	13J675
19	High speed counter module type A1SD62, A1SD62E, A1SD62D(S1) User's Manual	IB-66593	13J816
20	Positioning module type A1SD70 User's Manual	IB-66367	13JE04
21	Positioning module type A1SD75M1/M2/M3, AD75M1/M2/M3 User's Manual	IB-66715	13J870
22	A1SD75P1-S3/P2-S3/P3-S3, AD75P1-S3/P2-S3/P3-S3 Positioning Module User's Manual	IB-66716	13J871
23	Type A1S62LS User's Manual	IB-66647	13J837
24	Type A1SD51S Intelligent communication module User's Manual	IB-66551	13JE90

## Appendix 2.4 Programming Tool

No.	Manual name	Manual No.	Model code
1	GX Developer Version 8 Operating Manual	SH-080373E	13JU41
2	GX Developer Version 8 Operating Manual (SFC)	SH-080374E	13JU42
3	GX Simulator Version 7 Operating Manual	SH-080468ENG	13JU51
4	Type SW4IVD-GPPA (GPP) Operating Manual	IB-66855	13JL62

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## WARRANTY

Please confirm the following product warranty details before using this product.

#### 1. Gratis Warranty Term and Gratis Warranty Range

If any faults or defects (hereinafter "Failure") found to be the responsibility of Mitsubishi occurs during use of the product within the gratis warranty term, the product shall be repaired at no cost via the sales representative or Mitsubishi Service Company. However, if repairs are required onsite at domestic or overseas location, expenses to send an engineer will be solely at the customer's discretion. Mitsubishi shall not be held responsible for any re-commissioning, maintenance, or testing on-site that involves replacement of the failed module.

#### [Gratis Warranty Term]

The gratis warranty term of the product shall be for one year after the date of purchase or delivery to a designated place. Note that after manufacture and shipment from Mitsubishi, the maximum distribution period shall be six (6) months, and the longest gratis warranty term after manufacturing shall be eighteen (18) months. The gratis warranty term of repair parts shall not exceed the gratis warranty term before repairs.

### [Gratis Warranty Range]

- The range shall be limited to normal use within the usage state, usage methods and usage environment, etc., which follow the conditions and precautions, etc., given in the instruction manual, user's manual and caution labels on the product.
   Even within the gratis warranty term, repairs shall be charged for in the following cases.
  - Failure occurring from inappropriate storage or handling, carelessness or negligence by the user. Failure caused by the user's hardware or software design.
  - 2. Failure caused by unapproved modifications, etc., to the product by the user.
  - 3. When the Mitsubishi product is assembled into a user's device, Failure that could have been avoided if functions or structures, judged as necessary in the legal safety measures the user's device is subject to or as necessary by industry standards, had been provided.
  - 4. Failure that could have been avoided if consumable parts (battery, backlight, fuse, etc.) designated in the instruction manual had been correctly serviced or replaced.
  - 5. Failure caused by external irresistible forces such as fires or abnormal voltages, and Failure caused by force majeure such as earthquakes, lightning, wind and water damage.
  - 6. Failure caused by reasons unpredictable by scientific technology standards at time of shipment from Mitsubishi.
  - 7. Any other failure found not to be the responsibility of Mitsubishi or that admitted not to be so by the user.

#### 2. Onerous repair term after discontinuation of production

- (1) Mitsubishi shall accept onerous product repairs for seven (7) years after production of the product is discontinued.
- Discontinuation of production shall be notified with Mitsubishi Technical Bulletins, etc.
- (2) Product supply (including repair parts) is not available after production is discontinued.

#### 3. Overseas service

Overseas, repairs shall be accepted by Mitsubishi's local overseas FA Center. Note that the repair conditions at each FA Center may differ.

#### 4. Exclusion of loss in opportunity and secondary loss from warranty liability

Regardless of the gratis warranty term, Mitsubishi shall not be liable for compensation of damages caused by any cause found not to be the responsibility of Mitsubishi, loss in opportunity, lost profits incurred to the user by Failures of Mitsubishi products, special damages and secondary damages whether foreseeable or not, compensation for accidents, and compensation for damages to products other than Mitsubishi products, replacement by the user, maintenance of on-site equipment, start-up test run and other tasks.

#### 5. Changes in product specifications

The specifications given in the catalogs, manuals or technical documents are subject to change without prior notice.

#### 6. Product application

- (1) In using the Mitsubishi MELSEC programmable logic controller, the usage conditions shall be that the application will not lead to a major accident even if any problem or fault should occur in the programmable logic controller device, and that backup and fail-safe functions are systematically provided outside of the device for any problem or fault.
- (2) The Mitsubishi programmable logic controller has been designed and manufactured for applications in general industries, etc. Thus, applications in which the public could be affected such as in nuclear power plants and other power plants operated by respective power companies, and applications in which a special quality assurance system is required, such as for Railway companies or Public service purposes shall be excluded from the programmable logic controller applications.

In addition, applications in which human life or property that could be greatly affected, such as in aircraft, medical applications, incineration and fuel devices, manned transportation, equipment for recreation and amusement, and safety devices, shall also be excluded from the programmable logic controller range of applications.

However, in certain cases, some applications may be possible, providing the user consults their local Mitsubishi representative outlining the special requirements of the project, and providing that all parties concerned agree to the special circumstances, solely at the users discretion.

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## Mitsubishi Programmable Logic Controller



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