Changes for the Better

## Programmable Controller

## Transition from MELSEC-A0J2H Series to Q Series Handbook



## OSAFETY PRECAUTIONSO

(Read these precautions before using this product.)

Before using this product, please read this handbook and the relevant manuals introduced in this handbook carefully and pay full attention to safety to handle the product correctly.

In this manual, the safety precautions are classified into two levels: " ! WWRNING" and " $!$ CAUTION".


## ! CAUTION

Indicates that incorrect handling may cause hazardous conditions, resulting in death or severe injury.

Indicates that incorrect handling may cause hazardous conditions, resulting in minor or moderate injury or property damage.

Under some circumstances, failure to observe the precautions given under " $\$$ CAUTION" may lead to serious consequences.

Observe the precautions of both levels because they are important for personal and system safety. Make sure that the end users read this handbook and then keep the handbook in a safe place for future reference.

## [Design Precautions]

## WARNING

- Configure safety circuits external to the programmable controller to ensure that the entire system operates safely even when a fault occurs in the external power supply or the programmable controller. Failure to do so may result in an accident due to an incorrect output or malfunction.
(1) Configure external safety circuits, such as an emergency stop circuit, protection circuit, and protective interlock circuit for forward/reverse operation or upper/lower limit positioning.
(2) When the programmable controller detects an abnormal condition, it stops the operation and all outputs are:

|  | Q series module | A series module |
| :--- | :---: | :---: |
| Overcurrent or overvoltage protection of the power supply <br> module is activated. | All outputs are turned off | All outputs are turned off |
| The CPU module detects an error such as a watchdog <br> timer error by the self-diagnostic function. | All outputs are held or turned off <br> according to the parameter setting. | All outputs are turned off |

All outputs may turn on when an error occurs in the part, such as I/O control part, where the CPU module cannot detect any error. To ensure safety operation in such a case, provide a safety mechanism or a fail-safe circuit external to the programmable controller. For a fail-safe circuit example, refer to the QCPU User's Manual (Hardware Design, Maintenance and Inspection).
(3) Outputs may remain on or off due to a failure of an output module relay or transistor. Configure an external circuit for monitoring output signals that could cause a serious accident.

- In an output module, when a load current exceeding the rated current or an overcurrent caused by a load short-circuit flows for a long time, it may cause smoke and fire. To prevent this, configure an external safety circuit, such as a fuse.
- Configure a circuit so that the programmable controller is turned on first and then the external power supply.
If the external power supply is turned on first, an accident may occur due to an incorrect output or malfunction.
- For the operating status of each station after a communication failure, refer to relevant manuals for the network.
Incorrect output or malfunction due to a communication failure may result in an accident.


## [Design Precautions]

## 1. WARNING

- When changing data of the running programmable controller from a peripheral connected to the CPU module or from a personal computer connected to an intelligent function module/special function module, configure an interlock circuit in the sequence program to ensure that the entire system will always operate safely.
For other forms of control (such as program modification or operating status change) of a running programmable controller, read the relevant manuals carefully and ensure that the operation is safe before proceeding.
Especially, in the case of a control from an external device to a remote programmable controller, immediate action cannot be taken for a problem on the programmable controller due to a communication failure.
To prevent this, configure an interlock circuit in the sequence program, and determine corrective actions to be taken between the external device and CPU module in case of a communication failure.


## CAUTION

- Do not install the control lines or communication cables together with the main circuit lines or power cables.
Keep a distance of 100 mm or more between them.
Failure to do so may result in malfunction due to noise.
- When a device such as a lamp, heater, or solenoid valve is controlled through an output module, a large current (approximately ten times greater than normal) may flow when the output is turned from off to on.
Take measures such as replacing the module with one having a sufficient current rating.
- After the CPU module is powered on or is reset, the time taken to enter the RUN status varies depending on the system configuration, parameter settings, and/or program size. Design circuits so that the entire system will always operate safely, regardless of the time.


## [Installation Precautions]

## CAUTION

- Use the programmable controller in an environment that meets the general specifications in the QCPU User's Manual (Hardware Design, Maintenance and Inspection).
Failure to do so may result in electric shock, fire, malfunction, or damage to or deterioration of the product.
- To mount the module, while pressing the module mounting lever located in the lower part of the module, fully insert the module fixing projection(s) into the hole(s) in the base unit and press the module until it snaps into place.
Incorrect mounting may cause malfunction, failure or drop of the module.
When using the programmable controller in an environment of frequent vibrations, fix the module with a screw.
Tighten the screw within the specified torque range.
Undertightening can cause drop of the screw, short circuit or malfunction.
Overtightening can damage the screw and/or module, resulting in drop, short circuit, or malfunction.
- When using an extension cable, connect it to the extension cable connector of the base unit securely. Check the connection for looseness.
Poor contact may cause incorrect input or output.
- When using a memory card, fully insert it into the memory card slot. Check that it is inserted completely.
Poor contact may cause malfunction.
- When using an SD memory card, fully insert it into the SD memory card slot. Check that it is inserted completely. Poor contact may cause malfunction.
- Securely insert an extended SRAM cassette into the cassette connector of a CPU module. After insertion, close the cassette cover to prevent the cassette from coming off. Poor contact may cause malfunction.
- Shut off the external power supply for the system in all phases before mounting or removing the module. Failure to do so may result in damage to the product.
A module can be replaced online (while power is on) on any MELSECNET/H remote I/O station or in the system where a CPU module supporting the online module change function is used.
Note that there are restrictions on the modules that can be replaced online, and each module has its predetermined replacement procedure.
For details, refer to the relevant sections in the QCPU User's Manual (Hardware Design, Maintenance and Inspection) and in the manual for the corresponding module.
- Do not directly touch any conductive parts and electronic components of the module, memory card, SD memory card, or extended SRAM cassette. Doing so can cause malfunction or failure of the module.
- When using a Motion CPU module and modules designed for motion control, check that the combinations of these modules are correct before applying power. The modules may be damaged if the combination is incorrect. For details, refer to the user's manual for the Motion CPU module.


## [Wiring Precautions]

## 4. WARNING

- Shut off the external power supply for the system in all phases before wiring. Failure to do so may result in electric shock or damage to the product.
- After wiring, attach the included terminal cover to the module before turning it on for operation. Failure to do so may result in electric shock.


## CAUTION

- Ground the FG and LG terminals to the protective ground conductor dedicated to the programmable controller.
Failure to do so may result in electric shock or malfunction.
- Use applicable solderless terminals and tighten them within the specified torque range. If any spade solderless terminal is used, it may be disconnected when the terminal screw comes loose, resulting in failure.
- Check the rated voltage and terminal layout before wiring to the module, and connect the cables correctly.
Connecting a power supply with a different voltage rating or incorrect wiring may cause a fire or failure.
- Connectors for external connection must be crimped or pressed with the tool specified by the manufacturer, or must be correctly soldered.
Incomplete connections could result in short circuit, fire, or malfunction.
- Securely connect the connector to the module. Poor contact may cause malfunction.
- Do not install the control lines or communication cables together with the main circuit lines or power cables. Keep a distance of 100 mm or more between them. Failure to do so may result in malfunction due to noise.
- Place the cables in a duct or clamp them. If not, dangling cable may swing or inadvertently be pulled, resulting in damage to the module or cables or malfunction due to poor contact.
- Check the interface type and correctly connect the cable. Incorrect wiring (connecting the cable to an incorrect interface) may cause failure of the module and external device.
- Tighten the terminal screw within the specified torque range.

Undertightening can cause short circuit, fire, or malfunction.
Overtightening can damage the screw and/or module, resulting in drop, short circuit, or malfunction.

- Prevent foreign matter such as dust or wire chips from entering the module.

Such foreign matter can cause a fire, failure, or malfunction.

- A protective film is attached to the top of the module to prevent foreign matter, such as wire chips, from entering the module during wiring.
Do not remove the film during wiring.
Remove it for heat dissipation before system operation.
- When disconnecting the cable from the module, do not pull the cable by the cable part. For the cable with connector, hold the connector part of the cable. For the cable connected to the terminal block, loosen the terminal screw. Pulling the cable connected to the module may result in malfunction or damage to the module or cable.


## CAUTION

- Mitsubishi programmable controllers must be installed in control panels.

Connect the main power supply to the power supply module in the control panel through a relay terminal block.
Wiring and replacement of a power supply module must be performed by maintenance personnel who is familiar with protection against electric shock. (For wiring methods, refer to the QCPU User's Manual (Hardware Design, Maintenance and Inspection)).

## [Startup and Maintenance Precautions]

- Do not touch any terminal while power is on.
Doing so will cause electric shock or malfunction.
Correctly connect the battery connector.
Do not charge, disassemble, heat, short-circuit, solder, or throw the battery into the fire. Also, do not
expose it to liquid or strong shock. Doing so will cause the battery to produce heat, explode, ignite, or
leak, resulting in injury and fire.
Shut off the external power supply for the system in all phases before cleaning the module or
retightening the terminal screws or module fixing screws.
Failure to do so may result in electric shock.


## CAUTION

- Before performing online operations (especially, program modification, forced output, and operation status change) for the running CPU module from the peripheral connected, read relevant manuals carefully and ensure the safety.
Improper operation may damage machines or cause accidents.
- Do not disassemble or modify the modules.

Doing so may cause failure, malfunction, injury, or a fire.

- Use any radio communication device such as a cellular phone or PHS (Personal Handy-phone System) more than 25 cm away in all directions from the programmable controller.
Failure to do so may cause malfunction.
- Shut off the external power supply for the system in all phases before mounting or removing the module. Failure to do so may cause the module to fail or malfunction.
A module can be replaced online (while power is on) on any MELSECNET/H remote I/O station or in the system where a CPU module supporting the online module change function is used.
Note that there are restrictions on the modules that can be replaced online, and each module has its predetermined replacement procedure.
For details, refer to the relevant sections in the QCPU User's Manual (Hardware Design, Maintenance and Inspection) and in the manual for the corresponding module.
[Startup and Maintenance Precautions]


## CAUTION

- After the first use of the product, do not mount/remove the module to/from the base unit, and the terminal block to/from the module, and do not insert/remove the extended SRAM cassette to/from the CPU module more than 50 times (IEC 61131-2 compliant) respectively. Exceeding the limit may cause malfunction.
- After the first use of the SD memory card, do not insert/remove the memory card more than 500 times. Exceeding the limit may cause malfunction.
- Do not drop or apply shock to the battery to be installed in the module.

Doing so may damage the battery, causing the battery fluid to leak inside the battery.
If the battery is dropped or any shock is applied to it, dispose of it without using.

- Before handling the module, touch a grounded metal object to discharge the static electricity from the human body.
Failure to do so may cause the module to fail or malfunction.


## [Disposal Precautions]

## CAUTION

- When disposing of this product, treat it as industrial waste.

When disposing of batteries, separate them from other wastes according to the local regulations.
(For details of the battery directive in EU member states, refer to the QCPU User's Manual (Hardware Design, Maintenance and Inspection).)

## [Transportation Precautions]

## CAUTION

- When transporting lithium batteries, follow the transportation regulations.

For details on the regulated models, refer to the MELSEC-L CPU Module User's Manual (Hardware Design, Maintenance and Inspection).

## OCONDITIONS OF USE FOR THE PRODUCT

(1) Mitsubishi programmable controller ("the PRODUCT") shall be used in conditions; i) where any problem, fault or failure occurring in the PRODUCT, if any, shall not lead to any major or serious accident; and
ii) where the backup and fail-safe function are systematically or automatically provided outside of the PRODUCT for the case of any problem, fault or failure occurring in the PRODUCT.
(2) The PRODUCT has been designed and manufactured for the purpose of being used in general industries.

MITSUBISHI SHALL HAVE NO RESPONSIBILITY OR LIABILITY (INCLUDING, BUT NOT LIMITED TO ANY AND ALL RESPONSIBILITY OR LIABILITY BASED ON CONTRACT, WARRANTY, TORT, PRODUCT LIABILITY) FOR ANY INJURY OR DEATH TO PERSONS OR LOSS OR DAMAGE TO PROPERTY CAUSED BY the PRODUCT THAT ARE OPERATED OR USED IN APPLICATION NOT INTENDED OR EXCLUDED BY INSTRUCTIONS, PRECAUTIONS, OR WARNING CONTAINED IN MITSUBISHI'S USER, INSTRUCTION AND/OR SAFETY MANUALS, TECHNICAL BULLETINS AND GUIDELINES FOR the PRODUCT.
("Prohibited Application")
Prohibited Applications include, but not limited to, the use of the PRODUCT in;

- Nuclear Power Plants and any other power plants operated by Power companies, and/or any other cases in which the public could be affected if any problem or fault occurs in the PRODUCT.
- Railway companies or Public service purposes, and/or any other cases in which establishment of a special quality assurance system is required by the Purchaser or End User.
- Aircraft or Aerospace, Medical applications, Train equipment, transport equipment such as Elevator and Escalator, Incineration and Fuel devices, Vehicles, Manned transportation, Equipment for Recreation and Amusement, and Safety devices, handling of Nuclear or Hazardous Materials or Chemicals, Mining and Drilling, and/or other applications where there is a significant risk of injury to the public or property.
Notwithstanding the above restrictions, Mitsubishi may in its sole discretion, authorize use of the PRODUCT in one or more of the Prohibited Applications, provided that the usage of the PRODUCT is limited only for the specific applications agreed to by Mitsubishi and provided further that no special quality assurance or fail-safe, redundant or other safety features which exceed the general specifications of the PRODUCTs are required. For details, please contact the Mitsubishi representative in your region.
(3) Mitsubishi shall have no responsibility or liability for any problems involving programmable controller trouble and system trouble caused by DoS attacks, unauthorized access, computer viruses, and other cyberattacks.


## REVISIONS

* The handbook number is given on the bottom left of the back cover.

| Print Date | * Handbook Number | Revision |
| :---: | :---: | :---: |
| Dec., 2005 | L(NA)-08060ENG-A | First edition |
| Dec., 2007 | L(NA)-08060ENG-B | Addition of modules to be replaced <br> QX41Y41P, Renewal tool for A0J2 <br> Addition <br> Section 1.2, Section 1.4, Section 3.2.3 (4) (6), Section 7.7.7, Appendix 1, <br> Appendix 2.6 <br> Partial correction <br> SAFETY PRECAUTIONS, Section 1.1.3 $\rightarrow$ Section 1.3, Section 3.1, Section 3.2, <br> Section 7.6.2, Section 7.7.1, Section 7.7.7 to 7.7.8 $\rightarrow$ Section 7.7.8 to 7.7.9, <br> Section 8.1, Section 10.1, Section 11.1, Section 11.2, Appendix $1 \rightarrow$ Appendix 2 |
| Jul., 2011 | L(NA)-08060ENG-C | Addition of modules to be replaced <br> Universal Modul QCPU, Renewal tool for AOJ2 <br> Addition <br> Appendix 1 <br> Partial correction <br> SAFETY PRECAUTIONS, Section 1, Section 2, Section 3.2, Section 4, Section 7, Section 8.1, Section 9.1, Section 10.1, Section 10.2.1, Section 10.2.2, Section 11.1, Appendix 2 |
| Dec., 2015 | L(NA)-08060ENG-D | Addition <br> Section 7.1.4, Appendix 1 <br> Partial correction <br> SAFETY PRECAUTIONS, GENERIC TERMS AND ABBREVIATIONS, Section $\begin{array}{\|l} \text { 1.1.1, 1.1.2, 1.2.1, 1.2.2, 2.1, 2.2, 2.3.1, 2.4.1, 2.4.4, 3.1, 3.2.1, 3.2.2, 3.2.3, 4.1, } \\ 4.2,4.3,5.1,5.2,6.1,6.2 \text { Chapter } 7 \text {, Section } 7.1 .1,7.1 .3,7.2 .1,7.6 .3,7.7 .3, \\ \text { 7.7.6, 8.1, 8.2.1, 8.2.2, 8.3, 8.4, 8.5.1, 8.5.2, 8.6, 8.47, } 9.1,10.1,10.2 .1,10.2 .2, \\ \text { 10.2.3, 11.2, Appendix 2.1, 2.2, 3.1, 3.4, 3.6, WARRANTY } \end{array}$ |
| Sep., 2018 | L(NA)-08060ENG-E | Partial correction <br> Cover, GENERIC TERMS AND ABBREVIATIONS, Section 1.1.1, 1.3, 1.4, <br> Chapter 2, Section 3.1, 3.2, 3.3, 4.2, Chapter 6, 7, Section 8.4, 8.5.2, 8.6, 8.7, <br> Chapter 9, Section 10.2.3, 10.2.4, Appendix 3.4, 3.6 |
| Sep., 2020 | L(NA)-08060ENG-F | Partial correction <br> Section 10.1, 10.2.4 |
| Sep., 2020 | L(NA)-08060ENG-G | Partial correction <br> Front cover, back cover |
| Sep., 2023 | L(NA)-08060ENG-H | Partial correction <br> Chapter 8 |
|  |  |  |

Japanese Handbook Version L-08056-I

[^0]© 2005 MITSUBISHI ELECTRIC CORPORATION

## CONTENTS

SAFETY PRECAUTIONS ..... A-1
CONDITIONS OF USE FOR THE PRODUCT ..... A-8
REVISIONS ..... A-9
CONTENTS ..... A - 10
GENERIC TERMS AND ABBREVIATIONS ..... A-14
CHAPTER 1 INTRODUCTION ..... 1-1 to 1-13
1.1 Proposal to Replace A0J2HCPU with QCPU ..... 1-1
1.1.1 Advantages of replacement to QCPU ..... 1-1
1.1.2 Proposal of replacement to QCPU (Q00UCPU) ..... 1-2
1.2 Proposal of Replacement with Renewal tool for A0J2 ..... 1-7
1.2.1 Advantages of using renewal tool for A0J2 (manufactured by Mitsubishi Electric System \& Service Co., Ltd.) ..... 1-7
1.2.2 Proposal of replacement with renewal tool for AOJ2 ..... 1-10
1.3 Precautions for Replacement ..... 1-13
CHAPTER 2 REPLACING THE CPU MODULE ..... 2-1 to 2-12
2.1 List of Alternative CPU Module Models ..... 2-1
2.2 Specifications Comparison of CPU Module ..... 2-3
2.3 Functional Comparisons of CPU Module ..... 2-6
2.3.1 Functional comparisons between the A0J2HCPU and QCPU ..... 2-6
2.4 Precautions for Replacement of the CPU Module ..... 2-8
2.4.1 Memory in the CPU module ..... 2-8
2.4.2 Keyword registration and password registration ..... 2-9
2.4.3 Write during RUN ..... 2-10
2.4.4 I/O assignment ..... 2-11
CHAPTER 3 I/O MODULES REPLACEMENT ..... 3-1 to 3-63
3.1 Alternative I/O Module Models List ..... 3-1
3.2 I/O Module Specifications Comparison ..... 3-12
3.2.1 Input module specifications comparison ..... 3-12
3.2.2 Output module specifications comparison ..... 3-15
3.2.3 I/O Module specifications comparison ..... 3-22
3.3 Precautions for I/O Module Replacement ..... 3-62
CHAPTER 4 REPLACING POWER SUPPLY MODULES ..... 4-1 to 4-6
4.1 List of Alternative Power Supply Module Models ..... 4-1
4.2 Specifications Comparison of Power Supply Modules ..... 4-2
4.3 Precautions for Replacement of the CPU Module ..... 4-6
CHAPTER 5 EXTENSION CABLE REPLACEMENT ..... 5-1 to 5-1
5.1 List of Alternative Extension Cable Models ..... 5-1
5.2 Precautions for Extension Cable Replacement ..... 5-1
CHAPTER 6 MEMORY AND BATTERY REPLACEMENT ..... 6-1 to 6-1
6.1 List of Alternative Memory Models ..... 6-1
6.2 Precautions for Memory and Battery Replacement ..... 6-1
CHAPTER 7 PROGRAMS REPLACEMENT ..... 7-1 to 7-39
7.1 Program Replacement Procedures ..... 7-4
7.1.1 Program conversion procedure from A0J2HCPU to QCPU ..... 7-4
7.1.2 Change PLC type ..... 7-6
7.1.3 ACPU program conversion ratio ..... 7-8
7.1.4 Reading (Reusing) other format files ..... 7-10
7.1.5 How to reuse a program of a PLC type that is not supported by GX Developer ..... 7-15
7.2 Instruction Conversion ..... 7-18
7.2.1 List of instructions converted from A0J2HCPU to QCPU (Sequence/Basic/Application instructions) ..... 7-18
7.2.2 Instruction that may need replacement from A0J2HCPU to Basic model QCPU ..... 7-24
7.3 Precautions for Parameter Replacement ..... 7-25
7.4 Special Relay Replacement ..... 7-26
7.4.1 Replacement of A0J2HCPU with QCPU ..... 7-26
7.5 Special Register Replacement ..... 7-27
7.5.1 Replacement of A0J2HCPU with QCPU ..... 7-27
7.6 Precautions for Replacing MELSAP-II with MELSAP3 ..... 7-28
7.6.1 How to start the SFC program ..... 7-28
7.6.2 Block information (Information device for SFC) ..... 7-28
7.6.3 Specifications comparison between MELSAP-II and MELSAP3 ..... 7-29
7.6.4 SFC diagram that cannot be read normally in another format ..... 7-30
7.7 Precautions for Program Replacement ..... 7-31
7.7.1 Applicable devices list ..... 7-31
7.7.2 I/O control method ..... 7-32
7.7.3 Data formats that can be used by the instructions ..... 7-32
7.7.4 Timer ..... 7-33
7.7.5 Counter ..... 7-34
7.7.6 Display instruction ..... 7-34
7.7.7 Instructions with changed specified formats ..... 7-34
7.7.8 Index register ..... 7-36
7.7.9 Setting method when multiple sequence programs are created ..... 7-37
7.7.10 Precautions for file register replacement ..... 7-39
7.7.11 Boot operation method (storing the program to ROM) ..... 7-39
CHAPTER 8 REPLACING THE COMMUNICATION MODULES ..... 8-1 to 8-11
8.1 List of Alternative Communication Module Models ..... 8-1
8.2 Specifications Comparison of Communication Modules ..... 8-4
8.2.1 Performance comparisons of communication module specifications ..... 8-4
8.2.2 Cable specifications comparison ..... 8-5
8.3 Functional Comparisons of Data Modules ..... 8-6
8.4 Switch Settings Comparisons ..... 8-7
8.5 Program Comparisons ..... 8-8
8.5.1 I/O signal ..... 8-8
8.5.2 Buffer memory ..... 8-9
8.6 Program Reuse ..... 8-10
8.7 Other Precautions ..... 8-11
CHAPTER 9 REPLACING THE NETWORK SYSTEM ..... 9-1 to 9-1
9.1 List of Alternative Network System Models ..... 9-1
CHAPTER 10 REPLACING THE SPECIAL FUNCTION MODULE ..... 10-1 to 10-32
10.1 List of Alternative Special Function Module Models ..... 10-1
10.2 Special Function Module Comparison ..... 10-3
10.2.1 Analog input module comparisons ..... 10-3
10.2.2 Analog output module comparison ..... 10-9
10.2.3 High-speed counter module comparison ..... 10-14
10.2.4 Positioning module comparison ..... 10-21
APPENDICES App - 1 to App - 43
Appendix 1 External Dimensions ..... App - 1
Appendix 2 Performance Specifications Comparison between AOJ2H Series and Renewal Tool for A0J2 ..... App - 1
Appendix 2.1 Precautions for the performance specifications comparison ..... App - 1
Appendix 2.2 Performance specifications comparison ..... App - 3
Appendix 3 Related Manuals ..... App - 41
Appendix 3.1 Replacement handbooks ..... App-41
Appendix 3.2 A0J2H Series ..... App - 42
Appendix 3.3 Q series ..... App - 42
Appendix 3.4 Programming Tool ..... App - 43
Appendix 3.5 Products manufactured by Mitsubishi Electric Engineering Co., Ltd. ..... App - 43
Appendix 3.6 Products manufactured by Mitsubishi Electric System \& Service Co., Ltd. ..... App - 43

- For the products shown in handbooks for transition, Catalogue, and transition examples, refer to the manuals for the relevant products and check the detailed specifications, precautions for use, and restrictions before replacement.
For the products manufactured by Mitsubishi Electric Engineering Co., Ltd., Mitsubishi Electric System \& Service Co., Ltd., and other companies, refer to the catalogue for each product and check the detailed specifications, precautions for use, and restrictions before use.
The manuals and catalogues for our products, products manufactured by Mitsubishi Electric Engineering Co., Ltd., and Mitsubishi Electric System \& Service Co., Ltd., are shown in Appendix of each handbook for transition.
- For details on product compliance with the above standards, please contact your local Mitsubishi Electric sales office or representative.
- Products shown in this handbook are subject to change without notice.


## GENERIC TERMS AND ABBREVIATIONS

Unless otherwise specified, this handbook uses the following generic terms and abbreviations.

| Generic term/abbreviation | Description |
| :---: | :---: |
| -Series |  |
| A series | An abbreviation for large types of Mitsubishi Electric MELSEC-A series programmable controllers |
| AnS series | An abbreviation for compact types of Mitsubishi Electric MELSEC-A series programmable controllers |
| A/AnS series | Generic term for A series and AnS series |
| A0J2(H) series | An abbreviation for Mitsubishi Electric MELSEC-AOJ2(H) series programmable controllers |
| QnA series | An abbreviation for large types of Mitsubishi Electric MELSEC-QnA series programmable controllers |
| QnAS series | An abbreviation for compact types of Mitsubishi Electric MELSEC-QnA series programmable controllers |
| QnA/QnAS series | Generic term for QnA series and QnAS series |
| A/AnS/QnA/QnAS series | Generic term for A series, AnS series, QnA series, and QnAS series |
| Q series | An abbreviation for Mitsubishi Electric MELSEC-Q series programmable controllers |
| CCPU module type |  |
| CPU module | Generic term for A series, AnS series, QnA series, QnAS series, and Q series CPU modules |
| Process CPU | Generic term for the Q02PHCPU, Q06PHCPU, Q12PHCPU, and Q25PHCPU |
| Redundant CPU | Generic term for the Q12PRHCPU and Q25PRHCPU |
| Universal model QCPU | Generic term for the Q00UJCPU, Q00UCPU, Q01UCPU, Q02UCPU, Q03UDCPU, Q03UDVCPU, Q03UDECPU, Q04UDHCPU, Q04UDVCPU, Q04UDEHCPU, Q06UDHCPU, Q06UDVCPU, Q06UDEHCPU, Q10UDHCPU, Q10UDEHCPU, Q13UDHCPU, Q13UDVCPU, Q13UDEHCPU, Q20UDHCPU, Q20UDEHCPU, Q26UDHCPU, Q26UDVCPU, and Q26UDEHCPU |
| -CPU module model |  |
| ACPU | Generic term for MELSEC-A series programmable controller CPUs |
| AnSCPU | Generic term for MELSEC-AnS series programmable controller CPUs |
| A/AnSCPU | Generic term for MELSEC-A series and MELSEC-AnS series programmable controller CPUs |
| AnNCPU | Generic term for the A1NCPU, A1NCPUP21/R21, A1NCPUP21-S3, A2NCPU, A2NCPU-S1, A2NCPUP21/R21, A2NCPUP21/R21-S1, A2NCPUP21-S3(S4), A3NCPU, A3NCPUP21/R21, and A3NCPUP21-S3 |
| AnACPU | Generic term for the A2ACPU, A2ACPU-S1, A3ACPU, A2ACPUP21/R21, A2ACPUP21/R21S1, and A3ACPUP21/R21 |
| AnUCPU | Generic term for the A2UCPU, A2UCPU-S1, A3UCPU, A4UCPU, A2USCPU, A2USCPU-S1, and A2USHCPU-S1 |
| AnN/AnACPU | Generic term for the AnNCPU and AnACPU |
| AnN/AnA/AnSCPU | Generic term for the AnNCPU, AnACPU, and AnSCPU |
| QnACPU | Generic term for MELSEC-QnA series programmable controller CPUs |
| QnASCPU | Generic term for MELSEC-QnAS series programmable controller CPUs |
| QnA/QnASCPU | Generic term for MELSEC-QnA series and MELSEC-QnAS series programmable controller CPUs |
| A/AnS/QnA/QnASCPU | Generic term for A series, AnS series, QnA series, and QnAS series programmable controller CPUs |
| QCPU | Generic term for MELSEC-Q series programmable controller CPUs |

## INTRODUCTION

### 1.1 Proposal to Replace A0J2HCPU with QCPU

### 1.1.1 Advantages of replacement to QCPU

(1) Advanced performance of equipment is possible (reduced tact time)

Increased speed of the operation processing and bus realizes several times higher performance than the A0J2HCPU and significantly improves equipment performance.
(2) Ease of module selection

The building block type allows for flexible system configurations by selecting a module from a wide range of $Q$ series product lineups.
(3) Ease of programming by various kinds of instructions

PID control instruction, real number operating instruction, there are many convenient instructions available, i.e. and they allow complex data processing to be performed.
(4) Improved maintainability
(a) The high speed serial port and USB port significantly reduces the read/write time of the program and, improves factory maintainability.
(b) The flash ROM is adopted as the flash memory of the Universal model QCPU. When the CPU module is replaced with the Universal model QCPU, the ROM operation (battery-less operation) can be performed without using an option memory.

### 1.1.2 Proposal of replacement to QCPU (Q00UCPU)



## Remarks

When the A type extension base unit (A65B) is connected, refer to the following.
Transition from MELSEC-A/QnA (Large Type) Series to Q Series Handbook (Fundamentals)

## (1) Comparison of the installation areas

The following shows a comparison of the installation areas when replacing the A0J2HCPU with the QCPU.
Select the optimal base unit after taking into consideration the installation space, the number of modules loaded, etc.
(a) Comparison of the A0J2-E56ロ and the 3-slot main base unit Q33B

(b) Comparison of the A0J2-E56 $\square$ and the 5-slot main base unit Q35B
:AOJ2H (E56ロ) external dimensions
:QCPU external dimensions
（2）Replacement method without changing the I／O address of the AOJ2HCPU
（a）Replacing the A0J2－E56ロ
The AOJ2HCPU has I／O points consisting of 32 input points in the first half and 32 output points in the last half for each I／O module．
When replacing with the building block type，replacement with the same I／O address is possible by selecting a terminal block module to keep an equivalent wiring method and arranging in the order of two input modules and two output modules．


## （b）Replacing the A0J2－E32ロ，A0J2－E24ロ，and special module

As the input，output，and special modules are 64 point blocks， 32 points will become vacant when replacing with Q series I／O．
When replacing with the building block type，replacement with the same I／O address is possible by arranging the modules in actual usage mode and setting the head address of each slot using the I／O assignment of the parameter．

*Example of I/O assignment settings


## (c) Replacement by using connector/terminal block converter module, and relay terminal module

With 32-point (or 64-point) I/O modules (connector system), this method reduces the number of slots. As to the wiring from the module, connection with external devices can be made on the terminal block by using the connector/terminal block converter module or relay terminal module externally.


* Connector/terminal block converter module, relay terminal module

| Model name | Descriptions | Applicable models |
| :---: | :---: | :---: |
| A6TBXY36 | For positive common type input and sink type output modules (standard type) | QX41, QX42, QY41P, QY42P, QH42P |
| A6TBXY54 | For positive common type input and sink type output modules (2-wire type) |  |
| A6TBX70 | For positive common type input modules (3-wire type) | QX41, QX42, QH42P, QX41Y41P |
| * Cables |  |  |
| Model name | Descriptions | Applicable models |
| AC05TB | For 0.5m sink type modules | A6TBXY36 A6TBXY54 A6TBX70 |
| AC10TB | For 1m sink type modules |  |
| AC20TB | For 2 m sink type modules |  |
| AC30TB | For 3m sink type modules |  |
| AC50TB | For 5m sink type modules |  |
| AC80TB | For 8 m sink type modules (common current 0.5A or less) |  |
| AC100TB | For 10 m sink type modules (common current 0.5 A or less) |  |

* Relay terminal modules

| Model name | Descriptions | Applicable models |
| :---: | :--- | :--- |
| A6TE2-16SRN | For sink type output modules | QY41P, QY42P, QH42P, QX41Y41P |

* Relay terminal module connecting cables

| Model name | Cable length L |
| :--- | :---: |
| AC06TE | 0.6 m |
| AC10TE | 1 m |
| AC30TE | 3 m |
| AC50TE | 5 m |
| AC100TE | 10 m |



### 1.2 Proposal of Replacement with Renewal tool for A0J2

### 1.2.1 Advantages of using renewal tool for A0J2 (manufactured by Mitsubishi Electric System \& Service Co., Ltd.)

## (1) Renewal tool for A0J2

This tool is for replacing the A0J2(H) system with the Q series. It is composed of the interface module to which wiring terminal block of existing I/O module can be attached, components for a programmable controller, and connection cable.
Also, the interface module has the conversion function that converts AC input into DC input and DC output into relay output and triac output. The interface module can be replaced with the 40-pin connector type DC I/O module.
(a) Configuration example of Renewal tool for AOJ2


## (2) Using the existing wiring

Although the CPU module, A0J2HCPU is replaced with the QCPU, the external wiring terminal block attached to the existing A0J2 I/O module can be utilized to the interface module.
It allows to replace the modules without external wiring change.
Also, new wiring is unnecessary since the $Q$ series I/O module is connected to the interface module with the dedicated cable.


## XPoint

1) For specifications comparison and functional comparison between the existing A0J2HCPU and QCPU after replacement, refer to CHAPTER 2.
2) For specifications comparison and functional comparison between the existing A0J2 I/O module and the renewal tool for A0J2 after replacement, refer to APPENDICES.

## (3) Processing the mounting holes is unnecessary.

Dimensions when renewal tool for AOJ2 is mounted to base adapter is the same with existing AOJ2 I/O module.
Replacement without processing the mounting holes is possible.
(4) Using the QX41Y41P eliminates I/O address change

Changing the I/O assignment for A0J2 I/O module is unnecessary by replacing the module with the QX41Y41P (combined I/O module).
It eliminates I/O address change and allows substantial reduction of program correction.
The QX41Y41P can treat 32 points for input or output per module, which leads to reduction of the number of slots required for the replaced programmable controller.

## (5) List of alternative models

| Model to be discontinued (A0J2 I/O module) |  | Alternative model (Q series/renewal tool for A0J2) |  |  |
| :---: | :---: | :---: | :---: | :---: |
| Product | Model | Q series |  | Renewal tool for A0J2 ${ }^{* 1}$ |
|  |  |  | Interface module | Fixed stand kit of programmable controller*2 |
| Input module | A0J2-E32A | QX41 | SC-A0JQIF32A | SC-AOJQSES-U1 (Building-up type, single) SC-AOJQSES-F (Horizontal type, single) SC-AOJQBSS (Separate type, single) |
|  | A0J2-E32D |  | SC-A0JQIF32D |  |
| Output module | A0J2-E24R | QY41P | SC-A0JQIF24R |  |
|  | A0J2-E24S |  | SC-A0JQIF24S |  |
|  | A0J2-E24T |  | SC-A0JQIF24T |  |
| I/O module | A0J2-E28AR | QX41Y41P | SC-A0JQIF28AR |  |
|  | A0J2-E28AS |  | SC-A0JQIF28AS |  |
|  | A0J2-E28DR |  | SC-A0JQIF28DR |  |
|  | A0J2-E28DS |  | SC-A0JQIF28DS |  |
|  | A0J2-E28DT |  | SC-A0JQIF28DT |  |
|  | A0J2-E56AR |  | SC-A0JQIF56AR | SC-A0JQSEL-U1 (Building-up type, single) SC-A0JQSEL-U2 (Building-up type, double) SC-A0JQSEL-F (Horizontal type, single/double) SC-A0JQBSL (Separate type, single/double) |
|  | A0J2-E56AS |  | SC-A0JQIF56AS |  |
|  | A0J2-E56DR |  | SC-A0JQIF56DR |  |
|  | A0J2-E56DS |  | SC-A0JQIF56DS |  |
|  | A0J2-E56DT |  | SC-A0JQIF56DT |  |

*1 The connection cable (SC-A0JQCDIM) is required for connecting a interface module (renewal tool for A0J2) to a Q series I/O module.
*2 The fixed stand kit of programmable controller includes the mounting plate for the Q33B as standard. When a base unit other than the Q33B is used, the mounting plate (SC-A0JQPTD) is required (sold separately).

### 1.2.2 Proposal of replacement with renewal tool for A0J2

## (1) Building-up type

The programmable controller can be built up to the existing panel if there is room for depth in front of existing module, and can be installed on the installation surface of the existing panel.
( 236 mm or more is required for depth, when two interface modules are mounted.)


Use the Q33B as main base unit. (Up to three interface modules can be mounted to the main base unit (Q33B). (Refer to the following figure.) ${ }^{* 1}$ )
Reprocess is unnecessary since the installation dimensions are the same and the mounting holes can be utilized.
It can be replaced without changing the programs by using combined I/O module, QX41Y41P.*2

*1 If there are many existing units, use the 5 slot mounting frame (sold separately) to mounted the main base unit(Q35B).
*2 When replacing CPU module with data link function, using two QX41Y41Ps and network module allows to configure a network system.
(Before replacement)

(After replacement)


## (2) Horizontal type

The programmable controller can be installed horizontally, if there is room above the existing module.
(Before replacement)

(After replacement)


Use the Q33B as main base unit.(Up to three interface modules can be connected to the main base unit (Q33B). (Refer to the following figure.) ${ }^{* 1}$ )
92 mm or more room in addition to the current installation dimension is required above the existing module: however; the mounting holes can be utilized and reprocess is unnecessary.
It can be replaced without changing the programs by using combined I/O module, QX41Y41P.*2

*1 If there are many existing units, use the 5 slot mounting frame (sold separately) to mounted the main base unit(Q35B).
*2 When replacing CPU module with data link function, using two QX41Y41Ps and network module allows to configure a network system.


## (3) Separate type

Only the programmable controller can be installed separately.
(Before replacement)



By installing CPU module separately, base unit of the arbitrary number of slots can be used according to the current system configuration.


## Remarks

Apart from replacement with the QCPU, the renewal tool for AOJ2 can be used for replacement with the CC-Link module.
For details, contact Mitsubishi Electric System \& Service Co., Ltd.

### 1.3 Precautions for Replacement

(a) To replace the A0J2HCPU with the QCPU or replace modules using renewal tool for A0J2, be sure to refer to the following manuals.
Select correct products after checking the functions, specifications, grounding method, and usage.
(Reference manual)

- Manual for each Q series module
- Renewal tool for A0J2 series transition from MELSEC-A0J2(H) series to renewal system using renewal tool (Refer to Appendix 3.6.)
(b) After replacing A0J2HCPU, be sure to check operation of the entire system before actual operation.


## XPoint

Before replacement, make sure again that the frame ground of the programmable controller system is securely grounded.
The noise tolerance of programmable controllers is secured by diverting noise to ground via the frame ground as an EMC measure.
For this reason, the system might be affected by noise if the system is reconfigured with insufficient grounding.
Also, consider the following as a provisional measure when checking grounding status is difficult.
(1) Change the ground of the system into an exclusive ground.
(2) Add a ferrite core between the ground wire and the module FG terminal.

[^1]
## REPLACING THE CPU MODULE

### 2.1 List of Alternative CPU Module Models

The following is an example of alternative $Q$ series CPU modules that can be chosen based on compatibility with previous A0J2H series CPU. The optimal A0J2HCPU replacement may be selected based on type of control, specifications, system scalability and cost.

| Discontinued models in A0J2H series |  | Q series alternative models |  |
| :---: | :---: | :---: | :---: |
| Product name | Model name | Model name | Remarks (restrictions) |
| CPU module | A0J2HCPU <br> A0J2HCPUP $21^{* 1}$ <br> A0J2HCPUR21*1 | Q00UJCPU*3 | 1) I/O control: Refresh/direct switch $\rightarrow$ Refresh only <br> 2) Processing speed (LD instruction): During refresh $1.25 \mu \mathrm{~s} \rightarrow 0.12 \mu \mathrm{~s}$ <br> 3) PC MIX value: $0.2 \rightarrow 4.92$ <br> 4) Number of I/O points: 480 points $\rightarrow 256$ points <br> 5) Number of I/O device points: 512 points $\rightarrow 8192$ points <br> 6) Program capacity: 8 K step $\rightarrow 10 \mathrm{~K}$ step <br> 7) Number of file register points: 4096 points $\rightarrow 0$ points <br> 8) Microcomputer program: Usable $\rightarrow$ Not usable <br> 9) Number of I/O slots on main base: No main base (8 units connectable using connection cable) $\rightarrow$ Max. 5 slots (including extension, Max. 16 slots available) <br> 10) Number of extension stages: 4 modules +1 stage (A55B, A65B) $\rightarrow 2$ stages <br> 11) Applicable memory: Built-in RAM/4K/8K/16K ROM $\rightarrow$ Built-in program memory(RAM)/built-in flash ROM <br> 12) Configuration: Compact type $\rightarrow$ Building block type (including 5 slot base unit, CPU module and power supply module) |
|  | AOJ2HCPUP21-S3*1 <br> A0J2CPU-DC24*2 | Q00UCPU*3 | 1) I/O control: Refresh/direct switch $\rightarrow$ Refresh only <br> 2) Processing speed (LD instruction): During refresh $1.25 \mu \mathrm{~s} \rightarrow 0.08 \mu \mathrm{~s}$ <br> 3) PC MIX value: $0.2 \rightarrow 7.36$ <br> 4) Number of I/O points: 480 points $\rightarrow 1024$ points <br> 5) Number of I/O device points: 512 points $\rightarrow 8192$ points <br> 6) Program capacity: 8 K step $\rightarrow 10 \mathrm{~K}$ step <br> 7) Number of file register points: 4096 points $\rightarrow 64 \mathrm{~K}$ points <br> 8) Microcomputer program: Usable $\rightarrow$ Not usable <br> 9) Number of I/O slots on main base: No main base (8 units connectable using connection cable) $\rightarrow$ Max. 12 slots (including extension, Max. 24 slots available) <br> 10) Number of extension stages: 4 modules +1 stage (A55B, A65B) $\rightarrow 4$ stages <br> 11) Applicable memory: Built-in RAM/4K/8K/16K ROM $\rightarrow$ Built-in program memory (RAM)/built-in flash ROM <br> 12) Configuration: Compact type $\rightarrow$ Building block type |

*1 When replacing MELSECNET CPU module with link function, select both a CPU module and a network module from the table below.

| Product name |  | Alternative model |  |
| :--- | :--- | :--- | :--- |
|  |  |  |  |
| A0J2HCPUP21 | CPU model name | Network model name |  |
|  | Q00UJCPU | QJ71LP21-25 | Built-in link function <br> $\rightarrow$ mount network module on the base unit <br> (1 slot,32 points) |
|  | Q00UCPU | Q00UJCPU |  |

*2 Select "Q63P (24VDC input)" as a power supply module after the replacement.
*3 Use the SFC program and consider using the Q03UDVCPU/Q03UD(E)CPU if any of the following applies.

- Number of SFC blocks: 128 blocks or more
- Number of SFC steps: 128 steps or more/block

For details on the replacement method when using the SFC program, refer to Section 7.6.

## Remarks

- When the A0J2CPU is used, read the A0J2HCPU in the above table as the A0J2CPU. Refer to the following because the performance specifications differ. A0J2HCPU(P21/R21) User's Manual: IB-66268
- GX Developer does not support the A0J2CPU. Changing the existing CPU module type into the one which is supporting GX Developer is required using the A/QnA-Q conversion support tool when the program is used. For details, refer to Section 7.1.4.
- The modules other than CPU modules (such as I/O modules and special function modules) are common to specifications between the A0J2CPU and A0J2HCPU. For details, refer to CHAPTER 3.


### 2.2 Specifications Comparison of CPU Module

| Function | Description | A0J2HCPU | QnUCPU |  | Precautions for replacement | Reference sections |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  | Q00UJCPU | Q00UCPU |  |  |
| Control method | Cyclic operation (by the stored program) | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | - | - |
| I/O control method | Refresh mode/direct mode | O*1 | - *2 | $0^{* 2}$ | As QCPU supports only refresh mode, use the direct I/O instruction when inputting or outputting in direct mode. | $\begin{gathered} \text { Section } \\ \text { 7.7.2 } \end{gathered}$ |
| Programming language | Language dedicated to sequence control (relay symbol, logic symbol, and MELSAP languages) | $\bigcirc$ | 0 | $\bigcirc$ | Regarding MELSAP language, AOJ2HCPU uses MELSAP II and QCPU uses MELSAP3. | Section 7.6 |
| Processing speed | Sequence instruction ( $\mu \mathrm{s} / \mathrm{step}$ ) | 1.25 | 0.12 | 0.08 | - | - |
| Watch dog timer (WDT) | Watch dog timer (WDT) (ms) | 10 to 2000 | 10 to 2000 | 10 to 2000 | - | - |
| Memory capacity | User memory capacity (Byte) | 32K <br> (Built-in <br> RAM) | Program memory <br> (Flash ROM) $^{* 3}$ <br> 40K <br> Standard RAM:- <br> Standard ROM: <br> 256K | Program memory <br> (Flash ROM) ${ }^{* 3}$ <br> 40K <br> Standard RAM: <br> 128K <br> Standard ROM: <br> 512K | - | $\begin{gathered} \text { Section } \\ 2.4 .1 \end{gathered}$ |
| Program capacity | Sequence program (step) | Max. 8K | Max.10K | Max.10K | - | - |
|  | Microcomputer program (byte) | Max. 14K | $\times$ | $\times$ | QCPU does not have a microcomputer program. It is recommended to replace the microcomputer program with a sequence program. | - |
| Number of I/O points | Number of I/O points (points) ${ }^{*} 4$ | 480 | 256 | 1024 | - | - |

*1 The direct I/O is selectable using the I/O control mode settings switch.
*2 Although QCPU supports only refresh mode, it has some instructions/devices for I/O in direct mode.
*3 This is the capacity that can store the maximum number of steps of the sequence program.
*4 This is the number of accessible points of actual I/O modules.

| Function | Description |  | A0J2HCPU | QnUCPU |  | Precautions for replacement | Reference sections |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  | Q00UJCPU | Q00UCPU |  |  |
| Number of device points | Numbe points | $\begin{aligned} & \text { f input device (X) } \\ & \text { ints) }{ }^{* 5} \end{aligned}$ |  | 512 | 8192 | 8192 | - | - |
|  | Number points | output device (Y) ints) ${ }^{*}{ }^{5}$ | 512 | 8192 | 8192 | - | - |
|  | Number points | finternal relay (M) ints) | Total of 2048 | 8192 | 8192 | - | - |
|  | Number of latch relay (L) points (points) |  |  | 8192 | 8192 | - | - |
|  | Number of step relay (S) points (points) |  |  | 8192* ${ }^{*}$ | 8192* ${ }^{*}$ | - | - |
|  | Number of annunciator (F) points (points) |  | 256 | 2048 | 2048 | - | - |
|  | Number of edge relay (V) points (points) |  | $\times$ | 2048 | 2048 | - | - |
|  | Number of link relay (B) points (points) |  | 1024 | 8192 | 8192 | - | - |
|  | Number of timer ( T ) points (points) |  | 256 | 2048 | 2048 | - | - |
|  | Number of counter (C) points (points) |  | 256 | 1024 | 1024 | - | - |
|  | Number of data register (D) points (points) |  | 1024 | 12288 | 12288 | - | - |
|  | Number of link register (W) points (points) |  | 1024 | 8192 | 8192 | - | - |
|  | Number of file register (R) points (points) |  | 4096 | $\times$ | $32768 \times 2$ blocks | Q00UJCPU does not have a file register. It is recommended to substitute the data register (D). | - |
|  | Number of accumulator (A) points (points) |  | 2 | $\times$ | $\times$ | As QCPU does not have an accumulator, it is converted to a special register (SD718, SD719) during the program conversion from $\mathrm{A} \rightarrow \mathrm{Q}$. | - |
|  | Index register | Number of (Z) points (points) | 1 | 20 | 20 | - | - |
|  |  | Number of (V) points (points) | 1 | $\times$ | $\times$ | - | - |
|  | Number of nesting (N) points (points) |  | 8 | 15 | 15 | - | - |
|  | Number of pointer (P) points (points) |  | 256 | 512 | 512 | - | - |
|  | Number of interrupt pointer (I) points (points) |  | 1 | 128 | 128 | - | - |
|  | Number of special relay (M) points (points) |  | 256 | 2048 | 2048 | - | - |
|  | Number of special register (D) points (points) |  | 256 | 2048 | 2048 | - | - |
|  | Number of link special relay (SB) points (points) |  | - | 2048 | 2048 | - | - |
|  | Number of link special register (SW) points (points) |  | - | 2048 | 2048 | - | - |
|  | Number of function input (FX) points (points) |  | - | 16 | 16 | - | - |
|  | Number of function output (FY) points (points) |  | - | 16 | 16 | - | - |
|  | Number of function register (FD) points (points) |  | - | 5 | 5 | - | - |

O: Usable $\Delta$ : Usable, however, a section of the specifications, i.e. setting methods, is different $\times$ : Not used

| Function | Description | A0J2HCPU | QnUCPU |  | Precautions for replacement | Reference sections |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  | Q00UJCPU | Q00UCPU |  |  |
| Number of comments | Number of comments (points) ${ }^{* 7}$ | Max. 1600 | Within capacity of program memory + standard ROM | Within capacity of program memory + standard RAM + standard ROM | - | - |
| Selfdiagnostics | Watch Dog Timer (WDT), memory error detection, CPU error detection, battery error detection, etc. | $\bigcirc$ | O | $\bigcirc$ | - | - |
| Operation mode during error | Stop/continue selectable | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | - | - |
| Switching output mode during STOP $\rightarrow$ RUN | Selectable from re-output operation status before STOP and output after operation execution | $\bigcirc$ | $\bigcirc$ | O | - | - |
| *5 This is the number of points usable on the program. |  |  |  |  |  |  |
| *6 The step relay (S) of QCPU is an |  |  | dedicated relay. |  |  |  |
| fers |  |  | number of points | riting to the CPU. |  |  |

### 2.3 Functional Comparisons of CPU Module

### 2.3.1 Functional comparisons between the A0J2HCPU and QCPU


*1 When PLC type in GX Developer changes, the device number will change.

O: Usable $\triangle$ : Usable, however, a section of the specifications, i.e. setting methods, is different $\times$ : Not used

|  | Function | Description | A0J2HCPU | QnUCPU | Precautions for replacement | Reference sections |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| $\begin{aligned} & \text { Or } \\ & \stackrel{\rightharpoonup}{0} \\ & 0 \end{aligned}$ | Write during RUN | Changes (writes) the program while the CPU is in RUN. | $\bigcirc$ | $\bigcirc$ | Allocate memory for Write during RUN setting in advance for QCPU (default: 500 steps). | $\begin{gathered} \text { Section } \\ 2.4 .3 \end{gathered}$ |
|  | Status latch | Stores the contents of all the devices to the memory cassette or memory card when an error, etc. occurred and monitors the stored data using the peripheral devices. | O | $\times$ | QCPU does not have the status latch function. | - |
|  | Sampling trace | Stores the data of the specified devices at every specified interval to the memory cassette or memory card and monitors the stored data using the peripheral devices to confirm the modified status of the device. | $\bigcirc$ | $\bigcirc$ | Q00UJCPU does not have the sampling trace function. | - |
|  | Offline switch | Separates the devices used by the OUT instruction from the operation processing of the sequence program. | $\bigcirc$ | $\times$ | QCPU does not have the offline switch function. | - |
|  | Self-diagnostics function | Examines the presence of an error, detects errors, stops the CPU, etc. | O | $\bigcirc$ | The error codes differ between AOJ2HCPU and QCPU. | - |

### 2.4 Precautions for Replacement of the CPU Module

### 2.4.1 Memory in the CPU module

Referring to the memory configuration shown in (1), consider the memories for storage according to memory capacity/application before replacement.
(1) Memory configuration and storable data


[^2]
## (2) Capacity of the various memories

The following shows the memory storing user programs, etc. and its capacity in each CPU module.

| Item | Model name |  |  |
| :--- | :---: | :---: | :---: |
|  | A0J2HCPU | QnUCPU |  |
|  |  | Q00UJCPU | Q00UCPU |
| Program memory | 32 K bytes | 40 K bytes ${ }^{* 1}$ | 40 K bytes ${ }^{* 1}$ |
| ROM memory ${ }^{*} 2$ | $4 / 8 / 16 \mathrm{~K}$ bytes | - | - |
| Standard RAM | - | - | 128 K bytes |
| Standard ROM 3 | - | 256 K bytes | 512 K bytes |

*1 The program memory indicates the flash ROM.
*2 The memory is the EP-ROM (option) for the ROM operation.
*3 The memory saves data such as a device comment and PC user data.

### 2.4.2 Keyword registration and password registration

A0J2HCPU prohibits programs from being read/written by keyword registration, whereas QCPU uses password entries to do so. The following shows the details of executable functions.

| Item | Model name |  |
| :---: | :---: | :---: |
|  | A0J2HCPU | QCPU |
|  |  | The equivalent function can be implemented by collectively setting a password to all the files. |
| Method to prohibit writing to program, etc. | The following attribute can be configured to the specified memory. <br> - Prohibition of read/write | (Supplement) <br> The following attributes can be configured to each specified file of the specified memory (drive) using the password. <br> - Prohibition of read/write display <br> - Prohibition of write |

### 2.4.3 Write during RUN

The amount of program capacity increased by executing Write during RUN has to be secured before the operation.

## (1) A0J2HCPU

The program capacity is determined by the parameter (memory capacity settings) and the program can be increased within the range of the memory capacity settings when executing Write during RUN.

## (2) QCPU

When writing to the programmable controller, configure the program capacity increased by executing Write during RUN. (This capacity setting is called "Allocate memory for Write during RUN". As a default, 500 steps are secured.)

The following shows the setting screen of "Allocate memory for Write during RUN" as reference.


### 2.4.4 I/O assignment

I/O assignment is as follows.

| Item | Model name |  |  |
| :--- | :--- | :--- | :---: |
|  | Regardless of the number of I/O points on the module, <br> the number of occupied I/O points of one module is <br> fixed at 64 points (32 input points for the first half and <br> 32 output points for the last half). <br> The number of I/O points on the extension base is also <br> fixed at 64 points and the head of the I/O numbers on <br> the extension base starts from X/Y100. | QCPU <br> Configure the I/O assignment using the parameters to <br> ensure that the I/O numbers match before and after <br> replacement. |  |

The following shows the I/O assignment when replacing the AOJ2 series I/O module with the Q series I/O module.
(1) Configuring the A0J2 series I/O module number to " 0 "

| A0J2 series I/O module |  |  | Q series I/O module |  |
| :--- | :---: | :---: | :---: | :---: |
| Number of I/O points | I/O address <br> (64 fixed points/ module) | Number of I/O points | I/O address |  |

## (2) Replacement example

The following shows an example of I/O assignment when AOJ2HCPU + system including extension base unit is replaced with the $Q$ series.
Replace with Q00UCPU in the case that AOJ2HCPU is used with a system including extension base unit. Replacement with QOOUJCPU is possible when the I/O number is X/Y00 to X/YFF ( 256 points) or less.
(I/O address before replacement)
I/O address of system including extension base unit


The head address of each I/O module is set with the I/O assignment of the parameter.



## 3

## I/O MODULES REPLACEMENT

### 3.1 Alternative I/O Module Models List

| A0J2H models to be discontinued |  | Alternative Q series models |  |
| :---: | :---: | :---: | :---: |
| Product name | Model name | Model name | Remarks (restrictions) |
|  | AOJ2-E32A*1 | QX10 | 1) Change in external wiring: Required <br> 2) Change in the number of modules: Required (2 modules necessary) <br> 3) Change in a program Change in the number of occupied I/O points: Required (64 points $\rightarrow 16$ points $\times 2$ ) <br> 4) Change in specifications <br> Change in rated input voltage: Not required <br> Change in rated input current: Required (Approx. $10 \mathrm{~mA} \rightarrow$ <br> Approx. 8mA) <br> Change in ON voltage/ON current: Required <br> Change in OFF voltage/OFF current: Required <br> Change in input resistance: Required <br> 5) Change in functions: Not required |
| Input module | A0J2-E32D*1 | QX40 | 1) Change in external wiring: Required <br> 2) Change in the number of modules: Required (2 modules necessary) <br> 3) Change in a program Change in the number of occupied I/O points: Required (64 points $\rightarrow 16$ points $\times 2$ ) <br> 4) Change in specifications <br> Change in rated input voltage: Required (12VDC not applicable) ${ }^{*}$ <br> Change in rated input current: Required (Approx. $7 \mathrm{~mA} \rightarrow$ Approx. 4mA) <br> Change in ON voltage/ON current: Required Change in OFF voltage/OFF current: Required Change in input resistance: Required <br> 5) Change in functions: Not required |


| A0J2H models to be discontinued | Alternative Q series models |  |  |
| :--- | :--- | :--- | :--- |
| Product name | Model name | Model name | Remarks (restrictions) |


| A0J2H models to be discontinued |  | Alternative Q series models |  |
| :---: | :---: | :---: | :---: |
| Product name | Model name | Model name | Remarks (restrictions) |
| Output module | A0J2-E24T* ${ }^{*}$ | QY50 | 1) Change in external wiring: Required <br> 2) Change in the number of modules: Required (2 modules necessary) <br> 3) Change in a program Change in the number of occupied I/O points: Required (64 points $\rightarrow 16$ points $\times 2$ ) <br> 4) Change in specifications <br> Change in rated output voltage: Not required <br> Change in rated output current: Not required <br> 5) Change in functions: Not required |
|  | A0J2E-E24T | QY80 | 1) Change in external wiring: Required <br> 2) Change in the number of modules: Required (2 modules necessary) <br> 3) Change in a program Change in the number of occupied I/O points: Required (64 points $\rightarrow 16$ points $\times 2$ ) <br> 4) Change in specifications <br> Change in rated output voltage: Not required <br> Change in rated output current: Required (output $0.8 \mathrm{~A} \rightarrow 0.5 \mathrm{~A}$ ) <br> 5) Change in functions: Not required |


| A0J2H models to be discontinued |  | Alternative Q series models |  |
| :---: | :---: | :---: | :---: |
| Product name | Model name | Model name | Remarks (restrictions) |
|  | A0J2-E28DR*1 | $\begin{aligned} & \text { QX40 } \\ & + \\ & \text { QY10 } \end{aligned}$ | 1) Change in external wiring: Required <br> 2) Change in the number of modules: Required <br> (2 modules necessary: QX40 $\times 1$ module, QY10 $\times 1$ module) <br> 3) Change in a program <br> Change in the number of occupied I/O points: Required (64 points $\rightarrow 16$ points $\times 2$ ) <br> 4) Change in specifications <br> Change in rated input voltage: Required (12VDC not applicable)* ${ }^{*}$ <br> Change in rated input current: Required (Approx. 7mA $\rightarrow$ <br> Approx. 4mA) <br> Change in ON voltage/ON current: Required <br> Change in OFF voltage/OFF current: Required <br> Change in input resistance: Required <br> Change in rated output voltage: Not required <br> Change in rated output current: Not required <br> (Note that a contact life is half.) <br> 5) Change in functions: Not required |
| I/O module | A0J2-E56DR*1 | $\begin{aligned} & \text { QX40 } \\ & + \\ & \text { QY10 } \end{aligned}$ | 1) Change in external wiring: Required <br> 2) Change in the number of modules: Required <br> (4 modules necessary: QX40 $\times 2$ modules, QY10 $\times 2$ modules) <br> 3) Change in a program <br> Change in the number of occupied I/O points: Not required <br> 4) Change in specifications <br> Change in rated input voltage: Required (12VDC not applicable) ${ }^{*}{ }^{2}$ <br> Change in rated input current: Required (Approx. $7 \mathrm{~mA} \rightarrow$ <br> Approx. 4mA) <br> Change in ON voltage/ON current: Required <br> Change in OFF voltage/OFF current: Required <br> Change in input resistance: Required <br> Change in rated output voltage: Not required <br> Change in rated output current: Not required <br> (Note that a contact life is half.) <br> 5) Change in functions: Not required |
|  | A0J2-E28DT*1 | $\begin{aligned} & \text { QX40 } \\ & + \\ & \text { QY50 } \end{aligned}$ | 1) Change in external wiring: Required <br> 2) Change in the number of modules: Required (2 modules necessary: QX40 $\times 1$ module, QY50 $\times 1$ module) <br> 3) Change in a program <br> Change in the number of occupied I/O points: Required (64 points $\rightarrow 16$ points $\times 2$ ) <br> 4) Change in specifications <br> Change in rated input voltage: Required (12VDC not applicable) ${ }^{*}{ }^{2}$ <br> Change in rated input current: Required (Approx. 7mA $\rightarrow$ Approx. 4mA) <br> Change in ON voltage/ON current: Required Change in OFF voltage/OFF current: Required Change in input resistance: Required Change in rated output voltage: Not required Change in rated output current: Not required <br> 5) Change in functions: Not required |


| A0J2H models to be discontinued |  | Alternative Q series models |  |
| :---: | :---: | :---: | :---: |
| Product name | Model name | Model name | Remarks (restrictions) |
| I/O module | A0J2-E28DT** | QX41Y41P | 1) Change in external wiring: Required (Terminal block $\rightarrow$ connector) <br> 2) Change in the number of modules: Not required <br> 3) Change in a program <br> Change in the number of occupied I/O points: Not required <br> (Number of actual I/O points: Input 16 points, output 12 points <br> $\rightarrow$ Input 32 points, output 32 points) <br> 4) Change in specifications <br> Change in rated input voltage: Required (12VDC not applicable) ${ }^{*}{ }^{2}$ <br> Change in rated input current: Required (Approx. $7 \mathrm{~mA} \rightarrow$ <br> Approx. 4mA) <br> Change in ON voltage/ON current: Required <br> Change in OFF voltage/OFF current: Required <br> Change in input resistance: Required <br> Change in rated load voltage: Not required <br> Change in rated load current: Required (Output $0.5 \mathrm{~A} \rightarrow 0.1 \mathrm{~A}$ ) <br> 5) Change in functions: Not required |
|  | A0J2-E56DT*1 | $\begin{aligned} & \text { QX40 } \\ & + \\ & \text { QY50 } \end{aligned}$ | 1) Change in external wiring: Required <br> 2) Change in the number of modules: Required <br> (4 modules necessary: QX40 $\times 2$ modules, QY50 $\times 2$ modules) <br> 3) Change in a program <br> Change in the number of occupied I/O points: Not required <br> 4) Change in specifications <br> Change in rated input voltage: Required (12VDC not applicable) ${ }^{*}{ }^{2}$ <br> Change in rated input current: Required (Approx. $7 \mathrm{~mA} \rightarrow$ <br> Approx. 4mA) <br> Change in ON voltage/ON current: Required <br> Change in OFF voltage/OFF current: Required <br> Change in input resistance: Required <br> Change in rated output voltage: Not required <br> Change in rated output current: Not required <br> 5) Change in functions: Not required |
|  |  | QX41Y41P | 1) Change in external wiring: Required (Terminal block $\rightarrow$ connector) <br> 2) Change in the number of modules: Not required <br> 3) Change in a program <br> Change in the number of occupied I/O points: Not required <br> (Number of actual I/O points: Input 32 points, output 24 points <br> $\rightarrow$ Input 32 points, output 32 points) <br> 4) Change in specifications <br> Change in rated input voltage: Required (12VDC not applicable) ${ }^{*}{ }^{2}$ <br> Change in rated input current: Required (Approx. $7 \mathrm{~mA} \rightarrow$ <br> Approx. 4 mA ) <br> Change in ON voltage/ON current: Required <br> Change in OFF voltage/OFF current: Required <br> Change in input resistance: Required <br> Change in rated load voltage: Not required <br> Change in rated load current: Required (Output $0.5 \mathrm{~A} \rightarrow 0.1 \mathrm{~A}$ ) <br> 5) Change in functions: Not required |


| A0J2H models to be discontinued |  | Alternative Q series models |  |
| :---: | :---: | :---: | :---: |
| Product name | Model name | Model name | Remarks (restrictions) |
|  | A0J2-E28AR*1 | $\begin{aligned} & \text { QX10 } \\ & + \\ & \text { QY10 } \end{aligned}$ | 1) Change in external wiring: Required <br> 2) Change in the number of modules: Required <br> (2 modules necessary: QX10 $\times 1$ module, QY10 $\times 1$ module) <br> 3) Change in a program <br> Change in the number of occupied I/O points: Required (64 points $\rightarrow 16$ points $\times 2$ ) <br> 4) Change in specifications <br> Change in rated input voltage: Not required <br> Change in rated input current: Required (Approx. 10mA $\rightarrow$ Approx. 8mA) <br> Change in ON voltage/ON current: Required <br> Change in OFF voltage/OFF current: Required <br> Change in input resistance: Required <br> Change in rated output voltage: Not required <br> Change in rated output current: Not required <br> (Note that a contact life is half.) <br> 5) Change in functions: Not required |
| I/O module | A0J2-E56AR*1 | $\begin{aligned} & \text { QX10 } \\ & + \\ & \text { QY10 } \end{aligned}$ | 1) Change in external wiring: Required <br> 2) Change in the number of modules: Required <br> (4 modules necessary: QX10 $\times 2$ modules, QY10 $\times 2$ modules) <br> 3) Change in a program <br> Change in the number of occupied I/O points: Not required <br> 4) Change in specifications <br> Change in rated input voltage: Not required <br> Change in rated input current: Required (Approx. $10 \mathrm{~mA} \rightarrow$ Approx. 8mA) <br> Change in ON voltage/ON current: Required <br> Change in OFF voltage/OFF current: Required <br> Change in input resistance: Required <br> Change in rated output voltage: Not required <br> Change in rated output current: Not required <br> (Note that a contact life is half.) <br> 5) Change in functions: Not required |
|  | A0J2-E28AS*1 | $\begin{aligned} & \text { QX10 } \\ & + \\ & \text { QY22 } \end{aligned}$ | 1) Change in external wiring: Required <br> 2) Change in the number of modules: Required (2 modules necessary: QX10 $\times 1$ module, QY22 $\times 1$ module) <br> 3) Change in a program <br> Change in the number of occupied I/O points: Required (64 points $\rightarrow 16$ points $\times 2$ ) <br> 4) Change in specifications <br> Change in rated input voltage: Not required <br> Change in rated input current: Required (Approx. $10 \mathrm{~mA} \rightarrow$ <br> Approx. 8mA) <br> Change in ON voltage/ON current: Required <br> Change in OFF voltage/OFF current: Required <br> Change in input resistance: Required <br> Change in rated output voltage: Not required <br> Change in rated output current: Not required <br> 5) Change in functions: Required (without fuse) |


| A0J2H models to be discontinued |  | Alternative Q series models |  |
| :---: | :---: | :---: | :---: |
| Product name | Model name | Model name | Remarks (restrictions) |
|  | A0J2-E56AS*1 | $\begin{aligned} & \text { QX10 } \\ & + \\ & \text { QY22 } \end{aligned}$ | 1) Change in external wiring: Required <br> 2) Change in the number of modules: Required <br> (4 modules necessary: QX10 $\times 2$ modules, QY22 $\times 2$ modules) <br> 3) Change in a program <br> Change in the number of occupied I/O points: Not required <br> 4) Change in specifications <br> Change in rated input voltage: Not required <br> Change in rated input current: Required (Approx. 10mA $\rightarrow$ <br> Approx. 8mA) <br> Change in ON voltage/ON current: Required <br> Change in OFF voltage/OFF current: Required <br> Change in input resistance: Required <br> Change in rated output voltage: Not required <br> Change in rated output current: Not required <br> 5) Change in functions: Required (without fuse) |
| I/O module | A0J2-E28DS*1 | $\begin{aligned} & \text { QX40 } \\ & + \\ & \text { QY22 } \end{aligned}$ | 1) Change in external wiring: Required <br> 2) Change in the number of modules: Required <br> (2 modules necessary: QX40 $\times 1$ module, QY22 $\times 1$ module) <br> 3) Change in a program <br> Change in the number of occupied I/O points: Required (64 points $\rightarrow 16$ points $\times 2$ ) <br> 4) Change in specifications <br> Change in rated input voltage: Required (12VDC not applicable)* ${ }^{*}$ <br> Change in rated input current: Required (Approx. $7 \mathrm{~mA} \rightarrow$ <br> Approx. 4mA) <br> Change in ON voltage/ON current: Required <br> Change in OFF voltage/OFF current: Required <br> Change in input resistance: Required <br> Change in rated output voltage: Not required <br> Change in rated output current: Not required <br> 5) Change in functions: Required (without fuse) |
|  | A0J2-E56DS*1 | $\begin{aligned} & \text { QX40 } \\ & + \\ & \text { QY22 } \end{aligned}$ | 1) Change in external wiring: Required <br> 2) Change in the number of modules: Required <br> (4 modules necessary: QX40 $\times 2$ modules, QY22 $\times 2$ modules) <br> 3) Change in a program <br> Change in the number of occupied I/O points: Not required <br> 4) Change in specifications <br> Change in rated input voltage: Required (12VDC not applicable)* ${ }^{*}$ <br> Change in rated input current: Required (Approx. 7mA $\rightarrow$ Approx. 4 mA ) <br> Change in ON voltage/ON current: Required Change in OFF voltage/OFF current: Required Change in input resistance: Required Change in rated output voltage: Not required Change in rated output current: Not required <br> 5) Change in functions: Required (without fuse) |


| A0J2H models to be discontinued |  | Alternative Q series models |  |
| :--- | :--- | :--- | :--- | :--- |
| Product name | Model name | Model name | Remarks (restrictions) |


| A0J2H models to be discontinued |  | Alternative Q series models |  |
| :---: | :---: | :---: | :---: |
| Product name | Model name | Model name | Remarks (restrictions) |
|  | A0J2E-E28DR | $\begin{aligned} & \text { QX80 } \\ & + \\ & \text { QY10 } \end{aligned}$ | 1) Change in external wiring: Required <br> 2) Change in the number of modules: Required <br> (2 modules necessary: QX80 $\times 1$ module, QY10 $\times 1$ module) <br> 3) Change in a program <br> Change in the number of occupied I/O points: Required (64 points $\rightarrow 16$ points $\times 2$ ) <br> 4) Change in specifications <br> - Input module Change in rated input voltage: Required (12VDC not applicable) ${ }^{*}{ }^{2}$ <br> Change in rated input current: Required (Approx. 7mA $\rightarrow$ Approx. 4mA) <br> Change in ON voltage/ON current: Required <br> Change in OFF voltage/OFF current: Required <br> Change in input resistance: Required <br> - Output module <br> Change in rated output voltage: Not required <br> Change in rated output current: Not required <br> 5) Change in functions <br> - Input module <br> Response time: The QX80 does not support the high-speed mode. <br> - Output module <br> Surge suppressor and fuse: The QY10 does not have those. |
| I/O module | A0J2E-E28DT | $\begin{aligned} & \text { QX80 } \\ & + \\ & \text { QY80 } \end{aligned}$ | 1) Change in external wiring: Required <br> 2) Change in the number of modules: Required <br> (2 modules necessary: QX80 $\times 1$ module, QY80 $\times 1$ module) <br> 3) Change in a program <br> Change in the number of occupied I/O points: Required (64 points $\rightarrow 16$ points $\times 2$ ) <br> 4) Change in specifications <br> - Input module <br> Change in rated input voltage: Required (12VDC not applicable) ${ }^{*}{ }^{2}$ <br> Change in rated input current: Required (Approx. $7 \mathrm{~mA} \rightarrow$ <br> Approx. 4 mA ) <br> Change in ON voltage/ON current: Required <br> Change in OFF voltage/OFF current: Required <br> Change in input resistance: Required <br> - Output module <br> Change in rated output voltage: Not required <br> Change in rated output current: Required ( $0.8 \mathrm{~A} \rightarrow 0.5 \mathrm{~A}$ ) <br> 5) Change in functions <br> - Input module <br> Response time: The QX80 does not support the high-speed mode. <br> - Output module <br> Short circuit protection function: The QY80 does not support the function. <br> Fuse: The QY80 has the fuse. |


| A0J2H models to be discontinued |  | Alternative Q series models |  |
| :---: | :---: | :---: | :---: |
| Product name | Model name | Model name | R |
|  | A0J2E-E56DR | $\begin{aligned} & \text { QX80 } \\ & + \\ & \text { QY10 } \end{aligned}$ | 1) Change in external wiring: Required <br> 2) Change in the number of modules: Required ( 4 modules necessary: QX80 $\times 2$ modules, QY10 $\times 2$ modules) <br> 3) Change in a program <br> Change in the number of occupied I/O points: Not required (64 points $\rightarrow 16$ points $\times 4$ ) <br> 4) Change in specifications <br> - Input module Change in rated input voltage: Required (12VDC not applicable) ${ }^{*}{ }^{2}$ <br> Change in rated input current: Required (Approx. 7mA $\rightarrow$ Approx. 4 mA ) <br> Change in ON voltage/ON current: Required <br> Change in OFF voltage/OFF current: Required <br> Change in input resistance: Required <br> - Output module <br> Change in rated output voltage: Not required <br> Change in rated output current: Not required <br> 5) Change in functions <br> - Input module <br> Response time: The QX80 does not support the high-speed mode. <br> - Output module <br> Surge suppressor and fuse: The QY10 does not have those. |
| I/O module | A0J2E-E56DT | $\begin{aligned} & \text { QX80 } \\ & + \\ & \text { QY80 } \end{aligned}$ | 1) Change in external wiring: Required <br> 2) Change in the number of modules: Required <br> (4 modules necessary: QX80 $\times 2$ modules, QY80 $\times 2$ modules) <br> 3) Change in a program <br> Change in the number of occupied I/O points: Not required (64 points $\rightarrow 16$ points $\times 4$ ) <br> 4) Change in specifications <br> - Input module <br> Change in rated input voltage: Required (12VDC not applicable) ${ }^{*}{ }^{2}$ <br> Change in rated input current: Required (Approx. 7mA $\rightarrow$ Approx. 4mA) <br> Change in ON voltage/ON current: Required <br> Change in OFF voltage/OFF current: Required <br> Change in input resistance: Required <br> - Output module <br> Change in rated output voltage: Not required <br> Change in rated output current: Required ( $0.8 \mathrm{~A} \rightarrow 0.5 \mathrm{~A}$ ) <br> 5) Change in functions <br> - Input module <br> Response time: The QX80 does not support the high-speed mode. <br> - Output module <br> Short circuit protection function: The QY80 does not support the function. <br> Fuse: The QY80 has the fuse. |


| A0J2H models to be discontinued |  | Alternative Q series models |  |
| :---: | :---: | :---: | :---: |
| Product name | Model name | Model name | Remarks (restrictions) |
| I/O module | A0J2E-E56DS | $\begin{array}{\|l} \text { QX80 } \\ + \\ \text { QY22 } \end{array}$ | 1) Change in external wiring: Required <br> 2) Change in the number of modules: Required <br> ( 4 modules necessary: QX80 $\times 2$ modules, QY22 $\times 2$ modules) <br> 3) Change in a program <br> Change in the number of occupied I/O points: Not required (64 points $\rightarrow 16$ points $\times 4$ ) <br> 4) Change in specifications <br> - Input module Change in rated input voltage: Required (12VDC not applicable) ${ }^{*}{ }^{2}$ <br> Change in rated input current: Required (Approx. $7 \mathrm{~mA} \rightarrow$ Approx. 4mA) <br> Change in ON voltage/ON current: Required <br> Change in OFF voltage/OFF current: Required <br> Change in input resistance: Required <br> - Output module <br> Change in rated output voltage: Not required <br> Change in rated output current: Required (minimum load current: $10 \mathrm{~mA} \rightarrow 25 \mathrm{~mA}$ ) <br> 5) Change in functions <br> - Input module <br> Response time: The QX80 does not support the high-speed mode. <br> - Output module <br> Fuse: The QY22 does not have the fuse. |

[^3]
### 3.2 I/O Module Specifications Comparison

### 3.2.1 Input module specifications comparison

(1) Specifications comparison between A0J2-E32A and QX10

|  |  |  |  | Compatible, | Partially changed, $\times$ : Incompatible |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Specifications |  | A0J2-E32A | QX10 | Compatibility | Precautions for replacement |
| Number of input points |  | 32 points | 16 points | $\triangle$ | When seventeen or more points are used, use two QX10 modules. |
| Insulation method |  | Photocoupler | Photocoupler | $\bigcirc$ |  |
| Rated input voltage |  | 100-120VAC, $50 / 60 \mathrm{~Hz}$ | 100-120VAC, $50 / 60 \mathrm{~Hz}$ | $\bigcirc$ |  |
| Input voltage distortion |  | - | Within 5\% | - |  |
| Rated input current |  | $\begin{gathered} 10 \mathrm{~mA} \\ (100 \mathrm{VAC}, 60 \mathrm{~Hz}) \end{gathered}$ | Approx. 8mA (100VAC, 60Hz) <br> Approx. 7 mA (100VAC, 50 Hz ) | $\triangle$ | Rated input current is smaller.*1 |
| Inrush current |  | Max. 300 mA , within 0.3 ms (with 132VAC) | Max. 200 mA , within 1 ms (with 132VAC) | $\bigcirc$ |  |
| Operating voltage range |  | $\begin{aligned} & 85 \text { to 132VAC } \\ & (50 / 60 \mathrm{~Hz} \pm 5 \%) \end{aligned}$ | $\begin{aligned} & 85 \text { to } 132 \mathrm{VAC} \\ & (50 / 60 \mathrm{~Hz} \pm 3 \mathrm{~Hz}) \end{aligned}$ | $\bigcirc$ |  |
| Maximum number of simultaneous input points |  | 100\% (32 points) simultaneously ON | Refer to the derating chart. ${ }^{*}$ | $\triangle$ | Use it within the range shown in the derating chart. |
| ON voltage/ON current |  | 80VAC or more/6mA or more | 80VAC or more/5mA or more ( $50 \mathrm{~Hz}, 60 \mathrm{~Hz}$ ) | $\bigcirc$ |  |
| OFF voltage/OFF current |  | 40VAC or less/4mA or less | 30VAC or less/1.7mA or less $(50 \mathrm{~Hz}, 60 \mathrm{~Hz})$ | $\triangle$ | OFF current is smaller. ${ }^{* 1}$ |
| Input impedance |  | Approx. $10 \mathrm{k} \Omega(60 \mathrm{~Hz})$, <br> Approx. $12 \mathrm{k} \Omega(50 \mathrm{~Hz})$ | Approx. $12 \mathrm{k} \Omega(60 \mathrm{~Hz})$, <br> Approx. $15 \mathrm{k} \Omega(50 \mathrm{~Hz})$ | $\triangle$ | Input impedance is greater. ${ }^{* 1}$ |
| Response time | $\mathrm{OFF} \rightarrow \mathrm{ON}$ | 15 ms or less ( $6 \mathrm{~ms} \mathrm{TYP)}$. | 15 ms or less $(100 \mathrm{VAC} 50 \mathrm{~Hz}, 60 \mathrm{~Hz})$ | $\bigcirc$ |  |
|  | ON $\rightarrow$ OFF | 35 ms or less (16ms TYP.) | 15 ms or less <br> (100VAC $50 \mathrm{~Hz}, 60 \mathrm{~Hz}$ ) | $\bigcirc$ |  |
| Common terminal arrangement |  | 16 points/common <br> (Common terminal: TB17, TB34) | 16 points/common (Common terminal: TB17) | $\bigcirc$ |  |
| Operation indication |  | Available (Turning ON the input turns LED ON) | ON indication (LED) | O |  |
| External connection method |  | 36-point terminal block connector (M3 $\times 6$ screws) | 18-point terminal block $\text { (M3 } \times 6 \text { screws) }$ | $\times$ |  |
| Applicable wire size |  | 0.75 to $2 \mathrm{~mm}^{2}$ | 0.3 to $0.75 \mathrm{~mm}^{2}$ core (Outside diameter: 2.8 mm or less) | $\times$ | Wiring change is required. |
| Applicable solderless terminal |  | $\begin{gathered} \text { 1.25-3, 1.25-YS3A, } \\ 2-\mathrm{S} 3,2-\mathrm{YS} 3 \mathrm{~A}, \\ \mathrm{~V} 1.25-3, \mathrm{~V} 1.25-\mathrm{YS} 3 \mathrm{~A}, \\ \mathrm{~V} 2-\mathrm{S} 3, \mathrm{~V} 2-\mathrm{YS} 3 \mathrm{~A} \end{gathered}$ | R1.25-3 <br> (Sleeved solderless terminals cannot be used.) | $\times$ |  |
| Current consumption |  | 0.105A (TYP. All points are ON.) | 0.05A (TYP. All points are ON.) | $\bigcirc$ |  |
| External dimensions |  | 250(H) $\times 132(\mathrm{~W}) \times 41$ (D) mm | $98(\mathrm{H}) \times 27.4(\mathrm{~W}) \times 90$ (D) mm | $\times$ | The dimensions are different. |
| Weight |  | 0.68 kg | 0.17 kg | $\triangle$ |  |

*1 Check the specifications of the sensor or switches to be connected to the QX10.
*2 The following shows the derating chart.

(2) Specifications comparison between A0J2-E32D and QX40

O : Compatible, $\Delta$ : Partially changed, x : Incompatible

| Specifications |  | A0J2-E32D | QX40 | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Number of input points |  | 32 points | 16 points | $\Delta$ | When seventeen or more points are used, use two QX40 modules. |
| Insulation method |  | Photocoupler | Photocoupler | $\bigcirc$ |  |
| Rated input voltage |  | 12VDC/24VDC | 24VDC | $\triangle$ | 12VDC cannot be used. ${ }^{* 1}$ |
| Rated input current |  | Approx. 3mA/Approx. 7 mA | Approx. 4 mA | $\triangle$ | Rated input current is smaller.*2 |
| Operating voltage range |  | $\begin{gathered} 10.2 \text { to } 26.4 \mathrm{VDC} \\ \text { (Ripple ratio within } 5 \% \text { ) } \end{gathered}$ | $\begin{gathered} 20.4 \text { to } 28.8 \mathrm{VDC} \\ \text { (Ripple ratio within 5\%) } \end{gathered}$ | $\Delta$ | 12VDC cannot be used. ${ }^{* 1}$ |
| Maximum number of simultaneous input points |  | 100\% (32 points) <br> simultaneously ON | 100\% simultaneously ON | $\bigcirc$ |  |
| ON voltage/ON current |  | 9.5 VDC or more/2.6mA or more | 19VDC or more/3mA or more | $\triangle$ | 12VDC cannot be used. ${ }^{* 1}$ |
| OFF voltage/OFF current |  | 6 VDC or less/1.0mA or less | 11VDC or less/1.7mA or less | $\Delta$ | 12VDC cannot be used. ${ }^{* 1}$ |
| Input resistance |  | Approx. $3.4 \mathrm{k} \Omega$ | Approx. $5.6 \mathrm{k} \Omega$ | $\triangle$ | Input resistance is greater. ${ }^{*}$ |
| Input form |  | Sink input (Input current flows off.) | Positive common | $\bigcirc$ | The name is different, but the specificaton is equal. |
| Response time | OFF $\rightarrow$ ON | 10 ms or less (6ms TYP.) | $1 \mathrm{~ms} / 5 \mathrm{~ms} / 10 \mathrm{~ms} / 20 \mathrm{~ms} / 70 \mathrm{~ms}$ or less <br> (Set it with CPU parameter.) Initial setting is 10 ms . | O | Input response time of the parameter must be used at the initial value ( 10 ms ). |
|  | ON $\rightarrow$ OFF | 10 ms or less ( $7.5 \mathrm{~ms} \mathrm{TYP)}$. | $1 \mathrm{~ms} / 5 \mathrm{~ms} / 10 \mathrm{~ms} / 20 \mathrm{~ms} / 70 \mathrm{~ms}$ or less <br> (Set it with CPU parameter.) Initial setting is 10 ms . | $\bigcirc$ |  |
| Common terminal arrangement |  | 16 points/common (Common terminal: TB17, TB34) | 16 points/common (Common terminal: TB17) | $\bigcirc$ |  |
| Operation indication |  | Available (Turning ON the input turns LED ON) | ON indication (LED) | $\bigcirc$ |  |
| External connection method |  | 36-point terminal block connector (M3 $\times 6$ screws) | 18-point terminal block <br> (M3 $\times 6$ screws) | $\times$ | Wiring change is required. |
| Applicable wire size |  | 0.75 to $2 \mathrm{~mm}^{2}$ | 0.3 to $0.75 \mathrm{~mm}^{2}$ core (Outside diameter: 2.8 mm or less) | $\times$ |  |
| Applicable solderless terminal |  | $\begin{gathered} \text { 1.25-3, 1.25-YS3A, } \\ 2-\mathrm{S} 3,2-\mathrm{YS} 3 \mathrm{~A}, \\ \mathrm{~V} 1.25-3, \mathrm{~V} 1.25-\mathrm{YS} 3 \mathrm{~A}, \\ \mathrm{~V} 2-\mathrm{S} 3, \mathrm{~V} 2-\mathrm{YS} 3 \mathrm{~A} \end{gathered}$ | R1.25-3 <br> (Sleeved solderless terminals cannot be used.) | $\times$ |  |
| Current consumption |  | 0.105A (TYP. All points are ON.) | 0.05 A (TYP. All points are ON.) | $\bigcirc$ |  |
| External dimensions |  | 250(H) $\times 132(\mathrm{~W}) \times 41$ (D) mm | 98(H) $\times 27.4(\mathrm{~W}) \times 90$ (D) mm | $\times$ | The dimensions are different. |
| Weight |  | 0.63 kg | 0.16 kg | $\triangle$ |  |

*1 Use the QX70 when using 12VDC.
*2 Check the specifications of the sensor or switches to be connected to the QX40.
(3) Specifications comparison between A0J2E-E32D and QX80

O : Compatible, $\Delta$ : Partially changed, x : Incompatible

| Specifications |  | A0J2E-E32D | QX80 | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Number of input points |  | 32 points | 16 points | $\Delta$ | When seventeen or more points are used, use two QX80 modules. |
| Insulation method |  | Photocoupler | Photocoupler | 0 |  |
| Rated input voltage |  | 12VDC/24VDC | 24VDC | $\triangle$ | 12VDC cannot be used. ${ }^{* 1}$ |
| Rated input current |  | Approx. 3mA/Approx. 7 mA | Approx. 4 mA | $\Delta$ | Rated input current is smaller.*2 |
| Operating voltage range |  | $\begin{gathered} 10.2 \text { to } 26.4 \mathrm{VDC} \\ \text { (Ripple ratio within 5\%) } \\ \hline \end{gathered}$ | $\begin{gathered} 20.4 \text { to } 28.8 \mathrm{VDC} \\ \text { (Ripple ratio within 5\%) } \end{gathered}$ | $\Delta$ | 12VDC cannot be used. ${ }^{* 1}$ |
| Maximum number of simultaneous input points |  | 100\% (32 points) <br> simultaneously ON | 100\% simultaneously ON | O |  |
| ON voltage/ON current |  | 9.5VDC or more/2.6mA or more | 19VDC or more/3mA or more | $\Delta$ | 12VDC cannot be used. ${ }^{* 1}$ |
| OFF voltage/OFF current |  | 6 VDC or less $/ 1.0 \mathrm{~mA}$ or less | 11VDC or less/1.7mA or less | $\triangle$ | 12VDC cannot be used. ${ }^{* 1}$ |
| Input impedance |  | Approx. $3.4 \mathrm{k} \Omega$ | Approx. $5.6 \mathrm{k} \Omega$ | $\triangle$ | Input resistance is greater. ${ }^{*}$ |
| Input form |  | Source input (Input current flows in.) | Negative common | O | The name is different, but the specificaton is equal. |
| Response time | $\mathrm{OFF} \rightarrow \mathrm{ON}$ | 5.5 ms (TYP.) | $1 \mathrm{~ms} / 5 \mathrm{~ms} / 10 \mathrm{~ms} / 20 \mathrm{~ms} / 70 \mathrm{~ms}$ or less (Set it with CPU parameter.) Initial setting is 10 ms . | O | Set the input response time of the parameter to 5 ms before use. |
|  | $\mathrm{ON} \rightarrow \mathrm{OFF}$ | 6.0 ms (TYP.) | $1 \mathrm{~ms} / 5 \mathrm{~ms} / 10 \mathrm{~ms} / 20 \mathrm{~ms} / 70 \mathrm{~ms}$ or less (Set it with CPU parameter.) Initial setting is 10 ms . | O |  |
| Response <br> time <br> (High <br> speed <br> mode) <br> (Only upper <br> 8 points) | $\mathrm{OFF} \rightarrow \mathrm{ON}$ | 0.5 ms or less | $1 \mathrm{~ms} / 5 \mathrm{~ms} / 10 \mathrm{~ms} / 20 \mathrm{~ms} / 70 \mathrm{~ms}$ or less <br> (Set it with CPU parameter.) Initial setting is 10 ms . | $\Delta$ | Set the input response time of the parameter to 1 ms before use. |
|  | ON $\rightarrow$ OFF | 1.0 ms or less | $1 \mathrm{~ms} / 5 \mathrm{~ms} / 10 \mathrm{~ms} / 20 \mathrm{~ms} / 70 \mathrm{~ms}$ or less (Set it with CPU parameter.) Initial setting is 10 ms . | O |  |
| Common terminal arrangement |  | 16 points/common (Common terminal: TB17, TB34) | 16 points/common (Common terminal: TB18) | $\bigcirc$ |  |
| Operation indication |  | Available (Turning ON the input turns LED ON) | ON indication (LED) | $\bigcirc$ |  |
| External connection method |  | 36-point terminal block connector (M3 $\times 6$ screws) | 18-point terminal block <br> (M3 $\times 6$ screws) | $\times$ | Wiring change is required. |
| Applicable wire size |  | 0.75 to $2 \mathrm{~mm}^{2}$ | 0.3 to $0.75 \mathrm{~mm}^{2}$ core (Outside diameter: 2.8 mm or less) | $\times$ |  |
| Applicable solderless terminal |  | $\begin{gathered} \text { 1.25-3, 1.25-YS3A, 2-S3, } \\ 2-Y S 3 A, \\ \text { V1.25-3, V1.25-YS3A, } \\ \text { V2-S3, V2-YS3A } \end{gathered}$ | R1.25-3 <br> (Sleeved solderless terminals cannot be used.) | $\times$ |  |
| Current consumption |  | 0.105A (TYP. All points are ON.) | 0.05A (TYP. All points are ON.) | $\bigcirc$ |  |
| External dimensions |  | 250(H) $\times 132(\mathrm{~W}) \times 41(\mathrm{D}) \mathrm{mm}$ | 98(H) $\times 27.4(\mathrm{~W}) \times 90$ (D) mm | $\times$ | The dimensions are different. |
| Weight |  | 0.61 kg | 0.16 kg | $\triangle$ |  |

*1 Use the QX70 when using 12VDC.
*2 Check the specifications of the sensor or switches to be connected to the QX80.

### 3.2.2 Output module specifications comparison

(1) Specifications comparison between A0J2-E24R and QY10

O: Compatible, $\Delta$ : Partially changed, $\times$ : Incompatible

| Specifications |  | A0J2-E24R | QY10 | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Number of output points |  | 24 points | 16 points | $\Delta$ | When seventeen or more points are used, use two QY10 modules. |
| Insulation method |  | Photocoupler | Relay | $\Delta$ | Although the insulation methods differ, the performance of the Insulation is the same. |
| Rated switching voltage/ current |  | 24VDC 2A (Resistance load)/ point <br> 240VAC $2 \mathrm{~A}(\operatorname{COS} \phi=1) /$ point 5A/common | 24VDC 2A (Resistance load)/ point 240VAC $2 \mathrm{~A}(\operatorname{COS} \phi=1) /$ point 8A/common | 0 |  |
| Minimum switching load |  | 5VDC 1mA | 5 VDC 1 mA | $\bigcirc$ |  |
| Maximum switching voltage |  | 264VAC, 125VDC | 264VAC, 125VDC | $\bigcirc$ |  |
| Response time | OFF $\rightarrow$ ON | 10 ms or less | 10 ms or less | $\bigcirc$ |  |
|  | ON $\rightarrow$ OFF | 12 ms or less | 12 ms or less | $\bigcirc$ |  |
| Mechanical life |  | 20 million times or more | 20 million times or more | $\bigcirc$ |  |
| Electrical life |  | Rated switching voltage/current load 200,000 times or more | Rated switching voltage/current load 100,000 times or more | $\Delta$ |  |
|  |  | 200 VAC 1.5A, 240 VAC 1A $(\operatorname{COS} \phi=0.7)$ 200,000 times or more 200VAC 0.75A, 240VAC 0.5A $(\operatorname{COS} \phi=0.35)$ 200,000 times or more <br> 24VDC 1A, 100VDC 0.1A (L/R=7ms) 200,000 times or more | 200VAC 1.5A, 240VAC 1A $(\operatorname{COS} \phi=0.7)$ 100,000 times or more <br> 200VAC 0.4A, 240VAC 0.3A $(\operatorname{COS} \phi=0.7) 300,000$ times or more <br> 200VAC 1A, 240VAC 0.5A $(\operatorname{COS} \phi=0.35)$ 100,000 times or more 200VAC 0.3A, 240VAC 0.15A $(\operatorname{COS} \phi=0.35) 300,000$ times or more <br> 24VDC 1A, 100VDC 0.1A <br> (L/R=7ms) 100,000 times or more <br> 24VDC 0.3A, 100VDC 0.03A (L/R=7ms) 300,000 times or more | $\Delta$ | Reduce the exchange intervals of the modules as Mechanical/ Electrical Life is cut to about half. |
| Maximum switching frequency |  | 3600 times/hr | 3600 times/hr | $\bigcirc$ |  |
| Common terminal arrangement |  | 8 points/common (Common terminal: TB9, TB19, TB29) | 16 points/common (Common terminal: TB17) | $\Delta$ | As the common changes from three commons to a common, wiring with a different voltage for each common is not possible. |
| Operation indication |  | Available (Turning ON the output turns LED ON) | ON indication (LED) | $\bigcirc$ |  |
| External supply power (relay coil driving power) | Voltage | $24 \mathrm{VDC} \pm 10 \%$ Ripple voltage $4 \mathrm{Vp}-\mathrm{p}$ or less | - | O |  |
|  | Current | 230mA (TYP. 24VDC All points are ON.) | - | O | External supply power is not required. |

O : Compatible, $\Delta$ : Partially changed, $\times$ : Incompatible

| Specifications | A0J2-E24R | QY10 | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: |
| External connection method | 36-point terminal block connector (M3 $\times 6$ screws) | 18-point terminal block <br> (M3 $\times 6$ screws) | $\times$ |  |
| Applicable wire size | 0.75 to $2 \mathrm{~mm}^{2}$ | 0.3 to $0.75 \mathrm{~mm}^{2}$ core (Outside diameter: 2.8 mm or less) | $\times$ | Wiring change is required. |
| Applicable solderless terminal | $\begin{gathered} \hline 1.25-3,1.25-\mathrm{YS} 3 \mathrm{~A}, \\ 2-\mathrm{S} 3,2-\mathrm{YS} 3 \mathrm{~A}, \\ \mathrm{~V} 1.25-3, \mathrm{~V} 1.25-\mathrm{YS} 3 \mathrm{~A}, \\ \mathrm{~V} 2-\mathrm{S} 3, \mathrm{~V} 2-\mathrm{YS} 3 \mathrm{~A} \end{gathered}$ | R1.25-3 <br> (Sleeved solderless terminals cannot be used.) | $\times$ |  |
| Current consumption | 0.145 A (TYP. All points are ON.) | 0.43 A (TYP. All points are ON.) | $\Delta$ | Review current capacity since current consumption is increased. |
| External dimensions | $250(\mathrm{H}) \times 132(\mathrm{~W}) \times 41(\mathrm{D}) \mathrm{mm}$ | 98(H) $\times 27.4(\mathrm{~W}) \times 90(\mathrm{D}) \mathrm{mm}$ | $\times$ | The dimensions are different. |
| Weight | 0.71 kg | 0.22 kg | $\triangle$ |  |

(2) Specifications comparison between A0J2E-E24R and QY10

O : Compatible, $\Delta$ : Partially changed, x : Incompatible

| Specifications |  | A0J2E-E24R | QY10 | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Number of output points |  | 24 points | 16 points | $\triangle$ | When seventeen or more points are used, use two QY10 modules. |
| Insulation method |  | Photocoupler | Relay isolation | $\Delta$ | Although the insulation methods differ, the performance of the insulation is the same. |
| Rated switching voltage/ current |  | 24VDC 2A (Resistance load)/ point <br> 240VAC $2 \mathrm{~A}(\operatorname{COS} \phi=1) /$ point 5A/common | ```24VDC 2A (Resistance load)/ point 240VAC 2A (COS }=1)/\mathrm{ point 8A/common``` | O |  |
| Minimum switching load |  | 5VDC 1mA | 5VDC 1mA | $\bigcirc$ |  |
| Maximum switching voltage |  | 250VAC, 125VDC | 264VAC, 125VDC | $\bigcirc$ |  |
| Response time | OFF $\rightarrow$ ON | 10 ms or less | 10 ms or less | $\bigcirc$ |  |
|  | ON $\rightarrow$ OFF | 12 ms or less | 12 ms or less | $\bigcirc$ |  |
| Mechanical life |  | 20 million times or more | 20 million times or more | $\bigcirc$ |  |
| Electrical life |  | Rated switching voltage/current load 200,000 times or more | Rated switching voltage/current load 100,000 times or more | $\Delta$ |  |
|  |  | 200VAC 1.5A, 240VAC 1 A <br> (COS $\phi=0.7$ ) 200,000 times or more <br> 200VAC 0.75A, 240VAC 0.5A $(\operatorname{COS} \phi=0.35) 200,000$ times or more <br> 24VDC 1A, 100VDC 0.1A (L/R=7ms) 200,000 times or more | 200VAC 1.5A, 240VAC 1A $\left(\operatorname{COS}_{\phi}=0.7\right)$ 100,000 times or more <br> 200VAC 0.4A, 240VAC 0.3A $(\operatorname{COS} \phi=0.7) 300,000$ times or more <br> 200VAC 1A, 240VAC 0.5A (COS $\phi=0.35$ ) 100,000 times or more <br> 200VAC 0.3A, 240VAC 0.15A (COS $\phi=0.35$ ) 300,000 times or more <br> 24VDC 1A, 100VDC 0.1A (L/R=7ms) 100,000 times or more 24VDC 0.3A, 100VDC 0.03A (L/R=7ms) 300,000 times or more | $\Delta$ | Reduce the exchange intervals of the modules as Mechanical/ Electrical Life is cut to about half. |
| Maximum switching frequency |  | 3600 times/hr | 3600 times/hr | $\bigcirc$ |  |
| Surge suppressor |  | Varistor (387 to 473V) | None | $\times$ | The varistor is not built in. ${ }^{* 1}$ |
| Fuse |  | Available (8A)MF51NM8 or FGMA250V8A | None | $\times$ | The fuse is not built in. ${ }^{*}$ |
| Common terminal arrangement |  | 8 points/common (Common terminal: TB9, TB19, TB29) | 16 points/common (Common terminal: TB17) | $\Delta$ | As the common changes from three commons to a common, wiring with a different voltage for each common is not possible. |
| Operation indication |  | Available (Turning ON the output turns LED ON)) | ON indication (LED) | $\bigcirc$ |  |
| External supply power (relay coil driving power) | Voltage | $24 \mathrm{VDC} \pm 10 \%$ Ripple voltage 4 Vp -p or less | - | $\bigcirc$ |  |
|  | Current | 220 mA (24VDC All points are ON.) | - | $\bigcirc$ | External supply power is not required. |

O : Compatible, $\Delta$ : Partially changed, x : Incompatible

| Specifications | A0J2E-E24R | QY10 | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: |
| External connection method | 36-point terminal block connector (M3 $\times 6$ screws) | 18-point terminal block <br> (M3 $\times 6$ screws) | $\times$ | Wiring change is required. |
| Applicable wire size | 0.75 to $2 \mathrm{~mm}^{2}$ | 0.3 to $0.75 \mathrm{~mm}^{2}$ core (Outside diameter: 2.8 mm or less) | $\times$ |  |
| Applicable solderless terminal | $\begin{gathered} \text { 1.25-3, 1.25-YS3A, } \\ 2-\mathrm{S} 3,2-\mathrm{YS} 3 \mathrm{~A}, \\ \mathrm{~V} 1.25-3, \mathrm{~V} 1.25-\mathrm{YS} 3 \mathrm{~A}, \\ \mathrm{~V} 2-\mathrm{S} 3, \mathrm{~V} 2-\mathrm{YS} 3 \mathrm{~A} \end{gathered}$ | R1.25-3 <br> (Sleeved solderless terminals cannot be used.) | $\times$ |  |
| Current consumption | 0.145 A (TYP. All points are ON. | 0.43 A (TYP. All points are ON.) | $\Delta$ | Review current capacity since current consumption is increased. |
| External dimensions | $250(\mathrm{H}) \times 132(\mathrm{~W}) \times 41(\mathrm{D}) \mathrm{mm}$ | 98(H) $\times 27.4(\mathrm{~W}) \times 90(\mathrm{D}) \mathrm{mm}$ | $\times$ | The dimensions are different. |
| Weight | 0.75 kg | 0.22 kg | $\triangle$ |  |

*1 Connect the varistor exteriorly to reduce noise.
*2 Install a fuse for each external terminal point to prevent the burnout of the external devices and modules during load shorts.
(3) Specifications comparison between A0J2-E24S and QY22*1

O : Compatible, $\Delta$ : Partially changed, x : Incompatible

| Specifications |  | A0J2-E24S | QY22 | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Number of output points |  | 24 points | 16 points | $\Delta$ | When seventeen or more points are used, use two QY22 modules. |
| Insulation method |  | Photocoupler | Photocoupler | $\bigcirc$ |  |
| Rated load voltage |  | 100 to 240VAC, 40 to 70 Hz | $\begin{gathered} 100 \text { to } 240 \mathrm{VAC} \\ 50 / 60 \mathrm{~Hz} \pm 5 \% \end{gathered}$ | $\Delta$ | The frequency will be low. Check the specifications of external devices. |
| Maximum load voltage |  | 264VAC | 264VAC | $\bigcirc$ |  |
| Maximum load current |  | 0.6A/point, 2.4A/common | 0.6A/point, 4.8A/common | $\bigcirc$ |  |
| Minimum load voltage/ current |  | 24 VAC 100 mA 100VAC 10 mA 240VAC 10 mA | 24 VAC 100 mA 100VAC 25 mA 240VAC 25 mA | $\Delta$ | The minimum load current is greater. Use caution on selecting the load to use. |
| Maximum inrush current |  | 20A 10ms or less 8 A 100ms or less | 20A 1 cycle or less | $\bigcirc$ |  |
| Leakage current at OFF |  | $\begin{gathered} 1.5 \mathrm{~mA}(120 \mathrm{VAC} 60 \mathrm{~Hz}) \\ 3 \mathrm{~mA}(240 \mathrm{VAC} 60 \mathrm{~Hz}) \end{gathered}$ | 1.5 mA or less (During 120 V 60 Hz ) 3 mA or less (During 240 V 60 Hz ) | $\bigcirc$ |  |
| Maximum voltage drop at ON |  | 1.5 V or less ( 100 to 600 mA ) 1.8 V or less ( 100 mA or less) 2 V or less (10 to 50 mA ) | 1.5 V or less | $\bigcirc$ |  |
| Response time | $\mathrm{OFF} \rightarrow \mathrm{ON}$ | 1 ms or less | 1 ms or less | $\bigcirc$ |  |
|  | ON $\rightarrow$ OFF | 0.5 cycle +1 ms or less | $1 \mathrm{~ms}+0.5$ cycle or less (Rated load, resistance load) | $\bigcirc$ |  |
| Surge suppressor |  | $\begin{gathered} \text { CR absorber } \\ (0.022 \mu \mathrm{~F}+47 \Omega) \end{gathered}$ | CR absorber | 0 |  |
| Common terminal arrangement |  | 8 points/common (Common terminal: TB9, TB19, TB29) | 16 points/common (Common terminal: TB17) | $\triangle$ | As the common changes from three commons to a common, wiring with a different voltage for each common is not possible. |
| Operation indication |  | Available (Turning ON the output turns LED ON) | ON indication (LED) | $\bigcirc$ |  |
| Fuse rating |  | High speed type fuse 3.2A <br> (1/common) HP-32 | None | $\times$ |  |
| Fuse blown indication |  | Available (When a fuse blown occurs, LED is lit, and a signal is output to the CPU) | - | $\times$ | The fuse is not built in. ${ }^{*}$ |
| External connection method |  | 36-point terminal block connector (M3 $\times 6$ screws) | 18-point terminal block <br> (M3 $\times 6$ screws) | $\times$ |  |
| Applicable wire size |  | 0.75 to $2 \mathrm{~mm}^{2}$ | 0.3 to $0.75 \mathrm{~mm}^{2}$ core (Outside diameter: 2.8 mm or less) | $\times$ | Wiring change is required. |
| Applicable solderless terminal |  | $\begin{gathered} \text { 1.25-3, 1.25-YS3A, } \\ 2-\mathrm{S} 3,2-\mathrm{YS} 3 \mathrm{~A}, \\ \mathrm{~V} 1.25-3, \mathrm{~V} 1.25-\mathrm{YS} 3 \mathrm{~A}, \\ \mathrm{~V} 2-\mathrm{S} 3, \mathrm{~V} 2-\mathrm{YS} 3 \mathrm{~A} \end{gathered}$ | R1.25-3 <br> (Sleeved solderless terminals cannot be used.) | $\times$ |  |
| Current consumption |  | 400 mA (TYP. All points are ON.) | 0.25A (MAX. All points are ON.) | $\bigcirc$ |  |
| External dimensions |  | 250(H) $\times 132(\mathrm{~W}) \times 41(\mathrm{D}) \mathrm{mm}$ | $98(\mathrm{H}) \times 27.4(\mathrm{~W}) \times 112.3(\mathrm{D}) \mathrm{mm}$ | $\times$ | The dimensions are different. |
| Weight |  | 0.70 kg | 0.40 kg | $\triangle$ |  |

*1 Consider the characteristics of the triac and observe the necessary precautions by referring to Section 3.3 (3) before replacing the modules.
*2 Install a fuse for each external terminal point to prevent the burnout of the external devices and modules during load shorts. In addition, when a fuse blown indication is necessary, configure an external circuit.
(4) Specifications comparison between A0J2-E24T and QY50

O : Compatible, $\Delta$ : Partially changed, x : Incompatible

| Specifications |  | A0J2-E24T | QY50 | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Number of output points |  | 24 points | 16 points | $\Delta$ | When seventeen or more points are used, use two QY50 modules. |
| Insulation method |  | Photocoupler | Photocoupler | $\bigcirc$ |  |
| Rated load voltage |  | 12VDC/24VDC | 12VDC/24VDC | $\bigcirc$ |  |
| Operating load voltage range |  | 10.2 to 30VDC | 10.2 to 28.8VDC | $\Delta$ | Voltages exceeding 28.8VDC cannot be applied. |
| Maximum load current |  | 0.5A/point, 4A/common | 0.5A/point, 4A/common | $\triangle$ | Use caution on the used current of the entire module. |
| Maximum inrush current |  | 4A, 10ms or less | 4A, 10ms or less | $\bigcirc$ |  |
| Leakage current at OFF |  | 0.1 mA or less | 0.1 mA or less | $\bigcirc$ |  |
| Maximum voltage drop at ON |  | $\begin{aligned} & \hline 0.9 \mathrm{VDC} \text { (TYP.) } 0.5 \mathrm{~A} \\ & 1.5 \mathrm{VDC} \text { (MAX.) } 0.5 \mathrm{~A} \end{aligned}$ | $\begin{aligned} & \hline 0.2 \mathrm{VDC} \text { (TYP.) } 0.5 \mathrm{~A} \\ & 0.3 \mathrm{VDC} \text { (MAX.) } 0.5 \mathrm{~A} \end{aligned}$ | $\bigcirc$ |  |
| Response time | OFF $\rightarrow$ ON | 2 ms or less | 1 ms or less | $\bigcirc$ |  |
|  | ON $\rightarrow$ OFF | 2 ms or less (Resistance load) | 1 ms or less (Rated load, resistance load) | $\bigcirc$ |  |
| Surge suppressor |  | Varistor (52 to 62V) | Zener diode | $\bigcirc$ |  |
| Common terminal arrangement |  | 8 points/common (Common terminal: TB9, TB19, TB29) | 16 points/common (Common terminal: TB18) | $\triangle$ | As the common changes from three commons to a common, wiring with a different voltage for each common is not possible. |
| Operation indication |  | Available (Turning ON the output turns LED ON) | ON indication (LED) | $\bigcirc$ |  |
| Fuse |  | None | 6.7A (Not exchangeable) <br> (Fuse blown capacity: 50A) | $\bigcirc$ |  |
| Fuse blown indication |  | None | Available (When a fuse blown occurs, LED is lit, and a signal is output to the CPU) | $\bigcirc$ |  |
| External connection method |  | 36-point terminal block connector (M3 $\times 6$ screws) | 18-point terminal block $\text { (M3 } \times 6 \text { screws })$ | $\times$ |  |
| Applicable wire size |  | 0.75 to $2 \mathrm{~mm}^{2}$ | 0.3 to $0.75 \mathrm{~mm}^{2}$ core (Outside diameter: 2.8 mm or less) | $\times$ | Wiring change is required. |
| Applicable solderless terminal |  | $\begin{gathered} \hline 1.25-3,1.25-\mathrm{YS} 3 \mathrm{~A}, \\ 2-\mathrm{S} 3,2-\mathrm{YS} 3 \mathrm{~A}, \\ \mathrm{~V} 1.25-3, \mathrm{~V} 1.25-\mathrm{YS} 3 \mathrm{~A}, \\ \mathrm{~V} 2-\mathrm{S} 3, \mathrm{~V}-\mathrm{YS} 3 \mathrm{~A} \end{gathered}$ | R1.25-3 <br> (Sleeved solderless terminals cannot be used.) | $\times$ |  |
| External supply power | Voltage | $\begin{aligned} & \text { 12VDC/24VDC } \\ & (10.2 \text { to } 30 \mathrm{VDC}) \end{aligned}$ | 12VDC/24VDC (10.2VDC to 28.8 VDC ) (Ripple ratio within 5\%) | $\Delta$ | Voltages exceeding 28.8 VDC cannot be applied. |
|  | Current | 23 mA <br> (TYP. 24VDC/8 points/common ON) | 20mA (During 24VDC) | $\bigcirc$ |  |
| Current consumption |  | 0.145 A (TYP. All points are ON.) | 0.08 A (TYP. All points are ON.) | $\bigcirc$ |  |
| External dimensions |  | 250(H) × 132(W) $\times 41$ (D) mm | 98(H) $\times 27.4(\mathrm{~W}) \times 90(\mathrm{D}) \mathrm{mm}$ | $\times$ | The dimensions are different. |
| Weight |  | 0.68 kg | 0.17 kg | $\triangle$ |  |

(5) Specifications comparison between A0J2E-E24T and QY80

O: Compatible, $\Delta$ : Partially changed, $x$ : Incompatible

| Specifications |  | A0J2E-E24T | QY80 | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Number of output points |  | 24 points | 16 points | $\triangle$ | When seventeen or more points are used, use two QY80s. |
| Insulation method |  | Photocoupler | Photocoupler | $\bigcirc$ |  |
| Rated load voltage |  | 12VDC/24VDC | 12VDC/24VDC | $\bigcirc$ |  |
| Operating load voltage range |  | 10.2 to 26.4VDC | 10.2 to 28.8VDC | $\bigcirc$ |  |
| Maximum load current |  | 0.8A/point, 0.6A/point ( $60 \% \mathrm{ON}, 55^{\circ} \mathrm{C}$ ) | 0.5A/point, 4A/common | $\triangle$ | The maximum load current per point is smaller. Use caution on selecting the load to use. |
| Maximum inrush current |  | No restriction (Short protect) | 4A 10ms or less | $\triangle$ | The inrush current value differs. Use caution on selecting the load to use. |
| Leakage current at OFF |  | 1.0 mA or less | 0.1 mA or less | $\bigcirc$ |  |
| Maximum voltage drop at ON |  | $\begin{aligned} & \text { 1VDC (TYP.) } 0.8 \mathrm{~A} \\ & 1.5 \mathrm{VDC} \text { (MAX.) } 0.8 \mathrm{~A} \end{aligned}$ | $\begin{aligned} & \hline 0.2 \mathrm{VDC} \text { (TYP.) } 0.5 \mathrm{~A} \\ & 0.3 \mathrm{VDC} \text { (MAX.) } 0.5 \mathrm{~A} \end{aligned}$ | $\bigcirc$ |  |
| Response time | OFF $\rightarrow$ ON | 0.5 ms or less | 1 ms or less | $\triangle$ | The response times differ. |
|  | ON $\rightarrow$ OFF | 1.5 ms or less | 1 ms or less (Rated load, resistance load) | $\bigcirc$ |  |
| Surge suppressor |  | Surge absorbing diode | Zener diode | $\bigcirc$ |  |
| Common terminal arrangement |  | 8 points/common (Common terminal: TB9, TB19, TB29) | 16 points/common (Common terminal: TB17) | $\Delta$ | As the common changes from three commons to a common, wiring with a different voltage for each common is not possible. |
| Operation indication |  | Available (Turning ON the output turns LED ON) | ON indication (LED) | $\bigcirc$ |  |
| Fuse |  | None | 6.7A (Not exchangeable) <br> (Fuse blown capacity: 50A) | $\bigcirc$ |  |
| Fuse blown indication |  | - | Available (When a fuse blown occurs, LED is lit, and a signal is output to the CPU) | $\bigcirc$ | The QY80 has the fuse instead of overheat and short circuit |
| Protection function |  | Available (overheat protection and short circuit protection) | None | $\times$ | protection function. |
| Protection function reset |  | Automatic reset (by deactivating protection function) | - | - |  |
| External connection method |  | 36 -point terminal block connector (M3 $\times 6$ screws) | 18-point terminal block <br> (M3 $\times 6$ screws) | $\times$ |  |
| Applicable wire size |  | 0.75 to $2 \mathrm{~mm}^{2}$ | 0.3 to $0.75 \mathrm{~mm}^{2}$ core (Outside diameter: 2.8 mm or less) | $\times$ | Wiring change is required. |
| External supply power | Voltage | $12 \mathrm{VDC} / 24 \mathrm{VDC}$ <br> (10.2VDC to 26.4VDC) | 12VDC/24VDC (10.2VDC to 28.8 VDC ) (Ripple ratio within $5 \%$ ) | $\bigcirc$ |  |
|  | Current | 200 mA <br> (24VDC, All points are ON.) | 20 mA (During 24VDC) | $\bigcirc$ |  |
| Current consumption |  | 0.145 A (TYP. All points are ON.) | 0.08 A (TYP. All points are ON.) | $\bigcirc$ |  |
| External dimensions |  | 250(H) $\times 132(\mathrm{~W}) \times 41(\mathrm{D}) \mathrm{mm}$ | $98(\mathrm{H}) \times 27.4(\mathrm{~W}) \times 90(\mathrm{D}) \mathrm{mm}$ | $\times$ | The dimensions are different. |
| Weight |  | 0.73 kg | 0.17 kg | $\triangle$ |  |

### 3.2.3 I/O Module specifications comparison

(1) Specifications comparison between A0J2-E28DR and QX40+QY10

| Specifications |  | A0J2-E28DR input specifications | QX40 | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Number of input points |  | 16 points | 16 points | $\bigcirc$ |  |
| Insulation method |  | Photocoupler | Photocoupler | $\bigcirc$ |  |
| Rated input voltage |  | 12VDC/24VDC | 24VDC | $\triangle$ | 12VDC cannot be used. ${ }^{* 1}$ |
| Rated input current |  | Approx. 3mA/Approx. 7 mA | Approx. 4mA | $\triangle$ | Rated input current is smaller.*2 |
| Operating voltage range |  | 10.2 to 26.4 VDC <br> (Ripple ratio within 5\%) | 20.4 to 28.8 VDC <br> (Ripple ratio within 5\%) | $\Delta$ | 12VDC cannot be used. ${ }^{* 1}$ |
| Maximum number of simultaneous input points |  | 100\% (16 points) simultaneously ON | 100\% simultaneously ON | O |  |
| ON voltage/ON current |  | 9.5 VDC or more/ 2.6 mA or more | 19VDC or more/3mA or more | $\triangle$ | 12VDC cannot be used. ${ }^{* 1}$ |
| OFF voltage/OFF current |  | 6VDC or less/1.0mA or less | 11VDC or less/1.7mA or less | $\triangle$ | 12VDC cannot be used. ${ }^{* 1}$ |
| Input resistance |  | Approx. $3.4 \mathrm{k} \Omega$ | Approx. $5.6 \mathrm{k} \Omega$ | $\triangle$ | Input resistance is greater. ${ }^{2}$ |
| Input form |  | Sink input (Input current flows off.) | Positive common | O | The name is different, but the specificaton is equal. |
| Response time | $\mathrm{OFF} \rightarrow \mathrm{ON}$ | 10 ms or less (6ms TYP.) | $\begin{gathered} \hline 1 \mathrm{~ms} / 5 \mathrm{~ms} / 10 \mathrm{~ms} / 20 \mathrm{~ms} / 70 \mathrm{~ms} \text { or } \\ \text { less } \\ \text { (Set it with CPU parameter.) } \\ \text { Initial setting is } 10 \mathrm{~ms} . \\ \hline \end{gathered}$ | O | Input response time of the parameter must be used at the initial value ( 10 ms ). |
|  | ON $\rightarrow$ OFF | 10 ms or less ( $7.5 \mathrm{~ms} \mathrm{TYP)}$. | $1 \mathrm{~ms} / 5 \mathrm{~ms} / 10 \mathrm{~ms} / 20 \mathrm{~ms} / 70 \mathrm{~ms}$ or less <br> (Set it with CPU parameter.) Initial setting is 10 ms . | $\bigcirc$ |  |
| Common terminal arrangement |  | 16 points/common (Common terminal: TB17) | 16 points/common (Common terminal: TB17) | $\bigcirc$ |  |
| Operation indication |  | Available (Turning ON the input turns LED ON) | ON indication (LED) | O |  |
| Specifications |  | A0J2-E28DR output specifications | QY10 | Compatibility | Precautions for replacement |
| Number of output points |  | 12 points | 16 points | $\bigcirc$ |  |
| Insulation method |  | Photocoupler | Relay | $\Delta$ | Although the insulation methods differ, the performance of the insulation is the same. |
| Rated switching voltage/ current |  | 24VDC 2A (Resistance load)/ point <br> 240VAC $2 \mathrm{~A}(\operatorname{COS} \phi=1) /$ point 5A/common | 24VDC 2A (Resistance load)/ point <br> 240VAC $2 \mathrm{~A}(\operatorname{COS} \phi=1) /$ point 8A/common | $\bigcirc$ |  |
| Minimum switching load |  | 5VDC 1mA | 5VDC 1mA | $\bigcirc$ |  |
| Maximum switching voltage |  | 264VAC, 125VDC | 264VAC, 125VDC | $\bigcirc$ |  |
| Maximum switching frequency |  | 3600 times/hr | 3600 times/hr | $\bigcirc$ |  |

*1 Use the QX70 when using 12VDC.
*2 Check the specifications of the sensor or switches to be connected to the QX40.

| Specifications |  | O: Compatible, $\triangle$ : Partially changed, $\times$ : Incompatible |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | A0J2-E28DR output specifications | QY10 | Compatibility | Precautions for replacement |
| Mechanical life |  | 20 million times or more | 20 million times or more | $\bigcirc$ |  |
| Electrical life |  | Rated switching voltage/current load 200,000 times or more | Rated switching voltage/current load 100,000 times or more | $\triangle$ |  |
|  |  | 200VAC 1.5A, 240VAC 1A $(\operatorname{COS} \phi=0.7)$ 200,000 times or more 200VAC 0.75A, 240VAC 0.5A (COS $\phi=0.35$ ) 200,000 times or more <br> 24VDC 1A, 100VDC 0.1A (L/R=7ms) 200,000 times or more | 200VAC 1.5A, 240VAC 1A $(\operatorname{COS} \phi=0.7) 100,000$ times or more <br> 200VAC 0.4A, 240VAC 0.3A $(\operatorname{COS} \phi=0.7) 300,000$ times or more <br> 200VAC 1A, 240VAC 0.5A (COS $\phi=0.35$ ) 100,000 times or more 200VAC 0.3A, 240VAC 0.15A (COS $\phi=0.35$ ) 300,000 times or more <br> 24VDC 1A, 100VDC 0.1A (L/R=7ms) 100,000 times or more 24VDC 0.3A, 100VDC 0.03A (L/R=7ms) 300,000 times or more | $\triangle$ | Reduce the exchange intervals of the modules as Mechanical/ Electrical Life is cut to about half. |
| Response time | OFF $\rightarrow$ ON | 10 ms or less | 10 ms or less | $\bigcirc$ |  |
|  | ON $\rightarrow$ OFF | 12 ms or less | 12 ms or less | $\bigcirc$ |  |
| Externa supply power (Relay coil driving power) | Voltage | $24 \mathrm{VDC} \pm 10 \%$ Ripple voltage 4 Vp -p or less | - | $\bigcirc$ |  |
|  | Current | 125 mA <br> (24VDC TYP. All points are ON.) | - | O | External supply power is not required. |
| Specifications |  | A0J2-E28DR | QX40/QY10 | Compatibility | Precautions for replacement |
| Common terminal arrangement |  | 8 points/common (Common terminal: TB26) 3 points/common (Common terminal: TB31) Independent contact (Common terminal: TB33) | 16 points/common (Common terminal: TB17) | $\Delta$ | As the common changes from three commons to a common, wiring with a different voltage for each common is not possible. |
| Operation indication |  | Available (Turning ON the output turns LED ON.) | ON indication (LED) | $\bigcirc$ |  |
| Current consumption |  | $0.130 \mathrm{~A} \text { (TYP. }$ <br> All points are ON.) | $0.05+0.43=0.48 \mathrm{~A}$ <br> (TYP. All points are ON.) | $\Delta$ | Review current capacity since current consumption is increased. |
| External connection method |  | 36-point terminal block connector (M3 $\times 6$ screws) | 18-point terminal block (M3 $\times 6$ screws) 2 pieces | $\times$ |  |
| Applicable wire size |  | 0.75 to $2 \mathrm{~mm}^{2}$ | 0.3 to $0.75 \mathrm{~mm}^{2}$ core <br> (Outside diameter: 8 mm or less) | $\times$ | Wiring change is required. |
| Applicable solderless terminal |  | $\begin{gathered} \hline 1.25-3,1.25-\mathrm{YS} 3 \mathrm{~A}, \\ 2-\mathrm{S} 3,2-\mathrm{YS} 3 \mathrm{~A}, \\ \mathrm{~V} 1.25-3, \mathrm{~V} 1.25-\mathrm{YS} 3 \mathrm{~A}, \\ \mathrm{~V} 2-\mathrm{S} 3, \mathrm{~V} 2-\mathrm{YS} 3 \mathrm{~A} \end{gathered}$ | R1.25-3 <br> (Sleeved solderless terminals cannot be used.) | $\times$ |  |
| External dimensions |  | 250(H) $\times 132(\mathrm{~W}) \times 41(\mathrm{D}) \mathrm{mm}$ | $98(\mathrm{H}) \times 27.4(\mathrm{~W}) \times 90(\mathrm{D}) \mathrm{mm} \times 2$ | $\times$ | The dimensions are different. |
| Weight |  | 0.68 kg | $0.16+0.22=0.38 \mathrm{~kg}$ | $\triangle$ |  |

## (2) Specifications comparison between A0J2-E56DR and QX40+QY10

| Specifications |  | O: Compatible, $\triangle$ : Partially changed, $\times$ : Incompatible |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | A0J2-E56DR input specifications | QX40 | Compatibility | Precautions for replacement |
| Number of input points |  | 32 points | 16 points | $\triangle$ | When seventeen or more points are used, use two QX40 modules. |
| Insulation method |  | Photocoupler | Photocoupler | $\bigcirc$ |  |
| Rated input voltage |  | 12VDC/24VDC | 24VDC | $\triangle$ | 12VDC cannot be used.*1 |
| Rated input current |  | Approx. 3mA/Approx. 7 mA | Approx. 4 mA | $\triangle$ | Rated input current is smaller.* ${ }^{\text {2 }}$ |
| Operating voltage range |  | $\begin{gathered} 10.2 \text { to } 26.4 \mathrm{VDC} \\ \text { (Ripple ratio within } 5 \% \text { ) } \end{gathered}$ | $\begin{gathered} 20.4 \text { to } 28.8 \mathrm{VDC} \\ \text { (Ripple ratio within } 5 \% \text { ) } \end{gathered}$ | $\triangle$ | 12VDC cannot be used.*1 |
| Maximum number of simultaneous input points |  | 60\% (10 points/common) simultaneously ON | 100\% simultaneously ON | $\bigcirc$ |  |
| ON voltage/ON current |  | 9.5 VDC or more/2.6mA or more | 19VDC or more/3mA or more | $\triangle$ | 12VDC cannot be used. ${ }^{* 1}$ |
| OFF voltage/OFF current |  | 6VDC or less $/ 1.0 \mathrm{~mA}$ or less | 11 VDC or less $/ 1.7 \mathrm{~mA}$ or less | $\triangle$ | 12VDC cannot be used. ${ }^{* 1}$ |
| Input resistance |  | Approx. $3.4 \mathrm{k} \Omega$ | Approx. $5.6 \mathrm{k} \Omega$ | $\triangle$ | Input resistance is greater. ${ }^{*}$ |
| Input form |  | Sink input (Input current flows off.) | Positive common | $\bigcirc$ | The name is different, but the specificaton is equal. |
| Response time | OFF $\rightarrow$ ON | 10 ms or less ( $6 \mathrm{~ms} \mathrm{TYP)}$. | $1 \mathrm{~ms} / 5 \mathrm{~ms} / 10 \mathrm{~ms} / 20 \mathrm{~ms} / 70 \mathrm{~ms}$ or less <br> (Set it with CPU parameter.) Initial setting is 10 ms . | $\bigcirc$ | Input response time of the parameter must be used at the initial value ( 10 ms ). |
|  | ON $\rightarrow$ OFF | 10 ms or less ( $7.5 \mathrm{~ms} \mathrm{TYP)}$. | $1 \mathrm{~ms} / 5 \mathrm{~ms} / 10 \mathrm{~ms} / 20 \mathrm{~ms} / 70 \mathrm{~ms}$ or less <br> (Set it with CPU parameter.) Initial setting is 10 ms . | O |  |
| Common terminal arrangement |  | 16 points/common (Common terminal: TB17, TB34) | 16 points/common (Common terminal: TB17) | $\bigcirc$ |  |
| Operation indication |  | Available (Turning ON the output turns LED ON) | ON indication (LED) | $\bigcirc$ |  |
| Specifications |  | A0J2-E56DR output specifications | QY10 | Compatibility | Precautions for replacement |
| Number of output points |  | 24 points | 16 points | $\Delta$ | When seventeen or more points are used, use two QY10s. |
| Insulation method |  | Photocoupler | Relay | $\triangle$ | Although the insulation methods differ, the performance of the insulation is same. |
| Rated switching voltage/ current |  | 24VDC 2A (Resistance load)/ point 240VAC $2 \mathrm{~A}(\operatorname{COS} \phi=1) /$ point 5A/common | 24VDC 2A (Resistance load)/ point 240VAC $2 \mathrm{~A}(\operatorname{COS} \phi=1) /$ point 8A/common | $\bigcirc$ |  |
| Minimum switching load |  | 5VDC 1mA | 5 VDC 1 mA | $\bigcirc$ |  |
| Maximum switching voltage |  | 264VAC, 125VDC | 264VAC, 125VDC | $\bigcirc$ |  |
| Maximum switching frequency |  | 3600 times/hr | 3600 times/hr | $\bigcirc$ |  |

O : Compatible, $\triangle$ : Partially changed, $\times$ : Incompatible

| Specifications |  | A0J2-E56DR output specifications | QY10 | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Mechanical life |  | 20 million times or more | 20 million times or more | $\bigcirc$ |  |
| Electrical life |  | Rated switching voltage/current load 200,000 times or more | Rated switching voltage/current load 100,000 times or more | $\triangle$ |  |
|  |  | 200VAC 1.5A, 240VAC 1A (COS $\phi=0.7$ ) 200,000 times or more 200VAC 0.75A, 240VAC 0.5A $(\operatorname{COS} \phi=0.35) 200,000$ times or more <br> 24VDC 1A, 100VDC 0.1A (L/R=7ms) 200,000 times or more | 200VAC 1.5A, 240VAC 1A $(\operatorname{COS} \phi=0.7)$ 100,000 times or more <br> 200VAC 0.4A, 240VAC 0.3A $(\operatorname{COS} \phi=0.7) 300,000$ times or more <br> 200VAC 1A, 240VAC 0.5A <br> (COS $\phi=0.35$ ) 100,000 times or more <br> 200VAC 0.3A, 240VAC 0.15A <br> (COS $\phi=0.35$ ) 300,000 times or more <br> 24VDC 1A, 100VDC 0.1A (L/R=7ms) 100,000 times or more 24VDC 0.3A $100 \mathrm{VDC} 0.03 \mathrm{~A}(\mathrm{~L} / \mathrm{R}=7 \mathrm{~ms})$ 300,000 times or more | $\triangle$ | Reduce the exchange intervals of the modules as Mechanical/ Electrical Life is cut to about half. |
| Response time | OFF $\rightarrow$ ON | 10 ms or less | 10 ms or less | $\bigcirc$ |  |
|  | ON $\rightarrow$ OFF | 12 ms or less | 12 ms or less | $\bigcirc$ |  |
| External <br> supply <br> power <br> (Relay coil driving <br> power) | Voltage | $24 \mathrm{VDC} \pm 10 \%$ Ripple voltage 4 Vp -p or less | - | $\bigcirc$ |  |
|  | Current | 230 mA (24VDC All points are ON.) | - | $\bigcirc$ | required. |
| Specifications |  | A0J2-E56DR | QX40/QY10 | Compatibility | Precautions for replacement |
| Common terminal arrangement |  | 8 points/common (Common terminal: TB9, TB19, TB29) | 16 points/common (Common terminal: TB17) | $\triangle$ | As the common changes from three commons to a common, wiring with a different voltage for each common is not possible. |
| Operation indication |  | Available (Turning ON the output turns LED ON) | ON indication (LED) | O |  |
| Current consumption |  | 0.230A (TYP. All points are ON.) | $0.05 \times 2+0.43 \times 2=0.96 \mathrm{~A}$ <br> (TYP. All points are ON.) | $\Delta$ | Review current capacity since current consumption is increased. |
| External connection method |  | 36-point terminal block connector <br> (M3 $\times 6$ screws) 2 pieces | 18-point terminal block (M3 $\times 6$ screws) 4 pieces | $\times$ |  |
| Applicable wire size |  | 0.75 to $2 \mathrm{~mm}^{2}$ | 0.3 to $0.75 \mathrm{~mm}^{2}$ core (Outside diameter: 2.8 mm or less) | $\times$ | Wiring change is required. |
| Applicable solderless terminal |  | $\begin{gathered} \hline 1.25-3,1.25-\mathrm{YS} 3 \mathrm{~A}, \\ 2-\mathrm{S} 3,2-\mathrm{YS} 3 \mathrm{~A}, \\ \mathrm{~V} 1.25-3, \mathrm{~V} 1.25-\mathrm{YS} 3 \mathrm{~A}, \\ \mathrm{~V} 2-\mathrm{S} 3, \mathrm{~V} 2-\mathrm{YS} 3 \mathrm{~A} \end{gathered}$ | R1.25-3 <br> (Sleeved solderless terminals cannot be used.) | $\times$ |  |
| External dimensions |  | 250(H) $\times 190(\mathrm{~W}) \times 41(\mathrm{D}) \mathrm{mm}$ | $98(\mathrm{H}) \times 27.4(\mathrm{~W}) \times 90(\mathrm{D}) \mathrm{mm} \times 4$ | $\times$ | The dimensions are different. |
| Weight |  | 1.08 kg | $0.16 \times 2+0.22 \times 2=0.76 \mathrm{~kg}$ | $\triangle$ |  |

*1 Use the QX70 when using 12VDC.
*2 Check the specifications of the sensor or switches to be connected to the QX40.
(3) Specifications comparison between A0J2-E28DT and QX40+QY50

|  |  |  |  | Compat | Partially changed, $\times$ : Incompatible |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Specifications |  | A0J2-E28DT input specifications | QX40 | Compatibility | Precautions for replacement |
| Number of input points |  | 16 points | 16 points | $\bigcirc$ |  |
| Insulation method |  | Photocoupler | Photocoupler | $\bigcirc$ |  |
| Rated input voltage |  | 12VDC/24VDC | 24VDC | $\Delta$ | 12VDC cannot be used. ${ }^{* 1}$ |
| Rated input current |  | Approx. 3mA/Approx. 7 mA | Approx. 4 mA | $\Delta$ | Rated input current is smaller.* ${ }^{2}$ |
| Operating voltage range |  | 10.2 to 26.4 VDC (Ripple ratio within 5\%) | 20.4 to 28.8 VDC (Ripple ratio within 5\%) | $\triangle$ | 12VDC cannot be used. ${ }^{* 1}$ |
| Maximum number of simultaneous input points |  | 100\% (16 points) <br> simultaneously ON | 100\% simultaneously ON | $\bigcirc$ |  |
| ON voltage/ON current |  | 9.5 VDC or more/2.6mA or more | 19VDC or more/3mA or more | $\Delta$ | 12VDC cannot be used. ${ }^{* 1}$ |
| OFF voltage/OFF current |  | 6 VDC or less/1.0mA or less | 11 VDC or less/1.7mA or less | $\triangle$ | 12VDC cannot be used. ${ }^{* 1}$ |
| Input resistance |  | Approx. $3.4 \mathrm{k} \Omega$ | Approx. $5.6 \mathrm{k} \Omega$ | $\triangle$ | Input resistance is greater. ${ }^{*}$ |
| Input form |  | Sink input (Input current flows off.) | Positive common | $\bigcirc$ | The name is different, but the specificaton is equal. |
| Response time | $\mathrm{OFF} \rightarrow \mathrm{ON}$ | 10 ms or less (6ms TYP.) | $1 \mathrm{~ms} / 5 \mathrm{~ms} / 10 \mathrm{~ms} / 20 \mathrm{~ms} /$ 70 ms or less (Set it with CPU parameter.) Initial setting is 10 ms . | O | Input response time of the parameter must be used at the initial value ( 10 ms ). |
|  | ON $\rightarrow$ OFF | 10 ms or less ( $7.5 \mathrm{~ms} \mathrm{TYP)}$. | $1 \mathrm{~ms} / 5 \mathrm{~ms} / 10 \mathrm{~ms} / 20 \mathrm{~ms} /$ 70 ms or less (Set it with CPU parameter.) Initial setting is 10 ms . | $\bigcirc$ |  |
| Common terminal arrangement |  | 16 points/common (Common terminal: TB17) | 16 points/common (Common terminal: TB17) | $\bigcirc$ |  |
| Operation indication |  | Available (Turning ON the input turns LED ON) | ON indication (LED) | $\bigcirc$ |  |
| Specifications |  | A0J2-E28DT output specifications | QY50 | Compatibility | Precautions for replacement |
| Number of output points |  | 12 points | 16 points | $\bigcirc$ |  |
| Insulation method |  | Photocoupler | Photocoupler | $\bigcirc$ |  |
| Rated load voltage |  | $12 \mathrm{VDC} / 24 \mathrm{VDC}$ | 12VDC/24VDC | $\bigcirc$ |  |
| Operating load voltage range |  | 10.2 to 30 VDC | 10.2 to 28.8 VDC | $\Delta$ | Voltages exceeding 28.8 VDC cannot be applied. |
| Maximum load current |  | 0.5A/point, 4A/common | 0.5A/point, 4A/common | $\bigcirc$ |  |
| Maximum inrush current |  | 4A, 10ms or less | $4 \mathrm{~A}, 10 \mathrm{~ms}$ or less | $\bigcirc$ |  |
| Leakage current at OFF |  | 0.1 mA or less | 0.1 mA or less | O |  |
| Maximum voltage drop at ON |  | $\begin{aligned} & \hline 0.9 \mathrm{VDC} \text { (TYP.) } 0.5 \mathrm{~A} \\ & 1.5 \mathrm{VDC} \text { (MAX.) } 0.5 \mathrm{~A} \end{aligned}$ | $\begin{aligned} & \hline 0.2 \mathrm{VDC} \text { (TYP.) } 0.5 \mathrm{~A} \\ & 0.3 \mathrm{VDC} \text { (MAX.) } 0.5 \mathrm{~A} \end{aligned}$ | $\bigcirc$ |  |
| Response time | OFF $\rightarrow$ ON | 2 ms or less | 1 ms or less | $\bigcirc$ |  |
|  | ON $\rightarrow$ OFF | 2 ms or less (Resistance load) | 1 ms or less <br> (Rated load, resistance load) | $\bigcirc$ |  |
| Externalsupply power | Voltage | 12VDC/24VDC <br> (10.2VDC to 30VDC) | $12 \mathrm{VDC} / 24 \mathrm{VDC}$ (10.2VDC to 28.8 VDC ) (Ripple ratio within 5\%) | $\Delta$ | Voltages exceeding 28.8 VDC cannot be applied. |
|  | Current | 23mA <br> (TYP. 24VDC/8 points/common <br> ON) | 20 mA (During 24VDC) | $\bigcirc$ |  |
| Surge suppressor |  | Varistor (52 to 62V) | Zener diode | $\bigcirc$ |  |
| Common terminal arrangement |  | 8 points/common (Common terminal: TB26) <br> 4 points/common (Common terminal: TB33) | 16 points/common (Common terminal: TB18) | $\Delta$ | As the common changes from two commons to a common, wiring with a different voltage for each common is not possible. |
| Operation indication |  | Available (Turning ON the output turns LED ON) | ON indication (LED) | $\bigcirc$ |  |
| Fuse |  | None | 6.7A (Not exchangeable) <br> (Fuse blown capacity: 50A) | $\bigcirc$ |  |
| Fuse blown indication |  | None | Available (When a fuse blown occurs, LED is lit, and a signal is output to the CPU) | $\bigcirc$ |  |

O : Compatible, $\triangle$ : Partially changed, $\times$ : Incompatible

| Specifications | A0J2-E28DT | QX40/QY50 | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: |
| Current consumption | 0.125A (TYP. All points are ON.) | $0.05+0.08=0.13 \mathrm{~A}$ <br> (TYP. All points are ON.) | $\triangle$ | Review current capacity since current consumption is increased. |
| External connection method | 36-point terminal block connector (M3 $\times 6$ screws) | 18-point terminal block (M3 $\times 6$ screws) 2 pieces | $\times$ |  |
| Applicable wire size | 0.75 to $2 \mathrm{~mm}^{2}$ | 0.3 to $0.75 \mathrm{~mm}^{2}$ core (Outside diameter: 2.8 mm or less) | $\times$ | Wiring change is required. |
| Applicable solderless terminal | $\begin{gathered} \text { 1.25-3, 1.25-YS3A, } \\ 2-\mathrm{S} 3,2-\mathrm{YS} 3 \mathrm{~A}, \\ \mathrm{~V} 1.25-3, \mathrm{~V} 1.25-\mathrm{YS} 3 \mathrm{~A}, \\ \mathrm{~V} 2-\mathrm{S} 3, \mathrm{~V} 2-\mathrm{YS} 3 \mathrm{~A} \end{gathered}$ | R1.25-3 <br> (Sleeved solderless terminals cannot be used.) | × |  |
| External dimensions | $250(\mathrm{H}) \times 132(\mathrm{~W}) \times 41(\mathrm{D}) \mathrm{mm}$ | $98(\mathrm{H}) \times 27.4(\mathrm{~W}) \times 90(\mathrm{D}) \mathrm{mm} \times 2$ | $\times$ | The dimensions are different. |
| Weight | 0.65 kg | $0.16+0.17=0.33 \mathrm{~kg}$ | $\triangle$ |  |

*1 Use the QX70 when using 12VDC.
*2 Check the specifications of the sensor or switches to be connected to the QX40.
(4) Specifications comparison between A0J2-E28DT and QX41Y41P

| Specifications |  | O: Compatible, $\triangle$ : Partially changed, $\times$ : Incompatible |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | A0J2-E28DT input specifications | QX41Y41P input specifications | Compatibility | Precautions for replacement |
| Number of input points |  | 16 points | 32 points | $\bigcirc$ |  |
| Insulation method |  | Photocoupler | Photocoupler | $\bigcirc$ |  |
| Rated input voltage |  | 12VDC/24VDC | 24VDC | $\triangle$ | 12 VDC cannot be used. ${ }^{* 1}$ |
| Rated input current |  | Approx. 3mA/Approx. 7 mA | Approx. 4mA | $\triangle$ | Rated input current is smaller. |
| Operating voltage range |  | $\begin{gathered} 10.2 \text { to } 26.4 \mathrm{VDC} \\ \text { (Ripple ratio within 5\%) } \end{gathered}$ | $\begin{gathered} 20.4 \text { to } 28.8 \mathrm{VDC} \\ \text { (Ripple ratio within 5\%) } \end{gathered}$ | $\triangle$ | 12VDC cannot be used. ${ }^{* 1}$ |
| Maximum number of simultaneous input points |  | 100\% (16 points) simultaneously ON | Refer to the derating chart. ${ }^{* 3}$ | $\Delta$ | Use it within the range shown in the derating chart. |
| ON voltage/ON current |  | 9.5 VDC or more/ 2.6 mA or more | 19VDC or more/3mA or more | $\triangle$ | 12 VDC cannot be used. ${ }^{* 1}$ |
| OFF voltage/OFF current |  | 6VDC or less/1.0mA or less | 11 VDC or less/1.7mA or less | $\triangle$ | 12 VDC cannot be used. ${ }^{* 1}$ |
| Input resistance |  | Approx. $3.4 \mathrm{k} \Omega$ | Approx. $5.6 \mathrm{k} \Omega$ | $\triangle$ | Input resistance is greater. ${ }^{*}$ |
| Input form |  | Sink input (Input current flows off.) | Positive common type | $\bigcirc$ | The name is different, but the specificaton is equal. |
| Response time | OFF $\rightarrow$ ON | 10 ms or less (6ms TYP.) | $1 \mathrm{~ms} / 5 \mathrm{~ms} / 10 \mathrm{~ms} / 20 \mathrm{~ms} / 70 \mathrm{~ms}$ or less (Set it with CPU parameter.) Initial setting is 10 ms . | $\bigcirc$ | Input response time of the |
|  | ON $\rightarrow$ OFF | 10 ms or less ( $7.5 \mathrm{~ms} \mathrm{TYP)}$. | $1 \mathrm{~ms} / 5 \mathrm{~ms} / 10 \mathrm{~ms} / 20 \mathrm{~ms} / 70 \mathrm{~ms}$ or less (Set it with CPU parameter.) Initial setting is 10 ms . | $\bigcirc$ | initial value ( 10 ms ). |
| Common terminal arrangement |  | 16 points/common (Common terminal: TB17) | 32 points/common (Common terminal: 1B01, 1B02) | $\bigcirc$ |  |
| Operation indication |  | Available (Turning ON the input turns LED ON) | Available (Turning ON the input turns LED ON) <br> (32-point switching indication with SW) | $\bigcirc$ |  |
| Specifications |  | A0J2-E28DT output specifications | QX41Y41P output specifications | Compatibility | Precautions for replacement |
| Number of output points |  | 12 points | 32 points | $\bigcirc$ |  |
| Insulation method |  | Photocoupler | Photocoupler | $\bigcirc$ |  |
| Rated load voltage |  | 12VDC/24VDC | 12VDC/24VDC | $\bigcirc$ |  |
| Operating load voltage range |  | 10.2 to 30VDC | 10.2 to 28.8 VDC | $\triangle$ | Voltages exceeding 28.8 VDC cannot be applied. |
| Maximum load current |  | 0.5A/point, 4A/common | 0.1A/point, 2A/common | $\Delta$ | The maximum load current per point is smaller. Pay attention to the selection of the load to be used. |
| Maximum inrush current |  | 4A, 10ms or less | $0.7 \mathrm{~A}, 10 \mathrm{~ms}$ or less | $\triangle$ | Maximum inrush current is smaller. |
| Leakage current at OFF |  | 0.1 mA or less | 0.1 mA or less | $\bigcirc$ |  |
| Maximum voltage drop at ON |  | $\begin{aligned} & \hline 0.9 \mathrm{VDC} \text { (TYP.) } 0.5 \mathrm{~A} \\ & 1.5 \mathrm{VDC} \text { (MAX.) } 0.5 \mathrm{~A} \end{aligned}$ | $\begin{aligned} & \hline 0.1 \mathrm{VDC} \text { (TYP.) } 0.1 \mathrm{~A} \\ & 0.2 \mathrm{VDC} \text { (MAX.) } 0.1 \mathrm{~A} \end{aligned}$ | $\bigcirc$ | The maximum voltage drop at ON is smaller. |
| Response time | OFF $\rightarrow$ ON | 2 ms or less | 1 ms or less | O |  |
|  | ON $\rightarrow$ OFF | 2 ms or less (Resistance load) | 1 ms or less <br> (Rated load, resistance load) | $\bigcirc$ |  |
| Externalsupplypower | Voltage | $\begin{gathered} \hline 12 \mathrm{VDC} / 24 \mathrm{VDC} \\ \text { (10.2 to } 30 \mathrm{VDC} \text { ) } \end{gathered}$ | $\begin{gathered} \hline 12 \mathrm{VDC} / 24 \mathrm{VDC} \\ \text { (10.2 to } 28.8 \mathrm{VDC} \text { ) } \\ \hline \end{gathered}$ | $\Delta$ | Voltages exceeding 28.8 VDC cannot be applied. |
|  | Current | 23 mA <br> (TYP. 24VDC 8 points/common ON) | Max. $15 \mathrm{~mA} /$ common (24VDC, When all points are ON) | $\bigcirc$ |  |
| Surge suppressor |  | Varistor (52 to 62V) | Zener diode | O |  |
| Common terminal arrangement |  | 8 points/common (Common terminal: TB26) 4 points/common (Common terminal: TB33) | 32 points/common (Common terminal: 2A01, 2A02) | $\Delta$ | As the common changes from two commons to a common, wiring with a different voltage for each common is not possible. |
| Operation indication |  | Available (Turning ON the output turns LED ON) | Available (Turning ON the output turns LED ON) <br> (32-point switching indication with SW) | $\bigcirc$ |  |

O: Compatible, $\Delta$ : Partially changed, $\times$ : Incompatible

| Specifications | A0J2-E28DT | QX41Y41P | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: |
| Current consumption | 125mA (TYP. All points are ON.) | 130mA (TYP. All points are ON.) | $\triangle$ | Review current capacity since current consumption is increased. |
| External connection method | 36-point terminal block connector (M3 $\times 6$ screws) | 40-pin connector | $\times$ | Wiring change is required. |
| Applicable wire size | 0.75 to $2 \mathrm{~mm}^{2}$ | $0.3 \mathrm{~mm}^{2}$ | $\times$ |  |
| Applicable solderless terminal | $\begin{gathered} \text { 1.25-3, 1.25-YS3A, } \\ 2-\mathrm{S} 3,2-\mathrm{YS} 3 \mathrm{~A}, \\ \mathrm{~V} 1.25-3, \mathrm{~V} 1.25-\mathrm{YS} 3 \mathrm{~A}, \\ \mathrm{~V} 2-\mathrm{S} 3, \mathrm{~V} 2-\mathrm{YS} 3 \mathrm{~A} \end{gathered}$ | - | $\times$ |  |
| External dimensions | $250(\mathrm{H}) \times 132(\mathrm{~W}) \times 41(\mathrm{D}) \mathrm{mm}$ | $98(\mathrm{H}) \times 27.4(\mathrm{~W}) \times 90(\mathrm{D}) \mathrm{mm}$ | $\times$ | The dimensions are different. When a connector is attached: $D=135 \mathrm{~mm}$ |
| Weight | 0.65 kg | 0.20kg | $\triangle$ |  |

*1 Use the QX70 when using 12VDC.
*2 Check the specifications of the sensor or switches to be connected to the QX41Y41P.
*3 The following shows the derating chart.


## (5) Specifications comparison between A0J2-E56DTand QX40+QY50

| Specifications |  | O : Compatible, $\triangle$ : Partially changed, $\times$ : Incompatible |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | A0J2-E56DT input specifications | QX40 | Compatibility | Precautions for replacement |
| Number of input points |  | 32 points | 16 points | $\triangle$ | When seventeen or more points are used, use two QX40 modules. |
| Insulation method |  | Photocoupler | Photocoupler | $\bigcirc$ |  |
| Rated input voltage |  | 12VDC/24VDC | 24VDC | $\triangle$ | 12 VDC cannot be used. ${ }^{* 1}$ |
| Rated input current |  | Approx. 3mA/Approx. 7 mA | Approx. 4 mA | $\triangle$ | Rated input current is smaller.*2 |
| Operating voltage range |  | $\begin{gathered} 10.2 \text { to } 26.4 \mathrm{VDC} \\ \text { (Ripple ratio within } 5 \% \text { ) } \end{gathered}$ | $\begin{gathered} 20.4 \text { to } 28.8 \mathrm{VDC} \\ \text { (Ripple ratio within 5\%) } \end{gathered}$ | $\triangle$ | 12VDC cannot be used. ${ }^{* 1}$ |
| Maximum number of simultaneous input points |  | 60\% (10 points/common) simultaneously ON | 100\% simultaneously ON | $\bigcirc$ |  |
| ON voltage/ON current |  | 9.5 VDC or more/2.6mA or more | 19VDC or more/3mA or more | $\triangle$ | 12 VDC cannot be used. ${ }^{* 1}$ |
| OFF voltage/OFF current |  | 6 VDC or less $/ 1.0 \mathrm{~mA}$ or less | 11 VDC or less/1.7mA or less | $\triangle$ | 12VDC cannot be used. ${ }^{* 1}$ |
| Input resistance |  | Approx. $3.4 \mathrm{k} \Omega$ | Approx. $5.6 \mathrm{k} \Omega$ | $\triangle$ | Input resistance is greater. ${ }^{*}$ |
| Input form |  | Sink input (Input current flows off.) | Positive common | $\bigcirc$ | The name is different, but the specificaton is equal. |
| Response time | OFF $\rightarrow$ ON | 10 ms or less (6ms TYP.) | $1 \mathrm{~ms} / 5 \mathrm{~ms} / 10 \mathrm{~ms} / 20 \mathrm{~ms} / 70 \mathrm{~ms}$ or less <br> (Set it with CPU parameter.) Initial setting is 10 ms . | $\bigcirc$ | Input response time of the parameter must be used at the initial value ( 10 ms ). |
|  | ON $\rightarrow$ OFF | 10 ms or less (7.5ms TYP.) | $1 \mathrm{~ms} / 5 \mathrm{~ms} / 10 \mathrm{~ms} / 20 \mathrm{~ms} / 70 \mathrm{~ms}$ or less <br> (Set it with CPU parameter.) Initial setting is 10 ms . | $\bigcirc$ |  |
| Common terminal arrangement |  | 16 points/common (Common terminal: TB17, TB34) | 16 points/common (Common terminal: TB17) | $\bigcirc$ |  |
| Operation indication |  | Available (Turning ON the input turns LED ON) | ON indication (LED) | $\bigcirc$ |  |
| Specifications |  | A0J2-E56DT output specifications | QY50 | Compatibility | Precautions for replacement |
| Number of output points |  | 24 points | 16 points | $\triangle$ | When seventeen or more points are used, use two QY50 modules. |
| Insulation method |  | Photocoupler | Photocoupler | $\bigcirc$ |  |
| Rated load voltage |  | 12VDC/24VDC | 12VDC/24VDC | $\bigcirc$ |  |
| Operating load voltage range |  | 10.2 to 30 VDC | 10.2 to 28.8VDC | $\triangle$ | Voltages exceeding 28.8 VDC cannot be applied. |
| Maximum load current |  | 0.5A/point, 4A/common | 0.5A/point, 4A/common | $\bigcirc$ |  |
| Maximum inrush current |  | $4 \mathrm{~A}, 10 \mathrm{~ms}$ or less | $4 \mathrm{~A}, 10 \mathrm{~ms}$ or less | $\bigcirc$ |  |
| Leakage current at OFF |  | 0.1 mA or less | 0.1 mA or less | $\bigcirc$ |  |
| Maximum voltage drop at ON |  | $\begin{aligned} & \hline 0.9 \mathrm{VDC} \text { (TYP.) } 0.5 \mathrm{~A} \\ & 1.5 \mathrm{VDC} \text { (MAX.) } 0.5 \mathrm{~A} \end{aligned}$ | $\begin{aligned} & \hline 0.2 \mathrm{VDC} \text { (TYP.) } 0.5 \mathrm{~A} \\ & 0.3 \mathrm{VDC} \text { (MAX.) } 0.5 \mathrm{~A} \end{aligned}$ | $\bigcirc$ |  |
| Response time | OFF $\rightarrow$ ON | 2 ms or less | 1 ms or less | $\bigcirc$ |  |
|  | ON $\rightarrow$ OFF | 2 ms or less (Resistance load) | 1 ms or less <br> (Rated load, resistance load) | $\bigcirc$ |  |
| External supply power | Voltage | 12VDC/24VDC <br> (10.2VDC to 30VDC) | $12 \mathrm{VDC} / 24 \mathrm{VDC}$ (10.2VDC to 28.8 VDC ) (Ripple ratio within 5\%) | $\Delta$ | Voltages exceeding 28.8 VDC cannot be applied. |
|  | Current | 23 mA <br> (TYP. 24VDC/8 points common ON) | 20 mA (During 24VDC) | $\bigcirc$ |  |
| Surge suppressor |  | Varistor (52 to 62V) | Zener diode | $\bigcirc$ |  |
| Common terminal arrangement |  | 8 points/common (Common terminal: TB9, TB19, TB29) | 16 points/common <br> (Common terminal: 2A01, 2A02) | $\triangle$ | As the common changes from two commons to a common, wiring with a different voltage for each common is not possible. |
| Operation indication |  | Available (Turning ON the output turns LED ON) | ON indication (LED) | $\bigcirc$ |  |
| Fuse |  | None | 6.7A (Not exchangeable) (Fuse blown capacity: 50A) | $\bigcirc$ |  |
| Fuse blown indication |  | None | Available (When a fuse blown occurs, LED is lit, and a signal is output to the CPU) | $\bigcirc$ |  |

O : Compatible, $\triangle$ : Partially changed, x : Incompatible

| Specifications | A0J2-E56DT | QX40/QY50 | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: |
| Current consumption | 0.225A (TYP. All points are ON.) | $0.05 \times 2+0.08 \times 2=0.26 \mathrm{~A}$ <br> (TYP. All points are ON.) | $\triangle$ | Review current capacity since current consumption is increased. |
| External connection method | 36-point terminal block connector <br> (M3 $\times 6$ screws) 2 pieces | 18-point terminal block (M3 $\times 6$ screws) 4 pieces | $\times$ |  |
| Applicable wire size | 0.75 to $2 \mathrm{~mm}^{2}$ | 0.3 to $0.75 \mathrm{~mm}^{2}$ core (Outside diameter: 2.8 mm or less) | $\times$ | Wiring change is required. |
| Applicable solderless terminal | $\begin{gathered} \text { 1.25-3, 1.25-YS3A, } \\ 2-\mathrm{S} 3,2-\mathrm{YS} 3 \mathrm{~A}, \\ \mathrm{~V} 1.25-3, \mathrm{~V} 1.25-\mathrm{YS} 3 \mathrm{~A}, \\ \mathrm{~V} 2-\mathrm{S} 3, \mathrm{~V} 2-\mathrm{YS} 3 \mathrm{~A} \end{gathered}$ | R1.25-3 <br> (Sleeved solderless terminals cannot be used.) | × |  |
| External dimensions | 250(H) $\times 190(\mathrm{~W}) \times 41(\mathrm{D}) \mathrm{mm}$ | 98(H) $\times 27.4(\mathrm{~W}) \times 90(\mathrm{D}) \mathrm{mm} \times 4$ | $\times$ | The dimensions are different. |
| Weight | 1.04 kg | $0.16 \times 2+0.17 \times 2=0.66 \mathrm{~kg}$ | $\triangle$ |  |

*1 Use the QX70 when using 12VDC.
*2 Check the specifications of the sensor or switches to be connected to the QX40.
(6) Specifications comparison between A0J2-E56DT and QX41Y41P

| Specifications |  | O : Compatible, $\triangle$ : Partially changed, $\times$ : Incompatible |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | A0J2-E56DT input specifications | QX41Y41P input specifications | Compatibility | Precautions for replacement |
| Number of input points |  | 32 points | 32 points | $\bigcirc$ |  |
| Insulation method |  | Photocoupler | Photocoupler | $\bigcirc$ |  |
| Rated input voltage |  | 12VDC/24VDC | 24VDC | $\triangle$ | 12 VDC cannot be used. ${ }^{* 1}$ |
| Rated input current |  | Approx. 3mA/Approx. 7 mA | Approx. 4 mA | $\triangle$ | Rated input current is smaller. |
| Operating voltage range |  | 10.2 to 26.4 VDC <br> (Ripple ratio within 5\%) | 20.4 to 28.8 VDC <br> (Ripple ratio within 5\%) | $\triangle$ | 12VDC cannot be used. ${ }^{* 1}$ |
| Maximum number of simultaneous input points |  | 60\% 10 points/common) simultaneously ON | Refer to the derating chart. ${ }^{* 3}$ | $\triangle$ | Use it within the range shown in the derating chart. |
| ON voltage/ON current |  | 9.5 VDC or more/2.6mA or more | 19VDC or more/3mA or more | $\triangle$ | 12 VDC cannot be used. ${ }^{* 1}$ |
| OFF voltage/OFF current |  | 6 VDC or less/1.0mA or less | 11VDC or less/1.7mA or less | $\triangle$ | 12 VDC cannot be used. ${ }^{* 1}$ |
| Input resistance |  | Approx. $3.4 \mathrm{k} \Omega$ | Approx. $5.6 \mathrm{k} \Omega$ | $\triangle$ | Input resistance is greater. ${ }^{*}$ |
| Input form |  | Sink input (Input current flows off.) | Positive common type | $\bigcirc$ | The name is different, but the specificaton is equal. |
| Response time | OFF $\rightarrow$ ON | 10 ms or less (6ms TYP.) | $1 \mathrm{~ms} / 5 \mathrm{~ms} / 10 \mathrm{~ms} / 20 \mathrm{~ms} / 70 \mathrm{~ms}$ or less (Set it with CPU parameter.) Initial setting is 10 ms . | $\bigcirc$ | Input response time of the parameter must be used at the initial value ( 10 ms ). |
|  | ON $\rightarrow$ OFF | 10 ms or less ( $7.5 \mathrm{~ms} \mathrm{TYP)}$. | $1 \mathrm{~ms} / 5 \mathrm{~ms} / 10 \mathrm{~ms} / 20 \mathrm{~ms} / 70 \mathrm{~ms}$ or less (Set it with CPU parameter.) Initial setting is 10 ms . | $\bigcirc$ |  |
| Common terminal arrangement |  | 16 points/common <br> (Common terminal: TB17,TB34) | 32 points/common <br> (Common terminal: 1B01, 1B02) | $\bigcirc$ | As the common changes from two commons to a common, wiring with a different voltage for each common is not possible. |
| Operation indication |  | Available (Turning ON the input turns LED ON) | Available (Turning ON the input turns LED ON) <br> (32-point switching indication with SW) | $\bigcirc$ |  |
| Specifications |  | A0J2-E56DT output specifications | QX41Y41P output specifications | Compatibility | Precautions for replacement |
| Number of output points |  | 24 points | 32 points | $\bigcirc$ |  |
| Insulation method |  | Photocoupler | Photocoupler | $\bigcirc$ |  |
| Rated load voltage |  | 12VDC/24VDC | 12VDC/24VDC | $\bigcirc$ |  |
| Operating load voltage range |  | 10.2 to 30VDC | 10.2 to 28.8 VDC | $\Delta$ | Voltages exceeding 28.8 VDC cannot be applied. |
| Maximum load current |  | 0.5A/point, 4A/common | 0.1A/point, 2A/common | $\triangle$ | The maximum load current per point is smaller. Pay attention to the selection of the load to be used. |
| Maximum inrush current |  | 4A, 10ms or less | $0.7 \mathrm{~A}, 10 \mathrm{~ms}$ or less | $\triangle$ | Maximum inrush current is smaller. |
| Leakage current at OFF |  | 0.1 mA or less | 0.1 mA or less | $\bigcirc$ |  |
| Maximum voltage drop at ON |  | $\begin{aligned} & \hline 0.9 \mathrm{VDC} \text { (TYP.) } 0.5 \mathrm{~A} \\ & 1.5 \mathrm{VDC} \text { (MAX.) } 0.5 \mathrm{~A} \end{aligned}$ | $\begin{aligned} & \hline 0.1 \mathrm{VDC} \text { (TYP.) } 0.1 \mathrm{~A} \\ & 0.2 \mathrm{VDC} \text { (MAX.) } 0.1 \mathrm{~A} \end{aligned}$ | $\bigcirc$ | The maximum voltage drop at ON is smaller. |
| Response time | $\mathrm{OFF} \rightarrow \mathrm{ON}$ | 2 ms or less | 1 ms or less | $\bigcirc$ |  |
|  | ON $\rightarrow$ OFF | 2 ms or less (Resistance load) | 1 ms or less (Rated load, resistance load) | $\bigcirc$ |  |
| $\begin{aligned} & \text { External } \\ & \text { supply } \\ & \text { power } \end{aligned}$ | Voltage | 12VDC/24VDC <br> (10.2 to 30VDC) | $\begin{gathered} \hline 12 \mathrm{VDC} / 24 \mathrm{VDC} \\ \text { (10.2 to } 28.8 \mathrm{VDC} \text { ) } \end{gathered}$ | $\triangle$ | Voltages exceeding 28.8 VDC cannot be applied. |
|  | Current | 23 mA <br> (TYP. 24VDC 8 points/common <br> ON) | Max. $15 \mathrm{~mA} /$ common (24VDC, When all points are ON) | $\bigcirc$ |  |
| Surge suppressor |  | Varistor (52 to 62V) | Zener diode | 0 |  |
| Common terminal arrangement |  | 8 points/common (Common terminal: TB9, TB19, TB29) | 32 points/common (Common terminal: 2A01, 2A02) | $\triangle$ | As the common changes from three commons to a common, wiring with a different voltage for each common is not possible. |
| Operation indication |  | Available (Turning ON the output turns LED ON) | Available (Turning ON the output turns LED ON) <br> (32-point switching indication with SW) | $\bigcirc$ |  |

$O$ : Compatible, $\triangle$ : Partially changed, $\times$ :Incompatible

| Specifications | A0J2-E56DT | QX41Y41P | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: |
| Current consumption | 225mA (TYP. All points are ON.) | 130mA(TYP. All points are ON.) | $\bigcirc$ |  |
| External connection method | 36-point terminal block connector <br> (M3 $\times 6$ screws) 2 pieces | 40-pin connector | $\times$ | Wiring change is required. |
| Applicable wire size | 0.75 to $2 \mathrm{~mm}^{2}$ | $0.3 \mathrm{~mm}^{2}$ | $\times$ |  |
| Applicable solderless terminal | $\begin{gathered} \hline 1.25-3,1.25-\mathrm{YS} 3 \mathrm{~A}, \\ 2-\mathrm{S} 3,2-\mathrm{YS} 3 \mathrm{~A}, \\ \mathrm{~V} 1.25-3, \mathrm{~V} 1.25-\mathrm{YS} 3 \mathrm{~A}, \\ \mathrm{~V} 2-\mathrm{S} 3, \mathrm{~V} 2-\mathrm{YS} 3 \mathrm{~A} \end{gathered}$ | - | $\times$ |  |
| External dimensions | $250(\mathrm{H}) \times 190(\mathrm{~W}) \times 41(\mathrm{D}) \mathrm{mm}$ | $98(\mathrm{H}) \times 27.4(\mathrm{~W}) \times 90(\mathrm{D}) \mathrm{mm}$ | $\times$ | The dimensions are different. When a connector is attached: $\mathrm{D}=135 \mathrm{~mm}$ |
| Weight | 1.04 kg | 0.20kg | $\triangle$ |  |

*1 Use the QX70 when using 12VDC.
*2 Check the specifications of the sensor or switches to be connected to the QX41Y41P.
*3 The following shows the derating chart.


## (7) Specifications comparison between A0J2-E28AR and QX10+QY10

| Specifications |  | O : Compatible, $\triangle$ : Partially changed, $\times$ : Incompatible |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | A0J2-E28AR input specifications | QX10 | Compatibility | Precautions for replacement |
| Number of input points |  | 16 points | 16 points | $\bigcirc$ |  |
| Insulation method |  | Photocoupler | Photocoupler | $\bigcirc$ |  |
| Rated input voltage |  | 100-120VAC, $50 / 60 \mathrm{~Hz}$ | 100-120VAC, $50 / 60 \mathrm{~Hz}$ | $\bigcirc$ |  |
| Input voltage distortion |  | - | Within 5\% | - |  |
| Rated input current |  | 10 mA ( $100 \mathrm{VAC}, 60 \mathrm{~Hz}$ ) | Approx. 8 mA (100VAC, 60 Hz ) Approx. 7 mA (100VAC, 50 Hz ) | $\triangle$ | Rated input current is smaller. ${ }^{* 1}$ |
| Operating voltage range |  | $\begin{aligned} & \hline 85 \text { to 132VAC } \\ & (50 / 60 \mathrm{~Hz} \pm 5 \%) \end{aligned}$ | $\begin{aligned} & \hline 85 \text { to } 132 \mathrm{VAC} \\ & (50 / 60 \mathrm{~Hz} \pm 3 \mathrm{~Hz}) \\ & \hline \end{aligned}$ | $\bigcirc$ |  |
| Maximum number of simultaneous input points |  | 100\% (16 points) simultaneously ON | Refer to the derating chart. ${ }^{*}{ }^{2}$ | $\Delta$ | Use it within the range shown in the derating chart. |
| ON voltage/ON current |  | 80VAC or more/6mA or more | 80VAC or more/5mA or more $(50 \mathrm{~Hz}, 60 \mathrm{~Hz})$ | $\bigcirc$ |  |
| OFF voltage/OFF current |  | 40VAC or less/4mA or less | 30 VAC or less/ 1.7 mA or less $(50 \mathrm{~Hz}, 60 \mathrm{~Hz})$ | $\triangle$ | OFF current is smaller. ${ }^{* 1}$ |
| Inrush current |  | Max. 300 mA , within 0.3 ms (132VAC) | Max. 200 mA , within 1 ms (with 132VAC) | $\bigcirc$ |  |
| Input impedance |  | Approx. $10 \mathrm{k} \Omega(60 \mathrm{~Hz})$, <br> Approx. $12 \mathrm{k} \Omega(50 \mathrm{~Hz})$ | Approx. $12 \mathrm{k} \Omega(60 \mathrm{~Hz})$, <br> Approx. $15 \mathrm{k} \Omega(50 \mathrm{~Hz})$ | $\triangle$ | Input impedance is larger. ${ }^{* 1}$ |
| Response time | $\mathrm{OFF} \rightarrow \mathrm{ON}$ | 15 ms or less (6ms TYP.) | 15 ms or less $(100 \mathrm{VAC} 50 \mathrm{~Hz}, 60 \mathrm{~Hz}$ ) | $\bigcirc$ |  |
|  | $\mathrm{ON} \rightarrow$ OFF | 35 ms or less (16ms TYP.) | $\begin{gathered} 20 \mathrm{~ms} \text { or less } \\ (100 \mathrm{VAC} 50 \mathrm{~Hz}, 60 \mathrm{~Hz}) \end{gathered}$ | $\bigcirc$ |  |
| Common terminal arrangement |  | 16 points/common (Common terminal: TB17) | 16 points/common (Common terminal: TB17) | $\bigcirc$ |  |
| Operation indication |  | Available (Turning ON the input turns LED ON) | ON indication (LED) | $\bigcirc$ |  |
| Specifications |  | A0J2-E28AR output specifications | QY10 | Compatibility | Precautions for replacement |
| Number of output points |  | 12 points | 16 points | $\bigcirc$ |  |
| Insulation method |  | Photocoupler | Relay | $\Delta$ | Although the insulation methods differ, the performance of the insulation is the same. |
| Rated switching voltage/ current |  | 24VDC 2A (Resistance load)/ point 240VAC 2 A (COS $\phi=1$ )/point 5A/common | 24VDC 2A (Resistance load)/ point <br> 240 VAC $2 \mathrm{~A}(\operatorname{COS} \phi=1) /$ point 8A/common | $\bigcirc$ |  |
| Minimum switching load |  | 5 VDC 1 mA | 5 VDC 1 mA | $\bigcirc$ |  |
| Maximum switching voltage |  | 264VAC, 125VDC | 264VAC, 125VDC | $\bigcirc$ |  |
| Maximum switching frequency |  | 3600 times/hr | 3600 times/hr | $\bigcirc$ |  |

O : Compatible, $\Delta$ : Partially changed, $\times$ : Incompatible

| Specifications |  | A0J2-E28AR output specifications | QY10 | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Mechanical life |  | 20 million times or more | 20 million times or more | O |  |
| Electrical life |  | Rated switching voltage/current load 200,000 times or more | Rated switching voltage/current load 100,000 times or more | $\triangle$ |  |
|  |  | 200VAC 1.5A, 240VAC 1A (COS $\phi=0.7$ ) 200,000 times or more 200VAC 0.75A, 240VAC 0.5A $(\operatorname{COS} \phi=0.35)$ 200,000 times or more 24VDC 1A, 100VDC 0.1A (L/R=7ms) 200,000 times or more | 200VAC 1.5A, 240VAC 1A $(\operatorname{COS} \phi=0.7) 100,000$ times or more <br> 200VAC 0.4A, 240VAC 0.3A <br> $(\operatorname{COS} \phi=0.7) 300,000$ times or more <br> 200VAC 1A, 240VAC 0.5A <br> $(\operatorname{COS} \phi=0.35) 100,000$ times or more <br> 200VAC 0.3A, 240VAC 0.15A $(\operatorname{COS} \phi=0.35) 300,000$ times or more <br> 24VDC 1A, 100VDC 0.1A <br> (L/R=7ms) 100,000 times or more <br> 24VDC 0.3A, 100VDC 0.03A (L/R=7ms) 300,000 times or more | $\Delta$ | Reduce the exchange intervals of the modules as Mechanical/ Electrical Life is cut to about half. |
| Response time | OFF $\rightarrow$ ON | 10 ms or less | 10 ms or less | $\bigcirc$ |  |
|  | ON $\rightarrow$ OFF | 12 ms or less | 12 ms or less | $\bigcirc$ |  |
| External supply power (Relay coil driving power) | Voltage | $24 \mathrm{VDC} \pm 10 \%$ Ripple voltage $4 \mathrm{Vp}-\mathrm{p}$ or less | - | $\bigcirc$ | External supply power is not required. |
|  | Current | $125 \mathrm{~mA}$ <br> (24VDC. All points are ON.) | - | $\bigcirc$ |  |

O : Compatible, $\Delta$ : Partially changed, x : Incompatible

| Specifications | A0J2-E28AR | QX10/QY10 | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: |
| Common terminal arrangement | 8 points/common (Common terminal: TB26) <br> 3 points/common <br> (Common terminal: TB31) Independent contact (Common terminal: TB33) | 16 points/common (Common terminal: TB17) | $\Delta$ | As the common changes from three commons to a common, wiring with a different voltage for each common is not possible. |
| Operation indication | Available (Turning ON the output turns LED ON) | ON indication (LED) | $\bigcirc$ |  |
| Current consumption | 0.140A (TYP. All points are ON.) | $0.05+0.43=0.48 \mathrm{~A}$ <br> (TYP. All points are ON.) | $\Delta$ | Review current capacity since current consumption is increased. |
| External connection method | 36-point terminal block connector (M3 $\times 6$ screws) | 18-point terminal block (M3 $\times 6$ screws) 2 pieces | $\times$ |  |
| Applicable wire size | 0.75 to $2 \mathrm{~mm}^{2}$ | 0.3 to $0.75 \mathrm{~mm}^{2}$ core (Outside diameter: 2.8 mm or less) | $\times$ | Wiring change is required. |
| Applicable solderless terminal | $\begin{gathered} \text { 1.25-3, 1.25-YS3A, } \\ 2-\mathrm{S} 3,2-\mathrm{YS} 3 \mathrm{~A}, \\ \mathrm{~V} 1.25-3, \mathrm{~V} 1.25-\mathrm{YS} 3 \mathrm{~A}, \\ \mathrm{~V} 2-\mathrm{S} 3, \mathrm{~V} 2-\mathrm{YS} 3 \mathrm{~A} \end{gathered}$ | R1.25-3 <br> (Sleeved solderless terminals cannot be used.) | $\times$ |  |
| External dimensions | 250(H) $\times 132(\mathrm{~W}) \times 41(\mathrm{D}) \mathrm{mm}$ | 98(H) $\times 27.4(\mathrm{~W}) \times 90(\mathrm{D}) \mathrm{mm} \times 2$ | $\times$ | The dimensions are different. |
| Weight | 0.68 kg | $0.17+0.22=0.39 \mathrm{~kg}$ | $\triangle$ |  |

*1 Check the specifications of the sensor or switches to be connected to the QX10.
*2 The following shows the derating chart.


## (8) Specifications comparison between A0J2-E56AR and QX10+QY10

| Specifications |  | O: Compatible, $\triangle$ : Partially changed, $\times$ : Incompatible |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | A0J2-E56AR input specifications | QX10 | Compatibility | Precautions for replacement |
| Number of input points |  | 32 points | 16 points | $\triangle$ | When seventeen or more points are used, use two QX10s. |
| Insulation method |  | Photocoupler | Photocoupler | $\bigcirc$ |  |
| Rated input voltage |  | 100-120VAC, $50 / 60 \mathrm{~Hz}$ | 100-120VAC, $50 / 60 \mathrm{~Hz}$ | $\bigcirc$ |  |
| Input voltage distortion |  | - | Within 5\% | - |  |
| Rated input current |  | 10 mA (100VAC, 60 Hz ) | Approx. 8mA (100VAC, 60Hz) Approx. 7 mA (100VAC, 50 Hz ) | $\triangle$ | Rated input current is smaller. ${ }^{* 1}$ |
| Operating voltage range |  | $\begin{aligned} & 85 \text { to 132VAC } \\ & (50 / 60 \mathrm{~Hz} \pm 5 \%) \end{aligned}$ | $\begin{aligned} & \hline 85 \text { to } 132 \mathrm{VAC} \\ & (50 / 60 \mathrm{~Hz} \pm 3 \mathrm{~Hz}) \end{aligned}$ | $\bigcirc$ |  |
| Maximum number of simultaneous input points |  | 60\% (10 points/common) simultaneously ON | Refer to the derating chart. ${ }^{*}{ }^{2}$ | $\Delta$ | Use it within the range shown in the derating chart. |
| ON voltage/ON current |  | 80VAC or more/6mA or more | 80VAC or more/5mA or more ( $50 \mathrm{~Hz}, 60 \mathrm{~Hz}$ ) | $\bigcirc$ |  |
| OFF voltage/OFF current |  | 40VAC or less/4mA or less | 30 VAC or less $/ 1.7 \mathrm{~mA}$ or less $(50 \mathrm{~Hz}, 60 \mathrm{~Hz})$ | $\triangle$ | OFF current is smaller. ${ }^{* 1}$ |
| Inrush current |  | Max. 300 mA , within 0.3 ms (132VAC) | Max. 200 mA , within 1 ms (with 132VAC) | $\bigcirc$ |  |
| Input impedance |  | Approx. $10 \mathrm{k} \Omega(60 \mathrm{~Hz})$, <br> Approx. $12 \mathrm{k} \Omega(50 \mathrm{~Hz})$ | Approx. $12 \mathrm{k} \Omega(60 \mathrm{~Hz})$, <br> Approx. $15 \mathrm{k} \Omega(50 \mathrm{~Hz})$ | $\triangle$ | Input impedance is larger. ${ }^{* 1}$ |
| Response time | OFF $\rightarrow$ ON | 15 ms or less ( 6 ms TYP.) | 15 ms or less (100VAC $50 \mathrm{~Hz}, 60 \mathrm{~Hz}$ ) | $\bigcirc$ |  |
|  | ON $\rightarrow$ OFF | 35 ms or less (16ms TYP.) | 20 ms or less (100VAC $50 \mathrm{~Hz}, 60 \mathrm{~Hz}$ ) | $\bigcirc$ |  |
| Common terminal arrangement |  | 16 points/common (Common Terminal: TB17, TB34) | 16 points/common (Common terminal: TB17) | $\bigcirc$ |  |
| Operation indication |  | Available (Turning ON the input turns LED ON) | ON indication (LED) | $\bigcirc$ |  |
| Specifications |  | A0J2-E56AR output specifications | QY10 | Compatibility | Precautions for replacement |
| Number of output points |  | 24 points | 16 points | $\triangle$ | When seventeen or more points are used, use two QY10 modules. |
| Insulation method |  | Photocoupler | Relay | $\triangle$ | Although the insulation methods differ, the performance of the insulation is the same. |
| Rated switching voltage/ current |  | 24VDC 2A (Resistance load)/ point 240VAC $2 \mathrm{~A}(\operatorname{COS} \phi=1) /$ point 5A/common | 24VDC 2A (Resistance load)/ point <br> 240 VAC $2 \mathrm{~A}(\operatorname{COS} \phi=1) /$ point 8A/common | O |  |
| Minimum switching load |  | 5VDC 1mA | 5 VDC 1 mA | $\bigcirc$ |  |
| Maximum switching voltage |  | 264VAC, 125VDC | 264VAC, 125VDC | O |  |
| Maximum switching frequency |  | 3600 times/hr | 3600 times/hr | $\bigcirc$ |  |

O : Compatible, $\Delta$ : Partially changed, $\times$ : Incompatible

| Specifications |  | A0J2-E56AR output specifications | QY10 | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Mechanical life |  | 20 million times or more | 20 million times or more | O |  |
| Electrical life |  | Rated switching voltage/current load 200,000 times or more | Rated switching voltage/current load 100,000 times or more | $\Delta$ |  |
|  |  | 200VAC 1.5A, 240VAC 1A $(\operatorname{COS} \phi=0.7)$ 200,000 times or more 200VAC 0.75A, 240VAC 0.5A $(\operatorname{COS} \phi=0.35) 200,000$ times or more <br> 24VDC 1A, 100VDC 0.1A (L/R=7ms) 200,000 times or more | 200VAC 1.5A, 240VAC 1A <br> $(\operatorname{COS} \phi=0.7)$ 100,000 times or more <br> 200VAC 0.4A, 240VAC 0.3A $(\operatorname{COS} \phi=0.7) 300,000$ times or more <br> 200VAC 1A, 240VAC 0.5A $(\operatorname{COS} \phi=0.35) 100,000$ times or more 200VAC 0.3A, 240VAC 0.15A $(\operatorname{COS} \phi=0.35) 300,000$ times or more <br> 24VDC 1A, 100VDC 0.1A (L/R=7ms) 100,000 times or more 24VDC 0.3A, 100VDC 0.03A (L/R=7ms) 300,000 times or more | $\Delta$ | Reduce the exchange intervals of the modules as Mechanical/ Electrical Life is cut to about half. |
| Response time | OFF $\rightarrow$ ON | 10 ms or less | 10 ms or less | $\bigcirc$ |  |
|  | ON $\rightarrow$ OFF | 12 ms or less | 12 ms or less | $\bigcirc$ |  |
| External <br> supply <br> power <br> (Relay coil <br> driving <br> power) | Voltage | $24 \mathrm{VDC} \pm 10 \%$ Ripple voltage $4 \mathrm{Vp}-\mathrm{p}$ or less | - | O | External supply power is not required. |
|  | Current | 230 mA <br> (24VDC TYP. All points are ON.) | - | O |  |

O : Compatible, $\Delta$ : Partially changed, $\times$ : Incompatible

| Specifications | A0J2-E56AR | QX10/QY10 | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: |
| Common terminal arrangement | 8 points/common (Common terminal: TB9, TB19, TB29) | 16 points/common (Common terminal: TB17) | $\Delta$ | As the common changes from three commons to a common, wiring with a different voltage for each common is not possible. |
| Operation indication | Available (Turning ON the output turns LED ON) | ON indication (LED) | O |  |
| Current consumption | 0.225A (TYP. All points are ON.) | $0.05 \times 2+0.43 \times 2=0.96 \mathrm{~A}$ <br> (TYP. All points are ON.) | $\Delta$ | Review current capacity since current consumption is increased. |
| External connection method | ```36-point terminal block connector (M3 \(\times 6\) screws) 2 pieces``` | 18-point terminal block (M3 $\times 6$ screws) 4 pieces | $\times$ |  |
| Applicable wire size | 0.75 to $2 \mathrm{~mm}^{2}$ | 0.3 to $0.75 \mathrm{~mm}^{2}$ core (Outside diameter: 2.8 mm or less) | $\times$ | Wiring change is required. |
| Applicable solderless terminal | $\begin{gathered} \text { 1.25-3, 1.25-YS3A, } \\ 2-\mathrm{S} 3,2-\mathrm{YS} 3 \mathrm{~A}, \\ \mathrm{~V} 1.25-3, \mathrm{~V} 1.25-\mathrm{YS} 3 \mathrm{~A}, \\ \mathrm{~V} 2-\mathrm{S} 3, \mathrm{~V} 2-\mathrm{YS} 3 \mathrm{~A} \end{gathered}$ | R1.25-3 <br> (Sleeved solderless terminals cannot be used.) | $\times$ |  |
| External dimensions | 250(H) $\times 190(\mathrm{~W}) \times 41(\mathrm{D}) \mathrm{mm}$ | 98(H) $\times 27.4(\mathrm{~W}) \times 90(\mathrm{D}) \mathrm{mm} \times 4$ | $\times$ | The dimensions are different. |
| Weight | 1.10 kg | $0.17 \times 2+0.22 \times 2=0.78 \mathrm{~kg}$ | $\triangle$ |  |

*1 Check the specifications of the sensor or switches to be connected to the QX10.
*2 The following shows the derating chart.


## (9) Specifications comparison between A0J2-E28AS and QX10+QY22*1

| Specifications |  | O : Compatible, $\triangle$ : Partially changed, $\times$ : Incompatible |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | A0J2-E28AS input specifications | QX10 | Compatibility | Precautions for replacement |
| Number of input points |  | 16 points | 16 points | $\bigcirc$ |  |
| Insulation method |  | Photocoupler | Photocoupler | $\bigcirc$ |  |
| Rated input voltage |  | 100-120VAC, $50 / 60 \mathrm{~Hz}$ | 100-120VAC, $50 / 60 \mathrm{~Hz}$ | $\bigcirc$ |  |
| Input voltage distortion |  | - | Within 5\% | - |  |
| Rated input current |  | 10 mA ( $100 \mathrm{VAC}, 60 \mathrm{~Hz}$ ) | Approx. 8mA (100VAC, 60Hz) Approx. 7 mA (100VAC, 50 Hz ) | $\triangle$ | Rated input current is smaller.*2 |
| Operating voltage range |  | $\begin{aligned} & \hline 85 \text { to 132VAC } \\ & (50 / 60 \mathrm{~Hz} \pm 5 \%) \end{aligned}$ | $\begin{aligned} & \hline 85 \text { to } 132 \mathrm{VAC} \\ & (50 / 60 \mathrm{~Hz} \pm 3 \mathrm{~Hz}) \\ & \hline \end{aligned}$ | $\bigcirc$ |  |
| Maximum number of simultaneous input points |  | 100\% (16 points) simultaneously ON | Refer to the derating chart. ${ }^{* 3}$ | $\triangle$ | Use it within the range shown in the derating chart. |
| ON voltage/ON current |  | 80VAC or more/6mA or more | 80VAC or more/5mA or more ( $50 \mathrm{~Hz}, 60 \mathrm{~Hz}$ ) | $\bigcirc$ |  |
| OFF voltage/OFF current |  | 40VAC or less/4mA or less | 30VAC or less/1.7mA or less $(50 \mathrm{~Hz}, 60 \mathrm{~Hz})$ | $\triangle$ | OFF current is smaller. The shape configuration differs. |
| Inrush current |  | Max. 300 mA , within 0.3 ms (132VAC) | Max. 200mA, within 1 ms (with 132VAC) | $\bigcirc$ |  |
| Input impedance |  | Approx. $10 \mathrm{k} \Omega(60 \mathrm{~Hz})$, <br> Approx. $12 \mathrm{k} \Omega(50 \mathrm{~Hz})$ | Approx. $12 \mathrm{k} \Omega(60 \mathrm{~Hz})$, <br> Approx. $15 \mathrm{k} \Omega(50 \mathrm{~Hz})$ | $\triangle$ | Input impedance is larger. ${ }^{*}$ |
| Response time | $\mathrm{OFF} \rightarrow \mathrm{ON}$ | 15 ms or less (6ms TYP.) | $\begin{gathered} \hline 15 \mathrm{~ms} \text { or less } \\ (100 \mathrm{VAC} 50 \mathrm{~Hz}, 60 \mathrm{~Hz}) \end{gathered}$ | $\bigcirc$ |  |
|  | ON $\rightarrow$ OFF | 35 ms or less (16ms TYP.) | $\begin{gathered} 20 \mathrm{~ms} \text { or less } \\ (100 \mathrm{VAC} 50 \mathrm{~Hz}, 60 \mathrm{~Hz}) \end{gathered}$ | $\bigcirc$ |  |
| Common terminal arrangement |  | 16 points/common (Common terminal: TB17) | 16 points/common (Common terminal: TB17) | $\bigcirc$ |  |
| Operation indication |  | Available (Turning ON the input turns LED ON) | ON indication (LED) | $\bigcirc$ |  |
| Specifications |  | A0J2-E28AS output specifications | QY22 | Compatibility | Precautions for replacement |
| Number of output points |  | 12 points | 16 points | $\bigcirc$ |  |
| Insulation method |  | Photocoupler | Photocoupler | $\bigcirc$ |  |
| Rated load voltage |  | 100 to 240VAC, 40 to 70 Hz | $\begin{gathered} 100 \text { to } 240 \mathrm{VAC} \\ 50 / 60 \mathrm{~Hz} \pm 5 \% \end{gathered}$ | $\triangle$ | The frequency will be low. Check the specifications of external devices. |
| Maximum load voltage |  | 264VAC | 264VAC | $\bigcirc$ |  |
| Maximum load current |  | 0.6A/point, 2.4A/common | 0.6A/point, 4.8A/common | $\bigcirc$ |  |
| Minimum load voltage/ current |  | 24 VAC 100 mA 100VAC 10mA 240VAC 10 mA | 24 VAC 100 mA 100VAC 25 mA 240VAC 25 mA | $\triangle$ | The minimum load current is greater. Use caution on selecting the load to use. |
| Maximum inrush current |  | $20 \mathrm{~A}, 10 \mathrm{~ms}$ or less <br> 8 A 100 ms or less | 20A 1 cycle or less | $\bigcirc$ |  |
| Leakage current at OFF |  | 1.5 mA ( 120 VAC 60 Hz ) <br> 3 mA (240VAC 60Hz) | 1.5 mA or less (During 120 V 60 Hz ) 3 mA or less (During 240 V 60 Hz ) | O |  |
| Maximum voltage drop at ON |  | 1.5 V or less $(100$ to 600 mA$)$ 1.8 V or less ( 100 mA or less) 2.0 V or less $(10$ to 50 mA$)$ | 1.5 V or less | $\bigcirc$ |  |
| Response time | OFF $\rightarrow$ ON | 1 ms or less | 1 ms or less | O |  |
|  | $\mathrm{ON} \rightarrow$ OFF | 0.5 cycle +1 ms or less | $1 \mathrm{~ms}+0.5$ cycle or less (Rated load, resistance load) | $\bigcirc$ |  |
| Fuse rating |  | High speed type fuse 3.2A <br> (One/common) HP-32 | None | $\times$ |  |
| Fuse blown indication |  | Available (When a fuse blown occurs, LED is lit, and a signal is output to the CPU) | - | $\times$ | The fuse is not built in. ${ }^{4}$ |
| Surge suppressor |  | $\begin{gathered} \text { CR absorber } \\ (0.022 \mu \mathrm{~F}+47 \Omega) \\ \hline \end{gathered}$ | CR absorber | $\bigcirc$ |  |

O : Compatible, $\triangle$ : Partially changed, $\times$ : Incompatible

| Specifications | A0J2-E28AS output specifications | QY22 | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: |
| Common terminal arrangement | 8 points/common (Common terminal: TB26) <br> 4 points/common (Common terminal: TB33) | 16 points/common (Common terminal: TB17) | $\Delta$ | As the common changes from two commons to a common, wiring with a different voltage for each common is not possible. |
| Operation indication | Available (Turning ON the output turns LED ON) | ON indication (LED) | $\bigcirc$ |  |
| Specifications | A0J2-E28AS | QX10/QY22 | Compatibility | Precautions for replacement |
| Current consumption | $0.260 \mathrm{~A} \text { (TYP. }$ <br> All points are ON.) | $0.05+0.25=0.30 \mathrm{~A}$ <br> (TYP. All points are ON.) | $\triangle$ | Review current capacity since current consumption is increased. |
| External connection method | 36-point terminal block connector (M3 $\times 6$ screws) | 18-point terminal block (M3 $\times 6$ screws) 2 pieces | $\times$ |  |
| Applicable wire size | 0.75 to $2 \mathrm{~mm}^{2}$ | 0.3 to $0.75 \mathrm{~mm}^{2}$ core (Outside diameter: 2.8 mm or less) | $\times$ | Wiring change is required. |
| Applicable solderless terminal | $\begin{gathered} \text { 1.25-3, 1.25-YS3A, } \\ 2-\mathrm{S} 3,2-\mathrm{YS} 3 \mathrm{~A}, \\ \mathrm{~V} 1.25-3, \mathrm{~V} 1.25-\mathrm{YS} 3 \mathrm{~A}, \\ \mathrm{~V} 2-\mathrm{S} 3, \mathrm{~V} 2-\mathrm{YS} 3 \mathrm{~A} \end{gathered}$ | R1.25-3 <br> (Sleeved solderless terminals cannot be used.) | $\times$ |  |
| External dimensions | $250(\mathrm{H}) \times 132(\mathrm{~W}) \times 41(\mathrm{D}) \mathrm{mm}$ | $\begin{gathered} 98(\mathrm{H}) \times 27.4(\mathrm{~W}) \times 90(\mathrm{D}) \mathrm{mm}+ \\ 98(\mathrm{H}) \times 27.4(\mathrm{~W}) \times 112.3(\mathrm{D}) \mathrm{mm} \end{gathered}$ | $\times$ | The dimensions are different. |
| Weight | 0.68 kg | $0.17+0.40=0.57 \mathrm{~kg}$ | $\triangle$ |  |

*1 Consider the characteristics of the triac and observe the necessary precautions by referring to Section 3.3 (3) before replacing the modules.
*2 Check the specifications of the sensor or switches to be connected to the QX10.
*3 The following shows the derating chart.

*4 Install a fuse for each external terminal point to prevent the burnout of the external devices and modules during load shorts. In addition, when a fuse blown indication is necessary, configure an external circuit.

## (10)Specifications comparison between A0J2-E56AS and QX10+QY22*1

| Specifications |  | O: Compatible, $\Delta$ : Partially changed, $\times$ : Incompatible |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | A0J2-E56AS input specifications | QX10 | Compatibility | Precautions for replacement |
| Number of input points |  | 32 points | 16 points | $\triangle$ | When seventeen or more points are used, use two QX10 modules. |
| Insulation method |  | Photocoupler | Photocoupler | $\bigcirc$ |  |
| Rated input voltage |  | 100-120VAC, $50 / 60 \mathrm{~Hz}$ | 100-120VAC, $50 / 60 \mathrm{~Hz}$ | $\bigcirc$ |  |
| Input voltage distortion |  | - | Within 5\% | - |  |
| Rated input current |  | 10 mA (100VAC, 60 Hz ) | Approx. 8 mA (100VAC, 60Hz) <br> Approx. 7 mA (100VAC, 50 Hz ) | $\triangle$ | Rated input current is smaller.*2 |
| Operating voltage range |  | $\begin{aligned} & \hline 85 \text { to 132VAC } \\ & (50 / 60 \mathrm{~Hz} \pm 5 \%) \end{aligned}$ | $\begin{aligned} & \hline 85 \text { to } 132 \mathrm{VAC} \\ & (50 / 60 \mathrm{~Hz} \pm 3 \mathrm{~Hz}) \end{aligned}$ | $\bigcirc$ |  |
| Maximum number of simultaneous input points |  | 60\% (10 points/common) simultaneously ON | Refer to the derating chart. ${ }^{* 3}$ | $\triangle$ | Use it within the range shown in the derating chart. |
| ON voltage/ON current |  | 80VAC or more/6mA or more | 80VAC or more $/ 5 \mathrm{~mA}$ or more ( $50 \mathrm{~Hz}, 60 \mathrm{~Hz}$ ) | $\bigcirc$ |  |
| OFF voltage/OFF current |  | 40VAC or less/4mA or less | 30 VAC or less $/ 1.7 \mathrm{~mA}$ or less $(50 \mathrm{~Hz}, 60 \mathrm{~Hz})$ | $\triangle$ | OFF current is smaller. ${ }^{*}{ }^{2}$ |
| Inrush current |  | Max. 300 mA , within 0.3 ms (132VAC) | Max. 200 mA , within 1 ms (with 132VAC) | $\bigcirc$ |  |
| Input impedance |  | Approx. $10 \mathrm{k} \Omega(60 \mathrm{~Hz})$, <br> Approx. $12 \mathrm{k} \Omega(50 \mathrm{~Hz})$ | Approx. $12 \mathrm{k} \Omega(60 \mathrm{~Hz})$, <br> Approx. $15 \mathrm{k} \Omega(50 \mathrm{~Hz})$ | $\triangle$ | Input impedance is larger. ${ }^{* 2}$ |
| Response time | OFF $\rightarrow$ ON | 15 ms or less (6ms TYP.) | $\begin{gathered} 15 \mathrm{~ms} \text { or less } \\ (100 \mathrm{VAC} 50 \mathrm{~Hz}, 60 \mathrm{~Hz}) \end{gathered}$ | $\bigcirc$ |  |
|  | ON $\rightarrow$ OFF | 35 ms or less (16ms TYP.) | 20 ms or less $(100 \mathrm{VAC} 50 \mathrm{~Hz}, 60 \mathrm{~Hz})$ | $\bigcirc$ |  |
| Common terminal arrangement |  | 16 points/common (Common Terminal: TB17, TB34) | 16 points/common (Common terminal: TB17) | $\bigcirc$ |  |
| Operation indication |  | Available (Turning ON the input turns LED ON) | ON indication (LED) | $\bigcirc$ |  |
| Specifications |  | A0J2-E56AS output specifications | QY22 | Compatibility | Precautions for replacement |
| Number of output points |  | 24 points | 16 points | $\triangle$ | When seventeen or more points are used, use two QY22 modules. |
| Insulation method |  | Photocoupler | Photocoupler | $\bigcirc$ |  |
| Rated load voltage |  | 100-240VAC, 40-70Hz | $\begin{gathered} 100-240 \mathrm{VAC} \\ 50 / 60 \mathrm{~Hz} \pm 5 \% \end{gathered}$ | $\Delta$ | The frequency will be low. Check the specifications of external devices. |
| Maximum load voltage |  | 264VAC | 264VAC | 0 |  |
| Maximum load current |  | 0.6A/point, 2.4A/common | 0.6A/point, 4.8A/common | $\bigcirc$ |  |
| Minimum load voltage/ current |  | 24 VAC 100 mA 100VAC 10 mA 240VAC 10 mA | 24 VAC 100 mA 100 VAC 25 mA 240VAC 25 mA | $\Delta$ | The minimum load current is greater. Use caution on selecting the load to use. |
| Maximum inrush current |  | 20A 10 ms or less 8A 100ms or less | 20A 1 cycle or less | $\bigcirc$ |  |
| Leakage current at OFF |  | $\begin{gathered} 1.5 \mathrm{~mA}(120 \mathrm{VAC} 60 \mathrm{~Hz}) \\ 3 \mathrm{~mA}(240 \mathrm{VAC} 60 \mathrm{~Hz}) \end{gathered}$ | 1.5 mA or less (During 120 V 60 Hz ) 3 mA or less (During 240 V 60 Hz ) | $\bigcirc$ |  |
| Maximum voltage drop at ON |  | 1.5 V or less $(100$ to 600 mA$)$ 1.8 V or less $(100 \mathrm{~mA}$ or less $)$ 2.0 V or less $(10$ to 50 mA$)$ | 1.5 V or less | O |  |
| Response time | OFF $\rightarrow$ ON | 1 ms or less | 1 ms or less | $\bigcirc$ |  |
|  | ON $\rightarrow$ OFF | 0.5 cycle +1 ms or less | $1 \mathrm{~ms}+0.5$ cycle or less (Rated load, resistance load) | $\bigcirc$ |  |
| Fuse rating |  | High speed type fuse 3.2A (One/common) HP-32 | None | $\times$ |  |
| Fuse blown indication |  | Available (When a fuse blown occurs, LED is lit, and a signal is output to the CPU) | - | $\times$ | The fuse is not built in. ${ }^{4}$ |

O : Compatible, $\Delta$ : Partially changed, $\times$ : Incompatible

| Specifications | A0J2-E56AS output specifications | QY22 | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: |
| Surge suppressor | $\begin{gathered} \text { CR absorber } \\ (0.022 \mu \mathrm{~F}+47 \Omega) \end{gathered}$ | CR absorber | $\bigcirc$ |  |
| Common terminal arrangement | 8 points/common (Common terminal: TB9, TB19, TB29) | 16 points/common (Common terminal: TB17) | $\Delta$ | As the common changes from three commons to a common, wiring with a different voltage for each common is not possible. |
| Operation indication | Available (Turning ON the output turns LED ON) | ON indication (LED) | $\bigcirc$ |  |
| Specifications | A0J2-E56AS | QX10/QY22 | Compatibility | Precautions for replacement |
| Current consumption | 0.460A (TYP. All points are ON.) | $0.05 \times 2+0.25 \times 2=0.60 \mathrm{~A}$ <br> (TYP. All points are ON.) | $\triangle$ | Review current capacity since current consumption is increased. |
| External connection method | 36-point terminal block connector <br> (M3 $\times 6$ screws) 2 pieces | 18-point terminal block (M3 $\times 6$ screws) 4 pieces | $\times$ |  |
| Applicable wire size | 0.75 to $2 \mathrm{~mm}^{2}$ | 0.3 to $0.75 \mathrm{~mm}^{2}$ core (Outside diameter: 2.8 mm or less) | $\times$ | Wiring change is required. |
| Applicable solderless terminal | $\begin{gathered} \hline 1.25-3,1.25-\mathrm{YS} 3 \mathrm{~A}, \\ 2-\mathrm{S} 3,2-\mathrm{YS} 3 \mathrm{~A}, \\ \mathrm{~V} 1.25-3, \mathrm{~V} 1.25-\mathrm{YS} 3 \mathrm{~A}, \\ \mathrm{~V} 2-\mathrm{S} 3, \mathrm{~V} 2-\mathrm{YS} 3 \mathrm{~A} \end{gathered}$ | R1.25-3 <br> (Sleeved solderless terminals cannot be used.) | $\times$ |  |
| External dimensions | $250(\mathrm{H}) \times 190(\mathrm{~W}) \times 41(\mathrm{D}) \mathrm{mm}$ | $\begin{gathered} 98(\mathrm{H}) \times 27.4(\mathrm{~W}) \times 90(\mathrm{D}) \mathrm{mm} \times 2 \\ +98(\mathrm{H}) \times 27.4(\mathrm{~W}) \times 112.3(\mathrm{D}) \mathrm{mm} \\ \times 2 \end{gathered}$ | $\times$ | The dimensions are different. |
| Weight | 1.10 kg | $0.17 \times 2+0.40 \times 2=1.14 \mathrm{~kg}$ | $\Delta$ | Be aware of that the weight increases when calculating the weight. |

*1 Consider the characteristics of the triac and observe the necessary precautions by referring to Section 3.3 (3) before replacing the modules.
*2 Check the specifications of the sensor or switches to be connected to the QX10.
*3 The following shows the derating chart.

*4 Install a fuse for each external terminal point to prevent the burnout of the external devices and modules during load shorts. In addition, when a fuse blown indication is necessary, configure an external circuit.

## (11)Specifications comparison between A0J2-E28DS and QX40+QY22*1

| Specifications |  | O : Compatible, $\Delta$ : Partially changed, $\times$ : Incompatible |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | A0J2-E28DS input specifications | QX40 | Compatibility | Precautions for replacement |
| Number of input points |  | 16 points | 16 points | $\bigcirc$ |  |
| Insulation method |  | Photocoupler | Photocoupler | $\bigcirc$ |  |
| Rated input voltage |  | 12VDC/24VDC | 24VDC | $\Delta$ | 12VDC cannot be used. ${ }^{*}$ |
| Rated input current |  | Approx. 3mA/Approx. 7 mA | Approx. 4 mA | $\triangle$ | Rated input current is smaller. ${ }^{* 3}$ |
| Operating voltage range |  | $\begin{gathered} 10.2 \text { to } 26.4 \mathrm{VDC} \\ \text { (Ripple ratio within } 5 \% \text { ) } \end{gathered}$ | $\begin{gathered} 20.4 \text { to } 28.8 \mathrm{VDC} \\ \text { (Ripple ratio within 5\%) } \end{gathered}$ | $\triangle$ | 12VDC cannot be used. ${ }^{*}$ |
| Maximum number of simultaneous input points |  | 100\% (16 points) simultaneously ON | 100\% simultaneously ON | O |  |
| ON voltage/ON current |  | 9.5VDC or more/2.6mA or more | 19VDC or more/3mA or more | $\Delta$ | 12VDC cannot be used. ${ }^{*}$ |
| OFF voltage/OFF current |  | 6 VDC or less $/ 1.0 \mathrm{~mA}$ or less | 11 VDC or less/1.7mA or less | $\triangle$ | 12VDC cannot be used. ${ }^{*}$ |
| Input resistance |  | Approx. 3.4k $\Omega$ | Approx. $5.6 \mathrm{k} \Omega$ | $\triangle$ | Input resistance is greater. ${ }^{* 3}$ |
| Input form |  | Sink input (Input current flows off.) | Positive common | $\bigcirc$ | The name is different, but the specificaton is equal. |
| Response time | $\mathrm{OFF} \rightarrow \mathrm{ON}$ | 10 ms or less (6ms TYP.) | $\qquad$ | O | Input response time of the parameter must be used at the initial value ( 10 ms ). |
|  | ON $\rightarrow$ OFF | 10 ms or less ( $7.5 \mathrm{~ms} \mathrm{TYP)}$. | $1 \mathrm{~ms} / 5 \mathrm{~ms} / 10 \mathrm{~ms} / 20 \mathrm{~ms} / 70 \mathrm{~ms}$ or less <br> (Set it with CPU parameter.) Initial setting is 10 ms . | $\bigcirc$ |  |
| Common terminal arrangement |  | 16 points/common (Common terminal: TB17) | 16 points/common (Common terminal: TB17) | $\bigcirc$ |  |
| Operation indication |  | Available (Turning ON the input turns LED ON) | ON indication (LED) | $\bigcirc$ |  |
| Specifications |  | A0J2-E28DS output specifications | QY22 | Compatibility | Precautions for replacement |
| Number of output points |  | 12 points | 16 points | $\bigcirc$ |  |
| Insulation method |  | Photocoupler | Photocoupler | $\bigcirc$ |  |
| Rated load voltage |  | 100 to $240 \mathrm{VAC}, 40$ to 70 Hz | $\begin{gathered} 100 \text { to } 240 \mathrm{VAC} \\ 50 / 60 \mathrm{~Hz} \pm 5 \% \end{gathered}$ | $\Delta$ | The frequency will be low. Check the specifications of external devices. |
| Maximum load voltage |  | 264VAC | 264VAC | $\bigcirc$ |  |
| Maximum load current |  | 0.6A/point, 2.4A/common | 0.6A/point, 4.8A/common | $\bigcirc$ |  |
| Minimum load voltage, current |  | 24 VAC 100 mA 100VAC 10mA 240VAC 10 mA | 24 VAC 100 mA 100VAC 25 mA 240VAC 25mA | $\Delta$ | The minimum load current is greater. Use caution on selecting the load to use. |
| Maximum inrush current |  | 20A 10 ms or less <br> 8 A 100ms or less | 20A 1 cycle or less | $\bigcirc$ |  |
| Leakage current at OFF |  | $\begin{gathered} 1.5 \mathrm{~mA}(120 \mathrm{VAC} 60 \mathrm{~Hz}) \\ 3 \mathrm{~mA}(240 \mathrm{VAC} 60 \mathrm{~Hz}) \end{gathered}$ | 1.5 mA or less (During 120 V 60 Hz ) 3 mA or less (During 240 V 60Hz) | O |  |
| Maximum voltage drop at ON |  | 1.5 V or less $(100$ to 600 mA$)$ 1.8 V or less $(100 \mathrm{~mA}$ or less $)$ 2.0 V or less $(10$ to 50 mA$)$ | 1.5 V or less | $\bigcirc$ |  |
| Response time | $\mathrm{OFF} \rightarrow \mathrm{ON}$ | 1 ms or less | 1 ms or less | $\bigcirc$ |  |
|  | ON $\rightarrow$ OFF | 0.5 cycle +1 ms or less | $1 \mathrm{~ms}+0.5$ cycle or less <br> (Rated load, resistance load) | O |  |
| Fuse rating |  | High speed type fuse 3.2A <br> (1/common) HP-32 | None | $\times$ |  |
| Fuse blown indication |  | Available (When a fuse blown occurs, LED is lit, and a signal is output to the CPU) | - | $\times$ | The fuse is not built in. ${ }^{4}$ |
| Surge suppressor |  | $\begin{gathered} \text { CR absorber } \\ (0.022 \mu \mathrm{~F}+47 \Omega) \end{gathered}$ | CR absorber | $\bigcirc$ |  |

O : Compatible, $\triangle$ : Partially changed, $\times$ : Incompatible

| Specifications | A0J2-E28DS output specifications | QY22 | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: |
| Common terminal arrangement | 8 points/common (Common terminal: TB26) <br> 4 points/common (Common terminal: TB33) | 16 points/common (Common terminal: TB17) | $\Delta$ | As the common changes from two commons to a common, wiring with a different voltage for each common is not possible. |
| Operation indication | Available (Turning ON the output turns LED ON) | ON indication (LED) | $\bigcirc$ |  |
| Specifications | A0J2-E28DS | QX40/QY22 | Compatibility | Precautions for replacement |
| Current consumption | 0.260A (TYP. All points are ON.) | $0.05+0.25=0.30 \mathrm{~A}$ <br> (TYP. All points are ON.) | $\triangle$ | Review current capacity since current consumption is increased. |
| External connection method | 36-point terminal block connector (M3 $\times 6$ screws) | 18-point terminal block (M3 $\times 6$ screws) 2 pieces | $\times$ |  |
| Applicable wire size | 0.75 to $2 \mathrm{~mm}^{2}$ | 0.3 to $0.75 \mathrm{~mm}^{2}$ core (Outside diameter: 2.8 mm or less) | $\times$ | Wiring change is required. |
| Applicable solderless terminal | $\begin{gathered} \hline 1.25-3,1.25-\mathrm{YS} 3 \mathrm{~A}, \\ 2-\mathrm{S} 3,2-\mathrm{YS} 3 \mathrm{~A}, \\ \mathrm{~V} 1.25-3, \mathrm{~V} 1.25-\mathrm{YS} 3 \mathrm{~A}, \\ \mathrm{~V} 2-\mathrm{S} 3, \mathrm{~V} 2-\mathrm{YS} 3 \mathrm{~A} \end{gathered}$ | R1.25-3 <br> (Sleeved solderless terminals cannot be used.) | $\times$ |  |
| External dimensions | $250(\mathrm{H}) \times 132(\mathrm{~W}) \times 41(\mathrm{D}) \mathrm{mm}$ | $\begin{aligned} & 98(\mathrm{H}) \times 27.4(\mathrm{~W}) \times 90(\mathrm{D}) \mathrm{mm}+ \\ & 98(\mathrm{H}) \times 27.4(\mathrm{~W}) \times 112.3(\mathrm{D}) \mathrm{mm} \end{aligned}$ | $\times$ | The dimensions are different. |
| Weight | 0.65 kg | $0.16+0.40=0.56 \mathrm{~kg}$ | $\triangle$ |  |

*1 Consider the characteristics of the triac and observe the necessary precautions by referring to Section 3.3 (3) before replacing the modules.
*2 Use the QX70 when using 12VDC.
*3 Check the specifications of the sensor or switches to be connected to the QX40.
*4 Install a fuse for each external terminal point to prevent the burnout of the external devices and modules during load shorts. In addition, when a fuse blown indication is necessary, configure an external circuit.

## (12)Specifications comparison between A0J2-E56DS and QX40+QY22*1

| Specifications |  | O: Compatible, $\triangle$ : Partially changed, $\times$ : Incompatible |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | A0J2-E56DS input specifications | QX40 | Compatibility | Precautions for replacement |
| Number of input points |  | 32 points | 16 points | $\triangle$ | When seventeen or more points are used, use two QX40 modules. |
| Insulation method |  | Photocoupler | Photocoupler | $\bigcirc$ |  |
| Rated input voltage |  | 12VDC/24VDC | 24VDC | $\triangle$ | 12VDC cannot be used. ${ }^{*}$ |
| Rated input current |  | Approx. 3mA/Approx. 7 mA | Approx. 4mA | $\triangle$ | Rated input current is smaller.*3 |
| Operating voltage range |  | $\begin{gathered} 10.2 \text { to } 26.4 \mathrm{VDC} \\ \text { (Ripple ratio within 5\%) } \end{gathered}$ | $\begin{gathered} 20.4 \text { to } 28.8 \mathrm{VDC} \\ \text { (Ripple ratio within } 5 \% \text { ) } \end{gathered}$ | $\Delta$ | 12VDC cannot be used. ${ }^{*}$ |
| Maximum number of simultaneous input points |  | 60\% (10 points/common) simultaneously ON | 100\% simultaneously ON | $\bigcirc$ |  |
| ON voltage/ON current |  | 9.5 VDC or more/2.6mA or more | 19VDC or more/3mA or more | $\triangle$ | 12VDC cannot be used. ${ }^{*}{ }^{2}$ |
| OFF voltage/OFF current |  | 6 VDC or less/1.0mA or less | 11VDC or less/1.7mA or less | $\triangle$ | 12 VDC cannot be used. ${ }^{*}$ |
| Input resistance |  | Approx. $3.4 \mathrm{k} \Omega$ | Approx. $5.6 \mathrm{k} \Omega$ | $\triangle$ | Input resistance is greater. ${ }^{* 3}$ |
| Input format |  | Sink input (Input current flows off.) | Positive common | $\bigcirc$ | The name is different, but the specificaton is equal. |
| Response time | OFF $\rightarrow$ ON | 10 ms or less (6ms TYP.) | $1 \mathrm{~ms} / 5 \mathrm{~ms} / 10 \mathrm{~ms} / 20 \mathrm{~ms} / 70 \mathrm{~ms}$ or less <br> (Set it with CPU parameter.) Initial setting is 10 ms . | $\bigcirc$ | Input response time of the parameter must be used at the initial value ( 10 ms ). |
|  | ON $\rightarrow$ OFF | 10 ms or less ( $7.5 \mathrm{~ms} \mathrm{TYP)}$. | $1 \mathrm{~ms} / 5 \mathrm{~ms} / 10 \mathrm{~ms} / 20 \mathrm{~ms} / 70 \mathrm{~ms}$ or less <br> (Set it with CPU parameter.) Initial setting is 10 ms . | O |  |
| Common terminal arrangement |  | 16 points/common (Common terminal: TB17, TB34) | 16 points/common (Common terminal: TB17) | O |  |
| Operation indication |  | Available (Turning ON the input turns LED ON) | ON indication (LED) | $\bigcirc$ |  |
| Specifications |  | A0J2-E56DS output specifications | QY22 | Compatibility | Precautions for replacement |
| Number of output points |  | 24 points | 16 points | $\Delta$ | When seventeen or more points are used, use two QY22 modules. |
| Insulation method |  | Photocoupler | Photocoupler | O |  |
| Rated load voltage |  | 100 to 240VAC, 40 to 70 Hz | $\begin{gathered} 100 \text { to } 240 \mathrm{VAC} \\ 50 / 60 \mathrm{~Hz} \pm 5 \% \end{gathered}$ | $\Delta$ | The frequency will be low. Check the specifications of external devices. |
| Maximum load voltage |  | 264VAC | 264VAC | $\bigcirc$ |  |
| Maximum load current |  | 0.6A/point, 2.4A/common | 0.6A/point, 4.8A/common | O |  |
| Minimum load voltage/ current |  | 24VAC 100 mA 100 VAC 10 mA 240VAC 10 mA | 24 VAC 100 mA 100 VAC 25 mA 240VAC 25 mA | $\Delta$ | The minimum load current is greater. Use caution on selecting the load to use. |
| Maximum inrush current |  | $20 \mathrm{~A}, 10 \mathrm{~ms}$ or less 8A 100ms or less | 20A 1 cycle or less | $\bigcirc$ |  |
| Leakage current at OFF |  | 1.5 mA ( 120 VAC 60 Hz ) <br> 3 mA (240VAC 60Hz) | 1.5 mA or less (During 120V 60 Hz ) 3 mA or less (During 240V 60Hz) | $\bigcirc$ |  |
| Maximum voltage drop at ON |  | 1.5 V or less $(100$ to 600 mA$)$ 1.8 V or less $(100 \mathrm{~mA}$ or less) 2.0 V or less $(10$ to 50 mA$)$ | 1.5 V or less | O |  |
| Response time | OFF $\rightarrow$ ON | 1 ms or less | 1 ms or less | $\bigcirc$ |  |
|  | ON $\rightarrow$ OFF | 0.5 cycle +1 ms or less | $1 \mathrm{~ms}+0.5$ cycle or less (Rated load, resistance load) | $\bigcirc$ |  |
| Fuse rating |  | High speed type fuse 3.2A (1/common) HP-32 | None | $\times$ |  |
| Fuse blown indication |  | Available (When a fuse blown occurs, LED is lit, and a signal is output to the CPU) | - | $\times$ | The fuse is not built in. ${ }^{4}$ |
| Surge suppressor |  | $\begin{gathered} \text { CR absorber } \\ (0.022 \mu \mathrm{~F}+47 \Omega) \end{gathered}$ | CR absorber | $\bigcirc$ |  |

O : Compatible, $\Delta$ : Partially changed, $\times$ : Incompatible

| Specifications | A0J2-E56DS output specifications | QY22 | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: |
| Common terminal arrangement | 8 points/common (Common terminal: TB9, TB19, TB29) | 16 points/common (Common terminal: TB17) | $\triangle$ | As the common changes from three commons to a common, wiring with a different voltage for each common is not possible. |
| Operation indication | Available (Turning ON the output turns LED ON) | ON indication (LED) | $\bigcirc$ |  |
| Specifications | A0J2-E56DS | QX40/QY22 | Compatibility | Precautions for replacement |
| Current consumption | $0.460 \mathrm{~A} \text { (TYP. }$ <br> All points are ON.) | $0.05 \times 2+0.25 \times 2=0.60 \mathrm{~A}$ <br> (TYP. All points are ON.) | $\triangle$ | Review current capacity since current consumption is increased. |
| External connection method | 36-point terminal block connector <br> (M3 $\times 6$ screws) 2 pieces | 18-point terminal block (M3 $\times 6$ screws) 4 pieces | $\times$ |  |
| Applicable wire size | 0.75 to $2 \mathrm{~mm}^{2}$ | 0.3 to $0.75 \mathrm{~mm}^{2}$ core (Outside diameter: 2.8 mm or less) | $\times$ | Wiring change is required. |
| Applicable solderless terminal | $\begin{gathered} \hline 1.25-3,1.25-\mathrm{YS} 3 \mathrm{~A}, \\ 2-\mathrm{S} 3,2-\mathrm{YS} 3 \mathrm{~A}, \\ \mathrm{~V} 1.25-3, \mathrm{~V} 1.25-\mathrm{YS} 3 \mathrm{~A}, \\ \mathrm{~V} 2-\mathrm{S} 3, \mathrm{~V} 2-\mathrm{YS} 3 \mathrm{~A} \end{gathered}$ | R1.25-3 <br> (Sleeved solderless terminals cannot be used.) | $\times$ |  |
| External dimensions | $250(\mathrm{H}) \times 190(\mathrm{~W}) \times 41(\mathrm{D}) \mathrm{mm}$ | $\begin{gathered} 98(\mathrm{H}) \times 27.4(\mathrm{~W}) \times 90(\mathrm{D}) \mathrm{mm} \times 2+ \\ 98(\mathrm{H}) \times 27.4(\mathrm{~W}) \times 112.3(\mathrm{D}) \mathrm{mm} \times \\ 2 \end{gathered}$ | $\times$ | The dimensions are different. |
| Weight | 1.05 kg | $0.16 \times 2+0.40 \times 2=1.12 \mathrm{~kg}$ | $\triangle$ | Be aware of that the weight increases when calculating the weight. |

*1 Consider the characteristics of the triac and observe the necessary precautions by referring to Section 3.3 (3) before replacing the modules.
*2 Use the QX70 when using 12VDC.
*3 Check the specifications of the sensor or switches to be connected to the QX40.
*4 Install a fuse for each external terminal point to prevent the burnout of the external devices and modules during load shorts. In addition, when a fuse blown indication is necessary, configure an external circuit.
(13)Specifications comparison between A0J2E-E28DS and QX80+QY22*1

| Specifications |  | O : Compatible, $\triangle$ : Partially changed, $\times$ : Incompatible |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | A0J2E-E28DS input specifications | QX80 | Compatibility | Precautions for replacement |
| Number of input points |  | 16 points | 16 points | $\bigcirc$ |  |
| Insulation method |  | Photocoupler | Photocoupler | $\bigcirc$ |  |
| Rated input voltage |  | 12VDC/24VDC | 24VDC | $\triangle$ | 12VDC cannot be used. ${ }^{*}$ |
| Rated input current |  | Approx. 3mA/Approx. 7 mA | Approx. 4mA | $\triangle$ | Rated input current is smaller. ${ }^{* 3}$ |
| Operating voltage range |  | $\begin{gathered} 10.2 \text { to } 26.4 \mathrm{VDC} \\ \text { (Ripple ratio within } 5 \% \text { ) } \end{gathered}$ | $\begin{gathered} 20.4 \text { to } 28.8 \mathrm{VDC} \\ \text { (Ripple ratio within 5\%) } \end{gathered}$ | $\triangle$ | 12 VDC cannot be used. ${ }^{*}$ |
| Maximum number of simultaneous input points |  | 100\% (8 points/common) simultaneously ON | 100\% simultaneously ON | $\bigcirc$ |  |
| ON voltage/ON current |  | 9.5VDC or more/2.6mA or more | 19VDC or more/3mA or more | $\triangle$ | 12 VDC cannot be used. ${ }^{*}{ }^{2}$ |
| OFF voltage/OFF current |  | 6 VDC or less $/ 1.0 \mathrm{~mA}$ or less | 11VDC or less/1.7mA or less | $\triangle$ | 12 VDC cannot be used. ${ }^{*}$ |
| Input resistance |  | Approx. 3.4k $\Omega$ | Approx. $5.6 \mathrm{k} \Omega$ | $\triangle$ | Input resistance is greater. ${ }^{* 3}$ |
| Input format |  | Source loading input (Input current flows in) | Negative common | $\bigcirc$ | The name is different, but the specificaton is equal. |
| Response time | $\mathrm{OFF} \rightarrow \mathrm{ON}$ | 5.5 ms or less (TYP.) | $1 \mathrm{~ms} / 5 \mathrm{~ms} / 10 \mathrm{~ms} / 20 \mathrm{~ms} / 70 \mathrm{~ms}$ or less <br> (Set it with CPU parameter.) Initial setting is 10 ms . | $\bigcirc$ | Input response time of the parameter must be used at the initial value ( 10 ms ). |
|  | ON $\rightarrow$ OFF | 6.0 ms or less (TYP.) | $1 \mathrm{~ms} / 5 \mathrm{~ms} / 10 \mathrm{~ms} / 20 \mathrm{~ms} / 70 \mathrm{~ms}$ or less <br> (Set it with CPU parameter.) Initial setting is 10 ms . | $\bigcirc$ |  |
| Response time high- <br> speed <br> mode (Only <br> upper 8 <br> points) | $\mathrm{OFF} \rightarrow \mathrm{ON}$ | 0.5 ms or less | $1 \mathrm{~ms} / 5 \mathrm{~ms} / 10 \mathrm{~ms} / 20 \mathrm{~ms} / 70 \mathrm{~ms}$ or less <br> (Set it with CPU parameter.) Initial setting is 10 ms . | $\triangle$ | Set the input response time of the parameter to 1 ms before use. |
|  | ON $\rightarrow$ OFF | 1.0ms or less | $1 \mathrm{~ms} / 5 \mathrm{~ms} / 10 \mathrm{~ms} / 20 \mathrm{~ms} / 70 \mathrm{~ms}$ or less <br> (Set it with CPU parameter.) Initial setting is 10 ms . | $\bigcirc$ |  |
| Common terminal arrangement |  | 16 points/common (Common terminal: TB17) | 16 points/common (Common terminal: TB18) | $\bigcirc$ |  |
| Operation indication |  | Available (Turning ON the input turns LED ON) | ON indication (LED) | $\bigcirc$ |  |
| Specifications |  | A0J2E-E28DS output specifications | QY22 | Compatibility | Precautions for replacement |
| Number of output points |  | 12 points | 16 points | $\bigcirc$ |  |
| Insulation method |  | Photocoupler | Photocoupler | $\bigcirc$ |  |
| Rated load voltage |  | 100 to $240 \mathrm{VAC}, 40$ to 70 Hz | $\begin{gathered} 100 \text { to } 240 \mathrm{VAC} \\ 50 / 60 \mathrm{~Hz} \pm 5 \% \end{gathered}$ | $\Delta$ | The frequency will be low. Check the specifications of external devices. |
| Maximum load voltage |  | 264VAC | 264VAC | 0 |  |
| Maximum load current |  | 0.6A/point, 0.5A/point <br> ( $60 \% \mathrm{ON}, 55^{\circ} \mathrm{C}$ ) | 0.6A/point, 4.8A/common | $\bigcirc$ |  |
| Minimum load voltage/ current |  | 24 VAC 100 mA 100VAC 10 mA 240VAC 10 mA | 24 VAC 100 mA 100 VAC 25 mA 240VAC 25 mA | $\Delta$ | The minimum load current is greater. Use caution on selecting the load to use. |
| Maximum inrush current |  | 20A 10ms or less 8 A 100 ms or less | 20A 1 cycle or less | $\bigcirc$ |  |
| Leakage current at OFF |  | $\begin{gathered} 1.5 \mathrm{~mA}(120 \mathrm{VAC} 60 \mathrm{~Hz}) \\ 3 \mathrm{~mA}(240 \mathrm{VAC} 60 \mathrm{~Hz}) \end{gathered}$ | $\begin{gathered} \hline 1.5 \mathrm{~mA} \text { or less (During } 120 \mathrm{~V} \\ 60 \mathrm{~Hz} \text { ) } \\ 3 \mathrm{~mA} \text { or less (During } 240 \mathrm{~V} 60 \mathrm{~Hz} \text { ) } \\ \hline \end{gathered}$ | $\bigcirc$ |  |
| Maximum voltage drop at ON |  | 1.5 V or less $(100$ to 600 mA$)$ 1.8 V or less $(100 \mathrm{~mA}$ or less $)$ 2.0 V or less $(10$ to 50 mA$)$ | 1.5 V or less | $\bigcirc$ |  |
| Response time | OFF $\rightarrow$ ON | 1 ms or less | 1 ms or less | 0 |  |
|  | ON $\rightarrow$ OFF | 0.5 cycle +1 ms or less | $1 \mathrm{~ms}+0.5$ cycle or less (Rated load, resistance load) | $\bigcirc$ |  |

O : Compatible, $\Delta$ : Partially changed, $\times$ : Incompatible

| Specifications | A0J2E-E28DS output <br> specifications | QY22 | Compatibility | Precautions for replacement |
| :--- | :---: | :---: | :---: | :---: | :---: |
| Fuse rating | High speed type fuse 3.2A <br> (1/common) HP-32 | None | $\times$ |  |
| Fuse blown indication | Available (When a fuse blown <br> occurs, LED is lit, and <br> a signal is output to the CPU.) | - | $\times$ | The fuse is not built in. ${ }^{*} 4$ |

*1 Consider the characteristics of the triac and observe the necessary precautions by referring to Section 3.3 (3) before replacing the modules.
*2 Use the QX70 when using 12VDC.
*3 Check the specifications of the sensor or switches to be connected to the QX80.
*4 Install a fuse for each external terminal point to prevent the burnout of the external devices and modules during load shorts. In addition, when a fuse blown indication is necessary, configure an external circuit.
(14)Specifications comparison between A0J2E-E28DR and QX80+QY10

|  |  |  |  | Compatible, | Partially changed, $\times$ : Incompati |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Specifications |  | A0J2E-E28DR input specifications | QX80 | Compatibility | Precautions for replacement |
| Number of input points |  | 16 points | 16 points | $\bigcirc$ |  |
| Insulation method |  | Photocoupler | Photocoupler | $\bigcirc$ |  |
| Rated input voltage |  | 12VDC/24VDC | 24VDC | $\triangle$ | 12VDC cannot be used. ${ }^{* 1}$ |
| Rated input current |  | $3 \mathrm{~mA} / 7 \mathrm{~mA}$ | Approx. 4mA | $\triangle$ | Rated input current is smaller.*2 |
| Operating voltage range |  | 10.2 to 26.4 VDC (Ripple ratio within 5\%) | 20.4 to 28.8 VDC (Ripple ratio within $5 \%$ ) | $\triangle$ | 12VDC cannot be used. ${ }^{* 1}$ |
| Maximum number of simultaneous input points |  | 100\% simultaneously ON | 100\% simultaneously ON | $\bigcirc$ |  |
| ON voltage/ON current |  | 9.5VDC or more/2.6mA or more | 19VDC or more/3mA or more | $\triangle$ | 12 VDC cannot be used. ${ }^{* 1}$ |
| OFF voltage/OFF current |  | 6VDC or less/1.0mA or less | 11VDC or less/1.7mA or less | $\triangle$ | 12 VDC cannot be used. ${ }^{* 1}$ |
| Input resistance |  | Approx. 3.4k $\Omega$ | Approx. $5.6 \mathrm{k} \Omega$ | $\triangle$ | Input resistance is greater. ${ }^{*}$ |
| Input format |  | Source input (Input current flows in.) | Negative common | $\bigcirc$ | The name is different, but the specificaton is equal. |
| Response time | $\mathrm{OFF} \rightarrow \mathrm{ON}$ | 5.5 ms or less (TYP.) | $1 \mathrm{~ms} / 5 \mathrm{~ms} / 10 \mathrm{~ms} / 20 \mathrm{~ms} / 70 \mathrm{~ms}$ or less <br> (Set it with CPU parameter.) Initial setting is 10 ms . | O | Set the input response time of the parameter to 5 ms before use. |
|  | ON $\rightarrow$ OFF | 6.0 ms or less (TYP.) | $1 \mathrm{~ms} / 5 \mathrm{~ms} / 10 \mathrm{~ms} / 20 \mathrm{~ms} / 70 \mathrm{~ms}$ or less <br> (Set it with CPU parameter.) Initial setting is 10 ms . | $\bigcirc$ |  |
| Response time high- <br> speed mode (Only upper 8 points) | $\mathrm{OFF} \rightarrow \mathrm{ON}$ | 0.5 ms or less | $1 \mathrm{~ms} / 5 \mathrm{~ms} / 10 \mathrm{~ms} / 20 \mathrm{~ms} / 70 \mathrm{~ms}$ or less <br> (Set it with CPU parameter.) Initial setting is 10 ms . | $\triangle$ | Set the input response time of the parameter to 1 ms before use. |
|  | $\mathrm{ON} \rightarrow$ OFF | 1.0 ms or less | $1 \mathrm{~ms} / 5 \mathrm{~ms} / 10 \mathrm{~ms} / 20 \mathrm{~ms} / 70 \mathrm{~ms}$ or less (Set it with CPU parameter.) Initial setting is 10 ms . | O |  |
| Common terminal arrangement |  | 16 points/common (Common terminal: TB17) | 16 points/common (Common terminal: TB18) | $\bigcirc$ |  |
| Operation indication |  | Available (Turning ON the input turns LED ON) | ON indication (LED) | $\bigcirc$ |  |

O : Compatible, $\Delta$ : Partially changed, $\times$ : Incompatible

| Specifications |  | A0J2E-E28DR output specifications | QY10 | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Number of output points |  | 12 points | 16 points | O |  |
| Insulation method |  | Photocoupler | Relay | $\triangle$ | Although the insulation methods differ, the performance of the Insulation is the same. |
| Rated switching voltage/ current |  | $\begin{aligned} & \hline \text { 24VDC 2A (Resistance load)/ } \\ & \text { point } \\ & \text { 240VAC } 2 \mathrm{~A}(\operatorname{COS} \phi=1) / \text { point } \\ & 5 \mathrm{~A} / \text { common } \end{aligned}$ | $\begin{aligned} & \text { 24VDC 2A (Resistance load)/ } \\ & \text { point } \\ & \text { 240VAC 2A }(\operatorname{COS} \phi=1) / \text { point } \\ & 8 \mathrm{~A} / \text { common } \end{aligned}$ | $\bigcirc$ |  |
| Minimum switching load |  | 5 VDC 1 mA | $5 \mathrm{VDC} \mathrm{1mA}$ | $\bigcirc$ |  |
| Maximum switching voltage |  | 250VAC, 125VDC | 264VAC, 125VDC | $\bigcirc$ |  |
| Maximum switching frequency |  | 3600 times/hr | 3600 times/hr | $\bigcirc$ |  |
| Life | Mechanical | 20 million times or more | 20 million times or more | O |  |
|  | Electrical | Rated switching voltage/current load 200,000 times or more | Rated switching voltage/current load 100,000 times or more | $\triangle$ |  |
|  |  | 200VAC 1.5A, 240VAC 1 A $(\operatorname{COS} \phi=0.7)$ 200,000 times or more 200VAC 0.75A, 240VAC 0.5A (COS $\phi=0.35$ ) 200,000 times or more <br> 24VDC 1A, 100VDC 0.1A (L/R=7ms) 200,000 times or more | 200VAC 1.5A, 240VAC 1A <br> $(\operatorname{COS} \phi=0.7)$ 100,000 times or more <br> 200VAC 0.4A, 240VAC 0.3A <br> $(\operatorname{COS} \phi=0.7) 300,000$ times or more <br> 200VAC 1A, 240VAC 0.5A <br> $(\operatorname{COS} \phi=0.35)$ 100,000 times or more 200VAC 0.3A, 240VAC 0.15A $(\operatorname{COS} \phi=0.35) 300,000$ times or more <br> 24VDC 1A, 100VDC 0.1A (L/R=7ms) 100,000 times or more 24VDC 0.3A, 100VDC 0.03A (L/R=7ms) 300,000 times or more | $\triangle$ | Reduce the exchange intervals of the modules as mechanical/ electrical life is cut to about half. |
| Response time | OFF $\rightarrow$ ON | 10 ms or less | 10 ms or less | $\bigcirc$ |  |
|  | ON $\rightarrow$ OFF | 12 ms or less | 12 ms or less | $\bigcirc$ |  |
| External supply power (relay coil driving power) | Voltage | $24 \mathrm{VDC} \pm 10 \%$ Ripple voltage 4Vp-p or less | - | $\bigcirc$ |  |
|  | Current | 110 mA $(24 \mathrm{VDC}$ All points are ON.) | - | $\bigcirc$ | External supply power is not required. |
| Surge suppressor |  | Varistor (387 to 473V) | - | $\times$ | The varistor is not built in. ${ }^{* 3}$ |
| Fuse rating |  | 8A MF51NM8 or FGMA250V8A | - | $\times$ | The fuse is not built in. ${ }^{*}{ }^{4}$ |
| Common terminal arrangement |  | 8 points/common (Common terminal: TB26) 4 points/common (Common terminal: TB32) | 16 points/common (Common terminal: TB17) | $\triangle$ | As the common changes from two commons to a common, wiring with a different voltage for each common is not possible. |
| Operation indication |  | Available (Turning ON the output turns LED ON) | ON indication (LED) | $\bigcirc$ |  |
| Specifications |  | A0J2E-E28DR | QX80/QY10 | Compatibility | Precautions for replacement |
| Current consumption |  | 0.13A (TYP. All points are ON.) | $0.05+0.43=0.48 \mathrm{~A}$ <br> (TYP. All points are ON.) | $\Delta$ | Review current capacity since current consumption is increased. |
| External connection method |  | 36-point terminal block connector (M3 $\times 6$ screws) | $\begin{gathered} \text { 18-point terminal block } \\ \text { (M3 } \times 6 \text { screws) } \\ 1 \text { screw/module } \\ \hline \end{gathered}$ | $\times$ |  |
| Applicable wire size |  | 0.75 to $2 \mathrm{~mm}^{2}$ | 0.3 to $0.75 \mathrm{~mm}^{2}$ core (Outside diameter: 2.8 mm or less) | $\times$ | Wiring change is required. |
| Applicable solderless terminal |  | $\begin{gathered} \text { 1.25-3, 1.25-YS3A, } \\ 2-\mathrm{S} 3,2-\mathrm{YS} 3 \mathrm{~A}, \\ \mathrm{~V} 1.25-3, \mathrm{~V} 1.25-\mathrm{YS} 3 \mathrm{~A}, \\ \mathrm{~V} 2-\mathrm{S} 3, \mathrm{~V} 2-\mathrm{YS} 3 \mathrm{~A} \end{gathered}$ | R1.25-3 <br> (Sleeved solderless terminals cannot be used.) | $\times$ |  |


| Specifications | A0J2E-E28DR | QX80/QY10 | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: |
| External dimensions | 250(H) $\times 132(\mathrm{~W}) \times 41(\mathrm{D}) \mathrm{mm}$ | 98(H) $\times 27.4(\mathrm{~W}) \times 90(\mathrm{D}) \mathrm{mm} \times 2$ | $\times$ | The dimensions are different. |
| Weight | 0.7 kg | $0.16+0.22=0.38 \mathrm{~kg}$ | $\triangle$ |  |
| *1 Use the QX70 when using 12VDC. |  |  |  |  |
| Check the specifications of the sensor or switches to be connected to the QX80. |  |  |  |  |
| Connect the varistor exteriorly to reduce noise. |  |  |  |  |
| Install a fuse for each external terminal point to prevent the burnout of the external devices and modules during load shorts. |  |  |  |  |

(15)Specifications comparison between A0J2E-E28DT and QX80+QY80

| Specifications |  | O: Compatible, $\Delta$ : Partially changed, $\times$ : Incompatible |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | A0J2E-E28DT input specifications | QX80 | Compatibility | Precautions for replacement |
| Number of input points |  | 16 points | 16 points | $\bigcirc$ |  |
| Insulation method |  | Photocoupler | Photocoupler | $\bigcirc$ |  |
| Rated input voltage |  | 12VDC/24VDC | 24VDC | $\triangle$ | 12VDC cannot be used. ${ }^{* 1}$ |
| Rated input current |  | $3 \mathrm{~mA} / 7 \mathrm{~mA}$ | Approx. 4mA | $\triangle$ | Rated input current is smaller.* ${ }^{2}$ |
| Operating voltage range |  | 10.2 to 26.4 VDC (Ripple ratio within 5\%) | 20.4 to 28.8 VDC (Ripple ratio within 5\%) | $\Delta$ | 12 VDC cannot be used. ${ }^{* 1}$ |
| Maximum number of simultaneous input points |  | 100\% simultaneously ON | 100\% simultaneously ON | O |  |
| ON voltage/ON current |  | 9.5VDC or more/2.6mA or more | 19VDC or more/3mA or more | $\triangle$ | 12 VDC cannot be used. ${ }^{*}$ |
| OFF voltage/OFF current |  | 6VDC or less/1.0mA or less | 11VDC or less/1.7mA or less | $\triangle$ | 12 VDC cannot be used. ${ }^{* 1}$ |
| Input resistance |  | Approx. $3.4 \mathrm{k} \Omega$ | Approx. $5.6 \mathrm{k} \Omega$ | $\triangle$ | Input resistance is greater. ${ }^{*}$ |
| Input format |  | Source input (Input current flows in.) | Negative common | $\bigcirc$ | The name is different, but the specificaton is equal. |
| Response time | $\mathrm{OFF} \rightarrow \mathrm{ON}$ | 5.5 ms or less (TYP.) | $1 \mathrm{~ms} / 5 \mathrm{~ms} / 10 \mathrm{~ms} / 20 \mathrm{~ms} / 70 \mathrm{~ms}$ or less <br> (Set it with CPU parameter.) Initial setting is 10 ms . | O | Set the input response time of the parameter to 5 ms before use. |
|  | ON $\rightarrow$ OFF | $6.0 \mathrm{~ms} \mathrm{or} \mathrm{less} \mathrm{(TYP)}$. | $1 \mathrm{~ms} / 5 \mathrm{~ms} / 10 \mathrm{~ms} / 20 \mathrm{~ms} / 70 \mathrm{~ms}$ or less <br> (Set it with CPU parameter.) Initial setting is 10 ms . | $\bigcirc$ |  |
| Response time highspeed mode (Only upper 8 points) | $\mathrm{OFF} \rightarrow \mathrm{ON}$ | 0.5 ms or less | $1 \mathrm{~ms} / 5 \mathrm{~ms} / 10 \mathrm{~ms} / 20 \mathrm{~ms} / 70 \mathrm{~ms}$ or less <br> (Set it with CPU parameter.) Initial setting is 10 ms . | $\triangle$ | Set the input response time of the parameter to 1 ms before use. |
|  | $\mathrm{ON} \rightarrow$ OFF | 1.0 ms or less | $1 \mathrm{~ms} / 5 \mathrm{~ms} / 10 \mathrm{~ms} / 20 \mathrm{~ms} / 70 \mathrm{~ms}$ or less (Set it with CPU parameter.) Initial setting is 10 ms . | O |  |
| Common terminal arrangement |  | 16 points/common (Common terminal: TB17) | 16 points/common (Common terminal: TB18) | $\bigcirc$ |  |
| Operation indication |  | Available (Turning ON the input turns LED ON) | ON indication (LED) | $\bigcirc$ |  |
| Specifications |  | A0J2E-E28DT output specifications | QY80 | Compatibility | Precautions for replacement |
| Number of output points |  | 12 points | 16 points | $\bigcirc$ |  |
| Insulation method |  | Photocoupler | Photocoupler | $\bigcirc$ |  |
| Rated load voltage |  | 12/24VDC | 12 to 24VDC | $\bigcirc$ |  |
| Operating load voltage range |  | 10.2 to 26.4VDC | 10.2 to 28.8VDC | $\bigcirc$ |  |
| Maximum load current |  | 0.8A/1 point, 0.7A/1 point ( $60 \% \mathrm{ON}, 55^{\circ} \mathrm{C}$ ) | 0.5A/1 point, $4 \mathrm{~A} / 1$ common | $\Delta$ | The maximum load current per point is smaller. Use caution on selecting the load to use. |
| Maximum inrush current |  | No restriction (Short protect) | 4A 10ms or less | $\Delta$ | The inrush current value differs. Use caution on selecting the load to use. |
| Leakage current at OFF |  | 1.0 mA or less | 0.1 mA or less | $\bigcirc$ |  |
| Maximum voltage drop at ON |  | $\begin{aligned} & \text { 1.0V (TYP.) 0.8A, } \\ & 1.5 \mathrm{~V} \text { (MAX.) } 0.8 \mathrm{~A} \end{aligned}$ | $\begin{aligned} & 0.2 \mathrm{VDC} \text { (TYP.) 0.5A, } \\ & \text { 0.3VDC (MAX.) } 0.5 \mathrm{~A} \end{aligned}$ | $\bigcirc$ |  |
| Response time | OFF $\rightarrow$ ON | 0.5 ms or less | 1 ms or less | $\triangle$ | The response times differ. |
|  | ON $\rightarrow$ OFF | 1.5 ms or less | 1 ms or less (Rated load, resistance load) | $\bigcirc$ |  |
| External supply power | Voltage | $\begin{gathered} \hline 12 / 24 \mathrm{VDC} \\ (10.2 \text { to } 30 \mathrm{VDC}) \end{gathered}$ | 12 to 24VDC (+20/-15\%) <br> (Ripple ratio within 5\%) | $\Delta$ | Voltages exceeding 28.8VDC cannot be applied. |
|  | Current | 100 mA (24VDC, All points are ON.) | 20 mA (During 24VDC) | $\bigcirc$ |  |
| Surge suppressor |  | Surge absorbing diode | Zener diode | $\bigcirc$ |  |

O : Compatible, $\Delta$ : Partially changed, x : Incompatible

| Specifications | A0J2E-E28DT output specifications | QY80 | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: |
| Fuse rating | - | 6.7A (Not exchangeable) (Fuse blown capacity: 50A) | $\triangle$ | The QY80 has the fuse instead of overheat and short circuit protection function. |
| Fuse blown indication | - | Available (When a fuse blown occurs, LED is lit, and a signal is output to the CPU) ${ }^{* 3}$ |  |  |
| Protection function | Available (overheat protection and short circuit protection) Overheat protection: activated to two points | - |  |  |
| Protection function reset | Automatic reset (by deactivating overheat protection function) | - |  |  |
| Common terminal arrangement | 8 points/common (Common terminal: TB26) 4 points/common (Common terminal: TB32) | 16 points/common (Common terminal: TB17) | $\triangle$ | As the common changes from two commons to a common, wiring with a different voltage for each common is not possible. |
| Operation indication | Available (Turning ON the output turns LED ON) | ON indication (LED) | $\bigcirc$ |  |
| Specifications | A0J2E-E56DT | QX80/QY80 | Compatibility | Precautions for replacement |
| Current consumption | 0.125A (TYP. All points are ON.) | $0.05+0.08=0.13 \mathrm{~A}$ <br> (TYP. All points are ON.) | $\triangle$ | Review current capacity since current consumption is increased. |
| External connection method | 36-point terminal block connector (M3 $\times 6$ screws) | 18-point terminal block (M3 $\times 6$ screws) 1 piece/module | × | Wiring change is required. |
| Applicable wire size | 0.75 to $2 \mathrm{~mm}^{2}$ | 0.3 to $0.75 \mathrm{~mm}^{2}$ core (Outside diameter: 2.8 mm or less) | $\times$ |  |
| Applicable solderless terminal | $\begin{gathered} \text { 1.25-3, 1.25-YS3A, } \\ 2-\mathrm{S} 3,2-\mathrm{YS} 3 \mathrm{~A}, \\ \mathrm{~V} 1.25-3, \mathrm{~V} 1.25-\mathrm{YS} 3 \mathrm{~A}, \\ \mathrm{~V} 2-\mathrm{S} 3, \mathrm{~V} 2-\mathrm{YS} 3 \mathrm{~A} \end{gathered}$ | R1.25-3 <br> (Sleeved solderless terminals cannot be used.) | x |  |
| External dimensions | 250(H) $\times 132(\mathrm{~W}) \times 41(\mathrm{D}) \mathrm{mm}$ | 98(H) $\times 27.4(\mathrm{~W}) \times 90(\mathrm{D}) \mathrm{mm} \times 2$ | $\times$ | The dimensions are different. |
| Weight | 0.68 kg | $0.16+0.17=0.33 \mathrm{~kg}$ | $\triangle$ |  |

*1 Use the QX70 when using 12VDC.
*2 Check the specifications of the sensor or switches to be connected to the QX80.
*3 The fuse blown is not detected when the external power supply is off.
(16)Specifications comparison between A0J2E-E56DR and QX80+QY10

| Specifications |  | O: Compatible, $\triangle$ : Partially changed, $\times$ : Incompatible |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | A0J2E-E56DR input specifications | QX80 | Compatibility | Precautions for replacement |
| Number of input points |  | 32 points | 16 points | $\Delta$ | When seventeen or more points are used, use two QX80s. |
| Insulation method |  | Photocoupler | Photocoupler | $\bigcirc$ |  |
| Rated input voltage |  | 12VDC/24VDC | 24VDC | $\triangle$ | 12VDC cannot be used. ${ }^{* 1}$ |
| Rated input current |  | $3 \mathrm{~mA} / 7 \mathrm{~mA}$ | Approx. 4mA | $\triangle$ | Rated input current is smaller. ${ }^{*}{ }^{2}$ |
| Operating voltage range |  | 10.2 to 26.4 VDC (Ripple ratio within 5\%) | 20.4 to 28.8 VDC (Ripple ratio within 5\%) | $\triangle$ | 12VDC cannot be used. ${ }^{* 1}$ |
| Maximum number of simultaneous input points |  | 60\% (10 points/common) simultaneously ON | 100\% simultaneously ON | $\bigcirc$ |  |
| ON voltage/ON current |  | 9.5VDC or more/2.6mA or more | 19VDC or more/3mA or more | $\triangle$ | 12VDC cannot be used. ${ }^{* 1}$ |
| OFF voltage/OFF current |  | 6 VDC or less/1.0mA or less | 11VDC or less/1.7mA or less | $\triangle$ | 12VDC cannot be used. ${ }^{* 1}$ |
| Input resistance |  | Approx. 3.4k $\Omega$ | Approx. $5.6 \mathrm{k} \Omega$ | $\triangle$ | Input resistance is greater. ${ }^{*}$ |
| Input format |  | Source input (Input current flows in.) | Negative common | $\bigcirc$ | The name is different, but the specificaton is equal. |
| Response time | $\mathrm{OFF} \rightarrow \mathrm{ON}$ | 5.5 ms or less (TYP.) | $1 \mathrm{~ms} / 5 \mathrm{~ms} / 10 \mathrm{~ms} / 20 \mathrm{~ms} / 70 \mathrm{~ms}$ or less <br> (Set it with CPU parameter.) Initial setting is 10 ms . | O | Set the input response time of the parameter to 5 ms before use. |
|  | ON $\rightarrow$ OFF | 6.0 ms or less (TYP.) | $1 \mathrm{~ms} / 5 \mathrm{~ms} / 10 \mathrm{~ms} / 20 \mathrm{~ms} / 70 \mathrm{~ms}$ or less <br> (Set it with CPU parameter.) Initial setting is 10 ms . | $\bigcirc$ |  |
| Response time high- <br> speed mode (Only upper 8 points) | OFF $\rightarrow$ ON | 0.5 ms or less | $1 \mathrm{~ms} / 5 \mathrm{~ms} / 10 \mathrm{~ms} / 20 \mathrm{~ms} / 70 \mathrm{~ms}$ or less <br> (Set it with CPU parameter.) Initial setting is 10 ms . | $\Delta$ | Set the input response time of the parameter to 1 ms before use. |
|  | ON $\rightarrow$ OFF | 1.0 ms or less | $1 \mathrm{~ms} / 5 \mathrm{~ms} / 10 \mathrm{~ms} / 20 \mathrm{~ms} / 70 \mathrm{~ms}$ or less (Set it with CPU parameter.) Initial setting is 10 ms . | $\bigcirc$ |  |
| Common terminal arrangement |  | 16 points/common (Common terminal: TB17, TB34) | 16 points/common (Common terminal: TB18) | $\bigcirc$ |  |
| Operation indication |  | Available (Turning ON the input turns LED ON) | ON indication (LED) | $\bigcirc$ |  |


| Specifications |  | A0J2E-E56DR output specifications | QY10 | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Number of output points |  | 24 points | 16 points | $\Delta$ | When seventeen or more points are used, use two QY10s. |
| Insulation method |  | Photocoupler | Relay | $\Delta$ | Although the insulation methods differ, the performance of the Insulation is the same. |
| Rated switching voltage/ current |  | ```24VDC 2A (Resistance load)/ point 240VAC 2A (COS }= =1)/poin 5A/common``` | ```24VDC 2A (Resistance load)/ point 240VAC 2A (COS }\phi=1)/poin 8A/common``` | $\bigcirc$ |  |
| Minimum switching load |  | 5 VDC 1 mA | 5 VDC 1 mA | $\bigcirc$ |  |
| Maximum switching voltage |  | 250VAC, 125VDC | 264VAC, 125VDC | $\bigcirc$ |  |
| Maximum switching frequency |  | 3600 times/hr | 3600 times/hr | $\bigcirc$ |  |
| Life | Mechanical | 20 million times or more | 20 million times or more | $\bigcirc$ |  |
|  | Electrical | Rated switching voltage/current load 200,000 times or more | Rated switching voltage/current load 100,000 times or more | $\triangle$ |  |
|  |  | 200VAC 1.5A, 240VAC 1A <br> $(\operatorname{COS} \phi=0.7)$ 200,000 times or more <br> 200VAC 0.75A, 240VAC 0.5A (COS $\phi=0.35$ ) 200,000 times or more <br> 24VDC 1A, 100VDC 0.1A (L/R=7ms) 200,000 times or more | 200VAC 1.5A, 240VAC 1A (COS $\phi=0.7$ ) 100,000 times or more 200VAC 0.4A, 240VAC 0.3A (COS $\phi=0.7$ ) 300,000 times or more <br> 200VAC 1A, 240VAC 0.5A (COS $\phi=0.35$ ) 100,000 times or more <br> 200VAC 0.3A, 240VAC 0.15A (COS $\phi=0.35$ ) 300,000 times or more <br> 24VDC 1A, 100VDC 0.1A (L/R=7ms) 100,000 times or more 24VDC 0.3A, 100VDC 0.03A (L/R=7ms) 300,000 times or more | $\triangle$ | Reduce the exchange intervals of the modules as mechanical/ electrical life is cut to about half. |
| Response time | OFF $\rightarrow$ ON | 10 ms or less | 10 ms or less | $\bigcirc$ |  |
|  | ON $\rightarrow$ OFF | 12 ms or less | 12 ms or less | $\bigcirc$ |  |
| External supply power (relay coil driving power) | Voltage | $24 \mathrm{VDC} \pm 10 \%$ Ripple voltage $4 \mathrm{Vp}-\mathrm{p}$ or less | - | $\bigcirc$ |  |
|  | Current | 220 mA <br> (24VDC All points are ON.) | - | $\bigcirc$ | External supply power is not required. |
| Surge suppressor |  | Varistor (387 to 473V) | - | $\times$ | The varistor is not built in. ${ }^{* 3}$ |
| Fuse rating |  | 8A MF51NM8 or FGMA250V8A | - | $\times$ | The fuse is not built in. ${ }^{*} 4$ |
| Common terminal arrangement |  | 8 points/common (Common terminal: TB9, TB19, TB29) | 16 points/common (Common terminal: TB17) | $\triangle$ | As the common changes from three commons to a common, wiring with a different voltage for each common is not possible. |
| Operation indication |  | Available (Turning ON the output turns LED ON) | ON indication (LED) | $\bigcirc$ |  |
| Specifications |  | A0J2E-E56DR | QX80/QY10 | Compatibility | Precautions for replacement |
| Current consumption |  | 0.23A (TYP. All points are ON.) | $0.05 \times 2+0.43 \times 2=0.96 \mathrm{~A}$ <br> (TYP. All points are ON.) | $\Delta$ | Review current capacity since current consumption is increased. |
| External connection method |  | 36-point terminal block connector <br> (M3 $\times 6$ screws) 2 pieces | 18-point terminal block (M3 $\times 6$ screws) 1 screw/module | $\times$ |  |
| Applicable wire size |  | 0.75 to $2 \mathrm{~mm}^{2}$ | 0.3 to $0.75 \mathrm{~mm}^{2}$ core (Outside diameter: 2.8 mm or less) | $\times$ | Wiring change is required. |
| Applicable solderless terminal |  | $\begin{gathered} \text { 1.25-3, 1.25-YS3A, } \\ 2-\mathrm{S} 3,2-\mathrm{YS} 3 \mathrm{~A}, \\ \mathrm{~V} 1.25-3, \mathrm{~V} 1.25-\mathrm{YS} 3 \mathrm{~A}, \\ \mathrm{~V} 2-\mathrm{S} 3, \mathrm{~V} 2-\mathrm{YS} 3 \mathrm{~A} \end{gathered}$ | R1.25-3 <br> (Sleeved solderless terminals cannot be used.) | $\times$ |  |


| Specifications | A0J2E-E56DR | QX80/QY10 | O : Compatible, $\triangle:$ Partially changed, $\times$ : Incompatible |  |
| :--- | :---: | :---: | :---: | :---: | :---: |
| External dimensions | $250(\mathrm{H}) \times 190(\mathrm{~W}) \times 41(\mathrm{D}) \mathrm{mm}$ | $98(\mathrm{H}) \times 27.4(\mathrm{~W}) \times 90(\mathrm{D}) \mathrm{mm} \times 4$ | $\times$ | The dimensions are different. |
| Weight | 1.13 kg | $0.16 \times 2+0.22 \times 2=0.76 \mathrm{~kg}$ | $\Delta$ |  |

*1 Use the QX70 when using 12VDC.
*2 Check the specifications of the sensor or switches to be connected to the QX80.
*3 Connect the varistor exteriorly to reduce noise.
*4 Install a fuse for each external terminal point to prevent the burnout of the external devices and modules during load shorts.

## (17)Specifications comparison between A0J2E-E56DS and QX80+QY22*1

| Specifications |  | A0J2E-E56DS input specifications | QX80 | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Number of input points |  | 32 points | 16 points | $\Delta$ | When seventeen or more points are used, use two QX80s. |
| Insulation method |  | Photocoupler | Photocoupler | $\bigcirc$ |  |
| Rated input voltage |  | 12VDC/24VDC | 24VDC | $\triangle$ | 12VDC cannot be used. ${ }^{*}$ |
| Rated input current |  | $3 \mathrm{~mA} / 7 \mathrm{~mA}$ | Approx. 4mA | $\triangle$ | Rated input current is smaller. ${ }^{* 3}$ |
| Operating voltage range |  | 10.2 to 26.4 VDC (Ripple ratio within $5 \%$ ) | 20.4 to 28.8 VDC (Ripple ratio within 5\%) | $\Delta$ | 12VDC cannot be used. ${ }^{*}{ }^{2}$ |
| Maximum number of simultaneous input points |  | 60\% (10 points/common) simultaneously ON | 100\% simultaneously ON | O |  |
| ON voltage/ON current |  | 9.5VDC or more/2.6mA or more | 19VDC or more/3mA or more | $\triangle$ | 12VDC cannot be used. ${ }^{*}$ |
| OFF voltage/OFF current |  | 6VDC or less/1.0mA or less | 11VDC or less/1.7mA or less | $\Delta$ | 12VDC cannot be used. ${ }^{*}{ }^{2}$ |
| Input resistance |  | Approx. 3.4k $\Omega$ | Approx. $5.6 \mathrm{k} \Omega$ | $\triangle$ | Input resistance is greater. ${ }^{* 3}$ |
| Input format |  | Source input (Input current flows in.) | Negative common | $\bigcirc$ | The name is different, but the specificaton is equal. |
| Response time | $\mathrm{OFF} \rightarrow \mathrm{ON}$ | 5.5 ms or less (TYP.) | $1 \mathrm{~ms} / 5 \mathrm{~ms} / 10 \mathrm{~ms} / 20 \mathrm{~ms} / 70 \mathrm{~ms}$ or less <br> (Set it with CPU parameter.) Initial setting is 10 ms . | O | Set the input response time of the parameter to 5 ms before use. |
|  | $\mathrm{ON} \rightarrow \mathrm{OFF}$ | $6.0 \mathrm{~ms} \mathrm{or} \mathrm{less} \mathrm{(TYP)}$. | $1 \mathrm{~ms} / 5 \mathrm{~ms} / 10 \mathrm{~ms} / 20 \mathrm{~ms} / 70 \mathrm{~ms}$ or less <br> (Set it with CPU parameter.) Initial setting is 10 ms . | O |  |
| Response time highspeed mode (Only upper 8 points) | $\mathrm{OFF} \rightarrow \mathrm{ON}$ | 0.5 ms or less | $1 \mathrm{~ms} / 5 \mathrm{~ms} / 10 \mathrm{~ms} / 20 \mathrm{~ms} / 70 \mathrm{~ms}$ or less <br> (Set it with CPU parameter.) Initial setting is 10 ms . | $\Delta$ | Set the input response time of the parameter to 1 ms before use. |
|  | $\mathrm{ON} \rightarrow \mathrm{OFF}$ | 1.0 ms or less | $1 \mathrm{~ms} / 5 \mathrm{~ms} / 10 \mathrm{~ms} / 20 \mathrm{~ms} / 70 \mathrm{~ms}$ or less <br> (Set it with CPU parameter.) Initial setting is 10 ms . | O |  |
| Common terminal arrangement |  | 16 points/common (Common terminal: TB17, TB34) | 16 points/common (Common terminal: TB18) | $\bigcirc$ |  |
| Operation indication |  | Available (Turning ON the input turns LED ON) | ON indication (LED) | $\bigcirc$ |  |
| Specifications |  | A0J2E-E56DS output specifications | QY22 | Compatibility | Precautions for replacement |
| Number of output points |  | 12 points | 16 points | $\triangle$ | When seventeen or more points are used, use two QY22s. |
| Insulation method |  | Photocoupler | Photocoupler | $\bigcirc$ |  |
| Rated load voltage |  | 100 to 240VAC, 40 to 70 Hz | $\begin{gathered} 100 \text { to } 240 \mathrm{VAC} \\ 50 / 60 \mathrm{~Hz} \pm 5 \% \end{gathered}$ | $\triangle$ |  |
| Maximum load voltage |  | 264VAC | 264VAC | $\bigcirc$ |  |
| Maximum load current |  | $\begin{gathered} \hline 0.6 \mathrm{~A} / \text { point } \\ 0.6 \mathrm{~A} / \text { point }\left(60 \% \mathrm{ON}, 55^{\circ} \mathrm{C}\right) \end{gathered}$ | 0.6A/point, 4.8A/common | $\bigcirc$ |  |
| Minimum load voltage, current |  | 24VAC 100mA 100VAC 10mA 240VAC 10 mA | 24VAC 100mA 100VAC 25 mA 240VAC 25 mA | $\triangle$ | The minimum load current is greater. Use caution on selecting the load to use. |
| Maximum inrush current |  | 20A 10ms or less 8 A 100ms or less | 20A 1 cycle or less | $\bigcirc$ |  |
| Leakage current at OFF |  | $\begin{gathered} 1.5 \mathrm{~mA}(120 \mathrm{VAC} 60 \mathrm{~Hz}) \\ 3 \mathrm{~mA}(240 \mathrm{VAC} 60 \mathrm{~Hz}) \end{gathered}$ | 1.5 mA or less (During 120V 60Hz) 3 mA or less (During 240V 60Hz) | O |  |
| Maximum voltage drop at ON |  | 1.5 V or less $(100$ to 600 mA$)$ 1.8 V or less $(100 \mathrm{~mA}$ or less $)$ 2.0 V or less $(10$ to 50 mA$)$ | 1.5 V or less | $\bigcirc$ |  |

O : Compatible, $\triangle$ : Partially changed, x : Incompatible

| Specifications |  | A0J2E-E56DS output specifications | QY22 | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Response time | OFF $\rightarrow$ ON | 1 ms or less | 1 ms or less | $\bigcirc$ |  |
|  | ON $\rightarrow$ OFF | 0.5 cycle +1 ms or less | $1 \mathrm{~ms}+0.5$ cycle or less (Rated load, resistance load) | $\bigcirc$ |  |
| Surge suppressor |  | $\begin{gathered} \text { CR absorber } \\ (0.022 \mu \mathrm{~F}+47 \Omega) \end{gathered}$ | CR absorber | $\bigcirc$ |  |
| Fuse rating |  | High speed type fuse 3.2A (1/common) HP-32 | - | $\times$ |  |
| Fuse blown indication |  | Available (When a fuse blown occurs, LED is lit, and a signal is output to the CPU) | - | $\times$ | The fuse is not built in. ${ }^{4}$ |
| Common terminal arrangement |  | 8 points/common (Common terminal: TB9, TB19, TB29) | 16 points/common (Common terminal: TB17) | $\Delta$ | As the common changes from three commons to a common, wiring with a different voltage for each common is not possible. |
| Operation indication |  | Available (Turning ON the output turns LED ON) | ON indication (LED) | $\bigcirc$ |  |
| Specifications |  | A0J2E-E56DS | QX80/QY22 | Compatibility | Precautions for replacement |
| Current consumption |  | 0.46A (TYP. All points are ON.) | $0.05 \times 2+0.25 \times 2=0.60 \mathrm{~A}$ <br> (TYP. All points are ON.) | $\triangle$ | Review current capacity since current consumption is increased. |
| External connection method |  | 36-point terminal block connector <br> (M3 $\times 6$ screws) 2 pieces | 18-point terminal block <br> (M3 $\times 6$ screws) <br> 1 piece/module | $\times$ |  |
| Applicable wire size |  | 0.75 to $2 \mathrm{~mm}^{2}$ | 0.3 to $0.75 \mathrm{~mm}^{2}$ core (Outside diameter: 2.8 mm or less) | $\times$ | Wiring change is required. |
| Applicable solderless terminal |  | $\begin{gathered} \text { 1.25-3, 1.25-YS3A, } \\ 2-\mathrm{S} 3,2-\mathrm{YS} 3 \mathrm{~A}, \\ \mathrm{~V} 1.25-3, \mathrm{~V} 1.25-\mathrm{YS} 3 \mathrm{~A}, \\ \mathrm{~V} 2-\mathrm{S} 3, \mathrm{~V} 2-\mathrm{YS} 3 \mathrm{~A} \end{gathered}$ | R1.25-3 <br> (Sleeved solderless terminals cannot be used.) | x |  |
| External dimensions |  | 250(H) $\times 190(\mathrm{~W}) \times 41(\mathrm{D}) \mathrm{mm}$ | 98(H) $\times 27.4(\mathrm{~W}) \times 90(\mathrm{D}) \mathrm{mm} \times 4$ | $\times$ | The dimensions are different. |
| Weight |  | 1.08 kg | $0.16 \times 2+0.40 \times 2=1.12 \mathrm{~kg}$ | $\Delta$ | Be aware of that the weight increases when calculating the weight. |

*1 Consider the characteristics of the triac and observe the necessary precautions by referring to Section 3.3 (3) before replacing the modules.
*2 Use the QX70 when using 12VDC.
*3 Check the specifications of the sensor or switches to be connected to the QX80.
*4 Install a fuse for each external terminal point to prevent the burnout of the external devices and modules during load shorts.

## (18)Specifications comparison between A0J2E-E56DT and QX80+QY80

| Specifications |  | O : Compatible, $\triangle$ : Partially changed, $\times$ : Incompatible |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | A0J2E-E56DT input specifications | QX80 | Compatibility | Precautions for replacement |
| Number of input points |  | 32 points | 16 points | $\Delta$ | When seventeen or more points are used, use two QX80s. |
| Insulation method |  | Photocoupler | Photocoupler | $\bigcirc$ |  |
| Rated input voltage |  | 12VDC/24VDC | 24VDC | $\triangle$ | 12 VDC cannot be used. ${ }^{* 1}$ |
| Rated input current |  | $3 \mathrm{~mA} / 7 \mathrm{~mA}$ | Approx. 4mA | $\triangle$ | Rated input current is smaller. ${ }^{*}$ |
| Operating voltage range |  | 10.2 to 26.4 VDC (Ripple ratio within 5\%) | 20.4 to 28.8 VDC (Ripple ratio within 5\%) | $\Delta$ | 12 VDC cannot be used. ${ }^{* 1}$ |
| Maximum number of simultaneous input points |  | 60\% (10 points/common) simultaneously ON | 100\% simultaneously ON | O |  |
| ON voltage/ON current |  | 9.5VDC or more/2.6mA or more | 19VDC or more/3mA or more | $\triangle$ | 12 VDC cannot be used. ${ }^{*}$ |
| OFF voltage/OFF current |  | 6VDC or less/1.0mA or less | 11VDC or less/1.7mA or less | $\triangle$ | 12 VDC cannot be used. ${ }^{*}$ |
| Input resistance |  | Approx. 3.4k $\Omega$ | Approx. $5.6 \mathrm{k} \Omega$ | $\triangle$ | Input resistance is greater. ${ }^{*}$ |
| Input format |  | Source input (Input current flows in.) | Negative common | O | The name is different, but the specificaton is equal. |
| Response time | OFF $\rightarrow$ ON | 5.5 ms or less (TYP.) | $1 \mathrm{~ms} / 5 \mathrm{~ms} / 10 \mathrm{~ms} / 20 \mathrm{~ms} / 70 \mathrm{~ms}$ or less <br> (Set it with CPU parameter.) Initial setting is 10 ms . | O | Set the input response time of the parameter to 5 ms before use. |
|  | ON $\rightarrow$ OFF | 6.0 ms or less (TYP.) | $1 \mathrm{~ms} / 5 \mathrm{~ms} / 10 \mathrm{~ms} / 20 \mathrm{~ms} / 70 \mathrm{~ms}$ or less <br> (Set it with CPU parameter.) Initial setting is 10 ms . | O |  |
| Response time high- <br> speed mode (Only upper 8 points) | OFF $\rightarrow$ ON | 0.5 ms or less | $1 \mathrm{~ms} / 5 \mathrm{~ms} / 10 \mathrm{~ms} / 20 \mathrm{~ms} / 70 \mathrm{~ms}$ or less <br> (Set it with CPU parameter.) Initial setting is 10 ms . | $\Delta$ | Set the input response time of the parameter to 1 ms before use. |
|  | ON $\rightarrow$ OFF | 1.0 ms or less | $1 \mathrm{~ms} / 5 \mathrm{~ms} / 10 \mathrm{~ms} / 20 \mathrm{~ms} / 70 \mathrm{~ms}$ or less <br> (Set it with CPU parameter.) Initial setting is 10 ms . | O |  |
| Common terminal arrangement |  | 16 points/common (Common terminal: TB17, TB34) | 16 points/common (Common terminal: TB18) | $\bigcirc$ |  |
| Operation indication |  | Available (Turning ON the input turns LED ON) | ON indication (LED) | $\bigcirc$ |  |

O : Compatible, $\Delta$ : Partially changed, $\times$ : Incompatible

| Specifications |  | A0J2E-E56DT output specifications | QY80 | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Number of output points |  | 24 points | 16 points | $\triangle$ | When seventeen or more points are used, use two QY80s. |
| Insulation method |  | Photocoupler | Photocoupler | $\bigcirc$ |  |
| Rated load voltage |  | 12/24VDC | 12 to 24VDC | $\bigcirc$ |  |
| Operating load voltage range |  | 10.2 to 26.4VDC | 10.2 to 28.8VDC | $\bigcirc$ |  |
| Maximum load current |  | 0.8A/1 point, $0.8 \mathrm{~A} / 1$ point ( $60 \% \mathrm{ON}, 55^{\circ} \mathrm{C}$ ) | 0.5A/1 point, 4A/1 common | $\triangle$ | The maximum load current per point is smaller. Use caution on selecting the load to use. |
| Maximum inrush current |  | No restriction (Short protect) | 4A 10ms or less | $\Delta$ | The inrush current value differs. Use caution on selecting the load to use. |
| Leakage current at OFF |  | 1.0 mA or less | 0.1 mA or less | $\bigcirc$ |  |
| Maximum voltage drop at ON |  | $\begin{aligned} & \text { 1.0V (TYP.) 0.8A, } \\ & 1.5 \mathrm{~V} \text { (MAX.) } 0.8 \mathrm{~A} \end{aligned}$ | $\begin{aligned} & \hline 0.2 \mathrm{VDC} \text { (TYP.) 0.5A, } \\ & 0.3 \mathrm{VDC} \text { (MAX.) } 0.5 \mathrm{~A} \end{aligned}$ | $\bigcirc$ |  |
| Response time | OFF $\rightarrow$ ON | 0.5 ms or less | 1 ms or less | $\Delta$ | The response times differ. |
|  | $\mathrm{ON} \rightarrow$ OFF | 1.5 ms or less | 1 ms or less (Rated load, resistance load) | $\bigcirc$ |  |
| External supply power | Voltage | $\begin{gathered} \text { 12/24VDC } \\ (10.2 \text { to } 30 \mathrm{VDC}) \end{gathered}$ | 12 to 24VDC (+20/-15\%) <br> (Ripple ratio within 5\%) | $\Delta$ | Voltages exceeding 28.8VDC cannot be applied. |
|  | Current | 200 mA $(24 \mathrm{VDC}$, All points are ON.) | 20 mA (During 24VDC) | $\bigcirc$ |  |
| Surge suppressor |  | Surge absorbing diode | Zener diode | $\bigcirc$ |  |
| Fuse |  | - | 6.7A (Not exchangeable) (Fuse blown capacity: 50A) | O | The QY80 has the fuse instead of overheat and short circuit protection function. |
| Fuse blown indication |  | - | Available (When a fuse blown occurs, LED is lit, and a signal is output to the CPU) ${ }^{* 3}$ | $\bigcirc$ |  |
| Protection function |  | Available (overheat protection and short circuit protection) Overheat protection: activated to two points | - | $\times$ |  |
| Protection function reset |  | Automatic reset (by deactivating overheat protection function) | - | - |  |
| Common terminal arrangement |  | 8 points/common (Common terminal: TB9, TB19, TB29) | 16 points/common (Common terminal: TB17) | $\Delta$ | As the common changes from three commons to a common, wiring with a different voltage for each common is not possible. |
| Operation indication |  | Available (Turning ON the output turns LED ON) | ON indication (LED) | $\bigcirc$ |  |
| Specifications |  | A0J2E-E56DT | QX80/QY22 | Compatibility | Precautions for replacement |
| Current consumption |  | 0.225A (TYP. All points are ON.) | $0.05 \times 2+0.08 \times 2=0.26 \mathrm{~A}$ <br> (TYP. All points are ON.) | $\Delta$ | Review current capacity since current consumption is increased. |
| External connection method |  | 36-point terminal block connector <br> (M3 $\times 6$ screws) 2 pieces | 18-point terminal block (M3 $\times 6$ screws) 1 piece/module | $\times$ | Wiring change is required. |
| Applicable wire size |  | 0.75 to $2 \mathrm{~mm}^{2}$ | 0.3 to $0.75 \mathrm{~mm}^{2}$ core (Outside diameter: 2.8 mm or less) | $\times$ |  |
| Applicable solderless terminal |  | $\begin{gathered} \text { 1.25-3, 1.25-YS3A, } \\ 2-\mathrm{S} 3,2-\mathrm{YS} 3 \mathrm{~A}, \\ \mathrm{~V} 1.25-3, \mathrm{~V} 1.25-\mathrm{YS} 3 \mathrm{~A}, \\ \mathrm{~V} 2-\mathrm{S} 3, \mathrm{~V} 2-\mathrm{YS} 3 \mathrm{~A} \end{gathered}$ | R1.25-3 <br> (Sleeved solderless terminals cannot be used.) | $\times$ |  |
| External dimensions |  | 250(H) $\times 190(\mathrm{~W}) \times 41(\mathrm{D}) \mathrm{mm}$ | 98(H) $\times 27.4(\mathrm{~W}) \times 90(\mathrm{D}) \mathrm{mm} \times 4$ | $\times$ | The dimensions are different. |
| Weight |  | 1.08 kg | $0.16 \times 2+0.17 \times 2=0.66 \mathrm{~kg}$ | $\triangle$ |  |

*1 Use the QX70 when using 12VDC.
*2 Check the specifications of the sensor or switches to be connected to the QX80.
*3 The fuse blown is not detected when the external power supply is off.

### 3.3 Precautions for I/O Module Replacement

(1) Wiring
(a) Wire gauge and size of solderless terminals

As the $Q$ series supports compact modules and terminal blocks, the wire gauge and size of the solderless terminals that can be used on terminal blocks differ from those that can be used on the A0J2H series.
For this reason, when replacing with the $Q$ series, use wire gauges and solderless terminals that match the specifications of the $Q$ series I/O modules.
(2) Precautions for input module (specifications change)
(a) The rated input current

Some Q series input modules support a smaller rated input current than the A0J2H series input modules do. Confirm the specifications of the sensors or switches to be connected.
(b) The OFF current

Some Q series input modules support an OFF current than the A0J2H series input modules do. Confirm the specifications of the sensors or switches to be connected.
(c) The maximum number of simultaneous input points

Some $Q$ series input modules have less maximum number of simultaneous input points than the A0J2H series input modules do. When replacing with the $Q$ series, refer to the derating chart and use within the range indicated in the derating chart.
(d) The rated voltage value

The QX4 $\square$ and QX8 $\square$ model DC input modules of the Q series are dedicated to 24VDC. When using 12VDC, use the QX7口.
(e) The response time

On the DC input modules of the $Q$ series, the input response time can be configured using the parameter.
Use the parameter to configure the input response time according to the response time of the A0J2H series input module.
(f) Common terminal arrangement

Use caution when using voltages that differ depending on each common as common terminal arrangement may differ between the A0J2H series and the $Q$ series.
(3) Precautions for output module (specifications change)
(a) The output current values

Some $Q$ series output modules support a smaller output current than the A0J2H series output modules do. Before using $Q$ series output modules having a smaller output current, confirm the specifications on the load side.
(b) Common terminal arrangement

Use caution when using voltages that differ depending on each common as common terminal arrangement may differ between the A0J2H series and the Q series.
(c) The common maximum load current

The maximum load current per common may differ between A0J2H series and Q series. Check the maximum load current per common before use.
(d) Precautions when using the triac output module

Operation of the triac that is used on the triac output module may be unstable when a sudden change occurs in the voltage and current due to component characteristics.
Problems due to voltage and current fluctuation might become obvious depending on individual differences between components. For this reason, refer to the following manual and check for any corresponding items in the precautions.
MELSEC-Q I/O Module Type Building Block User's Manual

## 4 REPLACING POWER SUPPLY MODULES

### 4.1 List of Alternative Power Supply Module Models

| A0J2H series to be discontinued |  |  | Alternative Q series models |  |  |  |  |
| :--- | :--- | :--- | :--- | :---: | :---: | :---: | :---: |
| Product name |  |  |  |  | Model name | Model name | Remarks (restricted items) |

### 4.2 Specifications Comparison of Power Supply Modules

(1) Specifications comparison between A0J2HCPU power supply component and Q61P

| Specifications |  | $\bigcirc$ : Compatible, $\triangle$ : Partially changed, $\times$ : Incompatible |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | A0J2HCPU power supply component | Q61P | Compatibility | Precautions for replacement |
| Input power supply |  | $\begin{gathered} 100 \text { to } 120 \mathrm{VAC}+10 \%-15 \% \\ \text { (85 to 132VAC) } \\ \hline 200 \text { to } 240 \mathrm{VAC}+10 \%-15 \% \\ \text { (170 to } 264 \mathrm{VAC} \text { ) } \\ \hline \end{gathered}$ | 100 to $240 \mathrm{VAC}+10 \%-15 \%$ ( 85 to 264VAC) | $\bigcirc$ | The Q61P is the wide range type (100 to 240VAC). |
| Input frequency |  | 50/60Hz $\pm 5 \%$ | 50/60Hz $\pm 5 \%$ | $\bigcirc$ |  |
| Input voltage distortion |  | - | Within 5\% | $\bigcirc$ |  |
| Max. input apparent power |  | 56 VA or less | 130VA | $\triangle$ | The apparent power of Q61P is larger than the one of A0J2HCPU power supply component. When using a UPS, check the capacity by the calculation. |
| Inrush current |  | Within 40A 5ms | Within 20A 8ms | $\bigcirc$ |  |
| Rated output current | 5VDC | 2 A | 6 A | $\bigcirc$ |  |
|  | 24VDC | 0.5A | - | $\times$ | When 24VDC power supply is required, add it separately. |
| Overcurrent protection | 5VDC | 2.4A or more | 6.6A or more | $\bigcirc$ |  |
|  | 24VDC | 0.6 A or more | - | $\bigcirc$ |  |
| Overvoltage protection | 5VDC | - | 5.5 to 6.5 V | $\bigcirc$ |  |
|  | 24VDC | - | - | - |  |
| Efficiency |  | 65\% or more | 70\% or more | $\bigcirc$ |  |
| Operation indicator |  | LED indication of power supply | LED indication (Lit at 5VDC output) | $\bigcirc$ |  |
| Terminal screw size |  | $\mathrm{M} 4 \times 0.7 \times 8$ | M3.5 screw | $\times$ | Wiring change is required. |
| Applicable wire size |  | 0.75 to $2 \mathrm{~mm}^{2}$ | 0.75 to $2 \mathrm{~mm}^{2}$ | $\bigcirc$ |  |
| Applicable solderless terminal |  | $\begin{gathered} \text { V1.25-4, V1.25-YS4A } \\ \text { V2-S4, V2-YS4A } \end{gathered}$ | RAV1.25-3.5, RAV2-3.5 | $\times$ | Wiring change is required. |
| Applicable tightening torque |  | 98 to $137 \mathrm{~N} \cdot \mathrm{~cm}$ | 66 to $89 \mathrm{~N} \cdot \mathrm{~cm}$ | $\times$ | Tighten within the applicable tightening torque. |
| External dimensions |  | 250(H) $\times 112(\mathrm{~W}) \times 41(\mathrm{D}) \mathrm{mm}$ | $98(\mathrm{H}) \times 55.2(\mathrm{~W}) \times 90(\mathrm{D}) \mathrm{mm}$ | $\triangle$ | The shape configuration differs. |
| Weight |  | - | 0.40 kg | - |  |
| Allowable momentary power failure period |  | Within 20ms | Within 20ms | $\bigcirc$ |  |
| Noise durability |  | Noise voltage 1500Vp-p | According to a noise simulator with noise voltage $1500 \mathrm{Vp}-\mathrm{p}$, noise width of $1 \mu \mathrm{~s}$ and noise frequency of 25 to 60 Hz | $\bigcirc$ |  |
| Dielectric withstand voltage |  | Between AC external terminal batch and ground 1500VAC 1 minute <br> Between DC external terminal batch and ground 500VAC 1 minute | Between input•LG batch and output•FG batch <br> 2,830VACrms/3 cycles <br> (Elevation 2000m) | $\bigcirc$ |  |
| Isolation resistance |  | Between AC external terminal batch and ground 500 VDC <br> $10 \mathrm{M} \Omega$ or more according to isolation resistance tester | Between input batch and output batch(LG•FG separated), <br> Between input batch and LG•FG Between output batch and FG•LG <br> $10 \mathrm{M} \Omega$ or more according to 500 VDC isolation resistance tester | $\bigcirc$ |  |
| Accessory |  | Short bar for operating voltage switching terminal: 1 | None | $\bigcirc$ | Short bars are not included as switching an operating voltage is not necessary. |

(2) Specifications comparison between A0J2HCPU power supply component and Q62P

| Specifications |  | O : Compatible, $\triangle$ : Partially changed, $\times$ : Incompatible |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | A0J2HCPU power supply component | Q62P | Compatibility | Precautions for replacement |
| Input power supply |  | 100 to 120VAC +10\% -15\% ( 85 to 132 VAC ) 200 to $240 \mathrm{VAC}+10 \%-15 \%$ (170 to 264 VAC ) | 100 to 240VAC +10\% -15\% ( 85 to 264VAC) | O | The Q62P is the wide range type (100 to 240VAC). |
| Input frequency |  | 50/60Hz $\pm 5 \%$ | 50/60Hz $\pm 5 \%$ | $\bigcirc$ |  |
| Input voltage distortion |  | Within 5\% | Within 5\% | $\bigcirc$ |  |
| Max. input apparent power |  | 56 VA or less | 105VA | $\triangle$ |  |
| Inrush current |  | Within 40A 5ms | Within 20A 8ms | $\bigcirc$ |  |
| Rated output current | 5VDC | 2A | 3A | $\bigcirc$ |  |
|  | 24VDC | 0.5A | 0.6A | $\bigcirc$ |  |
| Overcurrent protection | 5VDC | 2.4 A or more | 3.3A or more | $\bigcirc$ |  |
|  | 24VDC | 0.6 A or more | 0.66 A or more | $\bigcirc$ |  |
| Overvoltage protection | 5VDC | - | 5.5 to 6.5 V | $\bigcirc$ |  |
|  | 24VDC | - | - | - |  |
| Efficiency |  | 65\% or more | 65\% or more | O |  |
| Operation indicator |  | LED indication of power supply | LED indication (Lit at 5VDC output) | $\bigcirc$ |  |
| Terminal screw size |  | $\mathrm{M} 4 \times 0.7 \times 8$ | M3.5 screw | $\times$ | Wiring change is required. |
| Applicable wire size |  | 0.75 to $2 \mathrm{~mm}^{2}$ | 0.75 to $2 \mathrm{~mm}^{2}$ | $\bigcirc$ |  |
| Applicable solderless terminal |  | $\begin{gathered} \text { V1.25-4, V1.25-YS4A, } \\ \text { V2-S4, V2-YS4A } \end{gathered}$ | RAV1.25-3.5, RAV2-3.5 | $\times$ | Wiring change is required. |
| Applicable tightening torque |  | 98 to $137 \mathrm{~N} \cdot \mathrm{~cm}$ | 66 to $89 \mathrm{~N} \cdot \mathrm{~cm}$ | $\times$ | Tighten within the applicable tightening torque. |
| External dimensions |  | 250(H) $\times 112(\mathrm{~W}) \times 41(\mathrm{D}) \mathrm{mm}$ | $98(\mathrm{H}) \times 55.2(\mathrm{~W}) \times 90(\mathrm{D}) \mathrm{mm}$ | $\triangle$ | The shape configuration differs. |
| Weight |  | - | 0.39 kg | - |  |
| Allowable momentary power failure period |  | Within 20ms | Within 20ms | $\bigcirc$ |  |
| Noise durability |  | Noise voltage 1500Vp-p | According to a noise simulator with noise voltage $1500 \mathrm{Vp}-\mathrm{p}$, noise width of $1 \mu \mathrm{~s}$ and noise frequency of 25 to 60 Hz | O |  |
| Dielectric withstand voltage |  | Between AC external terminal batch and ground 1500VAC 1 minute <br> Between DC external terminal batch and ground 500VAC 1 minute | Between input•LG batch and output $\cdot \mathrm{FG}$ batch <br> 2,830VACrms/3 cycles <br> (Elevation 2000m) | O |  |
| Isolation resistance |  | Between AC external terminal batch and ground 500VDC <br> $10 \mathrm{M} \Omega$ or more according to isolation resistance tester | Between input batch and output batch(LG•FG separated), <br> Between input batch and LG•FG <br> Between output batch and FG•LG <br> $10 \mathrm{M} \Omega$ or more according to 500 VDC isolation resistance tester | O |  |
| Accessory |  | Short bar for operating voltage switching terminal: 1 | None | $\bigcirc$ | Short bars are not included as switching an operating voltage is not necessary. |

(3) Specifications comparison between A0J2H-DC24 power supply component and Q63P

| Specifications |  | O: Compatible, $\triangle$ : Partially changed, $\times$ : Incompatible |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | A0J2H-DC24 power supply component | Q63P | Compatibility | Precautions for replacement |
| Input power supply |  | $\begin{gathered} 24 \mathrm{VDC}+30 \%-35 \% \\ (15.6 \text { to } 31.2 \mathrm{VDC}) \end{gathered}$ | 24VDC +30\% -35\% (15.6 to 31.2VDC) | $\bigcirc$ |  |
| Max. input apparent power |  | 24W | 45W | $\Delta$ |  |
| Inrush current |  | Within 50A 2ms | Within 100A 1ms (During 24VDC input) | $\Delta$ |  |
| Rated output current | 5VDC | 2A | 6A | $\bigcirc$ |  |
|  | 24VDC | - | - | - |  |
| Overcurrent protection | 5VDC | 2.4A or more | 6.6A or more | $\bigcirc$ |  |
|  | 24VDC | - | - | - |  |
| Overvoltage protection | 5VDC | - | 5.5 to 6.5 V | $\bigcirc$ |  |
|  | 24VDC | - | - | - |  |
| Efficiency |  | 65\% or more | 70\% or more | $\bigcirc$ |  |
| Operation indicator |  | LED indication of power supply | LED indication (Lit at 5VDC output) | $\bigcirc$ |  |
| Terminal screw size |  | $\mathrm{M} 4 \times 0.7 \times 8$ | M3.5 screw | $\times$ | Wiring change is required. |
| Applicable wire size |  | 0.75 to $2 \mathrm{~mm}^{2}$ | 0.75 to $2 \mathrm{~mm}^{2}$ | $\bigcirc$ |  |
| Applicable solderless terminal |  | $\begin{gathered} \hline \text { V1.25-4, V1.25-YS4A, } \\ \text { V2-S4, V2-YS4A } \end{gathered}$ | RAV1.25-3.5, RAV2-3.5 | $\times$ | Wiring change is required. |
| Applicable tightening torque |  | 98 to $137 \mathrm{~N} \cdot \mathrm{~cm}$ | 66 to $89 \mathrm{~N} \cdot \mathrm{~cm}$ | $\times$ | Tighten within the applicable tightening torque. |
| External dimensions |  | 250(H) $\times 112(\mathrm{~W}) \times 41(\mathrm{D}) \mathrm{mm}$ | $98(\mathrm{H}) \times 55.2(\mathrm{~W}) \times 90(\mathrm{D}) \mathrm{mm}$ | $\Delta$ | The dimensions are different. |
| Weight |  | - | 0.33 kg | - |  |
| Allowable momentary power failure period |  | Within 1ms | Within 10 ms During 24VDC input | $\bigcirc$ |  |
| Noise durability |  | Noise voltage 500Vp-p | According to a noise simulator with noise voltage 500 Vp -p, noise width of $1 \mu \mathrm{~s}$ and noise frequency 25 to 60 Hz | $\bigcirc$ |  |
| Dielectric withstand voltage |  | Between DC external terminal batch and ground 500VAC 1 minute | 500VAC between primary and 5VDC | $\bigcirc$ |  |
| Isolation resistance |  | Between AC external terminal batch and ground $10 \mathrm{M} \Omega$ or more according to 500VDC isolation resistance tester | $10 \mathrm{M} \Omega$ or more according to isolation resistance tester | $\bigcirc$ |  |
| Accessory |  | None | None | - |  |

## (4) A0J2PW specifications

| Specifications |  | A0J2PW |
| :---: | :---: | :---: |
| Input power supply |  | $\begin{gathered} 100 \text { to } 120 \mathrm{VAC}+10 \%-15 \% \\ (85 \text { to } 132 \mathrm{VAC}) \end{gathered}$ |
|  |  | 200 to $240 \mathrm{VAC}+10 \%-15 \%$ <br> (170 to 264VAC) |
| Input frequency |  | $50 / 60 \mathrm{~Hz} \pm 5 \%$ |
| Input voltage distortion |  | Within 5\% |
| Max. input apparent power |  | 120VA/150VA |
| Inrush current |  | Within 40A 5ms |
| Rated output current | 5VDC | 2.3A |
|  | 24VDC | 0.8A |
| Overcurrent protection | 5VDC | 2.6 A or more |
|  | 24VDC | 1.95A or more |
| Overvoltage protection | 5VDC | - |
|  | 24VDC | - |
| Efficiency |  | 65\% or more |
| Power supply indicator |  | LED indication of power supply |
| Terminal screw size |  | $\mathrm{M} 4 \times 0.7 \times 8$ |
| Applicable wire size |  | 0.75 to $2 \mathrm{~mm}^{2}$ |
| Applicable solderless terminal |  | $\begin{gathered} \hline \text { V1.25-4, V1.25-YS4A, } \\ \text { V2-S4, V2-YS4A } \end{gathered}$ |
| Applicable tightening torque |  | 98 to $137 \mathrm{~N} \cdot \mathrm{~cm}$ |
| External dimensions |  | $250(\mathrm{H}) \times 112(\mathrm{~W}) \times 41(\mathrm{D}) \mathrm{mm}$ |
| Weight |  | 0.71 kg |
| Allowable momentary power failure period |  | Within 20ms |
| Noise durability |  | Noise voltage 1500Vp-p |
| Dielectric withstand voltage |  | Between AC external terminal batch and ground 1500VAC 1 minute <br> Between DC external terminal batch and ground 500VAC 1 minute |
| Isolation resistance |  | Between AC external terminal batch and ground $10 \mathrm{M} \Omega$ or more according to 500VDC isolation resistance tester |
| Accessory |  | Short bar for operating voltage switching terminal: 1 |

## Point

When the A0J2HCPU's build-in power supply is insufficient, please use the A0J2PW power supply module.
For A0J2PW replacement, the following methods are recommended:

- When A0J2PW is used to supply 5VDC For an alternative Q series models, select a CPU module with large capacity of 5VDC output current, such as Q61P (Select the Q63P when the A0J2HCPU-DC24 is used).
- When A0J2PW is used to supply 24VDC

Purchase a commercially available 24VDC output power supply, and add it separately.

### 4.3 Precautions for Replacement of the CPU Module

(1) The current consumption of the Q series and the AOJ2H series differ. Calculate the current consumption of the entire system to select the power supply module.
(2) The wires and solderless terminals that can be used on the $Q$ series terminal block differ from those that can be used on the A0J2H series. Use wires and solderless terminals that match the specifications.
(3) The Q61P and Q62P (input power supply) is the wide range type. The modules support the operating voltage of 100 VAC and 200VAC.
(4) When the Q62P (5VDC output current: 3A) has insufficient current capacity, it is recommended to use Q61P (5VDC output, current: 6A) or Q64PN (5VDC output, current 8.5A).
However, as these power supply modules do not output 24VDC, it is necessary to separately prepare a commercially available 24VDC output power supply.

## EXTENSION CABLE REPLACEMENT

### 5.1 List of Alternative Extension Cable Models

| A0J2H series models to be discontinued |  | Q series alternative models |  |
| :---: | :---: | :---: | :---: |
| Product | Model | Model | Remarks (restrictions) |
| Extension cable | A0J2C01 | - | For the $Q$ series, cables for connecting between each module are not required. For details, refer to Section 5.2. |
|  | A0J2C03 |  |  |
|  | A0J2C03F |  |  |
|  | A0J2C06 |  |  |
|  | A0J2C10 |  |  |
|  | A0J2C20 |  |  |
|  | A0J2C04B | - | Select an extension cable only when an extension base unit such as the $Q$ series large type extension base unit is used. For details, refer to Section 5.2. |
|  | A0J2C10B |  |  |

### 5.2 Precautions for Extension Cable Replacement

Connecting each module by using an extension cable is required in the A0J2H series system while it is not required in the $Q$ series system.
Select an extension cable, only when an extension base unit is required because the number of slots of the main base unit is not enough or the $Q$ series large type main base unit is used due to increasing the number of modules after replacement.
Select the length of an extension cable considering the module configuration and installation position of the base unit.

* List of the Q series extension cables

| Product | Model | Cable length | Overall cable distance |
| :---: | :---: | :---: | :---: |
| Extension cable | QC05B | 0.45 m |  |
|  | QC06B | 0.6 m |  |
|  | QC12B | 1.2 m |  |
|  | QC30B | 3.0 m | 5.0 m |

## MEMORY AND BATTERY REPLACEMENT

### 6.1 List of Alternative Memory Models

| A0J2H series models to be discontinued |  |  | Alternative Q series models |  |
| :---: | :--- | :--- | :--- | :---: |
| Product name |  | Model name | Model name |  |

### 6.2 Precautions for Memory and Battery Replacement

(1) Precautions for memory replacement
(a) Copying programs into the ROM

The ROM memory is not required for the Universal model QCPU because the program memory is the flash ROM.
(b) Using the file register

When the existing CPU module is replaced with the Q00UJCPU, the file register cannot be used because the standard RAM is not built in the Q00UJCPU.
To use the file register, select the Q00UCPU.
(2) Precautions for battery replacement

Replace the A Series battery (A6BAT*) with a Q Series battery (Q6BAT, Q7BAT).
(The Q series CPU module comes with the Q6BAT as standard.)
Refer to the user's manuals of each CPU module regarding battery life because it varies depending on the type of CPU module.

* The A6BAT is not a model to be discontinued.


## 7

## PROGRAMS REPLACEMENT

This chapter describes the procedures and precautions when replacing (utilizing) the programs, comments, etc. from the A0J2HCPU to a Universal model QCPU.

O : Compatible, $\Delta$ : Partially changed, $\times$ : Incompatible

| Item |  | A0J2HCPU specifications | Universal model QCPU specifications and precautions for replacement | Compatibility | Reference sections |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Sequence program | Main <br> SFC | - The main program is required. <br> - The SFC is dealt as the microcomputer program of the main program. | [Specifications] <br> - Each of the programs is handled in one file with the data names "MAIN" and "MAIN-SFC". <br> [Measures] <br> - The program setting of the PLC parameters is required. | $\triangle$ | $\begin{aligned} & \text { Section } \\ & 7.7 .9 \end{aligned}$ |
| Microcom program |  | - The user's microcomputer program and the microcomputer program of the utility package are available. | [Specifications] <br> - The microcomputer program cannot be created. <br> [Measures] <br> - The user's microcomputer program of the A0J2HCPU cannot be executed. Replacing it with the sequence program, etc. is recommended. <br> - To use the instructions from the utility package, correcting it to the equivalent instructions of QCPU is required. | $\times$ | - |
| Instruction |  | - Instructions (LED instructions, etc.) dedicated to ACPU are available. | [Specifications] <br> - Although instructions are automatically converted by the change PLC type, some of the instructions are not converted. <br> [Measures] <br> - As the unconverted instructions are converted to SM1255 and SD1255 devices, the program needs to be corrected. | $\Delta$ | Section 7.2 |


| Item | A0J2HCPU specifications | Universal model QCPU specifications and precautions for replacement | Compatibility | Reference sections |
| :---: | :---: | :---: | :---: | :---: |
| File register | - A storage area is secured in the program memory. <br> - The maximum setting is 4 k points. | [Specifications] <br> - Stored to the standard RAM. <br> - One block is a 32 k point unit. <br> [Measures] <br> - Writing is required by the write to PLC. <br> - The Q00UJCPU is not equipped with the standard RAM. <br> - To use the file register, select the Q00UCPU. | $\triangle$ | $\begin{aligned} & \text { Section } \\ & 7.7 .10 \end{aligned}$ |
| Timer and Counter | - The timer and counter are processed with END. | [Specifications] <br> - The timer and counter are processed when the instructions are executed. <br> [Measures] <br> - The timing of the timer and counter processing are different, so the program needs to be reviewed. | $\Delta$ | $\begin{aligned} & \text { Section } \\ & 7.7 .4, \\ & \text { Section } \\ & 7.7 .5 \end{aligned}$ |
| Parameter | - Dedicated parameters are available. | [Specifications] <br> - Dedicated parameters are available for each CPU. <br> [Measures] <br> - As the specifications/functions differ, to replace with QCPU, confirm those differences and reconfigure the parameters. | $\triangle$ | Section 7.3 |
| Special relay | - 256 points of M9000 to M9255 are available. | [Specifications] <br> - 2048 points of SM0 to SM2047 are available. <br> [Measures] <br> - Although the setting values are automatically converted during replacement with QCPU, some specifications differ and need to be reviewed. | $\Delta$ | Section 7.4 |
| Special register | - 256 points of D9000 to D9255 are available. | [Specifications] <br> - 2048 points of SD0 to SD2047 are available. <br> [Measures] <br> - Although the setting values are automatically converted during replacement with QCPU, some specifications differ and need to be reviewed. | $\Delta$ | Section 7.5 |
| Comment | - Classifies comments into common comments and local comments. | [Specifications] <br> - Manages all comments as local comments and common comments. <br> - Comments are automatically replaced by changing the PLC type in GX Developer at QCPU conversion. <br> - QCPU comment capacity depends on the memory capacity. | $\bigcirc$ | Section 7.1.2 |


| Item | AOJ2HCPU specifications | Universal model QCPU <br> specifications and <br> precautions for replacement | Compatibility |
| :--- | :--- | :--- | :--- | :---: | | Reference |
| :---: |
| sections |

### 7.1 Program Replacement Procedures

The programs, comments of the A0J2HCPU with QCPU can be replaced by "Change PLC type" of the GX Developer.

### 7.1.1 Program conversion procedure from A0J2HCPU to QCPU

Program conversion is performed in the order of $(1) \rightarrow(2) \rightarrow(3)$ in the following diagram.
(1) Reading processing of the source data of conversion.
(2) Program conversion from the A0J2HCPU program to QCPU program by the Change PLC type operation.
(3) Writing process of the data after conversion.

Refer to Section 7.1.2 for the details of the change operation.

| (1) Reading A0J2HCPU program <br> (a) Reading data from A0J2HCPU <br> (b) Reading project data of A0J2HCPU <br> Read (Refer to the following <br> diagram, (1)-(a), and (1)-(b).) |
| :--- | :--- |



Remarks
Change of PLC type to convert it to the program of the CPU module that is supported by GX Developer is required in advance if the CPU type is "A0J2CPU", because GX Developer does not support the "A0J2CPU".
The change of PLC type can be performed with "A/QnA to Q conversion support tool". For details on the procedure, refer to Section 7.1.5.

### 7.1.2 Change PLC type

Change PLC type is a function for changing existing data to data for other programmable controller series for reuse.
This function changes the target programmable controller type of the data that is read to GX Developer. Some instructions that cannot be automatically converted are changed to OUT SM1255. Search for these instructions or SM1255 devices in the converted program and modify the program manually. In addition, programs and parameters need to be reviewed regarding intelligent function module and network modules.
(1) Convertible range from ACPU using GX Developer

The following list shows the convertible range from ACPU to other programmable controllers.

| Product name | Change source programmable controller | Change target programmable controller |  |  |
| :---: | :---: | :---: | :---: | :---: |
|  |  | ACPU/AnSCPU | QnACPU/QnASCPU | QCPU |
| GX Developer | ACPU | $\bigcirc$ | $\bigcirc$ | $\Delta^{* 1}$ |

*1 Changing of PLC type from the existing CPU module to the High-speed Universal model QCPU is not supported in GX Developer.
(2) GX Developer operation
(a) Select "Change PLC type" in the "Project" menu.

(b) Specify the target programmable controller type in the "Change PLC type" dialogue.


Click the [OK] button after setting the PLC type.


PLC type setting

(c) Select the conversion method of the special relay/register.


Specify the conversion destination of the special relay/register (ACPU: M9000/D9000 models) device.
Please check the box next to [Convert M9000/D9000-Q/L/QnACPU special devices].

- Checked: Converted to a device for the Basic model QCPU.
- Not checked: Converted to A compatible (SM1000/SD1000 models).

The box state is fixed to be checked when the Universal model QCPU is selected.

After specifying the destination of the device conversion, Change PLC type is executed by pressing the [Yes] or [Confirm Change] button.

- [Yes]: Executed without conducting intermediate steps and user confirmation.
- [Confirm Change]: Asks user confirmation to execute changes.


### 7.1.3 ACPU program conversion ratio

- Conversion ratio of common instructions (Sequence/basic/application instructions)

The following table shows the conversion ratio when changing the programmable controller type of the ACPU common instructions to the QCPU.
More than $90 \%$ of the common instructions are automatically converted.


## - Conversion ratio of dedicated instructions

The following table shows the conversion ratio when changing the programmable controller type of the ACPU dedicated instructions to the QCPU.

|  | Instruction type |  | QnUCPU |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | Number of instructions | Number of <br> instructions <br> applicable for <br> automatic <br> conversion | Number of instructions requiring manual change | Conversion ratio (rough standard) |
| Dedicated instruction (Functional extension) | Direct input/output instruction | 3 | 3 | 0 | 100\% |
|  | Structured program instruction | 6 | 2 | 4 | 33\% |
|  | Data operation instruction | 6 | 6 | 0 | 100\% |
|  | I/O operation instruction | 2 | 1 | 1 | 50\% |
|  | Real number processing instruction | 27 | 27 | 0 | 100\% |
|  | Character string processing instruction | 25 | 24 | 1 | 96\% |
|  | Data control instruction | 6 | 6 | 0 | 100\% |
|  | Clock instruction | 2 | 2 | 0 | 100\% |
|  | Extension file register instruction | 7 | 0 | 7 | 0\% |
|  | Program switching instruction | 4 | 0 | 4 | 0\% |
|  | Instruction for PID control | 3 | 2 | 1 | 67\% |
|  | Subtotal | 91 | 74 | 17 | 81\% |
| Dedicated instruction (For modules) | Instruction for data link | 9 | 5 | 4 | 56\% |
|  | Instruction for special function modules | 59 | 0 | 59 | 0\% |
|  | Subtotal | 68 | 5 | 63 | 7\% |
| Total number of dedicated instructions |  | 159 | 78 | 81 | 49\% |

## Remarks

The automatic conversion is applied to the instructions when equivalent functions and instructions exist in the change destination programmable controller.
Some instructions are not converted for the following cases.
Refer to Section 7.2 Instruction Conversion to change the program manually.
(1) The change target programmable controller does not have the equivalent functions and instructions.
(2) Instructions to specified modules cause the change of the module and buffer memory configuration.
(3) Multiple instructions with the same name and argument exist.
(4) The conversion causes a mismatch in the instructions.

### 7.1.4 Reading (Reusing) other format files

## (1) Reading (Reusing) GPPQ/GPPA files to GX Developer

This section explains the procedure to read (reuse) files in GPPQ/GPPA formats others than those of the GX Developer.
Follow this procedure to convert a file format to the GX Developer file format.

## XPoint

To read out (reuse) a program of a CPU module that is not supported by GX Developer, change PLC type to convert it to the program of the CPU module that is supported by GX Developer in advance, using "A/QnA to $Q$ conversion support tool".
For operating procedure of the $A / Q n A$ to $Q$ conversion support tool, refer to Section 7.1.5.
(a) GX Developer operating procedure

$$
\text { [Project] } \rightarrow \text { [Import file] } \longrightarrow \text { [Import from GPPQ format file] }
$$

(b) Setting screen


1) Drive/Path, System name, Machine name, and PLC type

Displays the location of the data created in GPPQ and GPPA.
Enter the system name and machine name of the data specified by the Drive/Path.
Click the [Browse] button to display the dialogue box asking you to select the system name and machine name. Double-click and specify the files to be read.

## 2) Source data list

Displays the data created in GPPQ and GPPA.
Place a $\checkmark$ mark in the checkbox of the data name to select it.
For the selected comments, use the program Common tab or Local tab to configure the range of the device comment to be read.

## 3) $[$ Param+prog] button/[Select all] button

- [Param+prog] button

Selects only the parameter data and program data of the reading source.

- [Select all] button

Selects all the data in the list of source data to be read.
On the A series, Kanji character comments are selected and the device memory is displayed according to the data count.
On the Q/QnA series, the head data name is selected for the comments and file registers.
4) [Cancel all selections] button

Cancels all the selected data.
5) <<Common>>tab screen (A series)

Go to this screen to specify the range of the common comments before reading.

6) <<Local>>tab screen (A series)

Go to this screen to specify the range of the local comments before reading.

7) Merge peripheral statement/note

For detail about Merge peripheral statement/note, please refer to the GX Developer Version8 Operating manual.
8) [Execute] button

Click after completing settings.

## (c) Setting procedure

## 1) To select

a) Set the drive/path to read by GPPQ and GPPA.
b) Set the system name and device name of the project to read using the [Browse] button.
c) Select by clicking the [Param+prog] button or [Select all] button, or using a mouse to place a $\checkmark$ mark in the checkbox.
d) Click the [Execute] button after completing settings.
2) To cancel selected data
a) To cancel selected data arbitrarily

Remove the $\checkmark$ mark from the checkbox using the mouse or space key.
b) To cancel all selected data Click the [Cancel all selection] button.
(d) Precautions for reading other format files

| A series |  |
| :--- | :--- |
| A6GPP format, | Read data with GX Developer after performing the corresponding format conversion with <br> SW0S-GPPA <br> format data |
| For details on how to proceed, refer to the SWDSRXV/NX/IVD-GPPA GPP Function |  |
| Software Package Operating Manual (Details). |  |


|  | QnA series |
| :--- | :--- |
| Ladder return positions | Returning places are different between GPRQ and GX Developer. <br> Because of this, if the total of return sources and return destinations exceeds 24 lines in a <br> single ladder block, the program is not displayed properly. <br> Corrective action: Add SM400 (normally on contact) to adjust the return positions. |
| For data selection | For the device memory and file register, only one data name can be selected for each <br> item. |

## (2) Procedure for reading files in GX Developer format to GX Works2

The following explains how to appropriately read files in GX Developer format to GX Works2. Follow this procedure to convert the read files to the file format of GX Works2.

## (a) GX Works2 operation procedure

[Project] $\rightarrow$ [Open Other Data] $\rightarrow$ [Open Other Project]
(b) Setting window


1) Look in

Display the place where the files in GX Developer format are stored and specify the file to be read.
2) Name

Select "*.gpj" for the file extension to use the file as a project file.
3) [Open] button

After selecting the file, click the [Open] button to open the execution window.
4) [Yes] button

Clicking [Yes] button executes the file read.
When the file read is completed, a completion message is displayed.
The file becomes available for GX Works2 operation.
(1) Performing the QCPU programming using GX Developer as a programming tool has following restrictions.

- Model of available CPU module: QCPUs excluding High-speed Universal model QCPU

When this restriction is applied, use GX Works2 as a programming tool.
(2) To use the existing A/QnACPU program with GX Works2, follow the procedure below.
(a) A/QnACPU program conversion procedure

1) Read project data from the existing A/QnACPU using GX Developer and save the file. $\downarrow$
2) By using "Change PLC type", convert the read A/QnACPU program to a Universal model QCPU, which can be specified with GX Developer. $\downarrow$
3) Read the converted QCPU program by other format read ([Project] - [Open Other Data] - [Open Other Project]) of GX Works2. $\downarrow$
4) After that, configure various settings and modify the program using GX Works2.
(b) Conversion procedure of the difference information embedded Q program (A/QnA-Q conversion support tool)
5) Read project data from the existing A/QnACPU using GX Developer and save the file. $\downarrow$
6) By using "Change PLC type", convert the read A/QnACPU program to a Universal model QCPU, which can be specified with GX Developer, and save it. $\downarrow$
7) Output the difference information embedded $Q$ program and the review information list using the $A / Q n A-Q$ conversion support tool. $\downarrow$
8) Modify the difference information embedded $Q$ program with GX Developer while referring to the review information list.
$\downarrow$
9) Read the difference information embedded Q program by other format read ([Project] - [Open Other Data] [Open Other Project]) of GX Works2.
$\downarrow$
10) After that, configure various settings and modify the program using GX Works2.
(c) Conversion procedure of the MELSECNET (II) local station dedicated module link refresh program (A/QnA-Q conversion support tool)
11) Using the $A / Q n A-Q$ conversion support tool, set the output type of CPU to a Universal model QCPU and output the MELSECNET (II) local station dedicated module link refresh program.
$\downarrow$
12) Read the MELSECNET (II) local station dedicated module link refresh program by other format read ([Project] [Open Other Data] - [Open Other Project]) of GX Works2. $\downarrow$
13) After that, configure various settings and modify the program using GX Works2.

### 7.1.5 How to reuse a program of a PLC type that is not supported by GX Developer

Reuse of a program of a PLC type that is not supported by GX Developer is possible using "A/QnA to Q conversion support tool". Follow the steps below.
(1) CPU modules that are not supported by GX Developer

The CPU modules listed below are not supported by GX Developer.
Change PLC type to convert it to the program of the CPU module that is supported by GX Developer in advance, by using "A/QnA to Q conversion support tool".

- A0J2CPU
- A1CPU
- A2CPU(-S1)
- A3CPU
- A73CPU
- A3HCPU
- A52GCPU
- A3VCPU
- A3MCPU
(2) Operating procedure

1) Start up "A/QnA to $Q$ conversion support tool".

2) Select "A0J2 conversion support tool execute".

3) Go to "File" and click "New", then select the corresponding PLC type.

4) Go to "Online" and click "Read from PLC", then read the program of the corresponding CPU module.

5) Go to "Auxiliary" and click "Change PC Type", then select a PLC type that is supported by GX Developer.

| Auxiliary Window Help |
| :--- |
| Parameter Setting... |
| Change PC Type ... |
| Change Mode... |
| Options |
| NOP Operation |


| Change PLC Type |  | 区 |
| :---: | :---: | :---: |
| C A0J2 |  |  |
| C A0J2H |  |  |
| C AIS,A1SJ |  |  |
| C A1(N) |  |  |
| C A2C.A2 JC,A52G |  |  |
| ( A2(N), A2S |  |  |
| C A3(N), A 1S(J)H,A2SH,A3V,A73 |  |  |
| C A3H.A3M |  |  |
| (c) A 2 A |  |  |
| C A3A |  |  |
| C A2U,A2US |  |  |
| C A3U,A2USH |  |  |
| C A4U |  |  |
| OK | Cancel |  |

6) Go to "File" and select "Save as".
"System Name" and "Machine Name" defined here constitute the name of another format file, which is mentioned in Section 7.1.4.

| File | Edit View Change mode | Online Aux |
| :---: | :---: | :---: |
|  | New... | $\mathrm{Ctrl}+\mathrm{N}$ |
|  | Open... | Ctrl +0 |
|  | Close |  |
|  | Save | Ctrl + S |
|  | Save as... |  |
|  | Open sub porgram | , |
|  | $1 \mathrm{C}: ¥ \mathrm{¥gpp}$ ¥usr¥TEMP \#TEMP |  |
|  | Exit |  |


©Point

- For details, refer to "A/QnA to Q conversion support tool: A0J2 Conversion Support Tool Operation Guide".
- For details on the A/QnA to Q conversion support tool, please contact your local Mitsubishi representative.


### 7.2 Instruction Conversion

Instructions are converted by Change PLC type of the GX Developer.
This section explains the corrective actions of converted and unconverted instructions.

### 7.2.1 List of instructions converted from A0J2HCPU to QCPU (Sequence/Basic/Application instructions)

O : Automatic converted, $\times$ : Manual conversion required

| Description | A0J2HCPU | QCPU |  | Reference sections |
| :---: | :---: | :---: | :---: | :---: |
|  | Instruction name | Instruction name | Convertibility |  |
| BIN 16-bit addition, subtraction | + | + | $\bigcirc$ |  |
|  | $+\mathrm{P}$ | +P | $\bigcirc$ |  |
|  | - | - | $\bigcirc$ |  |
|  | -P | -P | $\bigcirc$ |  |
| BIN 16-bit multiplication, division | * | * | $\bigcirc$ |  |
|  | *P | *P | $\bigcirc$ |  |
|  | 1 | / | $\bigcirc$ |  |
|  | /P | /P | $\bigcirc$ |  |
| Ladder block series connection | ANB | ANB | $\bigcirc$ |  |
| Series connection | AND | AND | $\bigcirc$ |  |
| BIN 16-bit data comparison | AND< | AND< | $\bigcirc$ |  |
|  | AND<= | AND<= | $\bigcirc$ |  |
|  | AND<> | AND<> | $\bigcirc$ |  |
|  | AND= | AND= | $\bigcirc$ |  |
|  | AND> | AND> | $\bigcirc$ |  |
|  | AND>= | AND>= | $\bigcirc$ |  |
| BIN 32-bit data comparison | ANDD< | ANDD< | $\bigcirc$ |  |
|  | ANDD<= | ANDD<= | $\bigcirc$ |  |
|  | ANDD<> | ANDD<> | $\bigcirc$ |  |
|  | ANDD= | ANDD= | $\bigcirc$ |  |
|  | ANDD> | ANDD> | $\bigcirc$ |  |
|  | ANDD>= | ANDD>= | $\bigcirc$ |  |
| Series connection | ANI | ANI | $\bigcirc$ |  |
| Hexadecimal BIN $\rightarrow$ ASCII conversion | ASC | OUT SM1255 | $\times$ | Section 7.2.2 (3) |
| BCD 4-digit addition, subtraction | B+ | B+ | $\bigcirc$ |  |
|  | B+P | B+P | $\bigcirc$ |  |
|  | B- | B- | $\bigcirc$ |  |
|  | B-P | B-P | $\bigcirc$ |  |
| BCD 4-digit multiplication, division | B* | B* | $\bigcirc$ |  |
|  | B*P | B*P | $\bigcirc$ |  |
|  | B/ | B/ | $\bigcirc$ |  |
|  | B/P | B/P | $\bigcirc$ |  |
| Conversion from BIN data to 4-digit BCD | BCD | BCD | $\bigcirc$ |  |
|  | BCDP | BCDP | $\bigcirc$ |  |
| Conversion from 4-digit BCD to BIN data | BIN | BIN | $\bigcirc$ |  |
|  | BINP | BINP | $\bigcirc$ |  |
| Block 16-bit data transfer | BMOV | BMOV | $\bigcirc$ |  |
|  | BMOVP | BMOVP | $\bigcirc$ |  |

O: Automatic converted, $\triangle$ : Partially changed, $\times$ : Manual conversion required

| Description | A0J2HCPU | QCPU |  | Reference sections |
| :---: | :---: | :---: | :---: | :---: |
|  | Instruction name | Instruction name | Convertibility |  |
| Bit reset of word device | BRST | BRST | $\bigcirc$ |  |
|  | BRSTP | BRSTP | $\bigcirc$ |  |
| Bit set of word device | BSET | BSET | $\bigcirc$ |  |
|  | BSETP | BSETP | $\bigcirc$ |  |
| 1-bit shif to left of $n$-bit data | BSFL | BSFL | $\bigcirc$ |  |
|  | BSFLP | BSFLP | $\bigcirc$ |  |
| 1-bit shift to right of $n$-bit data | BSFR | BSFR | $\bigcirc$ |  |
|  | BSFRP | BSFRP | $\bigcirc$ |  |
| Sub-routine program calls | CALL | CALL | $\bigcirc$ |  |
|  | CALLP | CALLP | $\bigcirc$ |  |
| Special format failure checks | CHK | OUT SM1255 | $\times$ | Section 7.2.2 (3) |
| Reverse of device output | CHK | OUT SM1255 | $\times$ | Section 7.2.2 (1) |
| Pointer branch instructions | CJ | CJ | $\bigcirc$ |  |
| Carry flag reset | CLC | OUT SM1255 | $\times$ | Section 7.2.2 (3) |
| 16-bit data negation transfer | CML | CML | $\bigcirc$ |  |
|  | CMLP | CMLP | $\bigcirc$ |  |
| Refresh instruction | COM | COM | $\bigcirc$ |  |
| BIN 32-bit addition, subtraction | D+ | D+ | $\bigcirc$ |  |
|  | D+P | D+P | $\bigcirc$ |  |
|  | D- | D- | $\bigcirc$ |  |
|  | D-P | D-P | $\bigcirc$ |  |
| BIN 32-bit multiplication, division | D* | D* | $\bigcirc$ |  |
|  | D*P | D*P | $\bigcirc$ |  |
|  | D/ | D/ | $\bigcirc$ |  |
|  | D/P | D/P | $\bigcirc$ |  |
| Logical products of 32-bit data | DAND | DAND | $\bigcirc$ |  |
|  | DANDP | DANDP | $\bigcirc$ |  |
| BCD 8-digit addition, subtraction | DB+ | DB+ | $\bigcirc$ |  |
|  | DB+P | DB+P | $\bigcirc$ |  |
|  | DB- | DB- | $\bigcirc$ |  |
|  | DB-P | DB-P | $\bigcirc$ |  |
| BCD 8-digit multiplication, division | DB* | DB* | $\bigcirc$ |  |
|  | DB*P | DB*P | $\bigcirc$ |  |
|  | DB/ | DB/ | $\bigcirc$ |  |
|  | DB/P | DB/P | $\bigcirc$ |  |
| Conversion from BIN data to BCD 8-digit | DBCD | DBCD | $\bigcirc$ |  |
|  | DBCDP | DBCDP | $\bigcirc$ |  |
| Conversion from BCD 8-digit to BIN data | DBIN | DBIN | $\bigcirc$ |  |
|  | DBINP | DBINP | $\bigcirc$ |  |
| 32-bit data negation transfer | DCML | DCML | $\bigcirc$ |  |
|  | DCMLP | DCMLP | $\bigcirc$ |  |
| 32-bit BIN data decrement | DDEC | DDEC | $\bigcirc$ |  |
|  | DDECP | DDECP | $\bigcirc$ |  |
| 16-bit BIN data decrement | DEC | DEC | $\bigcirc$ |  |
|  | DECP | DECP | $\bigcirc$ |  |
| $8 \rightarrow 256-$ bit decode | DECO | DECO | $\bigcirc$ |  |
|  | DECOP | DECOP | $\bigcirc$ |  |

O : Automatic converted, $\triangle$ : Partially changed, $\times$ : Manual conversion required

| Description | A0J2HCPU | QCPU |  | Reference sections |
| :---: | :---: | :---: | :---: | :---: |
|  | Instruction name | Instruction name | Convertibility |  |
| 2-word data read from intelligent function/special function modules | DFRO | DFRO | $\bigcirc$ |  |
|  | DFROP | DFROP | $\bigcirc$ |  |
| Interrupt disable instruction | DI | DI | $\bigcirc$ |  |
| Link refresh disable | DI | DI | $\bigcirc$ |  |
| 32-bit BIN data increment | DINC | DINC | $\bigcirc$ |  |
|  | DINCP | DINCP | $\bigcirc$ |  |
| 4bits groupings of 16-bit data | DIS | DIS | $\bigcirc$ |  |
|  | DISP | DISP | $\bigcirc$ |  |
| 32-bit data transfer | DMOV | DMOV | $\bigcirc$ |  |
|  | DMOVP | DMOVP | $\bigcirc$ |  |
| Logical sums of 32-bit data | DOR | DOR | $\bigcirc$ |  |
|  | DORP | DORP | $\bigcirc$ |  |
| Left rotation of 32-bit data | DRCL | DRCL | $\bigcirc$ | Section 7.7.7 |
|  | DRCLP | DRCLP | $\bigcirc$ | Section 7.7.7 |
| Right rotation of 32-bit data | DRCR | DRCR | $\bigcirc$ | Section 7.7.7 |
|  | DRCRP | DRCRP | $\bigcirc$ | Section 7.7.7 |
| Left rotation of 32-bit data | DROL | DROL | $\bigcirc$ | Section 7.7.7 |
|  | DROLP | DROLP | $\bigcirc$ | Section 7.7.7 |
| Right rotation of 32-bit data | DROR | DROR | $\bigcirc$ | Section 7.7.7 |
|  | DRORP | DRORP | $\bigcirc$ | Section 7.7.7 |
| 32-bit data search | DSER | DSER | $\bigcirc$ | Section 7.7.7 |
| 1-word shift to left n-word data | DSFL | DSFL | $\bigcirc$ |  |
|  | DSFLP | DSFLP | $\bigcirc$ |  |
| 1-word shift to right n-word data | DSFR | DSFR | $\bigcirc$ |  |
|  | DSFRP | DSFRP | $\bigcirc$ |  |
| 32-bit data checks | DSUM | DSUM | $\bigcirc$ | Section 7.7.7 |
|  | DSUMP | DSUMP | $\bigcirc$ | Section 7.7.7 |
| 2-word data write to intelligent function/special function modules | DTO | DTO | $\bigcirc$ |  |
|  | DTOP | DTOP | $\bigcirc$ |  |
| Timing pulse generation | DUTY | DUTY | $\bigcirc$ |  |
| 32-bit data conversion | DXCH | DXCH | $\bigcirc$ |  |
|  | DXCHP | DXCHP | $\bigcirc$ |  |
| 32-bit non-exclusive logical sum operations | DXNR | DXNR | $\bigcirc$ |  |
|  | DXNRP | DXNRP | $\bigcirc$ |  |
| 32-bit exclusive logical sum operations | DXOR | DXOR | $\bigcirc$ |  |
|  | DXORP | DXORP | $\bigcirc$ |  |
| Interrupt enable instruction | El | El | $\bigcirc$ |  |
| Link refresh enable | El | El | $\bigcirc$ |  |
| $256 \rightarrow 8$-bit encode | ENCO | ENCO | $\bigcirc$ |  |
|  | ENCOP | ENCOP | $\bigcirc$ |  |
| Sequence program completion | END | END | $\bigcirc$ |  |
| Main routine program completion | FEND | FEND | $\bigcirc$ |  |
| Reading oldest data from tables | FIFR | FIFR | $\bigcirc$ |  |
|  | FIFRP | FIFRP | $\bigcirc$ |  |
| Writing data to the data table | FIFW | FIFW | $\bigcirc$ |  |
|  | FIFWP | FIFWP | $\bigcirc$ |  |
| Identical 16-bit data block transfers | FMOV | FMOV | $\bigcirc$ |  |
|  | FMOVP | FMOVP | $\bigcirc$ |  |

$O$ : Automatic converted, $\triangle$ : Partially changed, $\times$ : Manual conversion required

| Description | A0J2HCPU | QCPU |  | Reference sections |
| :---: | :---: | :---: | :---: | :---: |
|  | Instruction name | Instruction name | Convertibility |  |
| FOR to NEXT instructions | FOR | FOR | $\bigcirc$ |  |
| 1-word data read from intelligent function/special function modules | FROM | FROM | - *1 |  |
|  | FROMP | FROMP | - *1 |  |
| 16-bit BIN data increment | INC | INC | $\bigcirc$ |  |
|  | INCP | INCP | $\bigcirc$ |  |
| Return from interrupt programs | IRET | IRET | $\bigcirc$ |  |
| Pointer branch instructions | JMP | JMP | $\bigcirc$ |  |
| Operation start | LD | LD | $\bigcirc$ |  |
| BIN 16-bit data comparison | LD< | LD< | $\bigcirc$ |  |
|  | LD<= | LD<= | $\bigcirc$ |  |
|  | LD<> | LD<> | $\bigcirc$ |  |
|  | LD= | LD= | $\bigcirc$ |  |
|  | LD> | LD> | $\bigcirc$ |  |
|  | LD>= | LD>= | $\bigcirc$ |  |
| BIN 32-bit data comparison | LDD< | LDD< | $\bigcirc$ |  |
|  | LDD<= | LDD<= | $\bigcirc$ |  |
|  | LDD<> | LDD<> | $\bigcirc$ |  |
|  | LDD= | LDD= | $\bigcirc$ |  |
|  | LDD> | LDD> | $\bigcirc$ |  |
|  | LDD>= | LDD>= | $\bigcirc$ |  |
| Operation start | LDI | LDI | $\bigcirc$ |  |
| Error indication or annunciator reset instruction | LEDR | LEDR | $\bigcirc$ |  |
| Local station data read | LRDP | OUT SM1255 | $\times$ | Section 7.2.2 (3) |
| Local station data write | LWTP | OUT SM1255 | $\times$ | Section 7.2.2 (3) |
| Master control set, reset | MC | MC | $\bigcirc$ |  |
|  | MCR | MCR | $\bigcirc$ |  |
| 16-bit data transfer | MOV | MOV | $\bigcirc$ |  |
|  | MOVP | MOVP | $\bigcirc$ |  |
| Operation result pop | MPP | MPP | $\bigcirc$ |  |
| Operation result push | MPS | MPS | $\bigcirc$ |  |
| Operation result read | MRD | MRD | $\bigcirc$ |  |
| BIN 16-bit data 2's complement (sign inversion) | NEG | NEG | $\bigcirc$ |  |
|  | NEGP | NEGP | $\bigcirc$ |  |
| FOR to NEXT instruction | NEXT | NEXT | $\bigcirc$ |  |
| No operation | NOP | NOP | $\bigcirc$ |  |
|  | NOPLF | NOPLF | $\bigcirc$ |  |
| Parallel connection | OR | OR | $\bigcirc$ |  |
| BIN 16-bit data comparison | OR< | OR< | $\bigcirc$ |  |
|  | $\mathrm{OR}<=$ | OR<= | $\bigcirc$ |  |
|  | OR<> | OR<> | $\bigcirc$ |  |
|  | OR= | OR= | $\bigcirc$ |  |
|  | OR> | OR> | $\bigcirc$ |  |
|  | OR>= | OR>= | $\bigcirc$ |  |
| Ladder block parallel connection | ORB | ORB | $\bigcirc$ |  |

O: Automatic converted, $\triangle$ : Partially changed, $\times$ : Manual conversion required

| Description | A0J2HCPU | QCPU |  | Reference sections |
| :---: | :---: | :---: | :---: | :---: |
|  | Instruction name | Instruction name | Convertibility |  |
| BIN 32-bit data comparison | ORD< | ORD< | $\bigcirc$ |  |
|  | ORD<= | ORD<= | $\bigcirc$ |  |
|  | ORD<> | ORD<> | $\bigcirc$ |  |
|  | ORD= | ORD= | $\bigcirc$ |  |
|  | ORD> | ORD> | $\bigcirc$ |  |
|  | ORD>= | ORD>= | $\bigcirc$ |  |
| Parallel connection | ORI | ORI | $\bigcirc$ |  |
| Out instruction | OUT | OUT | $0^{*}$ |  |
| Training edge output | PLF | PLF | $\bigcirc$ |  |
| Leading edge output | PLS | PLS | $\bigcirc$ |  |
| Print ASCII code instruction | PR | OUT SM1255 | $\times$ | Section 7.2.2 (3) |
| Print comment instruction | PRC | OUT SM1255 | $\times$ | Section 7.2.2 (3) |
| Left rotation of 16-bit data | RCL | RCL | $\bigcirc$ | Section 7.7.7 |
|  | RCLP | RCLP | $\bigcirc$ | Section 7.7.7 |
| Right rotation of 16-bit data | RCR | RCR | $\bigcirc$ | Section 7.7.7 |
|  | RCRP | RCRP | 0 | Section 7.7.7 |
| Return from subroutine program | RET | RET | $\bigcirc$ |  |
| Remote I/O station data read | RFRP | OUT SM1255 | $\times$ | Section 7.2.2 (3) |
| Left rotation of 16-bit data | ROL | ROL | $\bigcirc$ | Section 7.7.7 |
|  | ROLP | ROLP | 0 | Section 7.7.7 |
| Right rotation of 16-bit data | ROR | ROR | $\bigcirc$ | Section 7.7.7 |
|  | RORP | RORP | $\bigcirc$ | Section 7.7.7 |
| Bit device reset | RST | RST | $\bigcirc$ |  |
| Remote I/O station data write | RTOP | OUT SM1255 | $\times$ | Section 7.2.2 (3) |
| Pointer branch instructions | SCJ | SCJ | $\bigcirc$ |  |
| 7 segments decode | SEG | SEG | $\bigcirc$ |  |
| Partial refresh | SEG | SEG | $\times$ | Section 7.7.7 |
| 16-bit data search | SER | SER | $\bigcirc$ | Section 7.7.7 |
|  | SERP | SERP | 0 | Section 7.7.7 |
| Bit device set | SET | SET | $\bigcirc$ |  |
| n -bit shift to left of 16-bit data | SFL | SFL | $\bigcirc$ |  |
|  | SFLP | SFLP | 0 |  |
| n -bit shift to right of 16-bit data | SFR | SFR | $\bigcirc$ |  |
|  | SFRP | SFRP | $\bigcirc$ |  |
| Bit device shift | SFT | SFT | $\bigcirc$ |  |
|  | SFTP | SFTP | $\bigcirc$ |  |
| Set and rest of status latch | SLT | OUT SM1255 | $\times$ | Section 7.2.2 (3) |
|  | SLTR | OUT SM1255 | $\times$ | Section 7.2.2 (3) |
| Carry flag set | STC | OUT SM1255 | $\times$ | Section 7.2.2 (3) |
| Sequence program stop | STOP | STOP | 0 |  |
| Set and reset of sampling trace | STRA | OUT SM1255 | $\times$ | Section 7.2.2 (3) |
|  | STRAR | OUT SM1255 | $\times$ | Section 7.2.2 (3) |

[^4]$\bigcirc$ : Automatic converted, $\triangle$ : Partially changed, $\times$ : Manual conversion required

| Description | A0J2HCPU | Basic Model QCPU |  | Reference sections |
| :---: | :---: | :---: | :---: | :---: |
|  | Instruction name | Instruction name | Convertibility |  |
| 16-bit data check | SUM | SUM | $\bigcirc$ |  |
|  | SUMP | SUMP | $\bigcirc$ |  |
| Microcomputer program call | SUB | OUT SM1255 | $\times$ | Section 7.2.2 (2) |
|  | SUBP | OUT SM1255 | $\times$ | Section 7.2.2 (2) |
| 1-word write to intelligent function/special function modules | TO | TO | ${ }^{* 1}$ |  |
|  | TOP | TOP | - *1 |  |
| 4-bit linking of 16-bit data | UNI | UNI | $\bigcirc$ |  |
|  | UNIP | UNIP | $\bigcirc$ |  |
| Logical products with 16-bit data | WAND | WAND | $\bigcirc$ |  |
|  | WANDP | WANDP | $\bigcirc$ |  |
| WDT reset | WDT | WDT | $\bigcirc$ |  |
|  | WDTP | WDTP | $\bigcirc$ |  |
| Logical sums of 16-bit data | WOR | WOR | $\bigcirc$ |  |
|  | WORP | WORP | $\bigcirc$ |  |
| 16-bit non-exclusive logical sum operations | WXNR | WXNR | $\bigcirc$ |  |
|  | WXNRP | WXNRP | $\bigcirc$ |  |
| 16-bit exclusive logical sum operations | WXOR | WXOR | $\bigcirc$ |  |
|  | WXORP | WXORP | $\bigcirc$ |  |
| 16-bit data conversion | XCH | XCH | $\bigcirc$ |  |
|  | XCHP | XCHP | $\bigcirc$ |  |

[^5]
### 7.2.2 Instruction that may need replacement from A0J2HCPU to Basic model QCPU

Some instructions are not automatically converted during the replacement from A0J2HCPU to QCPU. The following table lists such instructions together with the corrective actions. Reviewing the program referring to the following is recommended.

| Item No. | Instruction type |  | A0J2HCPU <br> instruction name | Corrective actions |
| :---: | :---: | :---: | :---: | :---: |
| (1) | Sequence instructions | Bit device output reverse | CHK | (Counter Measures) <br> Review the program and convert manually. <br> (Supplement) <br> Candidate instruction to convert to: "FF" instruction |
| (2) | Basic instruction | Program switching instruction | SUB | (Counter Measures) <br> Change manually to the same instructions of the $Q$ series. |
|  |  |  | SUBP |  |
| (3) | Application instructions |  | ASC | (Counter Measures) <br> Review the program and convert manually. <br> (Supplement) <br> Candidate instruction to convert to: "\$MOV" instruction |
|  |  | Access instructions to local stations and remote I/ O stations on MELSECNET(II),/B | LRDP | (Counter Measures) <br> Create new programs for the network modules used by the Basic Model QCPU. |
|  |  |  | LWTP |  |
|  |  |  | RFRP |  |
|  |  |  | RTOP |  |
|  |  | Special format failure checks | CHK | (Counter Measures) <br> Replace the instruction by using an alternative program. |
|  |  | Status latch instruction | SLT | (Counter Measure) <br> There is no alternative action. |
|  |  |  | SLTR |  |
|  |  | Sampling trace instructions | STRA | (Counter Measures)Review the program and convert manually.(Supplement)Candidate instructions to convert to "STRA" $\rightarrow$ "TRACE"instructions"STRAR" $\rightarrow$ "TRACER" instructions |
|  |  |  | STRAR |  |
|  |  | Carry flag instructions | STC | (Counter Measures)Review the program and convert manually.(Supplement)Candidate instructions to convert to: "STC" $\rightarrow$ "SET SM700"instructions"CLC" $\rightarrow$ "RST SM700" instructions |
|  |  |  | CLC |  |
|  |  | Print ASCII code instruction | PR | (Counter Measures) <br> Replace the instruction by using an alternative program. ${ }^{* 1}$ |
|  |  | Print comment instruction | PRC |  |
| *1 For details, FA-A-00 |  | fer to the following. <br> 8 Precautions for replacing A | /QnA (large typ | series CPU with Universal model QCPU |

### 7.3 Precautions for Parameter Replacement

This section explains the conversion of the parameter when replacing the A0J2HCPU program with QCPU.
<Compatibility>
O: Common items of the A0J2HCPU and QCPU, and converted without any changes.
$\Delta$ : Items that needs resetting after conversion due to partial differences in functions/specifications.
$x$ : Items to be deleted because there are no common items between the A0J2HCPU and QCPU.
Confirm the contents after conversions, and correct/reset if necessary.

| Name |  |  | Compatibility | Remarks |
| :---: | :---: | :---: | :---: | :---: |
|  | Sequence program capacity |  | $\triangle$ | No attention is required to the program capacity. |
|  | Microcomputer program capacity |  | $\times$ | A microcomputer program is not available. |
|  | Comment capacity |  | $\triangle$ | No attention is required to the comment capacity. |
|  | File register capacity |  | $\triangle$ | Resetting is necessary as the specifications differ. |
|  | WDT settings |  | $\triangle$ | Becomes the default value (200ms). |
|  | Operation mode during errors |  | $\triangle$ | Becomes default (stop all). |
| $\begin{aligned} & \varepsilon \\ & \stackrel{\varepsilon}{\omega} \\ & \vdots \\ & \omega \\ & 0 \\ & 0 \end{aligned}$ | RUN-PAUSE contact |  | $\triangle$ | Resetting is necessary. |
|  | STOP $\rightarrow$ RUN output mode |  | $\triangle$ | Becomes default (output before STOP). |
|  | Interrupt counter settings |  | $\triangle$ | Resetting is necessary. |
| I/O assignment |  |  | $\triangle$ | Resetting is necessary as the specifications differ. |
| \& | Number of device points |  | $\bigcirc$ | Becomes the number of default points. <br> As the number of default points exceeds the number of A0J2HCPU points, program correction is unnecessary. |
|  |  | Latch relay (L) | $\bigcirc$ | $M$ and $L$ are separate devices. <br> "L" on the program is converted just as "L". |
|  |  | Data register (D) | $\bigcirc$ |  |
|  |  | Link relay (B) | $\bigcirc$ |  |
|  |  | Link register (W) | $\bigcirc$ |  |
|  |  | Low speed timer ( T ) High-speed timer (T) | $\triangle$ | Converted as a single device. <br> As the latch range is the entire range from the minimum device number to the maximum device number, it must be reviewed. |
|  |  | Retentive timer (ST) | $\bigcirc$ |  |
|  |  | Counter (C) | $\bigcirc$ |  |
|  | MELSECNET(II), /B |  | $\times$ | The parameter is deleted as it is not compatible with MELSECNET(II), /B. |

### 7.4 Special Relay Replacement

The special relay is an internal relay with applications determined within the programmable controller. This section explains the replacement of the special relay when replacing the A0J2HCPU program with QCPU.
For details on AnS/QnASCPU and QCPU special relays, refer to the QCPU User's Manual (Function Explanation, Program Fundamentals) and MELSEC-Q/L Programming Manual (Common Instruction).

### 7.4.1 Replacement of A0J2HCPU with QCPU

A special relay different from A0J2HCPU is used with QCPU.
Automatic conversion using the Change PLC type is possible when replacing the A0J2HCPU special relay (M9000 onwards) with the Basic Model QCPU special relay (SM). (Refer to Section 7.1.2)

## XPoint

Some A0J2HCPU special relays are not compatible with QCPU. The special relay not compatible with QCPU is converted to a dummy special relay (SM1255) ${ }^{* 1}$ when changing the PLC type. After changing the PLC type, retrieve the dummy special relay (SM1255) and correct the program if necessary.

### 7.5 Special Register Replacement

The special register is an internal register with applications determined within the programmable controller.
This section explains the replacement of special registers when replacing the A0J2HCPU program with QCPU.
For details on AnS/QnASCPU and QCPU special registers, refer to the QCPU User's Manual (Function Explanation, Program Fundamentals) and MELSEC-Q/L Programming Manual (Common Instruction).

### 7.5.1 Replacement of A0J2HCPU with QCPU

A special register different from A0J2HCPU is used with QCPU.
Automatic conversion using the Change PLC type is possible when replacing the A0J2HCPU special register (D9000 onwards) with QCPU special register (SD). (Refer to Section 7.1.2)

## 囚Point

Some A0J2HCPU special registers are not compatible with QCPU. The special register not compatible with the Basic model QCPU is converted to a dummy special register (SD1255) ${ }^{* 1}$ when changing the PLC type. After changing the PLC type, retrieve the dummy special register (SD1255) and correct the program if necessary.

### 7.6 Precautions for Replacing MELSAP-II with MELSAP3

Although the basic operations of the MELSAP3 are same as MELSAP-II, some specifications differ. This section explains items that require special cautions when replacing.

### 7.6.1 How to start the SFC program

The SFC program is started by using a special relay for SFC program start/stop.
The special relay (M9101) for SFC program start/stop of A0J2HCPU is replaced with the special relay (SM321) for SFC program start/stop of the Basic Model QCPU during the conversion from A0J2HCPU to QCPU.
Note that some specifications of the special relay for SFC program start/stop differ between A0J2HCPU and Basic Model QCPU.

| Specifications |  | Precautions for replacement |
| :---: | :--- | :--- |
| MELSAP-II(M9101) | MELSAP3(SM321) | To start or stop the SFC program <br> according to user conditions, turning <br> SM321 ON/OFF using the program is <br> Tequired. |

### 7.6.2 Block information (Information device for SFC)

The procedure to execute "Block START/STOP" and "Reading of the number of active steps and active step numbers", which use the block information (information device for SFC), differ between the MELSAP-II and MELSAP3.

|  | Specifications |  | Precautions for replacement |
| :---: | :---: | :---: | :---: |
|  | MELSAP-II | MELSAP3 |  |
| How to execute <br> Block START/ <br> STOP methods | [START] <br> Turn the block active bit ON to execute forced start. <br> [STOP] <br> Turn the block clear bit ON to stop, and turn it OFF to execute forced termination. | [START] <br> Turn the Block START/STOP bit ON to forcibly start the corresponding block. [STOP] <br> Turn the Block START/STOP bit OFF to forcibly terminate the corresponding block. | [START] <br> As "Block START/STOP bit" replaces "Block active bit" when the SFC program of A0J2HCPU is replaced with QCPU, the program correction is not required. <br> [STOP] <br> For the "Block clear bit", add a program that resets the "Block START/STOP bit". Delete the program that turns the block clear bit ON/OFF. |
| Reading the number of active steps and active step numbers | The number of active steps and active step numbers of the block can be read. | Only the number of active steps of the block can be read. | Use "Active step batch readout instructions (MOV, DMOV, BMOV)" to read active step numbers. |

### 7.6.3 Specifications comparison between MELSAP-II and MELSAP3

A part of the specifications of SFC program (MELSAP3) are different from those of SFC program (MELSAP-II).
Therefore, when utilizing the SFC program (MELSAP-II) of A0J2HCPU as the SFC program (MELSAP3) of QCPU, select the QCPU that meets the specifications of the existing SFC program (MELSAP-II).

| Contents | MELSAP-II | MELSAP3 |  |
| :---: | :---: | :---: | :---: |
|  | A0J2HCPU | QnUCPU |  |
|  |  | Q00UJCPU Q00UCPU | $\begin{aligned} & \text { Q03UDVCPU } \\ & \text { Q03UD(E)CPU } \end{aligned}$ |
| SFC block | Max. 256 | Max. 128 | Max. 320 |
| Number of SFC steps | Max. 255 steps/block | Max. 128 steps/block | Max. 512 steps/block |
| Step transition monitoring timer | Equipped (8 timers) | None | None |

### 7.6.4 SFC diagram that cannot be read normally in another format

SFC diagram created by SWDIVD/NX-GPPA may cause an error such as incorrect reading.
Add dummy steps before replacement with SWロIVD/NX-GPPA.
(Refer to "PRECAUTIONS FOR CREATING SFC PROGRAMS" in the GX Developer Version 8 Operating Manual (SFC).)
(Example)



## 7．7 Precautions for Program Replacement

## 7．7．1 Applicable devices list

| Device name |  |  | Q00U（J）CPU | A0J2HCPU |
| :---: | :---: | :---: | :---: | :---: |
| Number of I／O points ${ }^{*} 8$ |  |  | Q00UJCPU： 256 points Q00UCPU： 1024 points | 480 points |
| Number of I／O device points＊${ }^{*}$ |  |  | 8192 points | 512 points |
| Internal relay |  |  | 8192 points＊1 | Total of 2048 points |
| Latch relay |  |  | 8192 points |  |
| Step relay | For sequence program |  | － |  |
|  | For |  | 8192 points | － |
| Annunciator |  |  | 2048 points＊1 | 256 points |
| Edge relay |  |  | 2048 points＊1 | － |
| Link relay |  |  | 8192 points＊1 | 1024 points |
| Special link relay |  |  | 2048 points | 56 points |
| Timer |  |  | 2048 points＊${ }^{*}$ | Total of 256 points |
| Retentive timer |  |  | 0 points＊1 |  |
| Counter |  |  | 1024 points＊1 | 256 points |
| Data register |  |  | 12288 points＊1 | 1024 points |
| Link register |  |  | 8192 points＊1 | 1024 points |
| Special link register |  |  | 2048 points＊1 | 56 points |
| Function inputs |  |  | 16 points（FX0 to FXF）＊6 | － |
| Function outputs |  |  | 16 points（FY0 to FYF）＊6 | － |
| Special relay |  |  | 2048 points | 256 points |
| Function registers |  |  | 5 points（FD0 to FD4） | － |
| Special register |  |  | 2048 points | 256 points |
| Link direct device |  |  | Specified with Jप\ロロ | － |
| Special direct device |  |  | Specified with Uप\G口 | － |
| Index register |  | Z | 10 points（Z0 to Z9） | 1 point（Z） |
|  |  | $\mathrm{V}^{*}$ | － | 1 point（V） |
| File register |  |  | $\begin{gathered} 32768 \text { points/block }{ }^{* 5} \\ (\text { R0 to } \mathrm{R} 32,767) \end{gathered}$ | 4096 points（R0 to R4095） |
| Accumulator＊3 |  |  | － | 2 points |
| Nesting |  |  | 15 points | 8 points |
| Pointer |  |  | 512 points | 256 points |
| Interrupt pointer |  |  | 128 points | 32 points |
| SFC block |  |  | 128 blocks＊${ }^{*}$ | 256 blocks |
| Number of SFC steps |  |  | Max． 128 steps／block | － |
| Dec．constant |  |  | K－2147483648 to K2147483647 | K－2147483648 to K2147483647 |
| Hex．constant |  |  | H0 to HFFFFFFFF | H0 to HFFFFFFFF |
| Real constant ${ }^{*} 6$ |  |  | $\mathrm{E} \pm 1.17550-38$ to $\mathrm{E} \pm 3.40282+38$ | － |
| Character string |  |  | ＂QnACPU＂，＂ABCD＂＊4 | － |

＊1 The parameter can change the used points．
＊2 $\quad V$ is used as the edge relay for QCPU．
＊3 The A0J2HCPU instruction using the accumulator is changed in the instruction format on QCPU．
＊4 QCPU can only be used with the \＄MOV instructions．
＊5 The Q00UJCPU does not have file registers．
＊6 Only the five points of FX0 to FX4 and FY0 to FY4 can be used on the program．
＊7 Applicable number of points on the program．
＊8 Number of accessible points with actual I／O modules．

### 7.7.2 I/O control method

|  |  |  | O: Usable, -: Not used |
| :---: | :---: | :---: | :---: |
| I/O control method |  | Qnucpu | A0J2HCPU |
| Refresh method |  | $\bigcirc$ | -*1 |
| Direct I/O method | Partial refresh instruction | $\bigcirc$ | $\bigcirc$ |
|  | Direct access input | $\bigcirc$ | - |
|  | Direct access output | $\bigcirc$ | - |
| Direct mode |  | - | O* |

*1 To switch between the refresh method and the direct method, use the DIP switch of the A0J2HCPU.

### 7.7.3 Data formats that can be used by the instructions

| Set data |  | QnUCPU | A0J2HCPU |
| :---: | :---: | :---: | :---: |
| Bit data | Bit device | $\bigcirc$ | $\bigcirc$ |
|  | Word device | (Bits need to be specified) | - |
| Word data | Bit device | (Digits need to be specified) | (Digits need to be specified) |
|  | Word device | $\bigcirc$ | $\bigcirc$ |
| Double word data | Bit device | (Digits need to be specified) | (Digits need to be specified) |
|  | Word device | $\bigcirc$ | $\bigcirc$ |
| Real number data |  | $\bigcirc$ | $\Delta^{* 1}$ |
| Character string data |  | $\bigcirc$ | - |

*1 The microcomputer package for the floating point real number type of the SWOSRXV-FN2UP package can be used during entry.

### 7.7.4 Timer

| Function |  | QnUCPU | A0J2HCPU |
| :---: | :---: | :---: | :---: |
| Low-speed timer | Measurement units | -100ms(Default value) <br> Can be changed within a range of 1 to 1000ms (parameter) | - Fixed at 100 ms |
|  | Specifying method |  |  |
| High-speed timer | Measurement units | - 10ms(Default value) Can be changed within a range of 0.1 to 100 ms (parameter). | - Fixed at 10 ms |
|  | Specifying method |  |  |
| Retentive timer | Measurement units | - Same measurement units as the low-speed timer. | - Fixed at 100 ms |
|  | Specifying method |  |  |
| High-speed retentive timer | Measurement units | - Same measurement units as the high-speed timer. |  |
|  | Specifying method |  | - None |
| Setting range of set value |  | - 1 to 32767 | - 1 to 32767 |
| Processing set value 0 |  | - Momentarily ON | - Infinite (No times out) |
| Updating current value |  | - At execution of OUT Tn instruction | - At END processing |
| Turning contacts ON/OFF |  |  |  |

## (1) Precautions for using the timer

For details on precautions for using the timer, refer to the QCPU User's manual (Function Explanation, Program Fundamentals).

## (a) Programming method of the timer ladder on QCPU

Specify the number of points of the timer and retentive timer in the device settings of the parameter.
The appropriate uses of the low-speed timer, high-speed timer, retentive timer, and high-speed retentive timer are programmed by adding an " H " or " S " to the OUT instructions.

| Example Low-speed timer : | OUT | T0 | Kn |
| ---: | :--- | :--- | :--- |
| High-speed timer : | OUTH | T0 | Kn |
| Low-speed retentive timer : | OUT | ST0 | Kn |
| High-speed retentive timer : | OUTH | ST0 | Kn |

## (b) Programming method of the timer ladder on the AOJ2HCPU

In the device settings of the parameter, specify the total number of timer points and the head device numbers of the low-speed timer, high-speed timer, and retentive timer.
The default values are; number of points: 256 points (fixed); low-speed timer head: 0 (T0 to T199); high-speed timer head: 200 (T200 to T255); and retentive timer: 0 points.
When using retentive timers, change the settings to secure the necessary number of points.

### 7.7.5 Counter

| Function | QnUCPU | A0J2HCPU |
| :---: | :---: | :---: |
| Specifying method |  |  |
| Updating current value <br> Turning contacts ON/OFF | - At execution of OUT Cn instruction | - At END processing |

### 7.7.6 Display instruction

| Instruction | QnUCPU | A0J2HCPU |
| :---: | :---: | :---: |
| PR | Display instructions cannot be used for a Universal model QCPU. <br> Consider replacing with a display unit or touch | - With M9049 OFF: Outputs characters before $0^{0}$ H. <br> - With M9049 ON: Outputs 16 characters. |
| PRC | panel. | - Outputs comment in 16 characters. |

### 7.7.7 Instructions with changed specified formats

As QCPU does not have an accumulator (A0, A1), the A0J2HCPU instruction using the accumulator is changed in the instruction format on the Basic Model QCPU.
Accumulator A0 is changed to SD718 and accumulator A1 is changed to SD719.

| Function | QCPU |  | A0J2HCPU |  |
| :---: | :---: | :---: | :---: | :---: |
|  | Instruction format | Remarks | Instruction format | Remarks |
| 16-bit data search Rotating 32-bit data to right | ROR D n | - D: Rotation data | ROR n | - Set rotation data to A0 |
|  | $R C R$ D n | - D: Rotation data <br> - Use SM700 for a carry flag | RCR n | - Set rotation data to A0 <br> - Use M9012 for a carry flag |
| 16-bit data search Rotating 32-bit data to left | ROL D n | - D: Rotation data | ROL n | - Set rotation data to A0 |
|  | $R \mathrm{RCL}$ D n | - D: Rotation data <br> - Use SM700 for a carry flag | - RCL n - | - Set rotation data to A0 <br> - Use M9012 for a carry flag |
| 32-bit data search Rotating 32-bit data to right | DROR D n | - D: Rotation data | DROR n | - Set rotation data to A0 and A1 |
|  | DRCR D n | - D: Rotation data <br> - Use SM700 for a carry flag | DRCR n | - Set rotation data to A0 and A1 <br> - Use M9012 for a carry flag |
| 32-bit data search Rotating 32-bit data to left | DROL D n | - D: Rotation data | - DROL n - | - Set rotation data to A0 and A1 |
|  | DRCL D n | - D: Rotation data <br> - Use SM700 for a carry flag | DRCL n | - Set rotation data to A0 and A1 <br> - Use M9012 for a carry flag |


| Function | QCPU |  | A0J2HCPU |  |
| :---: | :---: | :---: | :---: | :---: |
|  | Instruction format | Remarks | Instruction format | Remarks |
| 16-bit data search | SER S1 S2 D n | - Stores search results to D and D+1 devices | SER S1 S2 n | - Stores search results to A0 and A1 |
| 32-bit data search | DSER S1 S2 D n | - Stores search results to D and D+1 devices | DSER S1 S2 n | - Stores search results to A0 and A1 |
| Bit check on 16-bit data | SUM S D | - Stores check results to $D$ device | SUM S | - Stores check results to A0 |
| Bit check on 32-bit data | DSUM S D | - Stores check results to $D$ device | - DSUM S $^{-1}$ | - Stores check results to A0 |
| Partial refresh | RFS D n | - Dedicated instruction added | SEG D n | - Only when M9052 is ON ${ }^{*}$ |
| ASCII conversion of 8 characters | $-\mathrm{SMOV}^{(\text {Charactor stings) }) \mathrm{D}}$ |  | $-$ASC (Charactor strings) D | *2 |
| Carry flag set | $\begin{array}{\|l\|l\|} \hline \text { SET } & \text { SM700 } \\ \hline \end{array}$ | - No dedicated instruction | STC | *2 |
| Carry flag reset | $\begin{array}{\|l\|l\|} \hline \text { RST } & \text { SM700 } \\ \hline \end{array}$ | - No dedicated instruction | CLC | *2 |
| Jump to END instruction | GOEND | - Dedicated instruction added | CJ P255SCJ P255JMP P255 | - P255:Specified END instruction ${ }^{* 2}$ |

[^6]
### 7.7.8 Index register

## (1) Replacing index register

" $Z, \mathrm{~V}$ " and "Z0 to Z9" are used as index register for the A0J2HCPU and QCPU, respectively. Therefore, their specifications differ.
" V " is used as edge relay for QCPU. The device is used to memorize the PLS/PLF information to contacts from the start of the ladder block.
The following table shows replacement of index register when A0J2HCPU program was utilized to QCPU with "Change PLC type".

| QCPU | A0J2HCPU |
| :---: | :---: |
| $Z 0$ | Z |
| Z 7 | V |

## (2) Index register 32-bit specification

When using index register as 32-bit instruction in the A0J2HCPU, $Z$ and $V$ that has the same number with $Z$ are processed as low-order 16-bit value and high-order 16-bit value, respectively.
However, QCPU processes Zn and $\mathrm{Zn}+1$ as low-order 16 bits and high-order 16 bits, respectively. If a program to which "Change PLC type" is performed includes index register with 32-bit specification, reviewing the index register after "Change PLC type" is necessary.
The following shows an example using an instruction whose operation result will be in 32 bits.

| Instruction | QCPU | A0J2HCPU |
| :---: | :---: | :---: |
| DMOV D0 Z | Z1, Z0 | V, Z |
|  | (High order) (Low order) | (High order) (Low order) |
| / D0 D1 Z | Z0 (Quotient) | Z (Quotient) |
|  | Z1 (Remainder) | (Remainder) |

When utilizing the A0J2HCPU program to QCPU with "Change PLC type", the operation result may be stored to the index register having different number as intended one.
(Example)


Device replaced with "Change PLC type".
Modify this to Z1.

### 7.7.9 Setting method when multiple sequence programs are created

For the A0J2HCPU, some programs include main program and subprogram, and main programs have SFC programs. When replacing those programs with the QCPU, they are separated into different programs.
For the separated programs in the QCPU, the Program setting of the parameter setting is required. This section provides precautions after replacement of program settings, etc.
(1) Program files at replacement
(a) When main program contains SFC program

For the A0J2HCPU, the SFC program operates as the microcomputer program of main program. Since the QCPU deals the SFC program as one program, the SFC program is converted to "MAINSFC". Accordingly, two separate programs are created when the ACPU is converted; "MAIN", converted from main program, and "MAIN-SFC".
Register in the order of MAIN, MAIN-SFC in the Program setting of the parameter setting of GX Developer, and set all execution types to "Scan". *1
Refer to Section 7.6 for precautions of replacing from the A0J2HCPU SFC (MELSAP-II) to the QCPU (MELSAP3).


## (2) Program setting of the GX Developer

The following explains required program settings for executing multiple programs.
The execution type of program is set in Program setting of the PLC parameter setting of the GX Developer.
CPU module executes the programs of the specified execution type in the setting order.

(a) Program name

Set a name for a program to be executed with a CPU module.
(b) Execution type

Select the execution type of files set in the program name.

1) Initial execution type (Initial)

This type of programs is executed only one time, when switching the power supply from off to on or STOP status to RUN status.
2) Scan execution type (Scan)

This type of programs is executed every scan, after having executed the initial execution type program.
3) Stand-by type (Wait)

This type of program is executed only when demanded.
4) Fixed scan execution type (Fixed scan)

This type of program is executed per interval set in the "Fixed scan interval" and "In unit".

- Fixed scan interval

Sets the program execution interval of fixed execution type program.
Setting range depends on the unit set in the fixed scan interval.

- For "ms": 0.5 to 999.5 ms ( 0.5 ms unit)
- For "s": 1 to 60s (1s unit)
- Unit

Selects the unit ("ms" or "s") for the fixed scan interval.

### 7.7.10 Precautions for file register replacement

Refer to the following notes in case where the file register is used when replacing A0J2HCPU with QCPU.
(1) Storage location and maximum number of points after replacement

|  | Q00(U)JCPU | Q00(U)/Q01(U)CPU | A0J2HCPU |
| :---: | :---: | :---: | :---: |
| Storage location | Not used | Standard RAM | Program memory |
| Maximum number of points |  | 128k points | 4K points |
| Number of points per block |  | 32k points | - |

(2) Operation after replacement

Create the device memory file and write the file register file to the programmable controller using GX Developer.

### 7.7.11 Boot operation method (storing the program to ROM)

The ROM operation of the A0J2HCPU changes to the boot operation of QCPU.
The Universal Model QCPU does not have to perform the boot operation since its program memory is a Flash ROM.
(The data written to files are not erased even if a battery error occurs.)

## - REPLACING THE COMMUNICATION MODULES

### 8.1 List of Alternative Communication Module Models

| A0J2H models to be discontinued |  | Q series alternative models |  |
| :---: | :---: | :---: | :---: |
| Product name | Model name | Model name | Remarks (restrictions) |
| Computer link module/ multidrop link (Master station) | A0J2-C214S1 | QJ71C24N <br> QJ71C24N-R2 <br> QJ71C24N-R4 | - Only the computer link function can be replaced. ${ }^{* 1}$ <br> The multidrop link function cannot be replaced. ${ }^{* 2}$ <br> - Select the model compatible with the communication interface being used. <br> - The sequence program is not compatible, so a change is required. |
| Multidrop link (Remote station) | A0J2C25 | None | No substitutions ${ }^{2}$ |

*1 $\quad 1$ Select the $Q$ series alternative model compatible with the communication interface being used.
(1) AOJ2H Models to be discontinued

|  | CH1 | CH2 |
| :--- | :--- | :--- |
| A0J2-C214-S1 | RS-232 | RS-422 |

(2) $Q$ series alternative models

|  | CH1 | CH2 |
| :--- | :--- | :--- |
| QJ71C24N | RS-232 | $R S-422 / 485$ |
| QJ71C24N-R2 | RS-232 | RS-232 |
| QJ71C24N-R4 | RS-422/485 | $R S-422 / 485$ |

*2 Replacement of the multidrop link
The $Q$ series communication modules do not have the multidrop link function.
Therefore, consider the replacement methods as follows.
(1) When the remote and local stations are all replaceable with CC-Link

The following figure shows a configuration example when the remote and local stations are all replaceable with CC-Link.
*Example of existing configuration

*Example of CC-Link replacement configuration

(2) When a product that cannot be replaced with CC-Link is included in the existing configuration

1) When a multidrop link module (A1SJ71UC24-R4) remains to be used as a spare

When a product from a partner manufacturer that cannot be replaced with CC-Link is included in a local station of the existing configuration, the product and the existing network can be used continuously by using the QA1S extension base unit (QA1S6ロB/QA1S51B) on which the multidrop link module for the master station (A1SJ71UC24-R4) is mounted.
The following figure shows a configuration example when the product from the partner manufacturer and the existing network are continuously used.

* Example of existing configuration

${ }^{\text {* }}$ Example of when the master station is replaced with the Q series
Station after replacement
with the $Q$ series
(by using QA(1S) extension base unit)


Extension cable (QCDB)
*Multidrop link master station*2

*1 This module is required only when two systems are used.
*2 • Base unit: QA1S extension base unit (QA1S6ロB or QA1S51B)

- Power supply module (The QA1S51B is not required.)
- Master module: A1SJ71UC24-R4
(Note that the module was discontinued on September 30, 2014.)

2) When a multidrop link module for the master station (A1SJ71UC24-R4) does not remain to be used as a spare When a product from a partner manufacturer that cannot be replaced with CC-Link is included in a local station of the existing configuration, replacement of the network with the one using $Q$ series modules is difficult.
Keep using all of the existing modules in the existing configuration.
Or replace only the replaceable module(s) with CC-Link and configure two networks.

## XPoint

(1) The I/O module of remote station A0J2C25 is an A0J2 I/O module.

Using the renewal tool for A0J2 (manufactured by Mitsubishi Electric System \& Service Co., Ltd.) for replacement with CC-Link can eliminate the need for mounting hole drilling for the alternative module and enables reuse of I/O external wiring, and thus can reduce replacement man-hours. For details, refer to the Section 1.2.
(2) For a product from a partner manufacturer, ask the partner manufacturer whether they have an alternative product with the equivalent functions and specifications for CC-Link. For contact information, check the website of "CC-Link Partner Association".
(3) When the multidrop link module does not remain to be used as a spare, the replacement of the A0J2-C214S1 is difficult.
(Note that new purchases of existing modules are not possible and the repair acceptance period cannot be extended.)

### 8.2 Specifications Comparison of Communication Modules

### 8.2.1 Performance comparisons of communication module specifications

O: Compatible, $\Delta$ : Partially changed, $x$ : Incompatible


### 8.2.2 Cable specifications comparison

O : Compatible, $\triangle$ : Partially changed, $\times$ : Incompatible

*1 RS-232 or RS-422/485 recommended cables are described in the manual of the Q Series Serial Communication Module.
*2 Use the exclusive connector shell for the cable connected to the $Q$ series serial communication module as described in the module's manual.

### 8.3 Functional Comparisons of Data Modules

O: Compatible, $\Delta$ : Partially changed, $\times$ : Incompatible

| Item |  | Descriptions |  | Compatibility | Precautions for replacement | Reference sections |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | A0J2-C214S1 | $\begin{gathered} \text { QJ71C24N } \\ \text { QJ71C24N-R2 } \\ \text { QJ71C24N-R4 } \end{gathered}$ |  |  |  |
| Dedicated protocol communication | Device memory read, write | Allows the device on the other end to read and write programmable controller CPU data. |  | $\Delta$ | The usable commands, accessible device range, and access to other stations are restricted. This may require program changes of the device on the other end. | Section 8.6 |
| Nonprocedural communication | Data transmission programmable controller $\rightarrow$ device on other end | Transmits data from device on other end to programmable controller CPU. |  | $\triangle$ | Dedicated instruction Change to a sequence program that uses (OUTPUT/INPUT). |  |
|  | Data reception programmable controller $\rightarrow$ device on other end | Receives transmitted data from device on other end. |  | $\Delta$ |  |  |
| Transmission control | DTR/DSR control | Controls data transmission/reception with device on other end via RS-232 control signal. |  | $\bigcirc$ |  |  |
|  | DC code control | Transmits and receives DC code (including Xon/Xoff) and controls data transmission/reception with device on other end. |  | $\bigcirc$ |  |  |

### 8.4 Switch Settings Comparisons

O: Compatible, $\triangle$ : Partially changed, $\times$ : Incompatible

| Switch name |  |  | Descriptions |  |  |  | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  | A0J2-C214S1 |  |  | $\begin{gathered} \text { QJ71C24N } \\ \text { QJ71C24N-R2 } \\ \text { QJ71C24N-R4 } \end{gathered}$ |  |  |
| Mode setting switch |  |  | Mode settings for each interface are made depending on the data communication function being used. |  |  | - | $\triangle$ | Perform switch settings at the intelligent function module switch setting of GX Works2 or the PLC parameter settings of GX Developer. |
|  |  |  |  | RS-232 | RS-422/485 |  |  |  |
|  |  |  | 0 | Not used |  |  |  |  |
|  |  |  | 1 | Dedicated protocol (format 1) | Nonprocedural mode |  |  |  |
|  |  |  | to | to |  |  |  |  |
|  |  |  | 4 | Dedicated protocol (format 4) |  |  |  |  |
|  |  |  | 5 |  | Dedicated protocol (format 1) |  |  |  |
|  |  |  | to | mode | to |  |  |  |
|  |  |  | 8 |  | Dedicated protocol (format 4) |  |  |  |
|  |  |  | 9 | Nonproced | dural mode |  |  |  |
|  |  |  | A | Dedicated prot | ocol (format 1) |  |  |  |
|  |  |  | to |  | o |  |  |  |
|  |  |  | D | Dedicated prot | ocol (format 4) |  |  |  |
|  |  |  | E | Not | used |  |  |  |
|  |  |  | F | Unit loop | back test |  |  |  |
| Station number setting switch |  |  | Sets the module station number that is used for data communication with a dedicated protocol. |  |  | - | $\triangle$ |  |
| Transmission specification setting switch | SW10 | Computer link/ multidrop link selection | Configures the computer link module function being used. |  |  | - | × | The Q series treats all channels equally. |
|  | SW11 | Main channel settings | Specifies interface for transmission processing and reception processing. |  |  | - | $\times$ | The Q series treats all channels equally. |
|  | SW12 | Write during RUN enable/ disable setting | Specifies whether write during RUN operation is enabled or disabled on dedicated protocol data communication. |  |  | - | $\triangle$ | Perform switch settings at the intelligent function module switch setting of GX Works2 or the PLC parameter settings of GX Developer. |
|  | to SW15 | Transmission speed setting | Specifies the transmission speed during data transmission/reception operations. |  |  | - | $\Delta$ |  |
|  | SW16 | Data bit setting | Specifies the data bit length of data to be transmitted/received. |  |  | - | $\Delta$ |  |
|  | SW17 | Parity bit enable/ disable setting | Specifies whether the parity bit exists in data to be transmitted/received. |  |  | - | $\Delta$ |  |
|  | SW18 | Even/odd parity setting | Specifies the type of parity bit added to data to be transmitted/received. |  |  | - | $\Delta$ |  |
|  | SW19 | Stop bit setting | Specifies the stop bit length of data to be transmitted/received. |  |  | - | $\triangle$ |  |
|  | SW20 | Sum check enable/disable setting | Specifies whether there is a sum check code when performing data communication with a dedicated protocol. |  |  | - | $\triangle$ |  |

### 8.5 Program Comparisons

### 8.5.1 I/O signal

I/O signal assignment on the A0J2-C214S1 and the Q series serial communication module is not compatible. Create a new sequence program.

| Signal name |  |  |  |
| :---: | :---: | :---: | :---: |
| Input signal | A0J2-C214S1 | Input signal | QJ71C24N(-R2/R4) |
| X00 | Transmission complete | X00 | CH1 Transmission normal completion |
| X01 | Receive data read request | X01 | CH1 Transmission abnormal completion |
| X02 | Global signal | X02 | CH1 Transmission processing |
| X03 | Use prohibited | X03 | CH1 Reception data read request |
| X04 | C214 Transmission sequence status | X04 | CH1 Reception abnormal detection |
| X05 |  | X05 | (For system) |
| X06 |  | X06 | CH1 Mode switching |
| X07 | Use prohibited | X07 | CH2 Transmission normal completion |
| X08 |  | X08 | CH2 Transmission abnormal completion |
| X09 |  | X09 | CH2 Transmission processing |
| X0A |  | X0A | CH2 Reception data read request |
| XOB |  | XOB | CH2 Reception abnormal detection |
| XOC |  | X0C | (For system) |
| XOD | Watchdog timer error | XOD | CH2 Mode switching |
| X0E | Use prohibited | X0E | CH1 ERR. occurrence |
| X0F |  | XOF | CH2 ERR. occurrence |
| X10 |  | X10 | Modem initialization completion |
| X11 |  | X11 | Dialing |
| X12 |  | X12 | Connection |
| X13 |  | X13 | Initialization/connection abnormal completion |
| X14 |  | X14 | Modem disconnection complete |
| X15 |  | X15 | Notification normal completion |
| X16 |  | X16 | Notification abnormal completion |
| X17 |  | X17 | Flash ROM read completion |
| X18 |  | X18 | Flash ROM write completion |
| X19 |  | X19 | Flash ROM system setting write completion |
| X1A |  | X1A | CH1 Global signal |
| X1B |  | X1B | CH2 Global signal |
| X1C |  | X1C | System setting default completion |
| X1D |  | X1D | Pre-defined protocol ready |
| X1E |  | X1E | Q series C24 ready |
| X1F |  | X1F | Watchdog timer error |


| Signal name |  |  |  |
| :---: | :---: | :---: | :---: |
| Output signal | A0J2-C214S1 | Output signal | QJ71C24N(-R2/R4) |
| Y00 | Use prohibited | Y00 | CH1 Transmission request |
| Y01 |  | Y01 | CH1 Reception data read completion |
| Y02 |  | Y02 | CH1 Mode switching request |
| Y03 |  | Y03 | Use prohibited |
| Y04 |  | Y04 |  |
| Y05 |  | Y05 |  |
| Y06 |  | Y06 |  |
| Y07 |  | Y07 | CH2 Transmission request |
| Y08 |  | Y08 | CH2 Reception data read completion |
| Y09 |  | Y09 | CH2 Mode switching request |
| Y0A |  | YOA | Use prohibited |
| YOB |  | YOB |  |
| YOC |  | YOC |  |
| YOD |  | YOD |  |
| Y0E |  | Y0E | CH1 ERR. clear request |
| YOF |  | Y0F | CH2 ERR. clear request |
| Y10 | Send request | Y10 | Modem initialization request (standby request) |
| Y11 | Receive data read complete | Y11 | Connection request |

REPLACING THE COMMUNICATION MODULES

| Signal name |  |  |  |
| :---: | :---: | :---: | :---: |
| Output signal | A0J2-C214S1 | Output signal | QJ71C24N(-R2/R4) |
| Y12 | Use prohibited | Y12 | Modem disconnection request |
| Y13 |  | Y13 | Use prohibited |
| Y14 |  | Y14 | Notification-issued request |
| Y15 |  | Y15 | Use prohibited |
| Y16 |  | Y16 | Use pronibited |
| Y17 |  | Y17 | Flash ROM read request |
| Y18 |  | Y18 | Flash ROM write request |
| Y19 |  | Y19 | Flash ROM system setting write request |
| Y1A |  | Y1A | Use prohibited |
| Y1B |  | Y1B | Use prohibted |
| Y1C |  | Y1C | System setting default request |
| Y1D |  | Y1D |  |
| Y1E |  | Y1E | Use prohibited |
| Y1F |  | Y1F |  |

### 8.5.2 Buffer memory

Buffer memory assignment on the A0J2-C214S1 and the Q series serial communication module is not compatible.
Initialize settings using the intelligent function module setting of GX Works2 or GX Configurator-SC and create a new sequence program.
The following table shows the main assigned areas for the initial setting and transmission/reception setting at default.

O: Compatible, $\Delta$ : Partially changed, $\times$ : Incompatible

| A0J2-C214S1 |  |  | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: |
| Buffer memory address |  | Buffer memory name |  |  |
| Hexadecimal | Decimal |  |  |  |
| OH | 0 | Nonprocedural send data count storage area | $\triangle$ | Use addresses $400 \mathrm{H}, 800 \mathrm{H}(1024,2048)$ as transmission data count specification areas on the $Q$ series. |
| $\begin{aligned} & \hline 1 \mathrm{H} \\ & \text { to } \\ & 7 \mathrm{FH} \end{aligned}$ | $\begin{aligned} & \hline 1 \\ & \text { to } \\ & 127 \end{aligned}$ | Send data storage area | $\Delta$ | Use addresses from $401 \mathrm{H}, 801 \mathrm{H}(1025,2049)$ as transmission data specification areas on the $Q$ series. |
| 80H | 128 | Nonprocedural receive data count storage area | $\triangle$ | Use addresses $600 \mathrm{H}, \mathrm{A} 00 \mathrm{H}(1536,2560)$ as receive data count storage areas on the Q series. |
| $\begin{aligned} & 81 \mathrm{H} \\ & \text { to } \\ & \text { FFH } \end{aligned}$ | $\begin{array}{\|l\|l} \hline 129 \\ \text { to } \\ 255 \end{array}$ | Receive data storage area | $\triangle$ | Use addresses from $601 \mathrm{H}, \mathrm{A} 01 \mathrm{H}(1537,2561)$ as receive data storage areas on the Q series. |
| 100 H | 256 | Nonprocedural reception end code designation area | $\Delta$ | Use addresses A5H, 145H $(165,325)$ as receive end code designation areas on the Q series. |
| 101H | 257 | Error LED indication area | $\Delta$ | Use addresses 201H, 202H $(513,514)$ for LED or communication error information initialization requests on the Q series. |
| 102H | 258 | Error LED off request area | $\Delta$ | Use addresses $\mathrm{OH}, 1 \mathrm{H}(0,1)$ for LED or communication error information initialization requests on the Q series. |
| $\begin{aligned} & \text { 103H } \\ & \text { to } \\ & 7 \mathrm{FFH} \end{aligned}$ | $\begin{aligned} & 259 \\ & \text { to } \\ & 2047 \end{aligned}$ | User free buffer memory | $\triangle$ | Use addresses C00H to 1AFFH (3072 to 6911) as user free buffer memory areas on the Q series. |

### 8.6 Program Reuse

The following shows the precautions for reusing the existing programs as the $Q$ series serial communication module program when changing modules.

| Item | Target device | Precautions | Remarks |
| :---: | :---: | :---: | :---: |
| Initial settings | Programmable controller CPU side | [Initial settings] Initialize settings using the intelligent function module setting of GX Works2 or GX Configurator-SC. <br> [Delete initial setting program] <br> Delete the initial setting program. | Refer to the Q <br> Corresponding Serial Communication Module User's Manual (Basic). |
| Dedicated protocol communication (MC protocol communication) | Device on the other end | [Access to programmable controller CPU] <br> The usable commands, accessible device range, and access to other stations are restricted. ${ }^{* 1^{*} 2}$ | Refer to the Q <br> Corresponding Serial Communication Module User's Manual (Basic). Refer to the MELSEC Communication Protocol Reference Manual. |
| Nonprocedural communication (Nonprocedural protocol communication) | Programmable controller CPU side | [Data transmission/reception] <br> Change to a sequence program that uses the dedicated instruction (OUTPUT/INPUT). | Refer to the Q Corresponding Serial Communication Module User's Manual (Basic). |
| Others | Programmable controller CPU side | [//O signal assignment] $\mathrm{I} / \mathrm{O}$ signal (X/Y) assignment on the AOJ2H series and the Q series is not compatible. <br> Confirm the I/O signal (X/Y) being used and correct the program. | Refer to the Q <br> Corresponding Serial <br> Communication Module <br> User's Manual (Basic). |
|  | Programmable controller CPU side and device on the other end. | [Buffer memory assignment] <br> Buffer memory assignment on the AOJ2H series and the Q series is not compatible. <br> Confirm the buffer memory and address of the read/write data, and correct the program. |  |

*1 Access only to the programmable controller CPU device memory is possible.
Accessible devices are those in the device range when ACPU common commands are used. Also, the following devices cannot be accessed from a device on the other end.

- Latch relay (L) and Step relay (S)
* For the QCPU, the latch relay (L) and step relay (S) is a separate device from the internal relay (M), but any can be specified and access the internal relay.
- File register (R)
- Special relay (M9000 or later), special register (D9000 or later)
*2 When using non-accessible devices (*1) and access functions outside device memory, use the Q series serial communication module's new commands to gain access.
(Change the program of the device on the other end.)


### 8.7 Other Precautions

The following shows the precautions for replacing the A0J2-C214S1 with the Q series serial communication module.

## (1) Processing time

The A0J2H series and the Q series module have different data communication processing times. For this reason, the data communication timing and related factors are different. Make adjustments as needed to wait time.
Refer to each module's manual for definite processing times.

## (2) Switch settings

When using the Q series serial communication module, always specify the mode, station number, and transmission specifications at the intelligent function module switch setting of GX Works2 or the switch setting of GX Developer.

## (3) Data communication via the RS-422/485 interface

The precautions regarding data communication via the RS-422/485 interface are the same as with the AOJ2H series computer link module.
When the device on the other end receives incorrect data, attach pull-up or pull-down resistor to the device on the other end. Refer to Section 3.3.3 of the Q Corresponding Serial Communication Module User's Manual (Basic).

## REPLACING THE NETWORK SYSTEM

### 9.1 List of Alternative Network System Models

| A0J2H models to be discontinued |  | Q series alternative models |  |
| :---: | :---: | :---: | :---: |
| Product name | Model name | Model name | Remarks (restrictions) |
| MELSECNET data link module | A0J2HCPUP21 | $\begin{aligned} & \text { Q00UCPU+QJ71LP21- } \\ & 25 \end{aligned}$ | It is recommended to change to the MELSECNET/H network system.* |
|  | A0J2HCPUR21 | Q00UCPU+QJ71BR11 |  |
|  | A0J2P25 | QJ72LP25-25 | When replacing remote I/O stations, replace the remote master station with the QCPU as well. <br> For remote I/O stations, replace all of the corresponding modules (including I/O module) with Q series alternative models. |
|  | A0J2R25 | QJ72BR15 |  |

Refer to the "Transition from MELSEC-A/QnA (Large Type), AnS/QnAS (Small Type) Series to Q Series Handbook (Network Modules)" for guidance on exchanging the MELSECNET system to the MELSECNET/H system.

Additionally, use caution on the following points when configuring the MELSECNET/H network system on the Q00UCPU.
(1) The Q00UJCPU, Q00UCPU, and Q01UCPU can only load one network module.

To load two or more network modules, use the Universal model QCPU other than the Q00UJCPU, Q00UCPU, or Q01UCPU.
(2) The Q00UJCPU, Q00UCPU, and Q01UCPU have functions and abilities that are not compatible with the MELSECNET/H network (PLC to PLC network).
Refer to Section 2.2.3 of Q Corresponding MELSECNET/H Network System Reference Manual (PLC to PLC Network) for details.

## 10 <br> REPLACING THE SPECIAL FUNCTION MODULE

### 10.1 List of Alternative Special Function Module Models

| A0J2H series to be discontinued |  | Q series alternative models |  |
| :--- | :---: | :---: | :---: |
| Product name |  |  |  |


| A0J2H series to be discontinued |  | Q series alternative models |  |
| :---: | :---: | :---: | :---: |
| Product name | Model name | Model name | Remarks (restrictions) |
| High-speed |  | QD62 | 1) Change in external wiring: Wiring using the terminal block $\rightarrow$ Wiring using the connector, change in wire size <br> 2) Change in a program: Change in the number of occupied I/O points, I/O signal, and buffer memory address <br> 3) Change in performance specifications: Change in counting speed (2-phase, 7kPPS/1-phase, 10kPPS $\rightarrow$ 200kPPS/100kPPS/10kPPS switch setting) <br> : Review of counting range <br> 24-bit binary ( 0 to 16777215 ) $\rightarrow$ 32-bit signed binary (-2147483648 to 2147483647) <br> 4) Change in functional specifications: No change (Upward compatible) |
| counter module |  | QD62-H02*1 | 1) Change in external wiring: Wiring using the terminal block $\rightarrow$ Wiring using the connector, change in wire size <br> 2) Change in a program: Change in the number of occupied I/O points, I/O signal, and buffer memory address <br> 3) Change in performance specifications: Change in counting speed No change <br> : Review of counting range <br> 24-bit binary ( 0 to 16777215) $\rightarrow$ 32-bit signed binary <br> (-2147483648 to 2147483647) <br> 4) Change in functional specifications: No change (Upward compatible) |
| Positioning module | A0J2-D71 | QD75P2N | 1) Change in external wiring: Due to differences in pin arrangement <br> 2) Change in a program: Due to differences in $X Y / b u f f e r$ memory array <br> 3) Change in performance specifications: Partially different, so re-examination is necessary. <br> 4) Change in functional specifications: Partially different, so re-examination is necessary. |

[^7]Remarks
"Special function module" of the A0J2H series and A series corresponds to "intelligent function module" of the Q series.

### 10.2 Special Function Module Comparison

### 10.2.1 Analog input module comparisons

(1) Specifications comparison of A0J2-68AD and Q68ADV/Q68ADI
(a) Performance specifications comparison

O : Compatible, $\Delta$ : Partially changed, $\times$ : Incompatible


| O: Compatible, $\triangle$ : Partially changed, $\times$ : Incompatible |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Item | A0J2-68AD | Q68ADV | Q68ADI | Compatibility | Precautions for replacement |
| Maximum conversion speed | Maximum $2.5 \mathrm{~ms} /$ channel | $80 \mu \mathrm{~s} /$ channel <br> (When there is temperature drift compensation, $160 \mu \mathrm{~s}$ is added to the time regardless of the number of channels used.) |  | O | With respect to A0J268AD, Q68ADV/I conversion speed increases. For this reason, for noise incorporated at A0J268AD, this noise can be incorporated as an analog signal at the Q68ADVII. In this type of case, use the averaging processing specification to remove the influence |
| Absolute maximum input | Voltage $\pm 15 \mathrm{~V}$ Current $\pm 30 \mathrm{~mA}$ | $\pm 15 \mathrm{~V}$ | $\pm 30 \mathrm{~mA}$ | $\bigcirc$ |  |
| Number of analog input points | 8 channels/module |  |  | $\bigcirc$ |  |
| E ${ }^{2}$ PROM <br> Write count | - | Max. 100000 times |  | $\bigcirc$ |  |
| Insulation method | I/O terminal and programmable controller power supply: Photocoupler <br> Between channels: Non-isolated | Between I/O terminal and programmable controller power supply: Photocoupler <br> Between channels: Non-isolated |  | $\bigcirc$ |  |
| Dielectric withstand voltage | - | Between I/O terminal and programmable controller power supply: 500VAC 1 minute |  | $\bigcirc$ |  |
| Insulation resistance | - | Between I/O terminal and programmable controller power supply: $500 \mathrm{VDC} 20 \mathrm{M} \Omega$ or greater |  | $\bigcirc$ |  |
| Number of occupied I/O points | 64 points <br> (I/O assignment: Special 64 points) | 16 points (I/O assignment: Intelligent 16 points) |  | $\times$ | The number of I/O points changes to 16. |
| Connected terminal | 36-point terminal block | 18-point terminal block |  | $\times$ | Wiring change is required. |
| Applicable wire size | $0.75 \text { to } 2 \mathrm{~mm}^{2}$ <br> (Applicable tightening torque 39 to 59 $\mathrm{N} \cdot \mathrm{cm}$ ) | 0.3 to $0.75 \mathrm{~mm}^{2}$ |  | $\times$ |  |
| Applicable solderless terminal | $\begin{gathered} \text { V1.25-3 V1.25-YS3A } \\ \text { V2-S3 V2-YS3A } \end{gathered}$ | R1.25-3 (A solderless terminal with sleeve cannot be used.) |  | $\times$ |  |
| 5VDC internal current consumption | 0.7A | 0.64A |  | $\bigcirc$ |  |
| Weight | 0.675 kg | 0.19 kg |  | $\Delta$ |  |

## (b) Functional comparisons

| Item | A0J2-68AD | Q68ADV Q68ADI | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: |
| A/D conversion possible/Prohibited setting | The number of channels where A/D conversion is possible/prohibited can be set. By making unused channels conversion prohibited, sampling time can be shortened. | A/D conversion possible/prohibited can be set. By making unused channels conversion prohibited, sampling time can be shortened. | O |  |
| Sampling processing | Analog input value for each channel can have A/D conversion performed, and the digital output value can be output. | Analog input value for each channel can have A/D conversion performed, and the digital output value can be output. | O |  |
| Averaging processing | Averaging processing specified channel A/D conversion is performed according to set times or set processing time, the maximum and minimum times are removed, and the remaining total is averaged and stored in the buffer memory. | Each channel undergoes A/D conversion for an average number of times or set time, and the average value is digitally output. | $\Delta$ | The valid range can be changed. |
| Maximum value/ minimum value hold function | - | The digital output maximum value and minimum value is stored in the module. | - |  |
| Temperature drift compensation function | - | The error depending on the module's ambient temperature conversion can be automatically compensated to increase the conversion accuracy. The temperature drift compensation function (all channel A/D conversion time) $+160 \mu$ s can be implemented. | - |  |
| Resolution mode | - | Depending on the application, the resolution mode switches, and resolution of $1 / 4000$, 1/12000, 1/16000 can be selected. Resolution mode setting can be done for all channels in batch. | - |  |
| Online module change | - | Module can be replaced without stopping the system. | - | The CPU modules that support the online module change are a Process CPU and a Redundant CPU. |

(c) Programmable controller CPU I/O signal comparison

Input signal is different, so the sequence program must be changed.
Refer to the Analog-Digital converter Module User's Manual for details regarding the I/O signals and sequence program.

| A0J2-68AD |  |  |  | Q68ADV/Q68ADI |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Device No. | Signal name | Device No. | Signal name | Device No. | Signal name | Device No. | Signal name |
| X0 | Watchdog timer error | Y0 | Use prohibited | X0 | Module READY | Y0 | Use prohibited |
| X1 | A/D conversion READY | Y1 |  | X1 | Temperature drift compensation flag | Y1 |  |
| X2 | Use prohibited | Y2 |  | X2 | Use prohibited | Y2 |  |
| X3 |  | Y3 |  | X3 |  | Y3 |  |
| X4 |  | Y4 |  | X4 |  | Y4 |  |
| X5 |  | Y5 |  | X5 |  | Y5 |  |
| X6 |  | Y6 |  | X6 |  | Y6 |  |
| X7 |  | Y7 |  | X7 |  | Y7 |  |
| X8 |  | Y8 |  | X8 | High resolution mode condition flag | Y8 |  |
| X9 |  | Y9 |  | X9 | Operating condition setting complete flag | Y9 | Operating condition setting request |
| XA |  | YA |  | XA | Offset gain setting mode condition flag | YA | User range write request |
| XB |  | YB |  | XB | Channel change complete flag | YB | Channel change request |
| XC |  | YC |  | XC | Use prohibited | YC | Use prohibited |
| XD |  | YD |  | XD | Maximum value /Minimum value reset complete flag | YD | Maximum value/ Minimum value reset request |
| XE |  | YE |  | XE | A/D conversion complete flag | YE | Use prohibited |
| XF |  | YF |  | XF | Error occurrence flag | YF | Error clear request |
| X10 |  | Y10 |  |  |  |  |  |
| X11 |  | Y11 |  |  |  |  |  |
| X12 |  | Y12 |  |  |  |  |  |
| X13 |  | Y13 |  |  |  |  |  |
| X14 |  | Y14 |  |  |  |  |  |
| X15 |  | Y15 |  |  |  |  |  |
| X16 |  | Y16 |  |  |  |  |  |
| X17 |  | Y17 |  |  |  |  |  |
| X18 |  | Y18 |  |  |  |  |  |
| X19 |  | Y19 |  |  |  |  |  |
| X1A |  | Y1A |  |  |  |  |  |
| X1B |  | Y1B |  |  |  |  |  |
| X1C |  | Y1C |  |  |  |  |  |
| X1D |  | Y1D |  |  |  |  |  |
| X1E |  | Y1E |  |  |  |  |  |
| X1F |  | Y1F |  |  |  |  |  |

(d) Buffer memory address comparisons

Buffer memory allocation is different, so the sequence program must be changed.
Refer to the Analog-Digital Converter Module User's Manual for details regarding the buffer memory and sequence program.

| A0J2-68AD |  |  | Q68ADV/Q68ADI |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Address (decimal) | Name | Read/Write | Address (decimal) | Name | Read/Write |
| 0 | Number of channels | R/W | 0 | A/D conversion enabled/disabled setting | R/W |
| 1 | Averaging processing specification |  | 1 | CH1 Average time/Average count setting |  |
| 2 | CH1 Average time, count |  | 2 | CH2 Average time/Average count setting |  |
| 3 | CH2 Average time, count |  | 3 | CH3 Average time/Average count setting |  |
| 4 | CH3 Average time, count |  | 4 | CH4 Average time/Average count setting |  |
| 5 | CH4 Average time, count |  | 5 | CH5 Average time/Average count setting |  |
| 6 | CH5 Average time, count |  | 6 | CH6 Average time/Average count setting |  |
| 7 | CH6 Average time, count |  | 7 | CH7 Average time/Average count setting |  |
| 8 | CH7 Average time, count |  | 8 | CH8 Average time/Average count setting |  |
| 9 | CH8 Average time, count |  | 9 | Averaging processing specification |  |
| 10 | CH1 Digital output value | R | 10 | A/D conversion completed flag | R |
| 11 | CH2 Digital output value |  | 11 | CH1 Digital output value |  |
| 12 | CH3 Digital output value |  | 12 | CH2 Digital output value |  |
| 13 | CH4 Digital output value |  | 13 | CH3 Digital output value |  |
| 14 | CH5 Digital output value |  | 14 | CH4 Digital output value |  |
| 15 | CH6 Digital output value |  | 15 | CH5 Digital output value |  |
| 16 | CH7 Digital output value |  | 16 | CH6 Digital output value |  |
| 17 | CH8 Digital output value |  | 17 | CH7 Digital output value |  |
| 18 | Unused area | - | 18 | CH8 Digital output value |  |
| 19 |  |  | 19 | Error code |  |
| 20 |  |  | 20 | Setting range (CH1 to CH 4 ) |  |
| 21 |  |  | 21 | Setting range (CH5 to CH8) |  |
| 22 |  |  | 22 | Offset gain setting mode Offset setting | R/W |
| 23 |  |  | 23 | Offset gain setting mode Gain setting |  |
| 24 |  |  | 24 | System area | - |
| 25 |  |  | 25 |  |  |
| 26 |  |  | 26 |  |  |
| 27 |  |  | 27 |  |  |
| 28 |  |  | 28 |  |  |
| 29 |  |  | 29 |  |  |
| 30 |  |  | 30 | CH1 Maximum value | R/W |
| 31 |  |  | 31 | CH1 Minimum value |  |
| 32 |  |  | 32 | CH 2 Maximum value |  |
| 33 |  |  | 33 | CH2 Minimum value |  |
| 34 | Write data error code | R/W | 34 | CH3 Maximum value |  |
| 35 | - | - | 35 | CH3 Minimum value |  |
| 36 |  |  | 36 | CH4 Maximum value |  |
| 37 |  |  | 37 | CH4 Minimum value |  |
| 38 |  |  | 38 | CH5 Maximum value |  |
| 39 |  |  | 39 | CH5 Minimum value |  |
|  |  |  | 40 | CH6 Maximum value |  |
|  |  |  | 41 | CH6 Minimum value |  |
|  |  |  | 42 | CH7 Maximum value |  |
|  |  |  | 43 | CH7 Minimum value |  |
|  |  |  | 44 | CH8 Maximum value |  |
|  |  |  | 45 | CH8 Minimum value |  |
|  |  |  | 46 | System area | - |
|  |  |  | to |  |  |
|  |  |  | 157 |  |  |
|  |  |  | 158 | Mode movement setting | R/W |
|  |  |  | 159 |  |  |
|  |  |  | 160 | System area | - |
|  |  |  | to |  |  |
|  |  |  | 201 |  |  |


| Q68ADV/Q68ADI |  |  |
| :---: | :---: | :---: |
| Address (decimal) | Name | Read/Write |
| 202 | CH1 Industrial shipment settings offset value | R/W |
| 203 | CH1 Industrial shipment settings gain value |  |
| 204 | CH2 Industrial shipment settings offset value |  |
| 205 | CH2 Industrial shipment settings gain value |  |
| 206 | CH3 Industrial shipment settings offset value |  |
| 207 | CH3 Industrial shipment settings gain value |  |
| 208 | CH4 Industrial shipment settings offset value |  |
| 209 | CH4 Industrial shipment settings gain value |  |
| 210 | CH5 Industrial shipment settings offset value |  |
| 211 | CH5 Industrial shipment settings gain value |  |
| 212 | CH6 Industrial shipment settings offset value |  |
| 213 | CH6 Industrial shipment settings gain value |  |
| 214 | CH7 Industrial shipment settings offset value |  |
| 215 | CH7 F Industrial shipment settings gain value |  |
| 216 | CH8 Industrial shipment settings offset value |  |
| 217 | CH8 Industrial shipment settings gain value |  |
| 218 | CH1 User range setting offset value |  |
| 219 | CH1 User range setting gain value |  |
| 220 | CH2 User range setting offset value |  |
| 221 | CH2 User range setting gain value |  |
| 222 | CH3 User range setting offset value |  |
| 223 | CH3 User range setting gain value |  |
| 224 | CH4 User range setting offset value |  |
| 225 | CH4 User range setting gain value |  |
| 226 | CH5 User range setting offset value |  |
| 227 | CH5 User range setting gain value |  |
| 228 | CH6 User range setting offset value |  |
| 229 | CH6 User range setting gain value |  |
| 230 | CH7 User range setting offset value |  |
| 231 | CH7 User range setting gain value |  |
| 232 | CH8 User range setting offset value |  |
| 233 | CH8 User range setting gain value |  |

### 10.2.2 Analog output module comparison

(1) A0J2-62DA and Q62DAN comparison
(a) Performance specifications comparison

O : Compatible, $\triangle$ : Partially changed, $\times$ : Incompatible


O : Compatible, $\triangle$ : Partially changed, x : Incompatible

| Item |  | A0J2-62DA | Q62DAN | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Absolute maximum output |  | Voltage $\pm 12 \mathrm{~V}$ <br> Current $\pm 28 \mathrm{~mA}$ <br> Caution: With an output protection circuit, the above voltage and current output will not be exceeded. | Voltage $\pm 12 \mathrm{~V}$ <br> Current 21 mA | $\Delta$ | Negative current output is not allowed. |
| Number of analog output points |  | 2 channels/ module |  | $\bigcirc$ |  |
| E2PROM write count |  | - | Max. 100000 times | $\bigcirc$ |  |
| Output short protection |  | - | Available | $\bigcirc$ |  |
| Insulation method |  | Between the output terminal and programmable controller power supply: Photocoupler <br> Between channels: Non-isolated | Between I/O terminal and programmable controller power supply: Photocoupler <br> Between output channels: Non-isolated <br> Between external supply power and analog output: <br> Non-isolated | $\bigcirc$ |  |
| Dielectric withstand voltage |  | - | Between the I/O terminal and programmable controller power supply: 500VAC 1 minute <br> Between external supply power and analog output: 500VAC 1 minute | $\bigcirc$ |  |
| Insulation resistance |  | - | Between the I/O terminal and programmable controller power supply: 500VDC $20 \mathrm{M} \Omega$ or greater <br> Between external supply power and analog output: $500 \mathrm{VDC} 20 \mathrm{M} \Omega$ or greater | $\bigcirc$ |  |
| Number of occupied I/O points |  | 64 points (I/O assignment: Special 64 points) | 16 points (I/O assignment: Intelligent 16 points) | $\times$ | The number of l/O points changes to 16. |
| Connected terminal |  | 36-point terminal block | 18-point terminal block | $\times$ | Wiring change is required. |
| Applicable wire size |  | 0.75 to $2 \mathrm{~mm}^{2}$ <br> (Applicable tightening torque <br> 39 to $59 \mathrm{~N} \cdot \mathrm{~cm}$ ) | 0.3 to $0.75 \mathrm{~mm}^{2}$ | $\times$ |  |
| Applicable solderless terminal |  | $\begin{gathered} \hline \text { V1.25-3 V1.25-YS3A } \\ \text { V2-S3 V2-YS3A } \end{gathered}$ | R1.25-3 (A solderless terminal with sleeve can not be used.) | $\times$ |  |
| 5 VDC internal current consumption |  | 0.55A | 0.33A | $\bigcirc$ |  |
| $\begin{aligned} & \text { External } \\ & \text { supply } \\ & \text { power } \end{aligned}$ | Voltage | 21.6 to 26.4VDC | 24VDC $+20 \%,-15 \%$ Ripple, spike $500 \mathrm{mV}_{\text {P-p }}$ or less | $\bigcirc$ |  |
|  | Current consumption | 0.23A | 0.15A | $\bigcirc$ |  |
|  | Inrush current | 0.6A, 100ms (24VDC) | $2.5 \mathrm{~A}, 250 \mu \mathrm{~s}$ or less | $\triangle$ | Peak current becomes larger. |
| Weight |  | 0.75 kg | 0.19 kg | $\triangle$ |  |

(b) Functional comparisons

| Item | A0J2-62DA | Q62DAN | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: |
| Output HOLD/CLEAR function | - | If programmable controller CPU is in stop state or an error has occurred, the output analog value is retained. | - |  |
| D/A conversion enable/ disable function | - | D/A conversion can be set to be enabled or disabled. | - |  |
| D/A output enable/ disable function | Specifies whether to output the D/A convertered value or the offset value. | Output D/A conversion values can be set to be output in batch, or output an offset value. | $\bigcirc$ |  |
| Synchronous output function | - | Synchronous analog output can be acquired at the programmable controller CPU. | - |  |
| Programmable controller CPU Analog output test in STOP | - | If $\mathrm{CH} \square$ output enable/disable flag at programmable controller CPU STOP is forced ON, D/A converted analog values will be output. | - |  |
| Resolution mode | - | Depending on the application, the resolution mode switches, and resolution of $1 / 4000,1 /$ 12000 , or $1 / 16000$ can be selected. | - |  |
| Online module change | - | Module can be replaced without stopping the system. | - | The CPU modules that support the online module change are a Process CPU and a Redundant CPU. |

(c) Programmable controller CPU I/O signal comparison

I/O signal is different, so the sequence program must be changed.
Refer to the Digital-Analog Converter Module User's Manual for details regarding the I/O signals and sequence program.

| A0J2-62DA |  |  |  | Q62DAN |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Device No. | Signal name | Device <br> No. | Signal name | Device <br> No. | Signal name | Device No. | Signal name |
| X0 | Watchdog timer error | Y0 | Use prohibited | X0 | Module READY | Y0 | Use prohibited |
| X1 | D/A conversion READY | Y1 |  | X1 | Use prohibited | Y1 | CH1 Output enable/ disable flag |
| X2 | Use prohibited | Y2 |  | X2 |  | Y2 | CH2 Output enable/ disable flag |
| X3 |  | Y3 |  | X3 |  | Y3 | Use prohibited |
| X4 |  | Y4 |  | X4 |  | Y4 |  |
| X5 |  | Y5 |  | X5 |  | Y5 |  |
| X6 |  | Y6 |  | X6 |  | Y6 |  |
| X7 |  | Y7 |  | X7 |  | Y7 |  |
| X8 |  | Y8 |  | X8 | High resolution mode condition flag | Y8 |  |
| X9 |  | Y9 |  | X9 | Operating condition setting complete flag | Y9 | Operating condition setting request |
| XA |  | YA |  | XA | Offset gain setting mode condition flag | YA | User range write request |
| XB |  | YB |  | XB | Channel change completed flag | YB | Channel change request |
| XC |  | YC |  | XC | Setting change completed flag | YC | Setting change request |
| XD |  | YD |  | XD | Synchronous output mode condition flag | YD | Synchronous output request |
| XE |  | YE |  | XE | Use prohibited | YE | Use prohibited |
| XF |  | YF |  | XF | Error occurrence flag | YF | Error clear request |
| X10 |  | Y10 |  |  |  |  |  |
| X11 |  | Y11 |  |  |  |  |  |
| X12 |  | Y12 |  |  |  |  |  |
| X13 |  | Y13 |  |  |  |  |  |
| X14 |  | Y14 |  |  |  |  |  |
| X15 |  | Y15 |  |  |  |  |  |
| X16 |  | Y16 |  |  |  |  |  |
| X17 |  | Y17 |  |  |  |  |  |
| X18 |  | Y18 |  |  |  |  |  |
| X19 |  | Y19 |  |  |  |  |  |
| X1A |  | Y1A |  |  |  |  |  |
| X1B |  | Y1B | Output enable |  |  |  |  |
| X1C |  | Y1C | Use prohibited |  |  |  |  |
| X1D |  | Y1D |  |  |  |  |  |
| X1E |  | Y1E |  |  |  |  |  |
| X1F |  | Y1F |  |  |  |  |  |

(d) Buffer memory addresses comparisons

Buffer memory allocation is different, so the sequence program must be changed.
Refer to the Digital -Analog Converter Module User's Manual for details regarding the buffer memory and sequence program.

| A0J2-62DA |  |  | Q62DAN |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Address (decimal) | Name | Read/Write | Address (decimal) | Name | Read/Write |
| 0 | CH1 Digital value | R/W | 0 | D/A conversion enable/disable |  |
| 1 | CH2 Digital value |  | 1 | CH1 Digital value | R/W |
| 2 | CH 1 voltage set value check code |  | 2 | CH2 Digital value |  |
| 3 | CH 2 voltage set value check code |  | 3 | System area | - |
| 4 | CH 1 current set value check code |  | 4 |  |  |
| 5 | CH 2 current set value check code |  | 5 |  |  |
|  |  |  | to |  |  |
|  |  |  | 10 |  |  |
|  |  |  | 11 | CH 1 Set value check code | R |
|  |  |  | 12 | CH 2 Set value check code |  |
|  |  |  | 13 | System area | - |
|  |  |  | to |  |  |
|  |  |  | 18 |  |  |
|  |  |  | 19 | Error code | R |
|  |  |  | 20 | Setting range (CH1 to CH2) |  |
|  |  |  | 21 | System area | - |
|  |  |  | 22 | Offset gain setting mode Offset setting | R/W |
|  |  |  | 23 | Offset gain setting mode Gain setting |  |
|  |  |  | 24 | Offset/gain adjustment value |  |
|  |  |  | 25 | System area | - |
|  |  |  | to |  |  |
|  |  |  | 157 |  |  |
|  |  |  | 158 | Mode movement setting | R/W |
|  |  |  | 159 |  |  |
|  |  |  | 160 | System area | - |
|  |  |  | to |  |  |
|  |  |  | 199 |  |  |
|  |  |  | 200 | Pass data classification setting | R/W |
|  |  |  | 201 | System area | - |
|  |  |  | 202 | CH1 Industrial shipment settings offset value | R/W |
|  |  |  | 203 | CH1 Industrial shipment settings gain value |  |
|  |  |  | 204 | CH2 Industrial shipment settings offset value |  |
|  |  |  | 205 | CH2 Industrial shipment settings gain value |  |
|  |  |  | 206 | CH1 User range setting offset value |  |
|  |  |  | 207 | CH1 User range setting gain value |  |
|  |  |  | 208 | CH2 User range setting offset value |  |
|  |  |  | 209 | CH2 User range setting gain value |  |

### 10.2.3 High-speed counter module comparison

(1) A0J2-D61S1 and QD62/QD62-H02 comparison
(a) Performance specifications comparison

1) Performance specifications comparison between A0J2-D61S1 and QD62

O : Compatible, $\Delta$ : Partially changed, x : Incompatible

| Item |  |  | A0J2-D61S1 |  | QD62 |  |  | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| I/O Occupied points |  |  |  | oints gnment: 4 points) | 16 points (I/O assignment: Intelligent 16 points) |  |  | $\times$ | The number of I/O points changes to 16 . |
| Number of channels |  |  | 2 channels |  |  |  |  | $\bigcirc$ |  |
| Count input signal |  | Phase | 1 phase -input, 2-phase input |  |  |  |  | $\bigcirc$ |  |
|  |  | Signal level $(\phi \mathrm{A}, \phi \mathrm{~B})$ | $\left.\begin{array}{r}\begin{array}{r}5 V D C \\ 12 V D C \\ 24 V D C\end{array}\end{array}\right\} 2$ to 5 mA |  |  |  |  | $\bigcirc$ |  |
|  | Counter | Counting speed | 1-phase input | 10KPPS | 200KPPS | 100KPPS | 10KPPS | $\bigcirc$ | * |
|  |  |  | 2-phase input | 7KPPS | 200KPPS | 100KPPS | 10KPPS | 0 |  |
|  |  | Counting range | Binary format <br> (binary) 24bits <br> 0 to 16777215 <br> (Decimal notation) |  | $\begin{gathered} \text { 32bits signed binary } \\ (-2147483648 \text { to } 2147483647) \end{gathered}$ |  |  | $\Delta$ | At QD62 the value is handled as a signed 32bits binary, so the sequence program must be changed. |
|  |  | Type | UP/DOWN Preset counter + Ring counter function |  |  |  |  | $\bigcirc$ |  |
|  |  | Minimum value count pulse width (Input rise time is $5 \mu \mathrm{~s}$ or less duty ratio is 50\%) |  |  | (Min. phase differential for 2-phase input: $1.25 \mu \mathrm{~s}$ | (Min. phase differential for 2-phase input: $2.5 \mu \mathrm{~s}$ | (Min. phase differential for 2-phase input: $25 \mu$ s | $\bigcirc$ |  |
|  | Maximum/ | Comparison range | Binary <br> (binary) | format 24bits |  | 2bits signed bina |  | 0 |  |
|  | minimum comparison (CPU $\leftrightarrow$ A0J2-D61S1/ QD62) | Comparison result | Set Cou Set Cou Se Cou | value< value alue= value alue> value |  | $\begin{aligned} & \text { t value }<\text { Count } v \\ & \text { et value }=\text { Count va } \\ & \text { t value }>\text { Count } v \end{aligned}$ |  | $\bigcirc$ |  |
|  |  | Preset | $\begin{array}{r} \hline 12 / 24 \mathrm{VV} \\ 5 \mathrm{VD} \end{array}$ | $\begin{aligned} & \mathrm{C} 3 / 6 \mathrm{~mA} \\ & 5 \mathrm{~mA} \end{aligned}$ |  | 12/24VDC 2 to 5 |  |  | At QD62, external input specifications |
|  | External input | Count disable | $\begin{array}{r} \hline 12 / 24 \mathrm{VI} \\ 5 \mathrm{VD} \end{array}$ | $\begin{aligned} & \mathrm{C} 3 / 6 \mathrm{~mA} \\ & 5 \mathrm{~mA} \\ & \hline \end{aligned}$ |  | - |  | $\Delta$ | are different, so confirm the external |
|  |  | Function start |  |  |  | 12/24VDC 2 to 5 |  |  | device specifications. |
|  | External output | Coincidence output | $\begin{array}{r} \text { Tra } \\ \text { (Open } \\ 0 \\ 12 / 24 \end{array}$ | istor <br> ollector) <br> put <br> CC 0.5 A | Transist 12/2 | k type) output 2 <br> C 0.5 A/point, 2 A | ts/channel mmon | 0 |  |
| 5 VD | C internal curre | nt consumption |  | OA |  | 0.30A |  | $\times$ | 5VDC internal current consumption requires recalculation. |
| Wei |  |  |  | 5 kg |  | 0.11 kg |  | $\triangle$ |  |

*1 The counting speed is affected by the rise/fall time of the pulse. The appropriate counting speed is as shown below.
Note that the count may be incorrect when pulses with longer rise/fall time are counted.
(For the A0J2-D61S1)

| Rise/fall time | 1-phase input | 2-phase input |
| :--- | :---: | :---: |
| $t=5 \mu \mathrm{~s}$ | 10KPPS | 7KPPS |
| $t=500 \mu \mathrm{~s}$ | 500 PPS | 250 PPS |

(For the QD62)

| Rise/fall time | Common in 1-phase input and <br> 2-phase input |  |  |
| :--- | :---: | :---: | :---: |
| Counting speed switch <br> setting | 200 K | 100 K | 10 K |
| $t=1.25 \mu \mathrm{~s}$ or less | 200 KPPS | 100 KPPS | 10 KPPS |
| $\mathrm{t}=2.5 \mu \mathrm{~s}$ or less | 100 KPPS | 100 KPPS | 10 KPPS |
| $\mathrm{t}=25 \mu \mathrm{~s}$ or less | - | 10 KPPS | 10 KPPS |
| $\mathrm{t}=500 \mu \mathrm{~s}$ | - | - | 500 KPPS |

2) Performance specifications comparison between A0J2-D61S1 and QD62-H02

| Item |  |  | A0J2-D61S1 |  | QD62-H02 |  | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| I/O occupied points |  |  | 64 points (I/O assignment: Special 64 points) |  | 16 points (I/O assignment: Intelligent 16 points) |  | $\triangle$ | The number of I/O points changes to 16 . |
| Number of channels |  |  | 2 channels |  |  |  | $\bigcirc$ |  |
| Counting speed switch setting |  |  | - |  | 10KPPS |  | O | Set "2 (counting speed 200kPPS)" in the intelligent function module switch setting. <br> Counting is performed using 10 kPPS by setting "2 (counting speed 200kPPS)". |
| Count input signal |  | Phase | 1-phase input, 2-phase input |  |  |  | O |  |
|  |  | Signal level $(\phi \mathrm{A}, \phi \mathrm{~B})$ | $\left.\begin{array}{r}\begin{array}{r}5 V D C \\ 12 \mathrm{VDC} \\ 24 \mathrm{VDC}\end{array}\end{array}\right\} 2$ to 5 mA |  |  |  | O |  |
|  | Counter | Counting speed <br> (Maximum) | 1-phase input | 10KPPS | $\begin{aligned} & \hline \text { 1-phase } \\ & \text { input } \end{aligned}$ | 10KPPS | 0 | *1 |
|  |  |  | $\begin{aligned} & \text { 2-phase } \\ & \text { input } \end{aligned}$ | 7KPPS | $\begin{aligned} & \text { 2-phase } \\ & \text { input } \end{aligned}$ | 7KPPS |  |  |
|  |  | Counting range | 24-bit unsigned binary <br> (0 to 16777215) |  | 32-bit signed binary (-2147483648 to 2147483647 ) |  | $\Delta$ | At QD62-H02, the value is handled as a 32 -bit signed binary, so the sequence program must be changed. |
|  |  | Type | UP/DOWN Preset counter + Ring counter function |  |  |  | $\bigcirc$ |  |
|  |  | Minimum count pulse width (Input rise time must be $5 \mu$ s or less. <br> Duty ratio is 50\%.) |  |  |  |  | $\bigcirc$ |  |
|  | Maximum/ minimum comparison (CPU $\leftrightarrow$ AOJ2D61S1/ QD62-H02) | Comparison range | 24-bit | binary | 32- | inary | $\bigcirc$ |  |
|  |  | Comparison result | Set value < Count value <br> Set value $=$ Count value <br> Set value >Count value |  |  |  | O |  |
|  | External input | Preset |  |  | 5/12/2 | 5 mA | $\triangle$ | At QD62-H02, external input specifications are different. Therefore, check the external device specifications. |
|  |  | Count disable |  |  |  |  |  |  |
|  |  | Function start |  |  | 5/12/2 | 5 mA |  |  |
|  | External output | Coincidence output |  |  |  | e) output <br> nel <br> 2A/common | $\bigcirc$ |  |
| 5VDC internal current consumption |  |  |  |  |  |  | $\Delta$ | The recalculation of 5VDC internal current consumption is required. |
| Weight |  |  |  |  |  |  | $\triangle$ |  |

*1 The counting speed is affected by the rise/fall time of the pulse. The appropriate counting speed is as shown below. Note that the count may be incorrect when pulses with longer rise/fall time are counted.
(For A0J2-D61S1 and QD62-H02)

| Rise/fall time | 1-phase input | 2-phase input |
| :--- | :---: | :---: |
| $\mathrm{t}=5 \mu \mathrm{~s}$ | 10KPPS | 7KPPS |
| $\mathrm{t}=500 \mu \mathrm{~s}$ | 500 PPS | 250 PPS |



## (b) Functional comparisons

| Item | A0J2-D61S1 | QD62/QD62-H02 | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: |
| Preset function | Preset is to overwrite counter current values to any values (initial values). The AOJ2D61S1 has no memory internal latch function, so if the power supply turns OFF or the CPU is reset, the D61S1 memory (counter values, current values, setting values, preset values) are initialized. Depending on the continuous work flow, the present counter value (present value) is stored in the CPU's data register, and when the next work is started, the stored data register values can be used as presets from which to continue counting. | Any value can be overwritten to the counter's present values. | O |  |
| Disable function | Disable is disallowed, meaning enable is possible. If the sequencer I/O signal allocation in the count enable signal is ON , the A0J2-D61S1 count starts. If voltage is applied to the $(\mathrm{CH} 1=\mathrm{Y} 14, \mathrm{CH} 2=\mathrm{Y} 1 \mathrm{~B})$ external input terminal's DIS (disable) terminal, the D61S1 count is stopped, so this can be used via external input to start or stop a count without relation to scan time. | Count is stopped. | O |  |
| Ring counter function | Depending on the settings when the ring counter setting switch on the A0J2-D61S1 circuit board is ON, the counter values and similar settings are automatically preset. This function is used in cyclic controls. | Any set value is returned to perform a count. | O |  |
| Linear counter function | - | Detects a count range overflow. | - |  |
| Coincidence output function | It is possible for the A0J2-D61S1 to output (open collector output) counter coincidence signals (counter values and similar setting values are ON) as external output to an external terminal. To output a counter coincidence signal to an external terminal block, the coincidence signal output enable command ( $\mathrm{CH} 1=\mathrm{Y} 12, \mathrm{CH} 2=\mathrm{Y} 19$ ), which is assigned to a programmable controller I/O signal, must be ON. | A signal is output when any set value coincides with the present value. | O |  |
| Coincidence detection interrupt function | - | During coincidence detection, a programmable controller CPU interrupt request is issued. | - |  |
| Latch counter function | - | The present value when a signal is input is latched. | - |  |
| Sampling counter function | - | The input pulses are counted for the set sampling time. | - |  |
| Cycle pulse counter function | - | For each specified cycle time, the present value and previous value are each stored in the present value and previous value. | - |  |

(c) Programmable controller CPU I/O signal comparison

Input signal is different, so the sequence program must be changed.
Refer to the High-Speed Counter Module User's Manual for details regarding the I/O signals and sequence program.

| A0J2-D61S1 |  |  |  | QD62/QD62-H02 |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Device No. | Signal name | Device No. | Signal name | Device No. | Signal name | Device No. | Signal name |
| X0 | CH1 Counter value large | Y0 | Use prohibited | X0 | Module READY | Y0 | CH1 Coincidence signal No. 1 reset command |
| X1 | CH 1 Counter value coincidence | Y1 |  | X1 | CH1 Counter value large (Point No. 1) | Y1 | CH1 Preset command |
| X2 | CH1 Counter value small | Y2 |  | X2 | CH 1 Counter value coincidence (Point No. 1) | Y2 | CH1 Coincidence signal enable command |
| X3 | CH1 External preset request detection | Y3 |  | X3 | CH 1 Counter value small (Point No. 1) | Y3 | CH1 Down count command |
| X4 | CH2 Counter value large | Y4 |  | X4 | CH1 External preset request detection | Y4 | CH 1 Counter enable command |
| X5 | CH 2 Counter value coincidence | Y5 |  | X5 | CH1 Counter value large (Point No. 2) | Y5 | CH1 External preset request detection |
| X6 | CH2 Counter value small | Y6 |  | X6 | CH 1 Counter value coincidence (Point No. 2) | Y6 | CH 1 Counter function selection start command |
| X7 | CH2 External preset request detection | Y7 |  | X7 | CH 1 Counter value small (Point No. 2) | Y7 | CH1Coincidence signal No. 2 |
| X8 | Use prohibited | Y8 |  | X8 | CH 2 Counter value large (Point No. 1) | Y8 | CH2 Coincidence signal No. 1 |
| X9 |  | Y9 |  | X9 | CH2 Counter value coincidence (Point No. 1) | Y9 | CH2 Preset command |
| XA |  | YA |  | XA | CH 2 Counter value small (Point No. 1) | YA | CH 2 Coincidence signal enable command |
| XB |  | YB |  | XB | CH 2 External preset request detection | YB | CH2 Down count command |
| XC |  | YC |  | XC | CH 2 Counter value large (Point No. 2) | YC | CH2 Count enable command |
| XD |  | YD |  | XD | CH 2 Counter value coincidence (Point No. 2) | YD | CH2 External preset request detection |
| XE |  | YE |  | XE | CH 2 Counter value small <br> (Point No. 2) | YE | CH 2 Counter function selection start command |
| XF |  | YF |  | XF | Fuse blown detection flag | YF | CH2 Coincidence signal No. 2 reset command |
| X10 |  | Y10 | CH1 Coincidence signal reset |  |  |  |  |
| X11 |  | Y11 | CH1 Preset command |  |  |  |  |
| X12 |  | Y12 | CH1 Coincidence signal output enable |  |  |  |  |
| X13 |  | Y13 | CH1 Down count command |  |  |  |  |
| X14 |  | Y14 | CH1 Count enable |  |  |  |  |
| X15 |  | Y15 | CH1 Present value read request |  |  |  |  |
| X16 |  | Y16 | CH1 External preset request detection |  |  |  |  |
| X17 |  | Y17 | CH2 Coincidence signal reset |  |  |  |  |
| X18 |  | Y18 | CH2 Preset command |  |  |  |  |
| X19 |  | Y19 | CH 2 Coincidence signal output enable |  |  |  |  |
| X1A |  | Y1A | CH2 Down count command |  |  |  |  |
| X1B |  | Y1B | CH2 count enable |  |  |  |  |
| X1C |  | Y1C | CH 2 Present value read request |  |  |  |  |
| X1D |  | Y1D | CH2 external preset detection reset command |  |  |  |  |
| X1E |  | Y1E | Use prohibited |  |  |  |  |
| X1F |  | Y1F |  |  |  |  |  |

(d) Buffer memory address comparisons

Buffer memory allocation is different, so the sequence program must be changed.
Refer to the High-Speed Counter Module User's Manual for details regarding the buffer memory and sequence program.

| A0J2-D61S1 |  |  |  | QD62/QD62-H02 |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Address |  | Name | Read/Write | Address |  | Name |  | Read/Write |
| CH1 | CH2 |  |  | CH1 | CH2 |  |  |  |
| 0 | 0 | - | - | 0 | 32 | Preset value setting |  | R/W |
| 1 | 33 | Preset value write (lower/middle) | W | 1 | 33 |  | (H) |  |
| (2) | (34) | Preset value write (upper) |  | 2 | 34 | Present value | (L) | R |
| 3 | 35 | Mode register | R/W | 3 | 35 |  | (H) |  |
| 4 | 36 | Present value read (lower/middle) | R | 4 | 36 | Coincidence output point No. 1 setting | (L) | R/W |
| (5) | (37) | Present value read (upper) |  | 5 | 37 |  | (H) |  |
| 6 | 38 | Set value read/write (lower/middle) | R/W | 6 | 38 | Coincidence output point No. 2 setting |  |  |
| (7) | (39) | Set value read/write (upper) |  | 7 | 39 |  |  |  |
|  |  |  |  | 8 | 40 | Overflow detection |  | R |
|  |  |  |  | 9 | 41 | Counter function selection setting |  | R/W |
|  |  |  |  | 10 | 42 | Sampling/periodic setting |  |  |
|  |  |  |  | 11 | 43 | Sampling/periodic counter flag |  | R |
|  |  |  |  | 12 | 44 | Latch count value | (L) |  |
|  |  |  |  | 13 | 45 |  | (H) |  |
|  |  |  |  | 14 | 46 | Sampling count value | (L) |  |
|  |  |  |  | 15 | 47 |  | (H) |  |
|  |  |  |  | 16 | 48 | Periodic pulse count previous value | (L) |  |
|  |  |  |  | 17 | 49 |  | (H) |  |
|  |  |  |  | 18 | 50 | Periodic pulse count present value | (L) |  |
|  |  |  |  | 19 | 51 |  | (H) |  |
|  |  |  |  | 20 | 52 | Ring counter lower minimum value | (L) | R/W |
|  |  |  |  | 21 | 53 |  | (H) |  |
|  |  |  |  | 22 | 54 | Ring counter maximum value | (L) |  |
|  |  |  |  | 23 | 55 |  |  |  |
|  |  |  |  | 24 | 56 | System area |  | - |
|  |  |  |  | to | to |  |  |  |
|  |  |  |  | 31 | 63 |  |  |  |

## (e) Wiring

The external wiring method differs between A0J2-D61S1 and QD62/QD62-H02.

- A0J2-D61S1: Wiring using a terminal block
- QD62 or QD62-H02: Wiring using a connector

When reusing the I/O signal wire with a solderless terminal of the A0J2-D61S1 to replace the module, use a connector/terminal block converter module.
Rewire the I/O signal wire with a solderless terminal of the existing module to the connector/terminal block converter module and connect the converter module with the alternative module by using the dedicated cable. In this way, the module can be replaced without considering the existing wire size. How to wire when using the connector/terminal block converter module is shown below.


### 10.2.4 Positioning module comparison

(1) A0J2-D71 and QD75P2N specifications comparison
(a) Performance specifications comparison

O : Compatible, $\triangle$ : Partially changed, x : Incompatible

| Item |  |  | A0J2-D71 | QD75P2N | Compatibilty | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| No. of control axes |  |  | axis 2 | axis 2 | $\bigcirc$ |  |
| Number of positioning data |  |  | 400/axis | 600/axis | $\bigcirc$ |  |
| Position control interpolation function | 2-axis linear interpolation |  | Available | Available | $\bigcirc$ |  |
|  | 2-axis circular interpolation |  | None | Available | - |  |
| Positioning system | Positioning control |  | Available | Available | $\bigcirc$ |  |
|  | Speed control |  | None | Available | - |  |
|  | Speed- <br> position/ <br> position-speed <br> switching <br> control |  | None | Available | - |  |
| Positioning range |  | System | Absolute Incremental system can be selected for each axis. | <Absolute system> -214748364.8 to 214748364.7 ( $\mu \mathrm{m}$ ) |  |  |
| Positioning range |  |  | Each axis can be selected with the 4 types of commands listed below. <br> 1 to 16252928 (pulses) <br> MAX. 162 (m) <br> (command unit 0.1 to $10 \mu \mathrm{~m} / \mathrm{pulse}$ ) <br> MAX. 16200 (inches) <br> (Command unit $1 \times 10^{-5}$ to 0.001 inch/ <br> pulse) <br> MAX. 16200 (degrees) <br> (Command unit $1 \times 10^{-5}$ to 0.001degree/pulse) | 0 to 359.99999 (degrees) <br> -2147483648 to 2147483647 (pulses) <br> <Incremental system> <br> -214748364.8 to 214748364.7 ( $\mu \mathrm{m}$ ) <br> -21474.83648 to 21474.83647 (inches) <br> -21474.83648 to 21474.83647 (degrees) <br> -2147483648 to 2147483647 (pulses) <br> <During speed-position switching control> 0 to 214748364.7 ( $\mu \mathrm{m}$ ) <br> 0 to 21474.83647 (inches) <br> 0 to 21474.83647 (degrees) <br> 0 to 2147483647 (pulses) | O |  |
| Speed command range |  |  | Each axis can be selected with the 4 types of commands listed below. <br> 10 to 200000 (pulse/sec) <br> (Command unit 10pulses/sec) <br> 10 to 120000 ( mm/min) <br> (Command unit $10 \mathrm{~mm} / \mathrm{min}$ ) 1 to 12000 (inch/min) <br> (Command unit 1inch/min) 1 to 12000 (degree/min) <br> (Command unit 1degree/min) | 0.01 to 20000000.00 ( $\mathrm{mm} / \mathrm{min}$ ) 0.001 to 2000000.000 (inch $/ \mathrm{min}$ ) 0.001 to 2000000.000 (degree $/ \mathrm{min}$ ) 1 to 1000000 (pulses/s) (Max. output pulse: 200kpps) | $\bigcirc$ |  |
| $\begin{aligned} & \text { Accele- } \\ & \text { ration/ } \\ & \text { decele- } \\ & \text { ration } \\ & \text { process- } \\ & \text { ing } \end{aligned}$ | Automatic trapezoidal acceleration/ deceleration |  | Available | Available | O |  |
|  | S-pattern acceleration/ deceleration |  | None | Available | - |  |
| Acceleration/ deceleration time | No. of patterns | Acceler | on time and deceleration time are the same period of time. (1 pattern) | Acceleration time and deceleration time can be specified. <br> (4 patterns for each) | O |  |
|  | Setting range |  | 64 to 4999 (ms) | 1 to 8388608 (ms) | $\bigcirc$ |  |
| Data storage destination |  |  | SRAM (battery backup) | Flash ROM (battery-less backup) | $\triangle$ | Flash ROM can be written 100000 times. |
| Connector |  |  | - | A6CON1 (soldering type, usable for straight, sold separately) <br> A6CON4 (soldering type, usable for straight out and diagonal, sold separately) | $\times$ | Wiring change is required as the connector differs. QD75P2N connector is sold separately. |


| Item | A0J2-D71 | QD75P2N | Compatibilty | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: |
| Applicable wire size | $0.3 \mathrm{~mm}^{2}$ | A6CON1, A6CON4: $0.3 \mathrm{~mm}{ }^{2}$ | O |  |
| Command pulse output type | Open collector | Open collector | $\bigcirc$ |  |
| Max. output pulse | 200kpps | 200kpps | $\bigcirc$ |  |
| Maximum connection distance between servos | 1 to 3 m | 2 m | $\triangle$ |  |
| 5 VDC internal current consumption | 0.65A | 0.30A | $\bigcirc$ |  |
| Flash ROM write count | - | Max. 100000 times | - |  |
| Number of occupied I/O points | 64 points (I/O assignment: Special 64 points) | 32 points (I/O assignment: Intelligent function module 32 points) | $\triangle$ | The number of I/O points is changed to 32. |
| Weight | 0.75 kg | 0.14 kg | $\triangle$ |  |

O : Compatible, $\Delta$ : Partially changed, $\times$ : Incompatible

| Item |  | A0J2-D71 | QD75P2N | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: | :---: |
| I/O signal for external devices | Upper/lower limit signals | None | Available | $\triangle$ | QD75P2N requires wiring. |
|  | START signal | Available | None | $\times$ | QD75P2N does not have this signal. To use this signal, output it from the output module. |
|  | Manual pulse generator A/B phase | On each axis | Only one pulse generator can be connected. | $\triangle$ | At the QD75P2N, the manual pulse generator can only be connected to one terminal. <br> Configure the buffer memory settings to specify the controlled system axis. |
|  | Drive module ready <br> Stop signal <br> Proximity <br> signal | Operating voltage range <br> 4.75 to 26.4 VDC | Operating voltage range $19.2 \text { to } 26.4 \mathrm{VDC}$ | $\Delta$ | Input specifications are different so check |
|  | Zero point signal | Operating voltage range 4.75 to 26.4 VDC <br> Pulse width: $50 \mu \mathrm{~s}$ or more | Operating voltage range 4.5 to 6.1 VDC <br> or 12 to 26.4 VDC <br> Pulse width: 1 ms or more | $\triangle$ | the specifications of the connecting device. |
|  | Forward/ reverse field pulse | Available | Available | O |  |
|  | Error value counter clear | Available | Available | O |  |
|  | Signal logic selection | None | Available | - |  |
| Peripheral device (data setting, etc.) | Peripheral device connections | Direct connection | Programmable controller CPU, Q-compatible serial communication module, Q-compatible MELSECNET/H <br> Connection via remote I/O module | O | Connection system is different. |
|  | Teaching module | AD71TU | None | $\times$ | The teaching module cannot be used. |
|  | Software package | SW0GP-AD71P (for A6GPP/A6PHP) SW1RX-AD71P (for A7PHP/A7HGP) SW1IVD-AD71P (for IBM-PC/AT-compatible personal computer) | GX Works2 <br> GX Configurator-QP | $\Delta$ | The usable software package are different. |

## (b) Functional comparisons

| Item |  | A0J2-D71 | QD75P2N | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Machine OPR function (OPR method) |  | Available (3 methods) | Available (6 methods) | $\bigcirc$ |  |
| JOG operation |  | Available | Available | $\bigcirc$ |  |
| Manual pulse generator operation |  | Available | Available |  | - On the QD75P2N the |
|  |  | Manual pulse generator connectibility is one pulse generator/axis. | Manual pulse generator connectibility is one pulse generator/module. |  | manual pulse generator cannot be used |
|  |  | Manual pulse generator's movement per pulse <br> 1 to $100000\left(\times 10^{-1} \mu \mathrm{~m}\right)$ <br> 1 to 100000 ( $\times 10^{-5}$ inches) <br> 1 to 100000 ( $\times 10^{-1}$ degrees) 1 to 100 (pulses) | Pulse input magnification for manually generated pulse: 1 to 100 times | $\triangle$ | axis. If the manual pulse generator must be connected to each axis, use the 1-axis module. <br> - The A0J2D71 and QD75P2N differ in the specifications of manual pulse inputs, so before connecting a manual pulse generator to the QD75P2N, confirm the specifications of the pulse generator. <br> - If not using the unit pulse on the QD75P2N, the magnification from the pulse input from the manual pulse generator will become smaller. |
| Positioning data | 1 time positioning (terminate) | Available | Available | O |  |
|  | n-time positioning (continue) | Available | Available | $\bigcirc$ |  |
|  | Change the speed and continue positioning (pattern change) | Available | Available | O |  |
| Present value change |  | Available | Available | $\bigcirc$ |  |
| M code output function |  | Available | Available | $\bigcirc$ |  |
| M code comment |  | Available | None | $\times$ | QD75P2N has no M code comment function. |
| Speed change function |  | Available | Available | $\bigcirc$ |  |
| Compensation |  | Backlash compensation, error compensation | Electronic gear, backlash compensation, near pass | $\Delta$ | The QD75P2N does not have the error compensation function. Use the electronic gear function instead. |
| Stroke limit function | Position control | Available | Available | $\bigcirc$ |  |
|  | JOG operation, manual pulse generator operation | None | Available <br> (Limit check can be selected on or off.) | $\bigcirc$ | At default, the limit check is on. |
| Error display |  | None | Error LED | - |  |
| History data <br> (start, error, warning) |  | None | Available (3 types and 16 data/module) | - |  |

(c) Programmable controller CPU I/O signal comparisons

| A0J2-D71 |  |  |  |  |  | QD75P2N |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Device No. |  | Signal name | Device No. |  | Signal name | Device No. | Signal name | Device No. | Signal name |
| X0 |  | Watchdog error (D71 detection) | Y0 | Use prohibited |  | X00 | QD75 READY | Y00 | Programmable controller READY |
| X1 |  | D71 READY | Y1 |  |  | X01 | Synchronization flag | Y01 | Use prohibited |
| X2 | X-axis | Positioning complete | Y2 |  |  | X02 | Use prohibited | Y02 |  |
| X3 | Y-axis |  | Y3 |  |  | X03 |  | Y03 |  |
| X4 | X-axis | BUSY | Y4 |  |  | X04 | Axis 1 M code ON | Y04 | Axis 1 stopped |
| X5 | Y-axis |  | Y5 |  |  | X05 | Axis 2 M code ON | Y05 | Axis 2 stopped |
| X6 | X-axis | OPR request | Y6 |  |  | X06 | Axis 3 M code ON | Y06 | Axis 3 stopped |
| X7 | Y-axis |  | Y7 |  |  | X07 | Axis 4 M code ON | Y07 | Axis 4 stopped |
| X8 | X-axis | Started | Y8 |  |  | X08 | Axis 1 error detection | Y08 | Axis 1 forward run JOG |
| X9 | Y-axis |  | Y9 |  |  | X09 | Axis 2 error detection | Y09 | Axis 1 reverse run JOG |
| XA | Battery error |  | YA |  |  | XOA | Axis 3 error detection | YOA | Axis 2 forward run JOG |
| XB | Error detection |  | YB |  |  | XOB | Axis 4 error detection | YOB | Axis 2 reverse run JOG |
| XC | X-axis | OPR complete | YC |  |  | XOC | Axis 1 BUSY | YOC | Axis 3 forward run JOG |
| XD | Y-axis |  | YD |  |  | XOD | Axis 2 BUSY | YOD | Axis 3 reverse run JOG |
| XE | X-axis | M code ON | YE |  |  | XOE | Axis 3 BUSY | YOE | Axis 4 forward run JOG |
| XF | Y-axis |  | YF |  |  | X0F | Axis 4 BUSY | Y0F | Axis 4 reverse run JOG |
| X10 | Use prohibited |  | Y10 | X-axis | Positioning start | X10 | Axis 1 started | Y10 | Axis 1 positioning start |
| X11 |  |  | Y11 | Y-axis |  | X11 | Axis 2 started | Y11 | Axis 2 positioning start |
| X12 |  |  | Y12 | Interpolation |  | X12 | Axis 3 started | Y12 | Axis 3 positioning start |
| X13 |  |  | Y13 | X-axis | OPR start | X13 | Axis 4 started | Y13 | Axis 4 positioning start |
| X14 |  |  | Y14 | Y-axis |  | X14 | Axis 1 positioning complete | Y14 | Axis 1 execution prohibited flag |
| X15 |  |  | Y15 | X-axis | Stop | X15 | Axis 2 positioning complete | Y15 | Axis 2 execution prohibited flag |
| X16 |  |  | Y16 | Y-axis |  | X16 | Axis 3 positioning complete | Y16 | Axis 3 execution prohibited flag |
| X17 |  |  | Y17 | X-axis forward run JOG start |  | X17 | Axis 4 positioning complete | Y17 | Axis 4 execution prohibited flag |
| X18 |  |  | Y18 | X-axis reverse run JOG start |  | X18 | Use prohibited | Y18 | Use prohibited |
| X19 |  |  | Y19 | Y-axis forward run JOG start |  | X19 |  | Y19 |  |
| X1A |  |  | Y1A | Y-axis reverse run JOG start |  | X1A |  | Y1A |  |
| X1B |  |  | Y1B | X -axis | M code OFF | X1B |  | Y1B |  |
| X1C |  |  | Y1C | Y-axis |  | X1C |  | Y1C |  |
| X1D |  |  | Y1D | Progran | mable controller ready | X1D |  | Y1D |  |
| X1E |  |  | Y1E | Use prohibited |  | X1E |  | Y1E |  |
| X1F |  |  | Y1F |  |  | X1F |  | Y1F |  |

## I/O signal difference

Large point differences apart from I/O number differences are described below.

| A0J2-D71 | QD75P2N |
| :--- | :--- |
| Watchdog error (X0) | No watchdog error signal is provided. <br> When a watch dog error occurs on the QD75P2N, the QD75 ready (X0) turns OFF. |
| OPR request (X6, X7) | Check OPR request flag at Md.31 (bit 3). <br> If OPR request is on, it is set to '1'. |
| Battery error (XA) | No battery error signal is provided. <br> QD75P2N has data stored in flash ROM, so memory backup batteries are not required. |
| Error detection (XB) is common <br> with X-axis and Y-axis | Error detection is performed for each axis. <br> Axis 1: X8, Axis 2: X9 |
| OPR complete (XC, XD) | Check OPR complete flag at Md.31 (bit 4). <br> If OPR complete is on, it is set to '1'. |
| Positioning start interpolation | No interpolation start signal is provided. <br> On QD75P2N, perform interpolation movement settings to the positioning data, and interpolation movement <br> can be performed by starting positioning. |
| Y12) | No OPR start signal is provided. <br> On QD75P2N, write "9001" to positioning start number Cd. 3, <br> positioning. and OPR can be performed by starting |
| OPR start (Y13, Y14) | M code OFF request is performed at Cd.7. <br> Write "1" to switch the M code signal from ON to OFF. |
| M code OFF (Y1B, Y1C) |  |

(d) Buffer memory addresses comparisons


| QD75P2N |  |  |  |
| :---: | :---: | :---: | :---: |
| Address |  | Name | Read/Write |
| Axis 1 | Axis 2 |  |  |
| 42 | 192 | Pr. 28 Deceleration time 1 | R/W |
| 43 | 193 |  |  |
| 44 | 194 | Pr. 29 Deceleration time 2 |  |
| 45 | 195 |  |  |
| 46 | 196 | Pr. 30 Deceleration time 3 |  |
| 47 | 197 |  |  |
| 48 | 198 | Pr. 31 JOG speed limit value |  |
| 49 | 199 |  |  |
| 50 | 200 | Pr. 32 JOG operation acceleration time selection |  |
| 51 | 201 | Pr. 33 JOG operation deceleration time selection |  |
| 52 | 202 | Pr. 34 Acceleration/deceleration process selection |  |
| 53 | 203 | Pr. 35 S-pattern proportion |  |
| 54 | 204 | Pr. 36 Sudden stop deceleration time |  |
| 55 | 205 |  |  |
| 56 | 206 | Pr. 37 Stop group 1 sudden stop selection |  |
| 57 | 207 | Pr. 38 Stop group 2 sudden stop selection |  |
| 58 | 208 | Pr. 39 Stop group 3 sudden stop selection |  |
| 59 | 209 | Pr. 40 Positioning complete signal output time |  |
| 60 | 210 | Pr. 41 Allowable circular interpolation error width |  |
| 61 | 211 |  |  |
| 62 | 212 | Pr. 42 External command function selection |  |
| $\begin{aligned} & 63 \\ & \text { to } \\ & 69 \end{aligned}$ | $\begin{gathered} 213 \\ \text { to } \\ 219 \end{gathered}$ | Use prohibited | - |
| 70 | 220 | Pr. 43 OPR method | R/W |
| 71 | 221 | Pr. 44 OPR direction |  |
| 72 | 222 | Pr. 45 OP address |  |
| 73 | 223 |  |  |
| 74 | 224 | Pr. 46 OPR speed |  |
| 75 | 225 |  |  |
| 76 | 226 | Pr. 47 Creep speed |  |
| 77 | 227 |  |  |
| 78 | 228 | Pr. 48 OPR retry |  |
| 79 | 229 | Pr. 49 OPR dwell time |  |
| 80 | 230 | Pr. 50 Setting for the movement amount after |  |
| 81 | 231 | near-point dog ON |  |
| 82 | 232 | Pr. 51 OPR acceleration time selection |  |
| 83 | 233 | Pr. 52 OPR deceleration time selection |  |
| 84 | 234 | Pr. 53 OP shift amount |  |
| 85 | 235 |  |  |
| 86 | 236 | Pr. 54 OPR torque limit value |  |
| 87 | 237 | Pr. 55 Deviation counter clear signal output time |  |
| 88 | 238 | Pr. 56 Speed designation during OP shift |  |
| 89 | 239 | Pr. 57 Dwell time during OPR retry |  |



| QD75P2N |  |  |  |
| :---: | :---: | :---: | :---: |
| Address |  | Name | Read/Write |
| Axis 1 | Axis 2 |  |  |
| 800 | 900 | Md. 20 Present feed value | R |
| 801 | 901 |  |  |
| 802 | 902 | Md. 21 Machine feed value |  |
| 803 | 903 |  |  |
| 804 | 904 | Md. 22 Feedrate |  |
| 805 | 905 |  |  |
| 806 | 906 | Md. 23 Axis error No. |  |
| 807 | 907 | Md. 24 Axis warning No. |  |
| 808 | 908 | Md. 25 Valid M code |  |
| 809 | 909 | Md. 26 Axis operation status |  |
| 810 | 910 | Md. 27 Current speed |  |
| 811 | 911 |  |  |
| 812 | 912 | Md. 28 Axis feedrate |  |
| 813 | 913 |  |  |
| 814 | 914 | Md. 29 Speed-position switching control positioning amount |  |
| 815 | 915 |  |  |
| 816 | 916 | Md. 30 External I/O signal |  |
| 817 | 917 | Md. 31 Status |  |
| 818 | 918 | Md. 32 Target value |  |
| 819 | 919 |  |  |
| 820 | 920 | Md. 33 Target speed |  |
| 821 | 921 |  |  |
| $\begin{array}{\|c\|} \hline 822 \text { to } \\ 823 \\ \hline \end{array}$ | $\begin{array}{\|c\|} \hline 922 \text { to } \\ 923 \end{array}$ | - | - |
| 824 | 924 | Md. 34 Movement amount after near-point dog | R |
| 825 | 925 | ON |  |
| 826 | 926 | Md. 35 Torque limit storage value |  |
| 827 | 927 | Md. 36 Special start data instruction code setting value |  |
| 828 | 928 | Md. 37 Special start data instruction parameter setting value |  |
| 829 | 929 | Md. 38 Start position positioning data No. setting value |  |
| 830 | 930 | Md. 39 ln speed control flag |  |
| 831 | 931 | Md. 40 In speed change processing flag |  |
| 832 | 932 | Md. 41 Special start repetition counter |  |
| 833 | 933 | Md. 42 Control method repetition counter |  |
| 834 | 934 | Md. 43 Executing start data pointer |  |
| 835 | 935 | Md. 44 Executing positioning data No. |  |
| 836 | 936 | Md. 45 Block No. being executed |  |
| 837 | 937 | Md. 46 Last executed positioning data No. |  |
| $\begin{array}{\|c\|} \hline 838 \text { to } \\ 847 \end{array}$ | $\begin{array}{\|c\|} \hline 938 \text { to } \\ 947 \end{array}$ | Md. 47 Executing positioning data |  |
| $\begin{array}{\|c\|} \hline 848 \text { to } \\ 898 \\ \hline \end{array}$ | $\begin{array}{\|c\|} \hline 948 \text { to } \\ 998 \end{array}$ | - | - |
| 899 | 999 | Md. 48 Deceleration start flag | R |


| QD75P2N |  |  |  |
| :---: | :---: | :---: | :---: |
| Address |  | Name | Read/Write |
| Axis 1 | Axis 2 |  |  |
| 1500 | 1600 | Cd. 3 Positioning start No. | R/W |
| 1501 | 1601 | Cd. 4 Positioning start point No. |  |
| 1502 | 1602 | Cd.5 Axis error reset |  |
| 1503 | 1603 | Cd. 6 Restart command |  |
| 1504 | 1604 | Cd. 7 M code OFF request |  |
| 1505 | 1605 | Cd. 8 External command valid |  |
| 1506 | 1606 | Cd. 9 New current value |  |
| 1507 | 1607 |  |  |
| 1508 | 1608 | Cd. 10 New acceleration time value |  |
| 1509 | 1609 |  |  |
| 1510 | 1610 | Cd. 11 New deceleration time value |  |
| 1511 | 1611 |  |  |
| 1512 | 1612 | Cd. 12 Acceleration/deceleration time change during speed change, enable /disable selection |  |
| 1513 | 1613 | Cd. 13 Positioning operation speed override |  |
| 1514 | 1614 | Cd. 14 New speed value |  |
| 1515 | 1615 |  |  |
| 1516 | 1616 | Cd. 15 Speed change request |  |
| 1517 | 1617 | Cd. 16 Inching movement amount |  |
| 1518 | 1618 | Cd. 17 JOG speed |  |
| 1519 | 1619 |  |  |
| 1520 | 1620 | Cd. 18 Interrupt request continuous operation |  |
| 1521 | 1621 | Cd. 19 OPR request flag OFF request |  |
| 1522 | 1622 | Cd. 20 Manual pulse generator 1 pulse input magnification |  |
| 1523 | 1623 |  |  |
| 1524 | 1624 | Cd. 21 Manual pulse generation enable flag |  |
| 1525 | 1625 | Cd. 22 Torque new value |  |
| 1526 | 1626 | Cd. 23 Speed-position switching control movement amount change register |  |
| 1527 | 1627 |  |  |
| 1528 | 1628 | Cd. 24 Speed-position switching enable flag |  |
| 1529 | 1629 | Use prohibited | - |
| 1530 | 1630 | Cd. 25 Speed-position switching control movement amount change register | R/W |
| 1531 | 1631 |  |  |
| 1532 | 1632 | Cd. 26 Speed-position switching enable flag |  |
| 1533 | 1633 | Use prohibited | - |
| 1534 | 1634 | Cd.27 New target position value (address) | R/W |
| 1535 | 1635 |  |  |
| 1536 | 1636 | Cd. 28 New target position value (speed) |  |
| 1537 | 1637 |  |  |
| 1538 | 1638 | Cd. 29 Target position change request flag |  |
| 1539 | 1639 | Use prohibited | - |
| 1540 | 1640 | Cd. 30 Simultaneous start target axis start data No. (Axis 1 start data No.) | R/W |
| 1541 | 1641 | Cd. 31 Simultaneous start target axis start data No. (Axis 2 start data No.) |  |
| 1542 | 1642 | Cd. 32 Simultaneous start target axis start data No. (Axis 3 start data No.) |  |
| 1543 | 1643 | Cd. 33 Simultaneous start target axis start data No. (Axis 4 start data No.) |  |
| 1544 | 1644 | Cd. 34 Step mode |  |
| 1545 | 1645 | Cd. 35 Step valid flag |  |
| 1546 | 1646 | Cd. 36 Step start information |  |
| 1547 | 1647 | Cd. 37 Skip command |  |
| 1548 | 1648 | Cd. 38 Teaching data selection |  |
| 1549 | 1649 | Cd. 39 Teaching positioning data No. |  |
| 1550 | 1650 | Cd. 40 ABS direction settings at degree |  |
| 1900 |  | Cd. 1 Flash ROM write request |  |
| 1901 |  | Cd. 2 Parameter initialization request |  |
| 1902 to 1904 |  | Use prohibited | - |
| 1905 |  | Cd. 41 Deceleration start flag valid | R/W |
| 1906 |  | Use prohibited | - |
| 1907 |  | Cd. 42 Stop command processing for deceleration stop selection | R/W |



## APPENDICES

## Appendix 1 External Dimensions

For external dimensions of modules described in this handbook, refer to the user's manual for each module.

## Appendix 2 Performance Specifications Comparison between A0J2H Series and Renewal Tool for A0J2

## Appendix 2.1 Precautions for the performance specifications comparison

This section describes the precautions when comparing the performance specifications between an A0J2H series I/O module and a renewal tool for A0J2.
(1) External supply power (24VDC)

The renewal tool for A0J2 requires an external supply power (24VDC). Reuse the I/O module terminal block of the existing A0J2H series and connect the external supply power (24VDC) to the renewal tool. For precautions or details when connecting the external supply power, refer to the following.

- Renewal tool for A0J2 series transition from MELSEC-A0J2(H) series to renewal system using renewal tool
(Published by Mitsubishi Electric System \& Service Co., Ltd.)

When the I/O module on the programmable controller side is connected to the renewal tool for A0J2 with the dedicated cable, the external supply power (24VDC) supplies the driving power for external devices of the I/O module on the programmable controller side.

## (2) Selection of I/O modules on the programmable controller side

The renewal tool for A0J2 has functions that convert AC input into DC input, and convert transistor output into relay output or triac output.
Therefore, select a DC input module and a transistor output module for the I/O modules on the programmable controller side, regardless of the type of the renewal tool for A0J2.
When the I/O module on the programmable controller side is connected to the renewal tool for A0J2 with the dedicated cable, select a module that can be wired using a connector for the I/O module on the programmable controller side.
In addition, a recommended module for the I/O module on the programmable controller side is the "QX41Y41P", which has the same I/O assignment with the A0J2H series I/O module. (Selecting the QX41Y41P enables the module replacement without the change of existing I/O addresses.)

## (3) Derating chart for the maximum number of simultaneous input points

(a) Input module on the programmable controller side

Check the number of simultaneous input points by referring to the derating chart of the selected $Q$ series input module.
Note that the maximum number of simultaneous input points is 100\% (all points simultaneously ON) when the QX41/QX41Y41P is selected because the applicable voltage range of the renewal tool for A 0 J 2 is 26.4 VDC at maximum.
(b) Renewal tool for A0J2

The maximum number of simultaneous input points of the renewal tool for A0J2 (input module) has the limitation depending on the external supply power (24VDC) that supplies the power to the module. Use the module within the range shown in the derating chart in the performance specifications comparison.

## (4) Temperature derating for the triac output module

The output load current of the renewal tool for A0J2 (triac output module) has the limitation depending on the ambient temperature in the environment where the module is used. Use the module within the range shown in the temperature derating chart in the performance specifications comparison.

## Appendix 2.2 Performance specifications comparison

This section shows the performance specifications comparison between A0J2 series I/O modules and interface modules of renewal tool for A0J2 described in Section 1.2.
(1) Specifications comparison between A0J2-E32A and interface module (SC-A0JQIF32A)

|  |  |  |  | O: Compatib | $\triangle$ : Partially changed, $\times$ : Incompatib |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Specifications |  | A0J2-E32A input specifications | SC-A0JQIF32A input specifications | Compatibility | Precautions for replacement |
| Number of input points |  | 32 points | 32 points | $\bigcirc$ |  |
| Insulation method |  | Photocoupler | Photocoupler | $\bigcirc$ |  |
| Rated input voltage |  | 100 to 120VAC $50 / 60 \mathrm{~Hz}$ | 100 to 120VAC $50 / 60 \mathrm{~Hz}$ | $\bigcirc$ |  |
| Rated input current |  | 10 mA (100VAC 60Hz) | 10 mA (100VAC 60Hz) | $\bigcirc$ |  |
| Operating voltage range |  | 85 to 132VAC ( $50 / 60 \mathrm{~Hz} \pm 5 \%$ ) | 85 to 132VAC ( $50 / 60 \mathrm{~Hz} \pm 5 \%$ ) | $\bigcirc$ |  |
| Maximum number of simultaneous input points |  | 100\% (16 points/common) simultaneously ON | Refer to the derating chart. ${ }^{* 1}$ | $\Delta$ | Use it within the range shown in the derating chart. <br> When the voltage of the external supply power (module power supply) is high, the rate of $A C$ input simultaneous ON becomes small. |
| ON voltage/ON current |  | 80VAC or more/6mA or more | 80VAC or more/6mA or more | 0 |  |
| OFF voltage/OFF current |  | 40VAC or less/4mA or less | 26VAC or less/1.7mA or less | $\Delta$ | OFF voltage/OFF current is smaller. ${ }^{\text {2 }}{ }^{\text {2 }}$ |
| Inrush current |  | Maximum 300 mA , Within 0.3 ms (132VAC) | Maximum 300 mA , Within 0.3 ms (132VAC) | $\bigcirc$ |  |
| Input impedance |  | Approx. $10 \mathrm{k} \Omega(60 \mathrm{~Hz})$, <br> Approx. $12 \mathrm{k} \Omega(50 \mathrm{~Hz})$ | Approx. $10 \mathrm{k} \Omega(60 \mathrm{~Hz})$, <br> Approx. $12 \mathrm{k} \Omega(50 \mathrm{~Hz})$ | $\bigcirc$ |  |
| Response time | $\mathrm{OFF} \rightarrow \mathrm{ON}$ | 15 ms or less (6ms TYP.) | 14 ms or less <br> (11ms TYP.) | $\Delta$ | In combination with $Q$ series input module: <br> 15 ms or less ( 12 ms TYP.) ${ }^{* 3}$ |
|  | ON $\rightarrow$ OFF | 35 ms or less ( $16 \mathrm{~ms} \mathrm{TYP}. \mathrm{)}$ | 19ms or less (13ms TYP.) | $\Delta$ | In combination with Q series input module: <br> 20 ms or less (14ms TYP.) ${ }^{* 3}$ |
| Common terminal arrangement |  | 16 points/common (Common terminal: TB17, TB34) | 16 points/common (Common terminal: TB17, TB34) | O |  |
| Operation indication |  | Available <br> (Turning ON the input turns LED ON) | None | $\triangle$ | Operation indication can be checked with Q series input module. |

O : Compatible, $\Delta$ : Partially changed, $\times$ : Incompatible.

| Specifications |  | A0J2-E32A | SC-A0JQIF32A | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: | :---: |
| 5 VDC internal current consumption |  | 105mA (TYP. All points are ON.) | - | - |  |
| External supply power (Module power supply) | Voltage | None | $24 \mathrm{VDC} \pm 10 \%$ <br> Ripple voltage 4Vp-p or less | $\times$ | To deliver a power for programmable controller operation, connecting a module power supply to the interface module, TB35 or TB36 is required. |
|  | Current | None | 210 mA | $\times$ | If the voltage exceeds existing power capacity, add 24VDC power supply separately. |
| External connection method |  | 36-point terminal block connector (M3 $\times 6$ screws) | 36-point terminal block connector (M3 $\times 6$ screws) | 0 |  |
| Applicable wire size |  | 0.75 to $2 \mathrm{~mm}^{2}$ (Applicable tightening torque $69 \mathrm{~N} \cdot \mathrm{~cm}$ ) | $0.75 \text { to } 2 \mathrm{~mm}^{2}$ <br> (Applicable tightening torque $69 \mathrm{~N} \cdot \mathrm{~cm}$ ) | $\bigcirc$ |  |
| Applicable solderless terminal |  | $\begin{gathered} \text { 1.25-3, 1.25-YS3A, 2-S3, } \\ \text { 2-YS3A, V1.25-3, V1.25-YS3A, } \\ \text { V2-S3, V2-YS3A } \end{gathered}$ | $\begin{gathered} \text { 1.25-3, 1.25-YS3A, 2-S3, } \\ \text { 2-YS3A, V1.25-3, V1.25-YS3A, } \\ \text { V2-S3, V2-YS3A } \end{gathered}$ | 0 |  |
| Weight |  | 0.68kg | 0.40kg | $\triangle$ | Also consider the weight of fixed stand of programmable controller. ${ }^{*}{ }^{4}$ |
| External dimensions |  | $250(\mathrm{H}) \times 132(\mathrm{~W}) \times 41(\mathrm{D}) \mathrm{mm}$ | $182(\mathrm{H}) \times 132(\mathrm{~W}) \times 41(\mathrm{D}) \mathrm{mm}{ }^{* 5}$ | $\times$ | Check the dimensions since they depend on the installation type (building-up/horizontal/separate type). |

*1 The figure below shows the derating chart.
When the voltage of the external supply power (module power supply) is high, the rate of AC input simultaneous ON becomes small.

*2 Check that the specifications of leakage current of the used sensor and switches are equal to or less than the OFF current value.
If leakage current is equal to or more than the OFF current specifications, take measures against it with referring to "Input Module Troubleshooting" in the following handbook.
(Handbook for replacement)
Renewal tool for A0J2 series Transition from MELSEC-A0J2(H) series to renewal system using renewal tool.
*3 The value when using the input module QX41, etc. and 1 ms is set at input response time in I/O assignment setting of PLC parameter ( 10 ms is set at default).
*4 The weight of fixed stand of programmable controller depends on replacement type of renewal tool for A0J2.
*5 The external dimensions of the SC-A0JQIF32A do not include those of its projection.
(2) Specifications comparison between A0J2-E32D and interface module (SC-A0JQIF32D)

| Specifications |  | A0J2-E32D input specifications | SC-A0JQIF32D input specifications | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Number of input points |  | 32 points | 32 points | $\bigcirc$ |  |
| Insulation method |  | Photocoupler | Photocoupler | $\bigcirc$ |  |
| Rated input voltage |  | 12VDC/24VDC | 12VDC/24VDC | $\bigcirc$ |  |
| Rated input current |  | Approx. 3mA/Approx. 7 mA | Approx. 3mA/Approx. 7 mA | $\bigcirc$ |  |
| Operating voltage range |  | $\begin{gathered} 10.2 \text { to } 26.4 \mathrm{VDC} \\ \text { (Ripple ratio within } 5 \% \text { ) } \end{gathered}$ | $\begin{gathered} 10.2 \text { to } 26.4 \mathrm{VDC} \\ \text { (Ripple ratio within } 5 \% \text { ) } \end{gathered}$ | $\bigcirc$ |  |
| Maximum number of simultaneous input points |  | 100\% (16 points/common) simultaneously ON | Refer to the derating chart. ${ }^{* 1}$ | $\Delta$ | Use it within the range shown in the derating chart. |
| ON voltage/ON current |  | 9.5 VDC or more/ 2.6 mA or more | 9.5 VDC or more/2.6mA or more | $\bigcirc$ |  |
| OFF voltage/OFF current |  | 6VDC or less/1.0mA or less | 6 VDC or less/1.0mA or less | $\bigcirc$ |  |
| Input resistance |  | Approx. $3.4 \mathrm{k} \Omega$ | Approx. 3.3k $\Omega$ | $\bigcirc$ | Input resistance is smaller. |
| Input form |  | Sink input (Input current flows off.) | Sink input (Input current flows off.) | O |  |
| Response time | OFF $\rightarrow$ ON | 10 ms or less (6ms TYP.) | 5 ms or less (1ms TYP.) | $\Delta$ | In combination with $Q$ series input module: <br> 6 ms or less (2ms TYP.) ${ }^{*}{ }^{2}$ |
|  | ON $\rightarrow$ OFF | 10 ms or less ( $7.5 \mathrm{~ms} \mathrm{TYP)}$. | 5 ms or less (1ms TYP.) | $\Delta$ | In combination with $Q$ series input module: <br> 6 ms or less (2ms TYP.) ${ }^{*}{ }^{2}$ |
| Common terminal arrangement |  | 16 points/common (Common terminal: TB17, TB34) | 16 points/common (Common terminal: TB17, TB34) | $\bigcirc$ |  |
| Operation indication |  | Available <br> (Turning ON the input turns LED ON) | None | $\triangle$ | Operation indication can be checked with Q series input module. |

O : Compatible, $\Delta$ : Partially changed, $\times$ : Incompatible

| Specifications |  | A0J2-E32D | SC-A0JQIF32D | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: | :---: |
| 5 VDC internal current consumption |  | 105mA (TYP. All points are ON.) | - | - |  |
|  | Voltage | None | $24 \mathrm{VDC} \pm 10 \%$ <br> Ripple voltage 4Vp-p or less | $\times$ | To deliver a power for programmable controller operation, connecting a module power supply to the interface module, TB35 or TB36 is required. |
|  | Current | None | 200 mA | $\times$ | If the voltage exceeds existing power capacity, add 24VDC power supply separately. |
| External connection method |  | 36-point terminal block connector (M3 $\times 6$ screws) | 36-point terminal block connector (M3 $\times 6$ screws) | $\bigcirc$ |  |
| Applicable wire size |  | 0.75 to $2 \mathrm{~mm}^{2}$ (Applicable tightening torque $69 \mathrm{~N} \cdot \mathrm{~cm}$ ) | 0.75 to $2 \mathrm{~mm}^{2}$ (Applicable tightening torque $69 \mathrm{~N} \cdot \mathrm{~cm}$ ) | $\bigcirc$ |  |
| Applicable solderless terminal |  | $\begin{gathered} \text { 1.25-3, 1.25-YS3A, 2-S3, } \\ \text { 2-YS3A, V1.25-3, V1.25-YS3A, } \\ \text { V2-S3, V2-YS3A } \end{gathered}$ | $\begin{gathered} \text { 1.25-3, 1.25-YS3A, 2-S3, } \\ \text { 2-YS3A, V1.25-3, V1.25-YS3A, } \\ \text { V2-S3, V2-YS3A } \end{gathered}$ | $\bigcirc$ |  |
| Weight |  | 0.63 kg | 0.34 kg | $\Delta$ | Also consider the weight of fixed stand of programmable controller. ${ }^{* 3}$ |
| External dimensions |  | $250(\mathrm{H}) \times 132(\mathrm{~W}) \times 41(\mathrm{D}) \mathrm{mm}$ | $182(\mathrm{H}) \times 132(\mathrm{~W}) \times 41(\mathrm{D}) \mathrm{mm}^{* 4}$ | $\times$ | Check the dimensions since they depend on the installation type (building-up/horizontal/separate type). |

*1 The figure below shows the derating chart of the renewal tool for A0J2.
For the derating chart of the I/O module on the programmable controller side, which is to be connected to the renewal tool for A0J2, refer to the user's manual for the module to be used.

*2 The value when using the input module QX41, etc. and 1 ms is set at input response time in I/O assignment setting of PLC parameter ( 10 ms is set at default).
*3 The weight of fixed stand of programmable controller depends on replacement type of renewal tool for A0J2.
*4 The external dimensions of the SC-A0JQIF32D do not include those of its projection.
(3) Specifications comparison between A0J2-E24R and interface module (SC-A0JQIF24R)

| Specifications |  |  |  | O : Compatible, $\triangle$ : Partially changed, $\times$ : Incompatible |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | A0J2-E24R output specifications | SC-A0JQIF24R output specifications | Compatibility | Precautions for replacement |
| Number of output points |  | 24 points | 24 points | O |  |
| Insulation method |  | Photocoupler | None | $\Delta$ | Photocoupler is provided on Q series output module side. |
| Rated switching voltage/ current |  | 24VDC 2A (Resistance load)/point 240VAC 2A (COS $\phi=1$ )/point 5A/common | 24VDC 2A (Resistance load)/point 240VAC 2A (COS $\phi=1$ )/point 5A/common | $\bigcirc$ |  |
| Minimum switching load |  | 5VDC 1mA | 5VDC 1mA | $\bigcirc$ |  |
| Maximum switching voltage |  | 264VAC 125VDC | 264VAC 125VDC | $\bigcirc$ |  |
| Maximum switching frequency |  | 3600 times/hr | 3600 times/hr | $\bigcirc$ |  |
| Mechanical life |  | 20 million times or more | 20 million times or more | $\bigcirc$ |  |
| Electrical life |  | Rated switching voltage/current load 200,000 times or more | Rated switching voltage/current load 200,000 times or more | O |  |
|  |  | 200VAC 1.5A, 240VAC 1A $(\operatorname{COS} \phi=0.7)$ 200,000 times or more 200VAC 0.75A, 240VAC 0.5A (COS $\phi=0.35$ ) 200,000 times or more 24VDC 1A, 100VDC 0.1A (L/R=7ms) 200,000 times or more | 200VAC 1.5A, 240VAC 1A $(\operatorname{COS} \phi=0.7)$ 200,000 times or more 200VAC 0.75A, 240VAC 0.5A (COS $\phi=0.35$ ) 200,000 times or more 24VDC 1A, 100VDC 0.1A (L/R=7ms) 200,000 times or more | O |  |
| Response time | $\mathrm{OFF} \rightarrow \mathrm{ON}$ | 10 ms or less | 9 ms or less | $\Delta$ | In combination with $Q$ series output module: <br> 10 ms or less ( 6 ms TYP.) ${ }^{* 1}$ |
|  | ON $\rightarrow$ OFF | 12 ms or less | 11 ms or less | $\Delta$ | In combination with $Q$ series output module: <br> 12 ms or less (6ms TYP.) ${ }^{* 1}$ |
| External supply power (Relay coil driving power) | Voltage | $24 \mathrm{VDC} \pm 10 \%$ Ripple voltage $4 \mathrm{Vp}-\mathrm{p}$ or less | $24 \mathrm{VDC} \pm 10 \%$ Ripple voltage $4 \mathrm{Vp}-\mathrm{p}$ or less | $\bigcirc$ |  |
|  | Current | 230 mA <br> (24VDC All points are ON.) | 230 mA <br> (24VDC All points are ON.) | O |  |
| Surge suppressor |  | None | None | $\bigcirc$ |  |
| Fuse rating |  | None | None | $\bigcirc$ |  |
| Fuse blown indication |  | - | - | $\bigcirc$ |  |
| Relay socket |  | None | None | $\bigcirc$ |  |
| Common terminal arrangement |  | 8 points/common (Common terminal: TB9, TB19, TB29) | 8 points/common (Common terminal: TB9, TB19, TB29) | $\bigcirc$ |  |
| Operation indication |  | Available <br> (Turning ON the output turns LED ON) | None | $\Delta$ | Operation indication can be checked with $Q$ series output module. |

O : Compatible, $\Delta$ : Partially changed, $\times$ : Incompatible

| Specifications | A0J2-E24R | SC-A0JQIF24R | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: |
| 5 VDC internal current consumption | 145mA (TYP. All points are ON.) | - | - |  |
| External connection method | 36-point terminal block connector $\text { (M3 } \times 6 \text { screws) }$ | 36-point terminal block connector $\text { (M3 } \times 6 \text { screws) }$ | 0 |  |
| Applicable wire size | 0.75 to $2 \mathrm{~mm}^{2}$ (Applicable tightening torque $69 \mathrm{~N} \cdot \mathrm{~cm}$ ) | 0.75 to $2 \mathrm{~mm}^{2}$ (Applicable tightening torque $69 \mathrm{~N} \cdot \mathrm{~cm}$ ) | $\bigcirc$ |  |
| Applicable solderless terminal | $\begin{gathered} 1.25-3,1.25-\mathrm{YS} 3 \mathrm{~A}, 2-\mathrm{S} 3, \\ 2-\mathrm{YS} 3 \mathrm{~A}, \mathrm{~V} 1.25-3, \mathrm{~V} 1.25-\mathrm{YS} 3 \mathrm{~A}, \\ \text { V2-S3, V2-YS3A } \end{gathered}$ | $\begin{gathered} \text { 1.25-3, 1.25-YS3A, 2-S3, } \\ 2-\mathrm{YS} 3 \mathrm{~A}, \mathrm{~V} 1.25-3, \text { V1.25-YS3A, } \\ \text { V2-S3, V2-YS3A } \end{gathered}$ | $\bigcirc$ |  |
| Weight | 0.71 kg | 0.47 kg | $\triangle$ | Also consider the weight of fixed stand of programmable controller. ${ }^{*}{ }^{2}$ |
| External dimensions | $250(\mathrm{H}) \times 132(\mathrm{~W}) \times 41(\mathrm{D}) \mathrm{mm}$ | $182(\mathrm{H}) \times 132(\mathrm{~W}) \times 41(\mathrm{D}) \mathrm{mm}^{* 3}$ | $\times$ | Check the dimensions since they depend on the installation type (building-up/horizontal/separate type). |

*1 The value when using the output module, QX41Y41P, QY41P, etc.
*2 The weight of fixed stand of programmable controller depends on replacement type of renewal tool for A0J2.
*3 The external dimensions of the SC-A0JQIF24R do not include those of its projection.
(4) Specifications comparison between A0J2E-E24R and interface module (SC-A0JQIF24R)

| Specifications |  | A0J2E-E24R | SC-A0JQIF24R output specifications | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Number of output points |  | 24 points | 24 points | $\bigcirc$ |  |
| Insulation method |  | Photocoupler | None | $\triangle$ | Photocoupler is provided on $Q$ series output module side. |
| Rated switching voltage/ current |  | 24VDC 2A (Resistance load)/point 240VAC 2A (COS $\phi=1$ )/point 5A/common | 24VDC 2A (Resistance load)/point 240VAC 2A (COS $\phi=1$ )/point 5A/common | O |  |
| Minimum switching load |  | 5VDC 1mA | 5VDC 1mA | $\bigcirc$ |  |
| Maximum switching voltage |  | 250VAC 125VDC | 250VAC 125VDC | 0 |  |
| Leakage current at OFF |  | - | - | - |  |
| Maximum switching frequency |  | 3600 times/hr | 3600 times/hr |  |  |
| Mechanical life |  | 20 million times or more | 20 million times or more | O |  |
| Electrical life |  | Rated switching voltage/current load 200,000 times or more | Rated switching voltage/current load 200,000 times or more | O |  |
|  |  | 200VAC 1.5A, 240VAC 1A (COS $\phi=0.7$ ) 200,000 times or more 200VAC 0.75A, 240VAC 0.5A (COS $\phi=0.35$ ) 200,000 times or more 24VDC 1A, 100VDC 0.1A (L/R=7ms) 200,000 times or more | 200VAC 1.5A, 240VAC 1A $(\operatorname{COS} \phi=0.7)$ 200,000 times or more 200VAC 0.75A, 240VAC 0.5A (COS $\phi=0.35$ ) 200,000 times or more 24VDC 1A, 100VDC 0.1A (L/R=7ms) 200,000 times or more | 0 |  |
| Response time | OFF $\rightarrow$ ON | 10 ms or less | 9 ms or less | $\Delta$ | In combination with $Q$ series output module: <br> 10 ms or less (6ms TYP.) ${ }^{* 1}$ |
|  | ON $\rightarrow$ OFF | 12 ms or less | 11 ms or less | $\Delta$ | In combination with $Q$ series output module: <br> 12 ms or less ( 6 ms TYP.) ${ }^{* 1}$ |
| External supply power (Relay coil driving power) | Voltage | $24 \mathrm{VDC} \pm 10 \%$ Ripple voltage 4Vp-p or less | $24 \mathrm{VDC} \pm 10 \%$ Ripple voltage 4 Vp -p or less | O |  |
|  | Current | 220 mA <br> (24VDC All points are ON.) | $230 \mathrm{~mA}$ <br> (24VDC All points are ON.) | O |  |
| Surge suppressor |  | Varistor (387 to 473V) | None | $\times$ | The varistor is not built in. ${ }^{*}$ |
| Fuse |  | Available (8A)MF51NM8 or FGMA250V8A | None | $\times$ | The fuse is not built in. ${ }^{\text {3 }}$ |
| Fuse blown indication |  | None | - | - |  |
| Relay socket |  | None | None | - |  |
| Common terminal arrangement |  | 8 points/common (Common terminal: TB9, TB19, TB29) | 8 points/common (Common terminal: TB9, TB19, TB29) | O |  |
| Operation indication |  | Available (Turning ON the output turns LED ON) | None | $\Delta$ | Operation indication can be checked with $Q$ series output module. |
| Specifications |  | A0J2E-E24R | SC-A0JQIF24R | Compatibility | Precautions for replacement |
| 5 VDC internal current consumption |  | 0.145A (TYP. All points are ON.) | - | - |  |
| External connection method |  | 36-point terminal block connector (M3 $\times 6$ screws) | 36-point terminal block connector (M3 $\times 6$ screws) | O |  |
| Applicable wire size |  | 0.75 to $2 \mathrm{~mm}^{2}$ (Applicable tightening torque $69 \mathrm{~N} \cdot \mathrm{~cm}$ ) | 0.75 to $2 \mathrm{~mm}^{2}$ (Applicable tightening torque $69 \mathrm{~N} \cdot \mathrm{~cm}$ ) | $\bigcirc$ |  |
| Applicable solderless terminal |  | $\begin{gathered} \hline 1.25-3,1.25-\mathrm{YS} 3 \mathrm{~A}, \\ 2-\mathrm{S} 3,2-\mathrm{YS} 3 \mathrm{~A}, \\ \mathrm{~V} 1.25-3, \mathrm{~V} 1.25-\mathrm{YS} 3 \mathrm{~A}, \\ \mathrm{~V} 2-\mathrm{S} 3, \mathrm{~V} 2-\mathrm{YS} 3 \mathrm{~A} \end{gathered}$ | $\begin{gathered} \hline 1.25-3,1.25-\mathrm{YS} 3 \mathrm{~A}, \\ 2-\mathrm{S} 3,2-\mathrm{YS} 3 \mathrm{~A}, \\ \mathrm{~V} 1.25-3, \mathrm{~V} 1.25-\mathrm{YS} 3 \mathrm{~A}, \\ \mathrm{~V} 2-\mathrm{S} 3, \mathrm{~V} 2-\mathrm{YS} 3 \mathrm{~A} \end{gathered}$ | $\bigcirc$ |  |
| Weight |  | 0.75 kg | 0.47 kg | $\Delta$ | Also consider the weight of fixed stand of programmable controller. ${ }^{*} 4$ |
| External dimensions |  | $250(\mathrm{H}) \times 132(\mathrm{~W}) \times 41(\mathrm{D}) \mathrm{mm}$ | $182(\mathrm{H}) \times 132(\mathrm{~W}) \times 41(\mathrm{D}) \mathrm{mm}^{* 5}$ | $\times$ | Check the dimensions since they depend on the installation type (building-up/horizontal/separate type). |

*1 The value when using the output module, QX41Y41P, QY41P, etc.
*2 Connect the varistor externally to reduce noise.
*3 Install a fuse for each external terminal point to prevent the burnout of the external devices and modules during load shorts.
*4 The weight of fixed stand of programmable controller depends on replacement type of renewal tool for A0J2.
*5 The external dimensions of the SC-AOJQIF24R do not include those of its projection.
(5) Specifications comparison between A0J2-E24S and interface module (SC-A0JQIF24S)

| Specifications |  |  | O: Compatible, $\triangle$ : Partially changed, $\times$ : Incompatible |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | A0J2-E24S output specifications | SC-A0JQIF24S output specifications | Compatibility | Precautions for replacement |
| Number of output points |  | 24 points | 24 points | $\bigcirc$ |  |
| Insulation method |  | Photocoupler | Photocoupler | $\bigcirc$ |  |
| Rated switching voltage |  | 100 to $240 \mathrm{VAC}, 40-70 \mathrm{~Hz}$ | 100 to $240 \mathrm{VAC}, 47-63 \mathrm{~Hz}$ | $\triangle$ | The maximum frequency of SCAOJQIF24S is smaller than the one of A0J2-E24S. |
| Maximum switching voltage |  | 264AAC | 264VAC | $\bigcirc$ |  |
| Maximum switching current |  | 0.6A/point, 2.4A/common | 0.6A/point, 2.4A/common | $\bigcirc$ |  |
| Minimum switching voltage/current |  | 24VAC100mA, 100V/240VAC10mA | 24VAC100mA, 100V/240VAC10mA | $\bigcirc$ |  |
| Maximum inrush current |  | 20A10ms or less 8A100ms or less | 20A10ms or less 8A100ms or less | $\bigcirc$ |  |
| Leakage current at off |  | $\begin{gathered} \hline 1.5 \mathrm{~mA}(120 \mathrm{VAC} 60 \mathrm{~Hz}) \\ 3 \mathrm{~mA}(240 \mathrm{VAC} 60 \mathrm{~Hz}) \end{gathered}$ | $\begin{gathered} \hline 1.5 \mathrm{~mA}(120 \mathrm{VAC} 60 \mathrm{~Hz}) \\ 3 \mathrm{~mA}(240 \mathrm{VAC} 0 \mathrm{~Hz}) \end{gathered}$ | O |  |
| Maximum voltage drop at on |  | 1.5 V or less ( $0.1 \mathrm{~A}-0.6 \mathrm{~A}$ ) 1.8 V or less ( 0.1 A or less) 2.0 V or less $(10-50 \mathrm{~mA})$ | 1.5 V or less ( $0.1 \mathrm{~A}-0.6 \mathrm{~A}$ ) 1.8 V or less ( 0.1 A or less) 2.0 V or less $(10-50 \mathrm{~mA})$ | $\bigcirc$ |  |
| Temperature derating |  | None | Refer to temperature derating chart ${ }^{* 1}$ | $\Delta$ | Use it within the range shown in the temperature derating chart. |
| Response time | OFF $\rightarrow$ ON | 1 ms or less (6ms TYP.) | $1 \mathrm{~ms} \mathrm{or} \mathrm{less} \mathrm{(1ms} \mathrm{TYP)}$. | $\triangle$ | In combination with $Q$ series output module: $2 \mathrm{~ms} \text { or less }{ }^{*} 2$ |
|  | ON $\rightarrow$ OFF | 0.5 cycle +1 ms or less | 0.5 cycle +1 ms or less | $\Delta$ | In combination with $Q$ series output module: $0.5 \text { cycle }+2 \mathrm{~ms} \text { or less. }{ }^{*}$ |
| Fuse rating |  | Fast blow fuse 3.2A (1/common) HP-32 | None | $\times$ | Install one fuse per common externally. (A fuse and fuse holder are included.) |
| Fuse blown indication |  | Available <br> (LED turns on by fuse blown, and a signal is output to the CPU module.) | None | $\times$ |  |
| Surge <br> suppressor | $\begin{aligned} & \hline \text { CR } \\ & \text { absorber } \end{aligned}$ | $0.022 \mu \mathrm{~F}+47 \Omega$ | $0.015 \mu \mathrm{~F}+22 \Omega$ | $\triangle$ |  |
|  | Varistor | None | Varistor (400 to 540V) | $\triangle$ |  |
| Common terminal arrangement |  | 8 points/common (Common terminal: TB9, TB19, TB29) | 8 points/common (Common terminal: TB9, TB19, TB29) | O |  |
| Operation indication |  | Available (Turning ON the input turns LED ON) | None | $\triangle$ | Operation indication can be checked with Q series input module. |
| Specifications |  | A0J2-E24S | SC-A0JQIF24S | Compatibility | Precautions for replacement |
| 5VDC internal current consumption |  | 400mA (TYP. All points are ON.) | - | - |  |
| Externalsupplypower(Modulepowersupply) | Voltage | None | $24 \mathrm{VDC} \pm 10 \%$ Ripple voltage 4 Vp -p or less | $\times$ | To deliver a power for programmable controller operation, connecting a module power supply to the interface module, TB35 or TB36 is required. |
|  | Current | None | 370mA | $\times$ | If the voltage exceeds existing power capacity, add 24VDC power supply separately. |
| External connection method |  | 36-point terminal block connector (M3 $\times 6$ screws) | 36-point terminal block connector (M3 $\times 6$ screws) | $\bigcirc$ |  |
| Applicable wire size |  | 0.75 to $2 \mathrm{~mm}^{2}$ (Applicable tightening torque $69 \mathrm{~N} \cdot \mathrm{~cm}$ ) | 0.75 to $2 \mathrm{~mm}^{2}$ (Applicable tightening torque $69 \mathrm{~N} \cdot \mathrm{~cm}$ ) | $\bigcirc$ |  |
| Applicable solderless terminal |  | $\begin{gathered} \text { 1.25-3, 1.25-YS3A, 2-S3, } \\ \text { 2-YS3A, V1.25-3, V1.25-YS3A, } \\ \text { V2-S3, V2-YS3A } \\ \hline \end{gathered}$ | 1.25-3, 1.25-YS3A, 2-S3, 2-YS3A, V1.25-3, V1.25-YS3A, V2-S3, V2-YS3A | O |  |
| Weight |  | 0.70kg | 0.46 kg | $\triangle$ | Also consider the weight of fixed stand of programmable controller. ${ }^{* 3}$ |
| External dimensions |  | $250(\mathrm{H}) \times 132(\mathrm{~W}) \times 41(\mathrm{D}) \mathrm{mm}$ | $182(\mathrm{H}) \times 132(\mathrm{~W}) \times 41(\mathrm{D}) \mathrm{mm}^{*} 4$ | $\times$ | Check the dimensions since they depend on the installation type (building-up/horizontal/separate type). |

*1 The figure below shows the temperature derating chart.

*2 The value when using the output module QY41P, etc.
*3 The weight of fixed stand of programmable controller depends on replacement type of renewal tool for A0J2.
*4 The external dimensions of the SC-A0JQIF24S do not include those of its projection.
(6) Specifications comparison between A0J2-E24T and interface module (SC-A0JQIF24T)

| Specifications |  |  |  | pa | artially changed, |
| :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | A0J2-E24T output specifications | SC-A0JQIF24T output specifications | Compatibility | Precautions for replacement |
| Number of output points |  | 24 points | 24 points | $\bigcirc$ |  |
| Insulation method |  | Photocoupler | Photocoupler | $\bigcirc$ |  |
| Rated input voltage |  | 12VDC/24VDC | 12VDC/24VDC | $\bigcirc$ |  |
| Operating voltage range |  | 10.2 to 30VDC | 10.2 to 30VDC | $\bigcirc$ |  |
| Maximum switching current |  | 0.5A/point, 4A/common | 0.5A/point, 4A/common | $\bigcirc$ |  |
| Maximum inrush current |  | 4A 10ms or less | 4A 10ms or less | $\bigcirc$ |  |
| Leakage current at off |  | 0.1 mA or less | 0.1 mA or less | $\bigcirc$ |  |
| Maximum voltage drop at on |  | $\begin{aligned} & \hline 0.9 \mathrm{VDC}(\mathrm{TYP} .) 0.5 \mathrm{~A} \\ & \text { 1.5VDC(MAX.) } 0.5 \mathrm{~A} \end{aligned}$ | 0.9VDC(TYP.)0.5A <br> $0.8 \mathrm{VDC}(\mathrm{MAX})$. | $\bigcirc$ |  |
| Response time | OFF $\rightarrow$ ON | 2 ms or less | 1 ms or less | $\triangle$ | In combination with $Q$ series output module: <br> 2 ms or less ${ }^{* 1}$ |
|  | ON $\rightarrow$ OFF | 2 ms or less (Resistance load) | 2 ms or less (Resistance load) | $\triangle$ | In combination with $Q$ series output module: <br> 3 ms or less (Resistance load) ${ }^{* 1}$ |
| External supply power | Voltage | $\begin{gathered} 12 \mathrm{VDC} / 24 \mathrm{VDC} \\ (10.2 \mathrm{VDC}-30 \mathrm{VDC}) \end{gathered}$ | $\begin{gathered} 12 \mathrm{VDC} / 24 \mathrm{VDC} \\ (10.2 \mathrm{VDC}-30 \mathrm{VDC}) \end{gathered}$ | $\bigcirc$ |  |
|  | Current | 23mA (TYP.24VDC 8 points are ON/common.) | 5 mA (TYP.24VDC <br> 8 points are ON/common.) | $\bigcirc$ |  |
| Surge suppressor |  | Varistor (52V-62V) | Varistor (50.4V-61.6V) | $\bigcirc$ |  |
| Common terminal arrangement |  | 8 points/common <br> (Common terminal: TB9, TB19, TB29) | 8 points/common <br> (Common terminal: TB9, TB19, TB29) | $\bigcirc$ |  |
| Operation indication |  | Available <br> (Turning ON the output turns LED ON) | None | $\triangle$ | Operation indication can be checked with $Q$ series output module. |
| Fuse rating |  | None | 6.7A (Cannot be changed.) <br> (Fuse blown capacity: 50A) | $\bigcirc$ |  |
| Fuse blown indication |  | - | - | 0 |  |

O : Compatible, $\Delta$ : Partially changed, $\times$ : Incompatible

| Specifications |  | A0J2-E24T | SC-A0JQIF24T | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: | :---: |
| 5 VDC internal current consumption |  | 145mA (TYP. All points are ON.) | - | - |  |
| supply power (Module power supply) | Voltage | None | $24 \mathrm{VDC} \pm 10 \%$ <br> Ripple voltage 4 V p-p or less | $\times$ | To deliver a power for programmable controller operation, connecting a module power supply to the interface module, TB35 or TB36 is required. |
|  | Current | None | 70mA | × | If the voltage exceeds existing power capacity, add 24 VDC power supply separately. |
| External connection method |  | 36-point terminal block connector (M3 $\times 6$ screws) | 36-point terminal block connector (M3 $\times 6$ screws) | $\bigcirc$ |  |
| Applicable wire size |  | 0.75 to $2 \mathrm{~mm}^{2}$ (Applicable tightening torque $69 \mathrm{~N} \cdot \mathrm{~cm}$ ) | 0.75 to $2 \mathrm{~mm}^{2}$ (Applicable tightening torque $69 \mathrm{~N} \cdot \mathrm{~cm}$ ) | $\bigcirc$ |  |
| Applicable solderless terminal |  | $\begin{gathered} \text { 1.25-3, 1.25-YS3A, 2-S3, } \\ \text { 2-YS3A, V1.25-3, V1.25-YS3A, } \\ \text { V2-S3, V2-YS3A } \end{gathered}$ | $\begin{gathered} \text { 1.25-3, 1.25-YS3A, 2-S3, } \\ \text { 2-YS3A, V1.25-3, V1.25-YS3A, } \\ \text { V2-S3, V2-YS3A } \end{gathered}$ | $\bigcirc$ |  |
| Weight |  | 0.68kg | 0.35kg | $\triangle$ | Also consider the weight of fixed stand of programmable controller. ${ }^{* 2}$ |
| External dimensions |  | $250(\mathrm{H}) \times 132(\mathrm{~W}) \times 41(\mathrm{D}) \mathrm{mm}$ | $182(\mathrm{H}) \times 132(\mathrm{~W}) \times 41(\mathrm{D}) \mathrm{mm}^{* 3}$ | $\times$ | Check the dimensions since they depend on the installation type (building-up/horizontal/separate type). |

*1 The value when using the output module QY41P, etc.
*2 The weight of fixed stand of programmable controller depends on replacement type of renewal tool for A0J2.
*3 The external dimensions of the SC-A0JQIF24T do not include those of its projection.

## (7) Specifications comparison between A0J2-E28AR and interface module (SCA0JQIF28AR)

| Specifications |  | O : Compatible, $\triangle$ : Partially changed, $\times$ : Incompatible |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | A0J2-E28AR input specifications | SC-A0JQIF28AR input specifications | Compatibility | Precautions for replacement |
| Number of input points |  | 16 points | 16 points | $\bigcirc$ |  |
| Insulation method |  | Photocoupler | Photocoupler | $\bigcirc$ |  |
| Rated input voltage |  | 100 to 120VAC $50 / 60 \mathrm{~Hz}$ | 100 to 120VAC $50 / 60 \mathrm{~Hz}$ | $\bigcirc$ |  |
| Rated input current |  | 10 mA ( 100 VAC 60 Hz ) | 10 mA (100VAC 60Hz) | $\bigcirc$ |  |
| Operating voltage range |  | 85 to 132VAC ( $50 / 60 \mathrm{~Hz} \pm 5 \%$ ) | 85 to 132VAC ( $50 / 60 \mathrm{~Hz} \pm 5 \%$ ) | $\bigcirc$ |  |
| Maximum number of simultaneous input points |  | 60\% (16 points/common) simultaneously ON | Refer to the derating chart. ${ }^{* 1}$ | $\Delta$ | Use it within the range shown in the derating chart. <br> When the voltage of the external supply power (module power supply) is high, the rate of $A C$ input simultaneous ON becomes small. |
| ON voltage/ON current |  | 80VAC or more/6mA or more | 80VAC or more/6mA or more | $\bigcirc$ |  |
| OFF voltage/OFF current |  | 40VAC or less/4mA or less | 26 VAC or less/ 1.7 mA or less | $\triangle$ | OFF voltage/OFF current is smaller. ${ }^{\text {2 }}$ 2 |
| Inrush current |  | Maximum 300 mA , Within 0.3 ms (132VAC) | Maximum 300 mA , Within 0.3 ms (132VAC) | $\bigcirc$ |  |
| Input impedance |  | Approx. $10 \mathrm{k} \Omega(60 \mathrm{~Hz})$, <br> Approx. $12 \mathrm{k} \Omega(50 \mathrm{~Hz})$ | Approx. $10 \mathrm{k} \Omega(60 \mathrm{~Hz})$, <br> Approx. $12 \mathrm{k} \Omega(50 \mathrm{~Hz})$ | $\bigcirc$ |  |
| Response time | $\mathrm{OFF} \rightarrow \mathrm{ON}$ | 15 ms or less (6ms TYP.) | 14 ms or less (11ms TYP.) | $\triangle$ | In combination with $Q$ series input module: <br> 15 ms or less (12ms TYP.) ${ }^{* 3}$ |
|  | ON $\rightarrow$ OFF | 35 ms or less (16ms TYP.) | 19 ms or less (13ms TYP.) | $\Delta$ | In combination with $Q$ series input module: <br> 20 ms or less (14ms TYP.) ${ }^{* 3}$ |
| Common terminal arrangement |  | 16 points/common (Common terminal: TB17) | 16 points/common (Common terminal: TB17) | $\bigcirc$ |  |
| Operation indication |  | Available <br> (Turning ON the input turns LED ON) | None | $\triangle$ | Operation indication can be checked with Q series input module. |

O : Compatible, $\Delta$ : Partially changed, x : Incompatible

| Specifications |  | A0J2-E28AR output specifications | SC-A0JQIF28AR output specifications | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Number of output points |  | 12 points | 12 points | $\bigcirc$ |  |
| Insulation method |  | Photocoupler | Relay isolation | $\bigcirc$ |  |
| Rated switching voltage/ current |  | 24VDC 2A (Resistance load)/point 240VAC 2A (COS $\phi=1$ )/point 5A/common | 24VDC 2A (Resistance load)/point 240VAC 2A (COS $\phi=1$ )/point 5A/common | $\bigcirc$ |  |
| Minimum switching load |  | 5VDC 1mA | 5VDC 1mA | $\bigcirc$ |  |
| Maximum switching voltage |  | 264VAC 125VDC | 264VAC 125VDC | $\bigcirc$ |  |
| Maximum switching frequency |  | 3600 times/hr | 3600 times/hr | $\bigcirc$ |  |
| Mechanical life |  | 20 million times or more | 20 million times or more | $\bigcirc$ |  |
| Electrical life |  | Rated switching voltage/current load 200,000 times or more | Rated switching voltage/current load 200,000 times or more | $\bigcirc$ |  |
|  |  | 200VAC 1.5A, 240VAC 1A ( $\operatorname{COS} \phi=0.7$ ) 200,000 times or more 200VAC 0.75A, 240VAC 0.5A (COS $\phi=0.35$ ) 200,000 times or more 24VDC 1A, 100VDC 0.1A (L/R=7ms) 200,000 times or more | 200VAC 1.5A, 240VAC 1A $(\operatorname{COS} \phi=0.7)$ 200,000 times or more 200VAC 0.75A, 240VAC 0.5A $(\operatorname{COS} \phi=0.35)$ 200,000 times or more 24VDC 1A, 100VDC 0.1A (L/R=7ms) 200,000 times or more | O |  |
| Response time | OFF $\rightarrow$ ON | 10 ms or less | 9 ms or less | $\Delta$ | In combination with $Q$ series output module: <br> 10 ms or less ${ }^{*} 4$ |
|  | ON $\rightarrow$ OFF | 12 ms or less | 11 ms or less | $\Delta$ | In combination with $Q$ series output module: <br> 12 ms or less ${ }^{*} 4$ |
| External supply power (Relay coil driving power) | Voltage | $24 \mathrm{VDC} \pm 10 \%$ Ripple voltage 4 Vp -p or less | $24 \mathrm{VDC} \pm 10 \%$ Ripple voltage 4 Vp -p or less | $\bigcirc$ |  |
|  | Current | 125 mA <br> (24VDC All points are ON.) | 125 mA <br> (24VDC All points are ON.) | O |  |
| Surge suppressor |  | None | None | $\bigcirc$ |  |
| Common terminal arrangement |  | 8 points/common (Common terminal: TB26) 3 points/common (Common terminal: TB31) independent (Common terminal: TB31) | 8 points/common (Common terminal: TB26) 3 points/common (Common terminal: TB31) independent (Common terminal: TB31) | $\bigcirc$ |  |
| Operation indication |  | Available (Turning ON the output turns LED ON) | None | $\Delta$ | Operation indication can be checked with Q series output module. |
| Fuse rating |  | None | None | $\bigcirc$ |  |
| Fuse blown indication |  | - | - | - |  |
| Relay socket |  | None | None | $\bigcirc$ |  |


|  |  |  |  | O: | $\Delta$ : Partially changed, $\times$ : Incompatible |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Specifications |  | A0J2-E28AR | SC-A0JQIF28AR | Compatibility\| | Precautions for replacement |
| 5 VDC internal current consumption |  | 140mA (TYP. All points are ON.) | - | - |  |
| $\begin{aligned} & \text { External } \\ & \text { supply } \\ & \text { power } \\ & \text { (Module } \\ & \text { power } \\ & \text { supply) } \end{aligned}$ | Voltage | None | $24 \mathrm{VDC} \pm 10 \%$ <br> Ripple voltage 4Vp-p or less | $\times$ | To deliver a power for programmable controller operation, connecting a module power supply to the interface module, TB35 or TB36 is required. |
|  | Current | None | 105 mA | $\times$ | If the voltage exceeds existing power capacity, add 24VDC power supply separately. |
| External connection method |  | 36-point terminal block connector (M3 $\times 6$ screws) | 36-point terminal block connector <br> (M3 $\times 6$ screws) | $\bigcirc$ |  |
| Applicable wire size |  | 0.75 to $2 \mathrm{~mm}^{2}$ (Applicable tightening torque $69 \mathrm{~N} \cdot \mathrm{~cm}$ ) | 0.75 to $2 \mathrm{~mm}^{2}$ (Applicable tightening torque $69 \mathrm{~N} \cdot \mathrm{~cm}$ ) | $\bigcirc$ |  |
| Applicable solderless terminal |  | $\begin{gathered} \text { 1.25-3, 1.25-YS3A, 2-S3, } \\ 2-\mathrm{YS} 3 \mathrm{~A}, \mathrm{~V} 1.25-3, \mathrm{~V} 1.25-\mathrm{YS} 3 \mathrm{~A}, \\ \text { V2-S3, V2-YS3A } \end{gathered}$ | $\begin{gathered} \text { 1.25-3, 1.25-YS3A, 2-S3, } \\ \text { 2-YS3A, V1.25-3, V1.25-YS3A, } \\ \text { V2-S3, V2-YS3A } \end{gathered}$ | $\bigcirc$ |  |
| Weight |  | 0.68 kg | 0.44 kg | $\Delta$ | Also consider the weight of fixed stand of programmable controller. ${ }^{* 5}$ |
| External dimensions |  | $250(\mathrm{H}) \times 132(\mathrm{~W}) \times 41(\mathrm{D}) \mathrm{mm}$ | $182(\mathrm{H}) \times 132(\mathrm{~W}) \times 41(\mathrm{D}) \mathrm{mm}{ }^{*}$ | $\times$ | Check the dimensions since they depend on the installation type (building-up/horizontal/separate type). |

*1 The figure below shows the derating chart.

*2 Check that the specifications of leakage current of the used sensor and switches are equal to or less than the OFF current value.
If leakage current is equal to or more than the OFF current specifications, take measures against it with referring to "Input Module Troubleshooting" in the following handbook.
(Handbook for replacement)
Renewal tool for A0J2 series Transition from MELSEC-A0J2(H) series to renewal system using renewal tool.
*3 The value when using the input module, QX41Y41P, QX41, etc. and 5 ms is set at input response time in I/O assignment setting of PLC parameter ( 10 ms is set at default).
*4 The value when using the output module, QX41Y41P, QY41P, etc.
*5 The weight of fixed stand of programmable controller depends on replacement type of renewal tool for A0J2.
*6 The external dimensions of the SC-A0JQIF28AR do not include those of its projection.

## (8) Specifications comparison between A0J2-E28AS and interface module (SCA0JQIF28AS)



| Specifications |  | A0J2-E28AS output specifications | SC-A0JQIF28AS output specifications | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Number of output points |  | 12 points | 12 points | $\bigcirc$ |  |
| Insulation method |  | Photocoupler | Photocoupler | $\bigcirc$ |  |
| Rated switching voltage |  | 100 to 240VAC, $40-70 \mathrm{~Hz}$ | 100 to 240VAC, $47-63 \mathrm{~Hz}$ | $\bigcirc$ |  |
| Maximum switching voltage |  | 264VAC | 264VAC | $\bigcirc$ |  |
| Maximum switching current |  | 0.6A/point, 2.4A/common | 0.6A/point, 2.4A/common | $\bigcirc$ |  |
| Minimum switching voltage/current |  | 24VAC100mA, 100V/240VAC10mA | 24VAC100mA, 100V/240VAC10mA | $\bigcirc$ |  |
| Maximum inrush current |  | 20A10ms or less <br> 8A100ms or less | 20A10ms or less 8A100ms or less | $\bigcirc$ |  |
| Leakage current at off |  | $\begin{aligned} & \hline 1.5 \mathrm{~mA}(120 \mathrm{VAC} 60 \mathrm{~Hz}) \\ & 3 \mathrm{~mA}(240 \mathrm{VAC} 60 \mathrm{~Hz}) \end{aligned}$ | $\begin{gathered} \hline 1.5 \mathrm{~mA}(120 \mathrm{VAC} 60 \mathrm{~Hz}) \\ 3 \mathrm{~mA}(240 \mathrm{VAC} 60 \mathrm{~Hz}) \end{gathered}$ | $\bigcirc$ |  |
| Maximum voltage drop at on |  | 1.5 V or less ( $0.1 \mathrm{~A}-0.6 \mathrm{~A}$ ) <br> 1.8 V or less ( 0.1 A or less) <br> 2.0 V or less $(10-50 \mathrm{~mA})$ | 1.5 V or less ( $0.1 \mathrm{~A}-0.6 \mathrm{~A}$ ) 1.8 V or less ( 0.1 A or less) <br> 2.0 V or less ( $10-50 \mathrm{~mA}$ ) | $\bigcirc$ |  |
| Temperature derating |  | None | Refer to temperature derating chart ${ }^{* 3}$ | $\triangle$ | Use it within the range shown in the temperature derating chart. |
| Response time | OFF $\rightarrow$ ON | 1 ms or less | 1 ms or less | $\triangle$ | In combination with Q series input module: <br> 2 ms or less ( 6 ms TYP.) ${ }^{*}{ }^{4}$ |
|  | ON $\rightarrow$ OFF | 0.5 cycle +1 ms or less | 0.5 cycle +1 ms or less | $\triangle$ | In combination with $Q$ series input module: $0.5 \text { cycle }+2 \mathrm{~ms} \text { or less. }{ }^{*} 4$ |
| Fuse rating |  | Fast blow fuse 3.2A (1/common) HP-32 | None | $\times$ | Install one fuse per common externally. The fuse and fuse holder are included. |
| Fuse blown indication |  | Available <br> (LED turns on by fuse blown, and a signal is output to the CPU module.) | None | $\times$ |  |
| Surge suppressor | CR absorber | $0.022 \mu \mathrm{~F}+47 \Omega$ | $0.015 \mu \mathrm{~F}+22 \Omega$ | $\triangle$ |  |
|  | Varistor | None | Varistor (400 to 540V) | $\Delta$ |  |
| Common terminal arrangement |  | 8 points/common (Common terminal: TB26) <br> 4 points/common (Common terminal: TB33) | 8 points/common (Common terminal: TB26) 4 points/common (Common terminal: TB33) | $\bigcirc$ |  |
| Operation indication |  | Available <br> (Turning ON the input turns LED ON) | None | $\triangle$ | Operation indication can be checked with Q series input module. |
| Specifications |  | A0J2-E28AS | SC-A0JQIF28AS | Compatibility | Precautions for replacement |
| 5VDC internal current consumption |  | 260mA (TYP. All points are ON.) | - | - |  |
| External <br> supply <br> power <br> (Module <br> power <br> supply) | Voltage | None | $24 \mathrm{VDC} \pm 10 \%$ <br> Ripple voltage 4 V p-p or less | $\times$ | To deliver a power for programmable controller operation, connecting a module power supply to the interface module, TB35 or TB36 is required. |
|  | Current | None | 290 mA | $\times$ | If the voltage exceeds existing power capacity, add 24VDC power supply separately. |
| External connection method |  | 36-point terminal block connector (M3 $\times 6$ screws) | 36-point terminal block connector (M3 $\times 6$ screws) | $\bigcirc$ |  |
| Applicable wire size |  | 0.75 to $2 \mathrm{~mm}^{2}$ (Applicable tightening torque $69 \mathrm{~N} \cdot \mathrm{~cm}$ ) | 0.75 to $2 \mathrm{~mm}^{2}$ (Applicable tightening torque $69 \mathrm{~N} \cdot \mathrm{~cm}$ ) | $\bigcirc$ |  |
| Applicable solderless terminal |  | $\begin{gathered} \text { 1.25-3, 1.25-YS3A, 2-S3, } \\ 2-\mathrm{YS} 3 \mathrm{~A}, \mathrm{~V} 1.25-3, \mathrm{~V} 1.25-\mathrm{YS} 3 \mathrm{~A}, \\ \text { V2-S3, V2-YS3A } \end{gathered}$ | $\begin{gathered} 1.25-3,1.25-\mathrm{YS} 3 \mathrm{~A}, 2-\mathrm{S} 3, \\ 2-\mathrm{YS} 3 \mathrm{~A}, \mathrm{~V} 1.25-3, \mathrm{~V} 1.25-\mathrm{YS} 3 \mathrm{~A}, \\ \text { V2-S3, V2-YS3A } \end{gathered}$ | 0 |  |
| Weight |  | 0.68 kg | 0.43 kg | $\Delta$ | Also consider the weight of fixed stand of programmable controller. ${ }^{* 5}$ |
| External dimensions |  | $250(\mathrm{H}) \times 132(\mathrm{~W}) \times 41(\mathrm{D}) \mathrm{mm}$ | $182(\mathrm{H}) \times 132(\mathrm{~W}) \times 41(\mathrm{D}) \mathrm{mm}{ }^{*}$ | $\times$ | Check the dimensions since they depend on the installation type (building-up/horizontal/separate type). |

*1 Check that the specifications of leakage current of the used sensor and switches are equal to or less than the OFF current value.
If leakage current is equal to or more than the OFF current specifications, take measures against it with referring to "Input Module Troubleshooting" in the following handbook.
(Handbook for replacement)
Renewal tool for A0J2 series Transition from MELSEC-A0J2(H) series to renewal system using renewal tool
*2 The value when using the input module, QX41Y41P, QX41, etc. and 5 ms is set at input response time in I/O assignment setting of PLC parameter ( 10 ms is set at default).
*3 The figure below shows the temperature derating chart.

*4 The value when using the output module, QX41Y41P, QY41P, etc.
*5 The weight of fixed stand of programmable controller depends on replacement type of renewal tool for A0J2.
The external dimensions of the SC-A0JQIF28AS do not include those of its projection.

## (9) Specifications comparison between A0J2-E28DR and interface module (SCA0JQIF28DR)

| Specifications |  | O: Compatible, $\triangle$ : Partially changed, $\times$ : Incompatible |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | A0J2-E28DR input specifications | SC-A0JQIF28DR input specifications | Compatibility | Precautions for replacement |
| Number of input points |  | 16 points | 16 points | $\bigcirc$ |  |
| Insulation method |  | Photocoupler | Photocoupler | $\bigcirc$ |  |
| Rated input voltage |  | 12VDC/24VDC | 12VDC/24VDC | $\bigcirc$ |  |
| Rated input current |  | Approx. 3mA/Approx. 7 mA | Approx. 3mA/Approx. 7 mA | $\bigcirc$ |  |
| Operating voltage range |  | $\begin{gathered} 10.2 \text { to } 26.4 \mathrm{VDC} \\ \text { (Ripple ratio within 5\%) } \end{gathered}$ | $\begin{gathered} 10.2 \text { to } 26.4 \mathrm{VDC} \\ \text { (Ripple ratio within } 5 \% \text { ) } \end{gathered}$ | $\bigcirc$ |  |
| Maximum number of simultaneous input points |  | 100\% (16 points/common) simultaneously ON | 100\% (16 points/common) simultaneously ON | $\bigcirc$ |  |
| ON voltage/ON current |  | 9.5 VDC or more/2.6mA or more | 9.5 VDC or more/2.6mA or more | $\bigcirc$ |  |
| OFF voltage/OFF current |  | 6 VDC or less/1.0mA or less | 6 VDC or less/1.0mA or less | $\bigcirc$ |  |
| Input resistance |  | Approx. $3.4 \mathrm{k} \Omega$ | Approx. 3.3k $\Omega$ | $\bigcirc$ | Input resistance is smaller. |
| Input form |  | Sink input (Input current flows off.) | Sink input (Input current flows off.) | $\bigcirc$ |  |
| Response time | OFF $\rightarrow$ ON | 10 ms or less (6ms TYP.) | 5 ms or less (1ms TYP.) | $\Delta$ | In combination with $Q$ series input module: <br> 10 ms or less ( 6 ms TYP.) ${ }^{* 1}$ |
|  | ON $\rightarrow$ OFF | 10 ms or less ( $7.5 \mathrm{~ms} \mathrm{TYP)}$. | 5 ms or less (1ms TYP.) | $\Delta$ | In combination with $Q$ series input module: <br> 10 ms or less (6ms TYP.) ${ }^{* 1}$ |
| Common terminal arrangement |  | 16 points/common (Common terminal: TB17) | 16 points/common (Common terminal: TB17) | $\bigcirc$ |  |
| Operation indication |  | Available (Turning ON the input turns LED ON) | None | $\triangle$ | Operation indication can be checked with Q series input module. |

O : Compatible, $\Delta$ : Partially changed, x : Incompatible

| Specifications |  | A0J2-E28DR output specifications | SC-A0JQIF28DR output specifications | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Number of output points |  | 12 points | 12 points | $\bigcirc$ |  |
| Insulation method |  | Photocoupler | None | $\triangle$ | Photocoupler is provided on Q series output module side. |
| Rated switching voltage/ current |  | 24VDC 2A (Resistance load)/point 240VAC 2A (COS $\phi=1$ )/point 5A/common | 24VDC 2A (Resistance load)/point 240VAC 2A (COS $\phi=1$ )/point 5A/common | $\bigcirc$ |  |
| Minimum switching load |  | 5 VDC 1 mA | 5 VDC 1 mA | $\bigcirc$ |  |
| Maximum switching voltage |  | 264VAC 125VDC | 264VAC 125VDC | $\bigcirc$ |  |
| Maximum switching frequency |  | 3600 times/hr | 3600 times/hr | $\bigcirc$ |  |
| Mechanical life |  | 20 million times or more | 20 million times or more | O |  |
| Electrical life |  | Rated switching voltage/current load 200,000 times or more | Rated switching voltage/current load 200,000 times or more | $\bigcirc$ |  |
|  |  | 200VAC 1.5A, 240VAC 1A (COS $\phi=0.7$ ) 200,000 times or more 200VAC 0.75A, 240VAC 0.5A (COS $\phi=0.35$ ) 200,000 times or more 24VDC 1A, 100VDC 0.1A (L/R=7ms) 200,000 times or more | 200VAC 1.5A, 240VAC 1A $(\operatorname{COS} \phi=0.7)$ 200,000 times or more 200VAC 0.75A, 240VAC 0.5A $(\operatorname{COS} \phi=0.35)$ 200,000 times or more 24VDC 1A, 100VDC 0.1A (L/R=7ms) 200,000 times or more | O |  |
| Response time | $\mathrm{OFF} \rightarrow \mathrm{ON}$ | 10 ms or less | 9 ms or less | $\Delta$ | In combination with $Q$ series output module: $10 \mathrm{~ms} \text { or less }{ }^{*} 2$ |
|  | $\mathrm{ON} \rightarrow$ OFF | 12 ms or less | 11 ms or less | $\triangle$ | In combination with $Q$ series output module: <br> 12 ms or less*2 |
| External supply power (Relay coil driving power) | Voltage | $\begin{gathered} 24 \mathrm{VDC} \pm 10 \% \\ \text { Ripple voltage } 4 \mathrm{Vp} \text {-p or less } \end{gathered}$ | $\begin{gathered} 24 \mathrm{VDC} \pm 10 \% \\ \text { Ripple voltage } 4 \mathrm{Vp} \text {-p or less } \end{gathered}$ | O |  |
|  | Current | 125 mA <br> (24VDC All points are ON.) | 125 mA <br> (24VDC All points are ON.) | O |  |
| Surge suppressor |  | None | None | $\bigcirc$ |  |
| Fuse rating |  | None | None | $\bigcirc$ |  |
| Fuse blown indication |  | - | - | $\bigcirc$ |  |
| Relay socket |  | None | None | $\bigcirc$ |  |
| Common terminal arrangement |  | 8 points/common (Common terminal: TB26) <br> 3 points/common (Common terminal: TB31) Independent contact (Common terminal: TB33) | 8 points/common (Common terminal: TB26) 3 points/common (Common terminal: TB31) Independent contact (Common terminal: TB33) | O |  |
| Operation indication |  | Available (Turning ON the output turns LED ON) | None | $\Delta$ | Operation indication can be checked with $Q$ series output module. |


| Specifications |  | A0J2-E28DR | SC-A0JQIF28DR | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: | :---: |
| 5 VDC internal current consumption |  | 130mA (TYP. All points are ON.) | - | - |  |
| External supply power (Module power supply) | Voltage | None | $24 \mathrm{VDC} \pm 10 \%$ <br> Ripple voltage 4Vp-p or less | $\times$ | To deliver a power for programmable controller operation, connecting a module power supply to the interface module, TB27 or TB36 is required. |
|  | Current | None | 100mA | $\times$ | If the voltage exceeds existing power capacity, add 24VDC power supply separately. |
| External connection method |  | 36-point terminal block connector (M3 $\times 6$ screws) | 36-point terminal block connector (M3 $\times 6$ screws) | $\bigcirc$ |  |
| Applicable wire size |  | 0.75 to $2 \mathrm{~mm}^{2}$ (Applicable tightening torque $69 \mathrm{~N} \cdot \mathrm{~cm}$ ) | 0.75 to $2 \mathrm{~mm}^{2}$ (Applicable tightening torque $69 \mathrm{~N} \cdot \mathrm{~cm}$ ) | $\bigcirc$ |  |
| Applicable solderless terminal |  | $\begin{gathered} \text { 1.25-3, 1.25-YS3A, 2-S3, } \\ \text { 2-YS3A, V1.25-3, V1.25-YS3A, } \\ \text { V2-S3, V2-YS3A } \end{gathered}$ | $\begin{gathered} \text { 1.25-3, 1.25-YS3A, 2-S3, } \\ \text { 2-YS3A, V1.25-3, V1.25-YS3A, } \\ \text { V2-S3, V2-YS3A } \end{gathered}$ | $\bigcirc$ |  |
| Weight |  | 0.68kg | 0.42 kg | $\Delta$ | Also consider the weight of fixed stand of programmable controller. ${ }^{* 3}$ |
| External dimensions |  | $250(\mathrm{H}) \times 132(\mathrm{~W}) \times 41(\mathrm{D}) \mathrm{mm}$ | 182(H) $\times 132(\mathrm{~W}) \times 41(\mathrm{D}) \mathrm{mm}^{* 4}$ | $\times$ | Check the dimensions since they depend on the installation type (building-up/horizontal/separate type). |

*1 The value when using the input module, QX41Y41P, QX41, etc. and 5 ms is set at input response time in I/O assignment setting of PLC parameter ( 10 ms is set at default).
*2 The value when using the output module, QX41Y41P, QY41P, etc.
*3 The weight of fixed stand of programmable controller depends on replacement type of renewal tool for A0J2.
*4 The external dimensions of the SC-A0JQIF28DR do not include those of its projection.

## (10)Specifications comparison between A0J2-E28DS and interface module (SCA0JQIF28DS)

| Specifications |  |  |  | O : Compatible, $\triangle$ : Partially changed, $\times$ : Incompatible |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | A0J2-E28DS input specifications | SC-A0JQIF28DS input specifications | Compatibility | Precautions for replacement |
| Number of input points |  | 16 points | 16 points | $\bigcirc$ |  |
| Insulation method |  | Photocoupler | Photocoupler | $\bigcirc$ |  |
| Rated input voltage |  | 12VDC/24VDC | 12VDC/24VDC | $\bigcirc$ |  |
| Rated input current |  | Approx.3mA /Approx. 7 mA | Approx.3mA/Approx. 7 mA | $\bigcirc$ |  |
| Operating voltage range |  | $\begin{gathered} 10.2 \text { to } 26.4 \mathrm{VDC} \\ \text { (Ripple ratio within } 5 \% \text { ) } \end{gathered}$ | $\begin{gathered} 10.2 \text { to } 26.4 \mathrm{VDC} \\ \text { (Ripple ratio within } 5 \% \text { ) } \end{gathered}$ | $\bigcirc$ |  |
| Maximum number of simultaneous input points |  | 100\% (16 points/common) simultaneously ON | 100\% (16 points/common) simultaneously ON | $\bigcirc$ |  |
| ON voltage/ON current |  | 9.5 VDC or more/2.6mA or more | 9.5 VDC or more/2.6mA or more | $\bigcirc$ |  |
| OFF voltage/OFF current |  | 6 VDC or less/1.0mA or less | 6 VDC or less/1.0mA or less | $\bigcirc$ |  |
| Input impedance |  | Approx. $3.4 \mathrm{k} \Omega$ | Approx. 3.3k $\Omega$ | $\bigcirc$ |  |
| Input form |  | Sink input (Input current flows off.) | Sink input (Input current flows off.) | O |  |
| Response time | $\mathrm{OFF} \rightarrow \mathrm{ON}$ | 10 ms or less (6ms TYP.) | 5 ms or less (1ms TYP.) | $\triangle$ | In combination with $Q$ series input module: <br> 6 ms or less ( 2 ms TYP.) ${ }^{* 1}$ |
|  | $\mathrm{ON} \rightarrow$ OFF | 10 ms or less ( $7.5 \mathrm{~ms} \mathrm{TYP)}$. | 5 ms or less (1ms TYP.) | $\triangle$ | In combination with Q series input module: <br> 6 ms or less (2ms TYP.) ${ }^{* 1}$ |
| Common terminal arrangement |  | 16 points/common (Common terminal: TB17) | 16 points/common (Common terminal: TB17) | $\bigcirc$ |  |
| Operation indication |  | Available <br> (Turning ON the input turns LED ON) | None | $\triangle$ | Operation indication can be checked with Q series input module. |
| Specifications |  | A0J2-E28DS output specifications | SC-A0JQIF28DS output specifications | Compatibility | Precautions for replacement |
| Number of output points |  | 12 points | 12 points | $\bigcirc$ |  |
| Insulation method |  | Photocoupler | Photocoupler | $\bigcirc$ |  |
| Rated switching voltage |  | 100 to 240VAC, $40-70 \mathrm{~Hz}$ | 100 to $240 \mathrm{VAC}, 47-63 \mathrm{~Hz}$ | $\triangle$ | The maximum frequency of SCA0JQIF28DS is smaller than the one of A0J2-E28DS. |
| Maximum switching voltage |  | 264VAC | 264VAC | $\bigcirc$ |  |
| Maximum switching current |  | 0.6A/point, 2.4A/common | 0.6A/point, 2.4A/common | $\bigcirc$ |  |
| Minimum switching voltage/current |  | 24VAC 100mA, 100V/240VAC10mA | 24VAC 100mA, 100V/240VAC10mA | $\bigcirc$ |  |
| Maximum inrush current |  | 20A10ms or less 8A100ms or less | 20A10ms or less <br> 8A100ms or less | $\bigcirc$ |  |
| Leakage current at off |  | $\begin{gathered} \hline 1.5 \mathrm{~mA}(120 \mathrm{VAC} 60 \mathrm{~Hz}) \\ 3 \mathrm{~mA}(240 \mathrm{VAC} 60 \mathrm{~Hz}) \end{gathered}$ | $\begin{gathered} \hline 1.5 \mathrm{~mA}(120 \mathrm{VAC} 60 \mathrm{~Hz}) \\ 3 \mathrm{~mA}(240 \mathrm{VAC} 60 \mathrm{~Hz}) \end{gathered}$ | $\bigcirc$ |  |
| Maximum voltage drop at on |  | 1.5 V or less ( $0.1 \mathrm{~A}-0.6 \mathrm{~A}$ ) 1.8 V or less ( 0.1 A or less) 2.0 V or less $(10-50 \mathrm{~mA})$ | 1.5 V or less ( $0.1 \mathrm{~A}-0.6 \mathrm{~A}$ ) <br> 1.8 V or less ( 0.1 A or less) <br> 2.0 V or less $(10-50 \mathrm{~mA})$ | $\bigcirc$ |  |
| Temperature derating |  | None | None | $\bigcirc$ |  |
| Response time | $\mathrm{OFF} \rightarrow \mathrm{ON}$ | 1 ms or less | 1 ms or less | $\Delta$ | In combination with $Q$ series output module: <br> 2 ms or less. ${ }^{*}$ |
|  | ON $\rightarrow$ OFF | 0.5 cycle +1 ms or less | 0.5 cycle +1 ms or less | $\triangle$ | In combination with $Q$ series output module: $0.5 \text { cycle }+2 \mathrm{~ms} \text { or less. }{ }^{*}$ |
| Fuse rating |  | Fast blow fuse 3.2A (1/common) HP-32 | None | $\times$ | Install one fuse per common externally. (A fuse and fuse holder are included.) |
| Fuse blown indication |  | Available <br> (LED turns on by fuse blown, and a signal is output to the CPU module.) | Not available | $\times$ |  |


| Specifications |  | A0J2-E28DS input specifications | SC-A0JQIF28DS input specifications | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Surge suppressor | $\begin{aligned} & \text { CR } \\ & \text { absorber } \end{aligned}$ | $0.022 \mu \mathrm{~F}+47 \Omega$ | $0.015 \mu \mathrm{~F}+22 \Omega$ | $\triangle$ |  |
|  | Varistor | None | Varistor (400 to 540V) | $\triangle$ |  |
| Common terminal arrangement |  | 8 points/common (Common terminal: TB26) <br> 4 points/common (Common terminal: TB33) | 8 points/common (Common terminal: TB26) 4 points/common (Common terminal: TB33) | O |  |
| Operation indication |  | Available <br> (Turning ON the input turns LED ON) | None | $\triangle$ | Operation indication can be checked with Q series input module. |
| Specifications |  | A0J2-E28AS | SC-A0JQIF28AS | Compatibility | Precautions for replacement |
| 5VDC internal current consumption |  | 260mA (TYP. All points are ON.) | - | - |  |
| External <br> supply <br> power <br> (Module <br> power <br> supply) | Voltage | None | $24 \mathrm{VDC} \pm 10 \%$ Ripple voltage 4Vp-p or less | $\times$ | To deliver a power for programmable controller operation, connecting a module power supply to the interface module, TB35 or TB36 is required. |
|  | Current | None | 285 mA | $\times$ | If the voltage exceeds existing power capacity, add 24VDC power supply separately. |
| External connection method |  | 36-point terminal block connector $\text { (M3 } \times 6 \text { screws) }$ | 36-point terminal block connector (M3 $\times 6$ screws) | $\bigcirc$ |  |
| Applicable wire size |  | 0.75 to $2 \mathrm{~mm}^{2}$ (Applicable tightening torque $69 \mathrm{~N} \cdot \mathrm{~cm}$ ) | 0.75 to $2 \mathrm{~mm}^{2}$ (Applicable tightening torque $69 \mathrm{~N} \cdot \mathrm{~cm}$ ) | $\bigcirc$ |  |
| Applicable solderless terminal |  | $\begin{gathered} \text { 1.25-3, 1.25-YS3A, 2-S3, } \\ \text { 2-YS3A, V1.25-3, V1.25-YS3A, } \\ \text { V2-S3, V2-YS3A } \end{gathered}$ | $\begin{gathered} 1.25-3,1.25-\mathrm{YS} 3 \mathrm{~A}, 2-\mathrm{S} 3, \\ 2-\mathrm{YS} 3 \mathrm{~A}, \mathrm{~V} 1.25-3, \mathrm{~V} 1.25-\mathrm{YS} 3 \mathrm{~A}, \\ \text { V2-S3, V2-YS3A } \end{gathered}$ | $\bigcirc$ |  |
| Weight |  | 0.65 kg | 0.41 kg | $\triangle$ | Also consider the weight of fixed stand of programmable controller. ${ }^{* 3}$ |
| External dimensions |  | $250(\mathrm{H}) \times 132(\mathrm{~W}) \times 41(\mathrm{D}) \mathrm{mm}$ | $182(H) \times 132(\mathrm{~W}) \times 41(\mathrm{D}) \mathrm{mm}{ }^{*}$ | $\times$ | Check the dimensions since they depend on the installation type (building-up/horizontal/separate type). |

*1 The value when using the input module, QX41Y41P, QX41, etc. and 5ms is set at input response time in I/O assignment setting of PLC parameter ( 10 ms is set at default).
*2 The value when using the output module, QX41Y41P, QY41P, etc.
*3 The weight of fixed stand of programmable controller depends on replacement type of renewal tool for A0J2.
*4 The external dimensions of the SC-A0JQIF28DS do not include those of its projection.

## (11)Specifications comparison between A0J2-E28DT and interface module (SCA0JQIF28DT)

| Specifications |  | A0J2-E28DT input specifications | SC-A0JQIF28DT input specifications | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Number of input points |  | 16 points | 16 points | $\bigcirc$ |  |
| Insulation method |  | Photocoupler | Photocoupler | $\bigcirc$ |  |
| Rated input voltage |  | 12VDC/24VDC | 12VDC/24VDC | $\bigcirc$ |  |
| Rated input current |  | Approx. 3mA/Approx. 7 mA | Approx. 3mA/Approx. 7 mA | $\bigcirc$ |  |
| Operating voltage range |  | 10.2 to 26.4 VDC (Ripple ratio within 5\%) | 10.2 to 26.4 VDC (Ripple ratio within $5 \%$ ) | $\bigcirc$ |  |
| Maximum number of simultaneous input points |  | 100\% (16 points/common) simultaneously ON | 100\% (16 points/common) simultaneously ON | $\bigcirc$ |  |
| ON voltage/ON current |  | 9.5 VDC or more/2.6mA or more | 9.5 VDC or more/2.6mA or more | $\bigcirc$ |  |
| OFF voltage/OFF current |  | 6VDC or less/1.0mA or less | 6 VDC or less/1.0mA or less | $\bigcirc$ |  |
| Input resistance |  | Approx. $3.4 \mathrm{k} \Omega$ | Approx. $3.3 \mathrm{k} \Omega$ | $\bigcirc$ |  |
| Input form |  | Sink input (Input current flows off.) | Sink input (Input current flows off.) | $\bigcirc$ |  |
| Response time | OFF $\rightarrow$ ON | 10 ms or less (6ms TYP.) | 5 ms or less (1ms TYP.) | $\triangle$ | In combination with $Q$ series input module: <br> 10 ms or less ( 6 ms TYP.) ${ }^{* 1}$ |
|  | ON $\rightarrow$ OFF | 10 ms or less ( $7.5 \mathrm{~ms} \mathrm{TYP)}$. | 5 ms or less (1ms TYP.) | $\triangle$ | In combination with Q series input module: <br> 10 ms or less (6ms TYP.) ${ }^{* 1}$ |
| Common terminal arrangement |  | 16 points/common (Common terminal: TB17) | 16 points/common (Common terminal: TB17) | $\bigcirc$ |  |
| Operation indication |  | Available (Turning ON the input turns LED ON) | None | $\triangle$ | Operation indication can be checked with Q series input module. |
| Specifications |  | A0J2-E28DT output specifications | SC-A0JQIF28DT output specifications | Compatibility | Precautions for replacement |
| Number of output points |  | 12 points | 12 points | $\bigcirc$ |  |
| Insulation method |  | Photocoupler | Photocoupler | $\bigcirc$ |  |
| Rated load voltage |  | 12VDC/24VDC | 12VDC/24VDC | $\bigcirc$ |  |
| Operating load voltage range |  | 10.2 to 30VDC | 10.2 to 30VDC | $\bigcirc$ |  |
| Maximum load current |  | 0.5A/point, 4A/common | 0.5A/point, 4A/common | $\bigcirc$ |  |
| Maximum inrush current |  | 4A 10ms or less | 4A 10ms or less | $\bigcirc$ |  |
| Leakage current at OFF |  | 0.1 mA or less | 0.1 mA or less | $\bigcirc$ |  |
| Maximum voltage drop at ON |  | $\begin{aligned} & \hline 0.9 \mathrm{VDC} \text { (TYP.) } 0.5 \mathrm{~A} \\ & 1.5 \mathrm{VDC} \text { (MAX.) } 0.5 \mathrm{~A} \end{aligned}$ | $\begin{aligned} & \hline 0.5 \mathrm{VDC} \text { (TYP.) } 0.5 \mathrm{~A} \\ & 0.8 \mathrm{VDC} \text { (MAX.) } 0.5 \mathrm{~A} \end{aligned}$ | $\bigcirc$ |  |
| Response time | OFF $\rightarrow$ ON | 2 ms or less | 1 ms or less | $\triangle$ | In combination with $Q$ series output module: <br> 2 ms or less ${ }^{* 2}$ |
|  | ON $\rightarrow$ OFF | 2 ms or less (Resistance load) | 1 ms or less (Resistance load) | $\triangle$ | In combination with $Q$ series output module: <br> 2 ms or less (Resistance load) ${ }^{*}{ }^{2}$ |
| External supply power | Voltage | 12VDC/24VDC (10.2 to 30VDC) | 12VDC/24VDC (10.2 to 30VDC) | $\bigcirc$ |  |
|  | Current | 23 mA (TYP. 24VDC 8 points/common ON) | 5 mA (TYP. 24VDC 8 points/common ON) | $\bigcirc$ |  |
| Surge suppressor |  | Varistor (52 to 62V) | Varistor (50.4 to 61.6V) | $\bigcirc$ |  |
| Common terminal arrangement |  | 8 points/common (Common terminal: TB26) <br> 4 points/common (Common terminal: TB33) | 8 points/common (Common terminal: TB26) 4 points/common (Common terminal: TB33) | $\bigcirc$ |  |
| Operation indication |  | Available (Turning ON the output turns LED ON) | None | $\triangle$ | Operation indication can be checked with Q series output module. |
| Fuse |  | None | None | $\bigcirc$ |  |
| Fuse blown indication |  | None | None | 0 |  |


| Specifications |  | A0J2-E28DT | SC-A0JQIF28DT | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: | :---: |
| 5 VDC internal current consumption |  | 125 mA (TYP. All points are ON.) | - | - |  |
| External <br> supply <br> power <br> (Module <br> power <br> supply) | Voltage | None | $24 \mathrm{VDC} \pm 10 \%$ <br> Ripple voltage 4 V p-p or less | $\times$ | To deliver a power for programmable controller operation, connecting a module power supply to the interface module, TB35 or TB36 is required. |
|  | Current | None | 130 mA | $\times$ | If the voltage exceeds existing power capacity, add 24 VDC power supply separately. |
| External connection method |  | 36-point terminal block connector (M3 $\times 6$ screws) | 36-point terminal block connector (M3 $\times 6$ screws) | $\bigcirc$ |  |
| Applicable wire size |  | 0.75 to $2 \mathrm{~mm}^{2}$ (Applicable tightening torque $69 \mathrm{~N} \cdot \mathrm{~cm}$ ) | 0.75 to $2 \mathrm{~mm}^{2}$ (Applicable tightening torque $69 \mathrm{~N} \cdot \mathrm{~cm}$ ) | $\bigcirc$ |  |
| Applicable solderless terminal |  | $\begin{gathered} \text { 1.25-3, 1.25-YS3A, 2-S3, } \\ \text { 2-YS3A, V1.25-3, V1.25-YS3A, } \\ \text { V2-S3, V2-YS3A } \end{gathered}$ | $\begin{gathered} \text { 1.25-3, 1.25-YS3A, 2-S3, } \\ \text { 2-YS3A, V1.25-3, V1.25-YS3A, } \\ \text { V2-S3, V2-YS3A } \end{gathered}$ | 0 |  |
| Weight |  | 0.65 kg | 0.36kg | $\triangle$ | Also consider the weight of fixed stand of programmable controller. ${ }^{* 3}$ |
| External dimensions |  | $250(\mathrm{H}) \times 132(\mathrm{~W}) \times 41(\mathrm{D}) \mathrm{mm}$ | $182(\mathrm{H}) \times 132(\mathrm{~W}) \times 41(\mathrm{D}) \mathrm{mm}^{* 4}$ | $\times$ | Check the dimensions since they depend on the installation type (building-up/horizontal/separate type). |

*1 The value when using the input module, QX41Y41P, QX41, etc. and 5 ms is set at input response time in I/O assignment setting of PLC parameter ( 10 ms is set at default).
*2 The value when using the output module, QX41Y41P, QY41P, etc.
*3 The weight of fixed stand of programmable controller depends on replacement type of renewal tool for A0J2.
*4 The external dimensions of the SC-A0JQIF28DT do not include those of its projection.

## (12)Specifications comparison between A0J2-E56AR and interface module (SCA0JQIF56AR)

| Specifications |  |  |  | O : Compatible, $\triangle$ : Partially changed, $\times$ : Incompatible |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | A0J2-E56AR input specifications | SC-A0JQIF56AR input specifications | Compatibility | Precautions for replacement |
| Number of input points |  | 32 points | 32 points | $\bigcirc$ |  |
| Insulation method |  | Photocoupler | Photocoupler | $\bigcirc$ |  |
| Rated input voltage |  | 100 to 120VAC $50 / 60 \mathrm{~Hz}$ | 100 to 120VAC $50 / 60 \mathrm{~Hz}$ | $\bigcirc$ |  |
| Rated input current |  | 10 mA (100VAC 60Hz) | 10 mA (100VAC 60Hz) | $\bigcirc$ |  |
| Operating voltage range |  | 85 to 132VAC (50/60Hz $\pm 5 \%$ ) | 85 to 132VAC (50/60Hz $\pm 5 \%$ ) | $\bigcirc$ |  |
| Maximum number of simultaneous input points |  | 60\% (10 points/common) simultaneously ON | 60\% (10 points/common) simultaneously ON | O |  |
| ON voltage/ON current |  | 80VAC or more/6mA or more | 80VAC or more/6mA or more | $\bigcirc$ |  |
| OFF voltage/OFF current |  | 40VAC or less/4mA or less | 26VAC or less/1.7mA or less | $\Delta$ | OFF voltage/OFF current is smaller. ${ }^{\text {* }}$, |
| Inrush current |  | Maximum 300 mA , Within 0.3 ms (132VAC) | Maximum 300 mA , Within 0.3ms (132VAC) | $\bigcirc$ |  |
| Input impedance |  | Approx. $10 \mathrm{k} \Omega(60 \mathrm{~Hz})$, <br> Approx. $12 \mathrm{k} \Omega(50 \mathrm{~Hz})$ | Approx. $10 \mathrm{k} \Omega(60 \mathrm{~Hz})$, <br> Approx. $12 \mathrm{k} \Omega(50 \mathrm{~Hz})$ | $\bigcirc$ |  |
| Response time | OFF $\rightarrow$ ON | 15 ms or less ( $6 \mathrm{~ms} \mathrm{TYP)}$. | 14 ms or less (11ms TYP.) | $\triangle$ | In combination with $Q$ series input module: <br> 15 ms or less ( 12 ms TYP.) ${ }^{*}{ }^{2}$ |
|  | ON $\rightarrow$ OFF | 35 ms or less ( $16 \mathrm{~ms} \mathrm{TYP}. \mathrm{)}$ | 19 ms or less (13ms TYP.) | $\Delta$ | In combination with $Q$ series input module: <br> 20 ms or less (14ms TYP.) ${ }^{*}{ }^{2}$ |
| Common terminal arrangement |  | 16 points/common (Common terminal: TB17, TB34) | 16 points/common (Common terminal: TB17, TB34) | $\bigcirc$ |  |
| Operation indication |  | Available <br> (Turning ON the input turns LED ON) | None | $\Delta$ | Operation indication can be checked with Q series input module. |

O : Compatible, $\Delta$ : Partially changed, x : Incompatible

| Specifications |  | A0J2-E56AR output specifications | SC-A0JQIF56AR output specifications | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Number of output points |  | 24 points | 24 points | O |  |
| Insulation method |  | Photocoupler | None | $\triangle$ | Photocoupler is provided on Q series output module side. |
| Rated switching voltage/ current |  | 24VDC 2A (Resistance load)/point 240VAC 2A (COS $\phi=1$ )/point 5A/common | 24VDC 2A (Resistance load)/point 240VAC 2 A (COS $\phi=1$ )/point 5A/common | $\bigcirc$ |  |
| Minimum switching load |  | 5 VDC 1 mA | 5 VDC 1 mA | $\bigcirc$ |  |
| Maximum switching voltage |  | 264VAC 125VDC | 264VAC 125VDC | $\bigcirc$ |  |
| Maximum switching frequency |  | 3600 times/hr | 3600 times/hr | $\bigcirc$ |  |
| Mechanical life |  | 20 million times or more | 20 million times or more | $\bigcirc$ |  |
| Electrical life |  | Rated switching voltage/current load 200,000 times or more | Rated switching voltage/current load 200,000 times or more | $\bigcirc$ |  |
|  |  | 200VAC 1.5A, 240VAC 1A $(\operatorname{COS} \phi=0.7)$ 200,000 times or more 200VAC 0.75A, 240VAC 0.5A $(\operatorname{COS} \phi=0.35) 200,000$ times or more 24VDC 1A, 100VDC 0.1A (L/R=7ms) 200,000 times or more | 200VAC 1.5A, 240VAC 1A $(\operatorname{COS} \phi=0.7)$ 200,000 times or more 200VAC 0.75A, 240VAC 0.5A $(\operatorname{COS} \phi=0.35) 200,000$ times or more 24VDC 1A, 100VDC 0.1A (L/R=7ms) 200,000 times or more | O |  |
| Response time | $\mathrm{OFF} \rightarrow \mathrm{ON}$ | 10 ms or less | 9 ms or less | $\triangle$ | In combination with $Q$ series output module: <br> 10 ms or less ${ }^{*} 3$ |
|  | ON $\rightarrow$ OFF | 12 ms or less | 11 ms or less | $\triangle$ | In combination with $Q$ series output module: <br> 12 ms or less ${ }^{*} 3$ |
| External supply power (Relay coil driving power) | Voltage | $24 \mathrm{VDC} \pm 10 \%$ <br> Ripple voltage 4Vp-p or less | $24 \mathrm{VDC} \pm 10 \%$ <br> Ripple voltage 4Vp-p or less | $\bigcirc$ |  |
|  | Current | 230 mA <br> (24VDC All points are ON.) | 230 mA <br> (24VDC All points are ON.) | $\bigcirc$ |  |
| Surge suppressor |  | None | None | $\bigcirc$ |  |
| Fuse rating |  | None | None | $\bigcirc$ |  |
| Fuse blown indication |  | - | - | $\bigcirc$ |  |
| Relay socket |  | None | None | $\bigcirc$ |  |
| Common terminal arrangement |  | 8 points/common (Common terminal: TB9, TB19, TB29) | 8 points/common (Common terminal: TB9, TB19, TB29) | $\bigcirc$ |  |
| Operation indication |  | Available (Turning ON the output turns LED ON) | None | $\triangle$ | Operation indication can be checked with Q series output module. |

O : Compatible, $\Delta$ : Partially changed, $\times$ : Incompatible

| Specifications |  | A0J2-E56AR | SC-A0JQIF56AR | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: | :---: |
| 5VDC internal current consumption |  | 225 mA (TYP. All points are ON.) | - | - |  |
| Externalsupplypower(Modulepowersupply) | Voltage | None | $24 \mathrm{VDC} \pm 10 \%$ <br> Ripple voltage 4Vp-p or less | $\times$ | To deliver a power for programmable controller operation, connecting a module power supply to the interface module, TB35 or TB36 is required. |
|  | Current | None | 210 mA | x | If the voltage exceeds existing power capacity, add 24VDC power supply separately. |
| External connection method |  | 36-point terminal block connector (M3 $\times 6$ screws) 2 pieces | 36-point terminal block connector (M3 $\times 6$ screws) 2 pieces | $\bigcirc$ |  |
| Applicable wire size |  | 0.75 to $2 \mathrm{~mm}^{2}$ (Applicable tightening torque $69 \mathrm{~N} \cdot \mathrm{~cm}$ ) | 0.75 to $2 \mathrm{~mm}^{2}$ (Applicable tightening torque $69 \mathrm{~N} \cdot \mathrm{~cm}$ ) | $\bigcirc$ |  |
| Applicable solderless terminal |  | $\begin{gathered} 1.25-3,1.25-\mathrm{YS} 3 \mathrm{~A}, 2-\mathrm{S} 3, \\ 2-\mathrm{YS} 3 \mathrm{~A}, \mathrm{~V} 1.25-3, \mathrm{~V} 1.25-\mathrm{YS} 3 \mathrm{~A}, \\ \text { V2-S3, V2-YS3A } \end{gathered}$ | $\begin{gathered} 1.25-3,1.25-\mathrm{YS} 3 \mathrm{~A}, 2-\mathrm{S} 3, \\ 2-\mathrm{YS} 3 \mathrm{~A}, \mathrm{~V} 1.25-3, \mathrm{~V} 1.25-\mathrm{YS} 3 \mathrm{~A}, \\ \text { V2-S3, V2-YS3A } \end{gathered}$ | $\bigcirc$ |  |
| Weight |  | 1.10kg | 0.66kg | $\Delta$ | Also consider the weight of fixed stand of programmable controller. ${ }^{* 4}$ |
| External dimensions |  | $250(\mathrm{H}) \times 190(\mathrm{~W}) \times 41(\mathrm{D}) \mathrm{mm}$ | $182(H) \times 190(\mathrm{~W}) \times 41(\mathrm{D}) \mathrm{mm}{ }^{* 5}$ | $\times$ | Check the dimensions since they depend on the installation type (building-up/horizontal/separate type). |

*1 Check that the specifications of leakage current of the used sensor and switches are equal to or less than the OFF current value.
If leakage current is equal to or more than the OFF current specifications, take measures against it with referring to "Input Module Troubleshooting" in the following handbook.
(Handbook for replacement)
Renewal tool for A0J2 series Transition from MELSEC-A0J2(H) series to renewal system using renewal tool (refer to
Appendix 3.5.)
*2 The value when using the input module, QX41Y41P, QX41, etc. and 1 ms is set at input response time in I/O assignment setting of PLC parameter ( 10 ms is set at default).
*3 The value when using the output module, QX41Y41P, QY41P, etc.
*4 The weight of fixed stand of programmable controller depends on replacement type of renewal tool for A0J2.
*5 The external dimensions of the SC-A0JQIF56AR do not include those of its projection.

## (13)Specifications comparison between A0J2-E56AS and interface module (SCA0JQIF56AS)

|  |  |  |  | O : Compatible | Partially changed, x : Incompatible |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Specifications |  | A0J2-E56AS input specifications | SC-A0JQIF56AS input specifications | Compatibility | Precautions for replacement |
| Number of input points |  | 32 points | 32 points | $\bigcirc$ |  |
| Insulation method |  | Photocoupler | Photocoupler | $\bigcirc$ |  |
| Rated input voltage |  | 100 to 120VAC 50/60Hz | 100 to 120VAC $50 / 60 \mathrm{~Hz}$ | $\bigcirc$ |  |
| Rated input current |  | 10 mA (100VAC 60Hz) | 10 mA (100VAC 60Hz) | $\bigcirc$ |  |
| Operating voltage range |  | 85 to 132VAC ( $50 / 60 \mathrm{~Hz} \pm 5 \%$ ) | 85 to 132VAC ( $50 / 60 \mathrm{~Hz} \pm 5 \%$ ) | $\bigcirc$ |  |
| Maximum number of simultaneous input points |  | 60\% (10 points/common) simultaneously ON | 60\% (10 points/common) simultaneously ON | $\bigcirc$ |  |
| ON voltage/ON current |  | 80VAC or more/6mA or more | 80VAC or more/6mA or more | $\bigcirc$ |  |
| OFF voltage/OFF current |  | 40VAC or less/4mA or less | 26VAC or less/1.7mA or less | $\Delta$ | OFF voltage/OFF current is smaller. ${ }^{\text {.1 }}$ |
| Inrush current |  | Maximum 300 mA , Within 0.3 ms (132VAC) | Maximum 300 mA , Within 0.3 ms (132VAC) | $\bigcirc$ |  |
| Input impedance |  | Approx. $10 \mathrm{k} \Omega(60 \mathrm{~Hz})$, <br> Approx. $12 \mathrm{k} \Omega(50 \mathrm{~Hz})$ | Approx. $10 \mathrm{k} \Omega(60 \mathrm{~Hz})$, <br> Approx. $12 \mathrm{k} \Omega(50 \mathrm{~Hz})$ | $\bigcirc$ |  |
| Response time | OFF $\rightarrow$ ON | 15 ms or less (6ms TYP.) | 14ms or less (11ms TYP.) | $\Delta$ | In combination with $Q$ series input module: <br> 15 ms or less ( 12 ms TYP.) ${ }^{*}{ }^{2}$ |
|  | ON $\rightarrow$ OFF | 35 ms or less (16ms TYP.) | 19ms or less (13ms TYP.) | $\Delta$ | In combination with $Q$ series input module: <br> 20 ms or less (14ms TYP.) ${ }^{*}{ }^{2}$ |
| Common terminal arrangement |  | 16 points/common (Common terminal: TB17) | 16 points/common (Common terminal: TB17) | $\bigcirc$ |  |
| Operation indication |  | Available <br> (Turning ON the input turns LED ON) | None | $\Delta$ | Operation indication can be checked with Q series input module. |
| Specifications |  | A0J2-E56AS output specifications | SC-A0JQIF56AS output specifications | Compatibility | Precautions for replacement |
| Number of output points |  | 24 points | 24 points | $\bigcirc$ |  |
| Insulation method |  | Photocoupler | Photocoupler | $\bigcirc$ |  |
| Rated switching voltage |  | 100 to $240 \mathrm{VAC}, 40-70 \mathrm{~Hz}$ | 100 to $240 \mathrm{VAC}, 47-63 \mathrm{~Hz}$ | $\triangle$ | The maximum frequency of SCA0JQIF56AS is smaller than the one of A0J2-E56AS. |
| Maximum switching voltage |  | 264VAC | 264VAC | O |  |
| Maximum switching current |  | 0.6A/point, 2.4A/common | 0.6A/point, 2.4A/common | O |  |
| Minimum switching voltage/current |  | 24V100mAAC, 100VAC/ 240VAC10mA | $24 \mathrm{~V} 100 \mathrm{mAAC}, 100 \mathrm{VAC} /$ 240VAC10mA | $\bigcirc$ |  |
| Maximum inrush current |  | 20A10ms or less <br> 8A100ms or less | 20A10ms or less <br> 8A100ms or less | O |  |
| Leakage current at off |  | $\begin{gathered} \hline 1.5 \mathrm{~mA}(120 \mathrm{VAC} 60 \mathrm{~Hz}) \\ 3 \mathrm{~mA}(240 \mathrm{VAC} 60 \mathrm{~Hz}) \end{gathered}$ | $\begin{gathered} \hline 1.5 \mathrm{~mA}(120 \mathrm{VAC} 60 \mathrm{~Hz}) \\ 3 \mathrm{~mA}(240 \mathrm{VAC} 60 \mathrm{~Hz}) \end{gathered}$ | $\bigcirc$ |  |
| Maximum voltage drop at on |  | 1.5 V or less ( $0.1 \mathrm{~A}-0.6 \mathrm{~A}$ ) 1.8 V or less ( 0.1 A or less) 2.0 V or less $(10-50 \mathrm{~mA})$ | 1.5 V or less ( $0.1 \mathrm{~A}-0.6 \mathrm{~A}$ ) 1.8 V or less ( 0.1 A or less) 2.0 V or less $(10-50 \mathrm{~mA})$ | $\bigcirc$ |  |
| Temperature derating |  | None | Refer to temperature derating chart ${ }^{* 3}$ | $\Delta$ | Use it within the range shown in the temperature derating chart. |
| Response time | OFF $\rightarrow$ ON | 1 ms or less | 1 ms or less | $\Delta$ | In combination with $Q$ series input module: <br> 2 ms or less. ${ }^{* 4}$ |
|  | ON $\rightarrow$ OFF | 0.5 cycle +1 ms or less | 0.5 cycle +1 ms or less | $\times$ | In combination with $Q$ series input module: $0.5 \text { cycle }+2 \mathrm{~ms} \text { or less. }{ }^{*}$ |
| Fuse rating |  | Fast blow fuse 3.2A (1/common) HP-32 | None | $\times$ | Install a fuse externally <br> (1/common). <br> (A fuse and fuse holder are included.) |
| Fuse blown indication |  | Available <br> (LED turns on by fuse blown, and a signal is output to the CPU module.) | Not available | $\times$ |  |


| Specifications |  | A0J2-E56AS output specifications | SC-A0JQIF56AS output specifications | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Surge <br> suppressor | CR <br> absorber | $0.022 \mu \mathrm{~F}+47 \Omega$ | $0.015 \mu \mathrm{~F}+22 \Omega$ | $\triangle$ |  |
|  | Varistor | None | Varistor (400 to 540V) | $\Delta$ |  |
| Common terminal arrangement |  | 8 points/common (Common terminal: TB9, TB19, TB29) | 8 points/common (Common terminal: TB9, TB19, TB29) | $\bigcirc$ |  |
| Operation indication |  | Available <br> (Turning ON the input turns LED ON) | None | $\triangle$ | Operation indication can be checked with Q series input module. |
| Specifications |  | A0J2-E56AS | SC-A0JQIF56AS | Compatibility | Precautions for replacement |
| 5 VDC internal current consumption |  | 460mA (TYP. All points are ON.) | - | - |  |
|  | Voltage | None | $24 \mathrm{VDC} \pm 10 \%$ <br> Ripple voltage 4Vp-p or less | $\times$ | To deliver a power for programmable controller operation, connecting a module power supply to the interface module, TB35 or TB36 is required. |
|  | Current | None | 580mA | $\times$ | If the voltage exceeds existing power capacity, add 24VDC power supply separately. |
| External connection method |  | 36-point terminal block connector (M3 $\times 6$ screws) 2 pieces | 36-point terminal block connector (M3 $\times 6$ screws) 2 pieces | $\bigcirc$ |  |
| Applicable wire size |  | 0.75 to $2 \mathrm{~mm}^{2}$ (Applicable tightening torque $69 \mathrm{~N} \cdot \mathrm{~cm}$ ) | 0.75 to $2 \mathrm{~mm}^{2}$ (Applicable tightening torque $69 \mathrm{~N} \cdot \mathrm{~cm}$ ) | $\bigcirc$ |  |
| Applicable solderless terminal |  | $\begin{gathered} 1.25-3,1.25-\mathrm{YS} 3 \mathrm{~A}, 2-\mathrm{S} 3, \\ 2-\mathrm{YS} 3 \mathrm{~A}, \mathrm{~V} 1.25-3, \mathrm{~V} 1.25-\mathrm{YS} 3 \mathrm{~A}, \\ \mathrm{~V} 2-\mathrm{S} 3, \mathrm{~V} 2-\mathrm{YS} 3 \mathrm{~A} \end{gathered}$ | $\begin{gathered} 1.25-3,1.25-\mathrm{YS} 3 \mathrm{~A}, 2-\mathrm{S} 3, \\ 2-\mathrm{YS} 3 \mathrm{~A}, \mathrm{~V} 1.25-3, \mathrm{~V} 1.25-\mathrm{YS} 3 \mathrm{~A}, \\ \text { V2-S3, V2-YS3A } \end{gathered}$ | $\bigcirc$ |  |
| Weight |  | 1.10 kg | 0.66kg | $\Delta$ | Also consider the weight of fixed stand of programmable controller. ${ }^{* 5}$ |
| External dimensions |  | $250(\mathrm{H}) \times 190(\mathrm{~W}) \times 41(\mathrm{D}) \mathrm{mm}$ | $182(H) \times 190(\mathrm{~W}) \times 41(\mathrm{D}) \mathrm{mm}{ }^{*} 6$ | $\times$ | Check the dimensions since they depend on the installation type (building-up/horizontal/separate type). |

*1 Check that the specifications of leakage current of the used sensor and switches are equal to or less than the OFF current value.
If leakage current is equal to or more than the OFF current specifications, take measures against it with referring to "Input Module Troubleshooting" in the following handbook.
(Handbook for replacement)
Renewal tool for A0J2 series Transition from MELSEC-A0J2(H) series to renewal system using renewal tool (refer to Appendix 3.5.)
*2 The value when using the input module, QX41Y41P, QX41, etc. and 5 ms is set at input response time in I/O assignment setting of PLC parameter ( 10 ms is set at default).
*3 The figure below shows the temperature derating chart.

*4 The value when using the output module, QX41Y41P, QY41P, etc.
*5 The weight of fixed stand of programmable controller depends on replacement type of renewal tool for A0J2.
*6 The external dimensions of the SC-A0JQIF56AS do not include those of its projection.
(14)Specifications comparison between A0J2-E56DR and interface module (SCA0JQIF56DR)

| Specifications |  | A0J2-E56DR input specifications | SC-A0JQIF56DR input specifications | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Number of input points |  | 32 points | 32 points | $\bigcirc$ |  |
| Insulation method |  | Photocoupler | Photocoupler | $\bigcirc$ |  |
| Rated input voltage |  | 12VDC/24VDC | 12VDC/24VDC | $\bigcirc$ |  |
| Rated input current |  | Approx. 3mA/Approx. 7 mA | Approx. 3mA/Approx. 7 mA | $\bigcirc$ |  |
| Operating voltage range |  | $\begin{gathered} 10.2 \text { to } 26.4 \mathrm{VDC} \\ \text { (Ripple ratio within } 5 \% \text { ) } \end{gathered}$ | $\begin{gathered} 10.2 \text { to } 26.4 \mathrm{VDC} \\ \text { (Ripple ratio within } 5 \% \text { ) } \end{gathered}$ | $\bigcirc$ |  |
| Maximum number of simultaneous input points |  | 60\% (10 points/common) simultaneously ON | 60\% (10 points/common) simultaneously ON | O |  |
| ON voltage/ON current |  | 9.5 VDC or more/2.6mA or more | 9.5VDC or more/2.6mA or more | $\bigcirc$ |  |
| OFF voltage/OFF current |  | 6 VDC or less/1.0mA or less | 6VDC or less/1.0mA or less | $\bigcirc$ |  |
| Input resistance |  | Approx. $3.4 \mathrm{k} \Omega$ | Approx. 3.3k $\Omega$ | $\bigcirc$ |  |
| Input form |  | Sink input (Input current flows off.) | Sink input (Input current flows off.) | $\bigcirc$ |  |
| Response time | $\mathrm{OFF} \rightarrow \mathrm{ON}$ | 10 ms or less (6ms TYP.) | 5 ms or less (1ms TYP.) | $\Delta$ | In combination with $Q$ series input module: <br> 10 ms or less ( 6 ms TYP.) ${ }^{* 1}$ |
|  | ON $\rightarrow$ OFF | 10 ms or less ( $7.5 \mathrm{~ms} \mathrm{TYP)}$. | 5 ms or less (1ms TYP.) | $\Delta$ | In combination with Q series input module: <br> 10 ms or less ( 6 ms TYP.) ${ }^{* 1}$ |
| Common terminal arrangement |  | 16 points/common (Common terminal: TB17, TB34) | 16 points/common (Common terminal: TB17, TB34) | $\bigcirc$ |  |
| Operation indication |  | Available <br> (Turning ON the input turns LED ON) | None | $\triangle$ | Operation indication can be checked with Q series input module. |

O : Compatible, $\Delta$ : Partially changed, x : Incompatible

| Specifications |  | A0J2-E56DR output specifications | SC-A0JQIF56DR output specifications | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Number of output points |  | 24 points | 24 points | $\bigcirc$ |  |
| Insulation method |  | Photocoupler | None | $\Delta$ | Photocoupler is provided on $Q$ series output module side. |
| Rated switching voltage/ current |  | 24VDC 2A (Resistance load)/point 240VAC 2A (COS $\phi=1$ )/point 5A/common | 24VDC 2A (Resistance load)/point 240VAC 2A (COS $\phi=1$ )/point 5A/common | $\bigcirc$ |  |
| Minimum switching load |  | 5VDC 1mA | 5VDC 1mA | $\bigcirc$ |  |
| Maximum switching voltage |  | 264VAC 125VDC | 264VAC 125VDC | O |  |
| Maximum switching frequency |  | 3600 times/hr | 3600 times/hr | $\bigcirc$ |  |
| Mechanical life |  | 20 million times or more | 20 million times or more | $\bigcirc$ |  |
| Electrical life |  | Rated switching voltage/current load 200,000 times or more | Rated switching voltage/current load 200,000 times or more | O |  |
|  |  | 200VAC 1.5A, 240VAC 1A $(\operatorname{COS} \phi=0.7)$ 200,000 times or more 200VAC 0.75A, 240VAC 0.5A (COS $\phi=0.35$ ) 200,000 times or more 24VDC 1A, 100VDC 0.1A (L/R=7ms) 200,000 times or more | 200VAC 1.5A, 240VAC 1A $(\operatorname{COS} \phi=0.7)$ 200,000 times or more 200VAC 0.75A, 240VAC 0.5A (COS $\phi=0.35$ ) 200,000 times or more 24VDC 1A, 100VDC 0.1A (L/R=7ms) 200,000 times or more | O |  |
| Response time | OFF $\rightarrow$ ON | 10 ms or less | 9 ms or less | $\Delta$ | In combination with $Q$ series output module: <br> 10 ms or less ${ }^{* 2}$ |
|  | ON $\rightarrow$ OFF | 12 ms or less | 11 ms or less | $\Delta$ | In combination with $Q$ series output module: <br> 12 ms or less ${ }^{* 2}$ |
| External supply power (Relay coil driving power) | Voltage | $24 \mathrm{VDC} \pm 10 \%$ Ripple voltage 4 Vp -p or less | $24 \mathrm{VDC} \pm 10 \%$ Ripple voltage 4 Vp -p or less | O |  |
|  | Current | 230 mA (24VDC All points are ON.) | 230 mA (24VDC All points are ON.) | O |  |
| Surge suppressor |  | None | None | $\bigcirc$ |  |
| Fuse rating |  | None | None | $\bigcirc$ |  |
| Fuse blown indication |  | - | - | $\bigcirc$ |  |
| Relay socket |  | None | None | $\bigcirc$ |  |
| Common terminal arrangement |  | 8 points/common (Common terminal: TB9, TB19, TB29) | 8 points/common (Common terminal: TB9, TB19, TB29) | $\bigcirc$ |  |
| Operation indication |  | Available (Turning ON the output turns LED ON) | None | $\triangle$ | Operation indication can be checked with Q series output module. |

O: Compatible, $\Delta$ : Partially changed, $x$ : Incompatible

| Specifications |  | A0J2-E56DR | SC-A0JQIF56DR | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: | :---: |
| 5 VDC internal current consumption |  | 230 mA (TYP. All points are ON.) | - | - |  |
| Externalsupplypower(Modulepowersupply) | Voltage | None | $24 \mathrm{VDC} \pm 10 \%$ <br> Ripple voltage 4Vp-p or less | $\times$ | To deliver a power for programmable controller operation, connecting a module power supply to the interface module, TB35 or TB36 is required. |
|  | Current | None | 200mA | $\times$ | If the voltage exceeds existing power capacity, add 24VDC power supply separately. |
| External connection method |  | 36-point terminal block connector (M3 $\times 6$ screws) 2 pieces | 36-point terminal block connector (M3 $\times 6$ screws) 2 pieces | $\bigcirc$ |  |
| Applicable wire size |  | 0.75 to $2 \mathrm{~mm}^{2}$ (Applicable tightening torque $69 \mathrm{~N} \cdot \mathrm{~cm}$ ) | 0.75 to $2 \mathrm{~mm}^{2}$ (Applicable tightening torque $69 \mathrm{~N} \cdot \mathrm{~cm}$ ) | $\bigcirc$ |  |
| Applicable solderless terminal |  | ```1.25-3, 1.25-YS3A, 2-S3, 2-YS3A, V1.25-3, V1.25-YS3A, V2-S3, V2-YS3A``` | $\begin{gathered} 1.25-3,1.25-\mathrm{YS} 3 \mathrm{~A}, 2-\mathrm{S} 3, \\ 2-\mathrm{YS} 3 \mathrm{~A}, \mathrm{~V} 1.25-3, \mathrm{~V} 1.25-\mathrm{YS} 3 \mathrm{~A}, \\ \text { V2-S3, V2-YS3A } \end{gathered}$ | O |  |
| Weight |  | 1.08kg | 0.62 kg | $\Delta$ | Also consider the weight of fixed stand of programmable controller. ${ }^{* 3}$ |
| External dimensions |  | $250(\mathrm{H}) \times 190(\mathrm{~W}) \times 41(\mathrm{D}) \mathrm{mm}$ | $182(H) \times 190(\mathrm{~W}) \times 41(\mathrm{D}) \mathrm{mm}^{* 4}$ | $\times$ | Check the dimensions since they depend on the installation type (building-up/horizontal/separate type). |

*1 The value when using the input module, QX41Y41P, QX41, etc. and 5 ms is set at input response time in I/O assignment setting of PLC parameter ( 10 ms is set at default).
*2 The value when using the output module, QX41Y41P, QY41P, etc.
*3 The weight of fixed stand of programmable controller depends on replacement type of renewal tool for A0J2.
*4 The external dimensions of the SC-A0JQIF56DR do not include those of its projection.

## (15)Specifications comparison between A0J2-E56DS and interface module (SCA0JQIF56DS)

| Specifications |  | A0J2-E56DS input specifications | SC-A0JQIF56DS input specifications | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Number of input points |  | 32 points | 32 points | $\bigcirc$ |  |
| Insulation method |  | Photocoupler | Photocoupler | $\bigcirc$ |  |
| Rated input voltage |  | 12VDC/24VDC | 12VDC/24VDC | $\bigcirc$ |  |
| Rated input current |  | Approx. 3mA/Approx. 7 mA | Approx. 3mA/Approx. 7 mA | $\bigcirc$ |  |
| Operating voltage range |  | $\begin{gathered} 10.2 \text { to } 26.4 \mathrm{VDC} \\ \text { (Ripple ratio within } 5 \% \text { ) } \\ \hline \end{gathered}$ | $\begin{gathered} 10.2 \text { to } 26.4 \mathrm{VDC} \\ \text { (Ripple ratio within 5\%) } \\ \hline \end{gathered}$ | $\bigcirc$ |  |
| Maximum number of simultaneous input points |  | 60\% (10 points/common) simultaneously ON | 60\% (10 points/common) simultaneously ON | $\bigcirc$ |  |
| ON voltage/ON current |  | 9.5 VDC or more/2.6mA or more | 9.5VDC or more/2.6mA or more | $\bigcirc$ |  |
| OFF voltage/OFF current |  | 6 VDC or less/1.0mA or less | 6 VDC or less/1.0mA or less | $\bigcirc$ |  |
| Input resistance |  | Approx. $3.4 \mathrm{k} \Omega$ | Approx. 3.3k $\Omega$ | $\bigcirc$ |  |
| Input form |  | Sink input (Input current flows off.) | Sink input (Input current flows off.) | $\bigcirc$ |  |
| Response time | $\mathrm{OFF} \rightarrow \mathrm{ON}$ | 10 ms or less (6ms TYP.) | 5 ms or less (1ms TYP.) | $\Delta$ | In combination with $Q$ series input module: <br> 6 ms or less (2ms TYP.) ${ }^{* 1}$ |
|  | ON $\rightarrow$ OFF | 10 ms or less ( $7.5 \mathrm{~ms} \mathrm{TYP)}$. | 5 ms or less (1ms TYP.) | $\Delta$ | In combination with $Q$ series input module: <br> 6 ms or less (2ms TYP.) ${ }^{* 1}$ |
| Common terminal arrangement |  | 16 points/common <br> (Common terminal: TB17,TB34) | 16 points/common (Common terminal: TB17,TB34) | $\bigcirc$ |  |
| Operation indication |  | Available <br> (Turning ON the input turns LED ON) | None | $\triangle$ | Operation indication can be checked with Q series input module. |


| Specifications |  |  | O: Compatible, $\triangle$ : Partially changed, $\times$ : Incompatible |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | A0J2-E56DS output specifications | SC-A0JQIF56DS output specifications | Compatibility | Precautions for replacement |
| Number of output points |  | 24 points | 24 points | $\bigcirc$ |  |
| Insulation method |  | Photocoupler | Photocoupler | $\bigcirc$ |  |
| Rated load voltage |  | 100-240VAC, $40-70 \mathrm{~Hz}$ | 100-240VAC, $47-63 \mathrm{~Hz}$ | $\triangle$ | The maximum frequency of SCA0JQIF56DS is smaller than the one of A0J2-E56DS. |
| Maximum load voltage |  | 264VAC | 264VAC | $\bigcirc$ |  |
| Maximum load current |  | 0.6A/point, 2.4A/common | 0.6A/point, 2.4A/common | $\bigcirc$ |  |
| Minimum load voltage/ current |  | 24VAC100mA, AC100V/ 240VAC10mA | 24VAC100mA, AC100V/ 240VAC10mA | $\bigcirc$ |  |
| Maximum inrush current |  | 20A10ms or less 8A100ms or less | 20A10ms or less <br> 8A100ms or less | $\bigcirc$ |  |
| Leakage current at off |  | $\begin{aligned} & \hline 1.5 \mathrm{~mA}(120 \mathrm{VAC} 60 \mathrm{~Hz}) \\ & 3 \mathrm{~mA}(240 \mathrm{VAC} 60 \mathrm{~Hz}) \end{aligned}$ | $\begin{aligned} & \hline 1.5 \mathrm{~mA}(120 \mathrm{VAC} 60 \mathrm{~Hz}) \\ & 3 \mathrm{~mA}(240 \mathrm{VAC} 0 \mathrm{~Hz}) \end{aligned}$ | $\bigcirc$ |  |
| Maximum voltage drop at on |  | $\begin{gathered} 1.5 \mathrm{~V} \text { or less }(0.1 \mathrm{~A}-0.6 \mathrm{~A}) \\ 1.8 \mathrm{~V} \text { or less }(0.1 \mathrm{~A} \text { or less) } \\ 2.0 \mathrm{~V} \text { or less }(10-50 \mathrm{~mA}) \end{gathered}$ | $\begin{gathered} \hline 1.5 \mathrm{~V} \text { or less }(0.1 \mathrm{~A}-0.6 \mathrm{~A}) \\ 1.8 \mathrm{~V} \text { or less }(0.1 \mathrm{~A} \text { or less) } \\ 2.0 \mathrm{~V} \text { or less }(10-50 \mathrm{~mA}) \end{gathered}$ | $\bigcirc$ |  |
| Temperature derating |  | None | Refer to temperature derating chart ${ }^{* 2}$ | $\triangle$ | Use it within the range shown in the temperature derating chart. |
| Response time | OFF $\rightarrow$ ON | 1 ms or less | 1 ms or less | $\triangle$ | In combination with $Q$ series input module: <br> 2 ms or less. ${ }^{*}$ |
|  | ON $\rightarrow$ OFF | 0.5 cycle +1 ms or less | 0.5 cycle +1 ms or less | $\times$ | In combination with $Q$ series input module: <br> 0.5 cycle +2 ms or less. ${ }^{*}$ |
| Fuse rating |  | Fast blow fuse 3.2A (1/common) HP-32 | None | $\times$ | Install one fuse per common externally. <br> (A fuse and fuse holder are included.) |
| Fuse blown indication |  | Available <br> (LED turns on by fuse blown, and a signal is output to the CPU module.) | None | $\times$ |  |
| Surge suppressor | $\begin{aligned} & \hline \text { CR } \\ & \text { absorber } \end{aligned}$ | $0.022 \mu \mathrm{~F}+47 \Omega$ | $0.015 \mu \mathrm{~F}+22 \Omega$ | $\triangle$ |  |
|  | Varistor | None | Varistor (400 to 540V) | $\triangle$ |  |
| Common terminal arrangement |  | 8 points/common (Common terminal: TB9, TB19, TB29) | 8 points/common <br> (Common terminal: TB9, TB19, TB29) | $\bigcirc$ |  |
| Operation indication |  | Available (Turning ON the input turns LED ON) | None | $\triangle$ | Operation indication can be checked with Q series input module. |
| Specifications |  | A0J2-E56DS | SC-A0JQIF56DS | Compatibility | Precautions for replacement |
| 5VDC internal current consumption |  | 460 mA (TYP. All points are ON.) | - | - |  |
| External supply power (Module power supply) | Voltage | None | $24 \mathrm{VDC} \pm 10 \%$ <br> Ripple voltage 4Vp-p or less | $\times$ | To deliver a power for programmable controller operation, connecting a module power supply to the interface module, TB35 or TB36 is required. |
|  | Current | None | 570 mA | $\times$ | If the voltage exceeds existing power capacity, add 24VDC power supply separately. |
| External connection method |  | 36-point terminal block connector (M3 $\times 6$ screws) 2 pieces | 36-point terminal block connector (M3 $\times 6$ screws) 2 pieces | $\bigcirc$ |  |
| Applicable wire size |  | 0.75 to $2 \mathrm{~mm}^{2}$ (Applicable tightening torque $69 \mathrm{~N} \cdot \mathrm{~cm}$ ) | 0.75 to $2 \mathrm{~mm}^{2}$ (Applicable tightening torque $69 \mathrm{~N} \cdot \mathrm{~cm}$ ) | $\bigcirc$ |  |
| Applicable solderless terminal |  | $\begin{gathered} \text { 1.25-3, 1.25-YS3A, 2-S3, } \\ \text { 2-YS3A, V1.25-3, V1.25-YS3A, } \\ \text { V2-S3, V2-YS3A } \end{gathered}$ | 1.25-3, 1.25-YS3A, 2-S3, 2-YS3A, V1.25-3, V1.25-YS3A, V2-S3, V2-YS3A | $\bigcirc$ |  |
| Weight |  | 1.05 kg | 0.61 kg | $\Delta$ | Also consider the weight of fixed stand of programmable controller. ${ }^{*}$ 4 |
| External dimensions |  | $250(\mathrm{H}) \times 190(\mathrm{~W}) \times 41(\mathrm{D}) \mathrm{mm}$ | $182(H) \times 190(W) \times 41(\mathrm{D}) \mathrm{mm}^{* 5}$ | $\times$ | Check the dimensions since they depend on the installation type (building-up/horizontal/separate type). |

*1 The value when using the input module, QX41Y41P, QX41, etc. and 5 ms is set at input response time in I/O assignment setting of PLC parameter ( 10 ms is set at default).
*2 The figure below shows the temperature derating chart.

*3 The value when using the output module, QX41Y41P, QY41P, etc.
*4 The weight of fixed stand of programmable controller depends on replacement type of renewal tool for A0J2.
*5 The external dimensions of the SC-A0JQIF56DS do not include those of its projection.
(16)Specifications comparison between A0J2-E56DT and interface module (SCA0JQIF56DT)

O : Compatible, $\Delta$ : Partially changed, $\times$ : Incompatible

| Specifications |  | A0J2-E56DT input specifications | SC-A0JQIF56DT input specifications | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Number of input points |  | 32 points | 32 points | $\bigcirc$ |  |
| Insulation method |  | Photocoupler | Photocoupler | $\bigcirc$ |  |
| Rated input voltage |  | 12VDC/24VDC | 12VDC/24VDC | $\bigcirc$ |  |
| Rated input current |  | Approx. 3mA/Approx. 7 mA | Approx. 3mA/Approx. 7 mA | $\bigcirc$ |  |
| Operating voltage range |  | 10.2 to 26.4 VDC (Ripple ratio within $5 \%$ ) | $\begin{gathered} 10.2 \text { to } 26.4 \mathrm{VDC} \\ \text { (Ripple ratio within } 5 \% \text { ) } \end{gathered}$ | $\bigcirc$ |  |
| Maximum number of simultaneous input points |  | 60\% (10 points/common) simultaneously ON | 60\% (10 points/common) simultaneously ON | $\bigcirc$ |  |
| ON voltage/ON current |  | 9.5 VDC or more/ 2.6 mA or more | 9.5 VDC or more/2.6mA or more | $\bigcirc$ |  |
| OFF voltage/OFF current |  | 6 VDC or less $/ 1.0 \mathrm{~mA}$ or less | 6VDC or less/1.0mA or less | $\bigcirc$ |  |
| Input resistance |  | Approx. 3.4k $\Omega$ | Approx. 3.3k $\Omega$ | $\bigcirc$ |  |
| Input form |  | Sink input (Input current flows off.) | Sink input (Input current flows off.) | $\bigcirc$ |  |
| Response time | $\mathrm{OFF} \rightarrow \mathrm{ON}$ | 10 ms or less (6ms TYP.) | 5 ms or less (1ms TYP.) | $\triangle$ | In combination with Q series input module: $10 \mathrm{~ms} \text { or less }\left(6 \mathrm{~ms} \text { TYP.) }{ }^{*}\right.$ |
|  | ON $\rightarrow$ OFF | 10 ms or less ( $7.5 \mathrm{~ms} \mathrm{TYP)}$. | 5 ms or less (1ms TYP.) | $\Delta$ | In combination with $Q$ series input module: <br> 10 ms or less (6ms TYP.) ${ }^{*}$ |
| Common terminal arrangement |  | 16 points/common (Common terminal: TB17, TB34) | 16 points/common (Common terminal: TB17, TB34) | $\bigcirc$ |  |
| Operation indication |  | Available <br> (Turning ON the input turns LED ON) | None | $\triangle$ | Operation indication can be checked with $Q$ series input module. |
| Specifications |  | A0J2-E56DT output specifications | SC-A0JQIF56DT output specifications | Compatibility | Precautions for replacement |
| Number of output points |  | 24 points | 24 points | $\bigcirc$ |  |
| Insulation method |  | Photocoupler | Photocoupler | $\bigcirc$ |  |
| Rated load voltage |  | 12VDC/24VDC | 12VDC/24VDC | $\bigcirc$ |  |
| Operating load voltage range |  | 10.2 to 30VDC | 10.2 to 30VDC | $\bigcirc$ |  |
| Maximum load current |  | 0.5A/point, 4A/common | 0.5A/point, 4A/common | $\bigcirc$ |  |
| Maximum inrush current |  | 4A 10 ms or less | 4A 10 ms or less | $\bigcirc$ |  |
| Leakage current at OFF |  | 0.1 mA or less | 0.1 mA or less | $\bigcirc$ |  |
| Maximum voltage drop at ON |  | $\begin{aligned} & \hline 0.9 \mathrm{VDC} \text { (TYP.) } 0.5 \mathrm{~A} \\ & 1.5 \mathrm{VDC} \text { (MAX.) } 0.5 \mathrm{~A} \\ & \hline \end{aligned}$ | $\begin{aligned} & \hline 0.5 \mathrm{VDC} \text { (TYP.) } 0.5 \mathrm{~A} \\ & 0.8 \mathrm{VDC} \text { (MAX.) } 0.5 \mathrm{~A} \\ & \hline \end{aligned}$ | $\bigcirc$ |  |
| Response time | OFF $\rightarrow$ ON | 2 ms or less | 1 ms or less | $\triangle$ | In combination with $Q$ series output module: <br> 2 ms or less ${ }^{*}{ }^{2}$ |
|  | $\mathrm{ON} \rightarrow$ OFF | 2 ms or less (Resistance load) | 1ms or less (Resistance load) | $\Delta$ | In combination with $Q$ series output module: <br> 2 ms or less (Resistance load) ${ }^{*}$ 2 |
| External supply power | Voltage | 12VDC/24VDC (10.2 to 30VDC) | 12VDC/24VDC (10.2 to 30VDC) | $\bigcirc$ |  |
|  | Current | 23 mA (TYP. 24VDC 8 points/common ON) | 5 mA (TYP. 24VDC 8 points/common ON) | $\bigcirc$ |  |
| Surge suppressor |  | Varistor (52 to 62V) | Varistor (50.4 to 61.6V) | $\bigcirc$ |  |
| Common terminal arrangement |  | 8 points/common (Common terminal: TB9, TB19, TB29) | 8 points/common (Common terminal: TB9, TB19, TB29) | 0 |  |
| Operation indication |  | Available (Turning ON the output turns LED ON) | None | $\triangle$ | Operation indication can be checked with Q series output module. |
| Fuse |  | None | None | $\bigcirc$ |  |
| Fuse blown indication |  | None | None | $\bigcirc$ |  |

O : Compatible, $\Delta$ : Partially changed, x : Incompatible

| Specifications |  | A0J2-E56DT | SC-A0JQIF56DT | Compatibility | Precautions for replacement |
| :---: | :---: | :---: | :---: | :---: | :---: |
| 5 VDC internal current consumption |  | 225 mA (TYP. All points are ON.) | - | - |  |
| External supply power (Module power <br> supply) | Voltage | None | $24 \mathrm{VDC} \pm 10 \%$ <br> Ripple voltage 4Vp-p or less | $\times$ | To deliver a power for programmable controller operation, connecting a module power supply to the interface module, TB35 or TB36 is required. |
|  | Current | None | 260 mA | $\times$ | If the voltage exceeds existing power capacity, add 24VDC power supply separately. |
| External connection method |  | 36-point terminal block connector (M3 $\times 6$ screws) 2 pieces | 36-point terminal block connector (M3 $\times 6$ screws) 2 pieces | $\bigcirc$ |  |
| Applicable wire size |  | 0.75 to $2 \mathrm{~mm}^{2}$ (Applicable tightening torque $69 \mathrm{~N} \cdot \mathrm{~cm}$ ) | 0.75 to $2 \mathrm{~mm}^{2}$ (Applicable tightening torque $69 \mathrm{~N} \cdot \mathrm{~cm}$ ) | $\bigcirc$ |  |
| Applicable solderless terminal |  | $\begin{gathered} \text { 1.25-3, 1.25-YS3A, 2-S3, } \\ \text { 2-YS3A, V1.25-3, V1.25-YS3A, } \\ \text { V2-S3, V2-YS3A } \end{gathered}$ | $\begin{gathered} 1.25-3,1.25-\mathrm{YS} 3 \mathrm{~A}, 2-\mathrm{S} 3, \\ 2-\mathrm{YS} 3 \mathrm{~A}, \mathrm{~V} 1.25-3, \mathrm{~V} 1.25-\mathrm{YS} 3 \mathrm{~A}, \\ \text { V2-S3, V2-YS3A } \end{gathered}$ | $\bigcirc$ |  |
| Weight |  | 1.04 kg | 0.49 kg | $\Delta$ | Also consider the weight of fixed stand of programmable controller.*3 |
| External dimensions |  | $250(\mathrm{H}) \times 190(\mathrm{~W}) \times 41(\mathrm{D}) \mathrm{mm}$ | $182(H) \times 190(\mathrm{~W}) \times 41(\mathrm{D}) \mathrm{mm}^{* 4}$ | $\times$ | Check the dimensions since they depend on the installation type (building-up/horizontal/separate type). |

*1 The value when using the input module, QX41Y41P, QX41, etc. and 5 ms is set at input response time in I/O assignment setting of PLC parameter ( 10 ms is set at default).
*2 The value when using the output module, QX41Y41P, QY41P, etc.
*3 The weight of fixed stand of programmable controller depends on replacement type of renewal tool for A0J2.
*4 The external dimensions of the SC-A0JQIF56DT do not include those of its projection.

## Appendix 3 Related Manuals

## Appendix 3.1 Replacement handbooks

(1) Renewal catalogue

| No. | Manual name | Manual number | Model code |
| ---: | :--- | :--- | :---: | :---: |
| 1 | MELSEC-A/QnA Series Transition Guide | L08077E | - |
| 2 | MELSEC-AnS/QnAS Series Transition Guide | L08236E | - |

(2) Handbook for transition

| No. | Manual name | Manual number | Model code |
| :---: | :--- | :--- | :---: |
| 1 | Transition from MELSEC-A/QnA (Large Type) Series to Q Series Handbook <br> (Fundamentals) | L08043ENG | - |
| 2 | Transition from MELSEC-A/QnA (Large Type) Series to Q Series Handbook <br> (Intelligent Function Modules) | L08046ENG | - |
| 3 | Transition from MELSEC-A/QnA (Large Type), AnS/QnAS Series (Small <br> Type) to Q Series Handbook (Network Modules) | L08048ENG | - |
| 4 | Transition from MELSEC-A/QnA (Large Type) Series, AnS/QnAS Series <br> (Small Type) to Q Series Handbook (Communications) | L08050ENG | - |
| 5 | Transition from MELSEC-A0J2H Series to Q Series Handbook | L08060ENG | - |
| 6 | Transition from MELSECNET/MINI-S3, A2C(I/O) to CC-Link Handbook | L08061ENG | - |
| 7 | Transition from MELSEC-I/O LINK to CC-Link/LT Handbook | L08062ENG | - |
| 8 | Transition from MELSEC-I/O LINK to AnyWire DB A20 Handbook | L08263ENG | - |
| 9 | Transition of CPUs in MELSEC Redundant System Handbook <br> (Transition from Q4ARCPU to QnPRHCPU) | L08117ENG | - |

(3) Renewal examples

| No. | Manual name | Manual number | Model code |
| ---: | :---: | :---: | :---: |
| 1 | MELSEC-A/QnA Series Transition Examples | L08121E | - |

(4) Others

| No. | Manual name (TECHNICAL BULLETIN) | Manual number | Model code |
| :---: | :--- | :--- | :---: |
| 1 | Procedures for Replacing Positioning Module AD71 with QD75 | FA-A-0060 | - |
| 2 | Precautions for replacing A/QnA (large type) series CPU with Universal <br> model QCPU | FA-A-0068 | - |

## Appendix 3.2 A0J2H Series

| No. | Manual name | Manual number | Model code |
| :---: | :---: | :---: | :---: |
| 1 | MELSEC-A/QnA Catalog | L08033E | - |
| 2 | MELSEC-A/QnA Data Book | L08029E | - |
| 3 | A0J2HCPU(P21/R21) User's Manual | IB-66268 | 13 J 788 |
| 4 | Type ACPU/QCPU-A (A Mode) (Fundamentals) Programming Manual | IB-66249 | 13 J 440 |
| 5 | Type ACPU/QCPU-A (A Mode) (Common Instructions) Programming Manual | IB-66250 | 13 J 441 |
| 6 | Type MELSAP-II(SFC) Programming Manual | IB-66361 | 13JF40 |
| 7 | Type A0J2 (Input/Output unit) User's Manual | IB-66068 | 13 J 602 |
| 8 | A/D converter unit for A0J2 type A0J2-68AD User's Manual | IB-66098 | 13 J 614 |
| 9 | D/A converter unit for A0J2 type A0J2-62DA User's Manual | IB-66093 | 13 J 612 |
| 10 | High Speed Counter Module for A0J2 Type A0J2-D61S1 User's Manual | IB-66094 | 13 J 613 |
| 11 | Positioning Module for A0J2 Type A0J2-D71 User's Manual | IB-66133 | 13 J 626 |
| 12 | Type A0J2-C214S1 User's Manual | IB-66266 | 13 J 659 |
| 13 | Remote I/O unit type A0J2C25 User's Manual | IB-66129 | 13 J 632 |
| 14 | type A0J2(Data link) User's Manual | IB-66069 | 13 J 603 |
| 15 | PC fault detection module type AS91, A1SS91, A0J2-S91 User's Manual | IB-66626 | 13J828 |

## Appendix 3.3 Q series

| No. | Manual name | Manual number | Model code |
| ---: | :--- | :--- | :--- |
| 1 | MELSEC-Q Catalog | L08033E | - |
| 2 | QCPU User's Manual(Hardware Design, Maintenance and Inspection) | SH-080483ENG | 13JP73 |
| 3 | QCPU User's Manual(Function Explanation, Program Fundamentals) | SH-080484ENG | 13JP74 |
| 4 | Qn(H)/QnPH/QnPRHCPU User's Manual(Function Explanation, Program <br> Fundamentals) | SH-080808ENG | $13 J Z 28$ |
| 5 | QCPU(Q Mode)/QnACPU Programming Manual (Common Instructions) | SH-080039 | 13JF58 |
| 6 | QCPU(Q Mode)/QnACPU Programming Manual (PID Control Instructions) | SH-080040 | 13JF59 |
| 7 | QCPU(Q Mode)/QnACPU Programming Manual (SFC) | SH-080041 | 13JF60 |
| 8 | QA65B/QA68B Extension Base Unit User's Manual | IB-0800158 | 13JR26 |
| 9 | I/O Module Type Building Block User's Manual | SH-080042 | 13JL99 |
| 10 | Insulation Displacement Connector for MELSEC-Q Series 32-Point I/O <br>  <br> Module User's manual | IB-0800228 | 13JT92 |
| 11 | Analog-Digital Converter Module User's Manual Q64AD/Q68ADV/Q68ADI/ <br> GX Configurator-AD | SH-080055 | 13JR03 |
| 12 | Digital-Analog Converter Module User's Manual | SH-080054 | 13JR02 |
| 13 | Analog Input/Output Module Use's Manual | SH-080793 | 13JZ25 |
| 14 | High-Speed Counter Module User's Manual | SH-080036 | 13JL95 |
| 15 | High-Speed Counter Module User's Manual (Hardware) | IB-0800421 | 13JY78 |
| 16 | Type QD75P/QD75D Positioning Module User's Manual | SH-080058 | 13JR09 |
| 17 | Q Corresponding Serial Communication Module User's Manual (Basic) | SH-080006 | 13JL86 |
| 18 | Q Corresponding Serial Communication Module User's Manual (Application) | SH-080007 | 13JL87 |
| 19 | Q Corresponding MELSEC Communication Protocol Reference Manual | SH-080008 | 13JF89 |

## Appendix 3.4 Programming Tool

| No. | Manual name | Manual number | Model code |
| ---: | :--- | :--- | :--- |
| 1 | GX Developer Version 8 Operating Manual | SH-080373E | 13JU41 |
| 2 | GX Developer Version 8 Operating Manual (SFC) | SH-080374E | 13JU42 |
| 3 | GX Simulator Version 7 Operating Manual | SH-080468ENG | 13JU51 |
| 4 | GX Works2 Version 1 Operating Manual (Common) | SH-080779ENG | 13JU63 |
| 5 | GX Works2 Version 1 Operating Manual (Intelligent Function Module) | SH-080921ENG | 13JU69 |
| 6 | Type SW4IVD-GPPA (GPP) Operating Manual | IB-66855 | 13JL62 |

Appendix 3.5 Products manufactured by Mitsubishi Electric Engineering Co., Ltd.

| No. | Catalog name | Catalog number |
| :---: | :---: | :---: |
| 1 | Programmable Controller Upgrade Tool General Catalog | SAN C044•068R |

Appendix 3.6 Products manufactured by Mitsubishi Electric System \& Service Co., Ltd.

| No. | Data/catalog | Number |
| :---: | :--- | :--- |
| 1 | Renewal tool for A0J2 series Transition from MELSEC-A0J2(H) series to <br> renewal system using renewal tool | X903071003 |
| 2 | Replace A0J2(H) system with Q series using existing wiring! | X900707-115 |

## WARRANTY

Please confirm the following product warranty details before using this product.

## 1. Gratis Warranty Term and Gratis Warranty Range

If any faults or defects (hereinafter "Failure") found to be the responsibility of Mitsubishi occurs during use of the product within the gratis warranty term, the product shall be repaired at no cost via the sales representative or Mitsubishi Service Company.
However, if repairs are required onsite at domestic or overseas location, expenses to send an engineer will be solely at the customer's discretion. Mitsubishi shall not be held responsible for any re-commissioning, maintenance, or testing on-site that involves replacement of the failed module.
[Gratis Warranty Term]
The gratis warranty term of the product shall be for one year after the date of purchase or delivery to a designated place. Note that after manufacture and shipment from Mitsubishi, the maximum distribution period shall be six (6) months, and the longest gratis warranty term after manufacturing shall be eighteen (18) months. The gratis warranty term of repair parts shall not exceed the gratis warranty term before repairs.
[Gratis Warranty Range]
(1) The range shall be limited to normal use within the usage state, usage methods and usage environment, etc., which follow the conditions and precautions, etc., given in the instruction manual, user's manual and caution labels on the product.
(2) Even within the gratis warranty term, repairs shall be charged for in the following cases.

1. Failure occurring from inappropriate storage or handling, carelessness or negligence by the user. Failure caused by the user's hardware or software design.
2. Failure caused by unapproved modifications, etc., to the product by the user.
3. When the Mitsubishi product is assembled into a user's device, Failure that could have been avoided if functions or structures, judged as necessary in the legal safety measures the user's device is subject to or as necessary by industry standards, had been provided.
4. Failure that could have been avoided if consumable parts (battery, backlight, fuse, etc.) designated in the instruction manual had been correctly serviced or replaced.
5. Failure caused by external irresistible forces such as fires or abnormal voltages, and Failure caused by force majeure such as earthquakes, lightning, wind and water damage.
6. Failure caused by reasons unpredictable by scientific technology standards at time of shipment from Mitsubishi.
7. Any other failure found not to be the responsibility of Mitsubishi or that admitted not to be so by the user.
8. Onerous repair term after discontinuation of production
(1) Mitsubishi shall accept onerous product repairs for seven (7) years after production of the product is discontinued. Discontinuation of production shall be notified with Mitsubishi Technical Bulletins, etc.
(2) Product supply (including repair parts) is not available after production is discontinued.

## 3. Overseas service

Overseas, repairs shall be accepted by Mitsubishi's local overseas FA Center. Note that the repair conditions at each FA Center may differ.
4. Exclusion of loss in opportunity and secondary loss from warranty liability

Regardless of the gratis warranty term, Mitsubishi shall not be liable for compensation to:
(1) Damages caused by any cause found not to be the responsibility of Mitsubishi.
(2) Loss in opportunity, lost profits incurred to the user by Failures of Mitsubishi products.
(3) Special damages and secondary damages whether foreseeable or not, compensation for accidents, and compensation for damages to products other than Mitsubishi products.
(4) Replacement by the user, maintenance of on-site equipment, start-up test run and other tasks.

## 5. Changes in product specifications

The specifications given in the catalogs, manuals or technical documents are subject to change without prior notice.

The company names, system names and product names mentioned in this manual are either registered trademarks or trademarks of their respective companies.

## Programmable Controller

| Country/Region | Sales office | Tel/Fax |
| :---: | :---: | :---: |
| USA | MITSUBISHI ELECTRIC AUTOMATION, INC. <br> 500 Corporate Woods Parkway, Vernon Hills, IL 60061, U.S.A. | $\begin{aligned} & \text { Tel : +1-847-478-2100 } \\ & \text { Fax : +1-847-478-2253 } \end{aligned}$ |
| Mexico | MITSUBISHI ELECTRIC AUTOMATION, INC. Mexico Branch Mariano Escobedo \#69, Col. Zona Industrial, Tlalnepantla Edo. Mexico, C.P. 54030 | Tel : +52-55-3067-7500 |
| Brazil | MITSUBISHI ELECTRIC DO BRASIL COMÉRCIO E SERVIÇOS LTDA. Avenida Adelino Cardana, 293, 21 andar, Bethaville, Barueri SP, Brazil | $\begin{aligned} & \text { Tel : +55-11-4689-3000 } \\ & \text { Fax : +55-11-4689-3016 } \end{aligned}$ |
| Germany | MITSUBISHI ELECTRIC EUROPE B.V. German Branch Mitsubishi-Electric-Platz 1, 40882 Ratingen, Germany | $\begin{aligned} & \text { Tel : +49-2102-486-0 } \\ & \text { Fax : +49-2102-486-1120 } \end{aligned}$ |
| UK | MITSUBISHI ELECTRIC EUROPE B.V. UK Branch Travellers Lane, Hattield, Hertfordshire, AL10 8XB, U.K. | $\begin{aligned} & \text { Tel : +44-1707-28-8780 } \\ & \text { Fax : +44-1707-27-8695 } \end{aligned}$ |
| Ireland | MITSUBISHI ELECTRIC EUROPE B.V. Irish Branch Westgate Business Park, Ballymount, Dublin 24, Ireland | $\begin{aligned} & \text { Tel : + }+353-1-4198800 \\ & \text { Fax : }+353-1-4198890 \end{aligned}$ |
| Italy | MITSUBISHI ELECTRIC EUROPE B.V. Italian Branch Centro Direzionale Colleoni-Palazzo Sirio Viale Colleoni 7, 20864 Agrate Brianza(Milano) Italy | $\begin{aligned} & \text { Tel : +39-039-60531 } \\ & \text { Fax : +39-039-6053-312 } \end{aligned}$ |
| Spain | MITSUBISHI ELECTRIC EUROPE, B.V. Spanish Branch Carretera de Rubí, 76-80-Apdo. 420, 08190 Sant Cugat del Vallés (Barcelona), Spain | $\begin{aligned} & \text { Tel: : +34-935-65-3131 } \\ & \text { Fax : +34-935-89-1579 } \end{aligned}$ |
| France | MITSUBISHI ELECTRIC EUROPE B.V. French Branch 25, Boulevard des Bouvets, 92741 Nanterre Cedex, France | $\begin{aligned} & \text { Tel : + } 33-1-55-68-55-68 \\ & \text { Fax : +33-1-55-68-57-57 } \end{aligned}$ |
| Czech Republic | MITSUBISHI ELECTRIC EUROPE B.V. Czech Branch Avenir Business Park, Radlicka 751/113e, 15800 Praha5, Czech Republic | $\begin{aligned} & \text { Tel : +420-251-551-470 } \\ & \text { Fax : +420-251-551-471 } \end{aligned}$ |
| Poland | MITSUBISHI ELECTRIC EUROPE B.V. Polish Branch ul. Krakowska 50, 32-083 Balice, Poland | $\begin{aligned} & \text { Tel: }+48-12-347-65-00 \\ & \text { Fax : +48-12-630-47-01 } \end{aligned}$ |
| Sweden | MITSUBISHI ELECTRIC EUROPE B.V. (Scandinavia) Fjelievägen 8, SE-22736 Lund, Sweden | $\begin{aligned} & \text { Tel : + 46-8-625-10-00 } \\ & \text { Fax : +46-46-39-70-18 } \end{aligned}$ |
| Russia | MITSUBISHI ELECTRIC (RUSSIA) LLC St. Petersburg Branch <br> Piskarevsky pr. 2, bld 2, lit "Sch", BC "Benua", office 720; 195027 St. Petersburg, Russia | $\begin{aligned} & \text { Tel : +7-812-633-3497 } \\ & \text { Fax : +7-812-633-3499 } \end{aligned}$ |
| Turkey | MITSUBISHI ELECTRIC TURKEY A.Ş Ümraniye Branch Serifali Mah. Kale Sok. No:41 34775 Umraniye - Istanbul, Turkey | $\begin{aligned} & \text { Tel : +90-216-969-2500 } \\ & \text { Fax : +90-216-526-3995 } \end{aligned}$ |
| UAE | MITSUBISHI ELECTRIC EUROPE B.V. Dubai Branch Dubai Silicon Oasis, P.O.BOX 341241, Dubai, U.A.E. | $\begin{aligned} & \text { Tel : +971-4-3724716 } \\ & \text { Fax : +971-4-3724721 } \end{aligned}$ |
| South Africa | ADROIT TECHNOLOGIES <br> 20 Waterford Office Park, 189 Witkoppen Road, Fourways, South Africa | $\begin{aligned} & \text { Tel: }+27-11-658-8100 \\ & \text { Fax : +27-11-658-8101 } \end{aligned}$ |
| China | MITSUBISHI ELECTRIC AUTOMATION (CHINA) LTD. <br> No. 1386 Hongqiao Road, Mitsubishi Electric Automation Center, Shanghai, China | $\begin{aligned} & \text { Tel : +86-21-2322-3030 } \\ & \text { Fax : +86-21-2322-3000 } \end{aligned}$ |
| Korea | MITSUBISHI ELECTRIC AUTOMATION KOREA CO., LTD. <br> 7F-9F, Gangseo Hangang Xi-tower A, 401, Yangcheon-ro, Gangseo-Gu, Seoul 07528, Korea | $\begin{aligned} & \text { Tel : + 82-2-3660-9530 } \\ & \text { Fax : +82-2-3664-8372 } \end{aligned}$ |
| Singapore | MITSUBISHI ELECTRIC ASIA PTE. LTD. <br> 307, Alexandra Road, Mitsubishi Electric Building, Singapore 159943 | $\begin{aligned} & \text { Tel : }+65-6473-2308 \\ & \text { Fax : }+65-6476-7439 \end{aligned}$ |
| Thailand | MITSUBISHI ELECTRIC FACTORY AUTOMATION (THAILAND) CO., LTD. 12th Floor, SV.City Building, Office Tower 1, No. 896/19 and 20 Rama 3 Road, Kwaeng Bangpongpang, Khet Yannawa, Bangkok 10120, Thailand | $\begin{aligned} & \text { Tel : +66-2682-6522 } \\ & \text { Fax : +66-2682-6020 } \end{aligned}$ |
| Vietnam | MITSUBISHI ELECTRIC VIETNAM COMPANY LIMITED Hanoi Branch 6th Floor, Detech Tower, 8 Ton That Thuyet Street, My Dinh 2 Ward, Nam Tu Liem District, Hanoi, Vietnam | $\begin{aligned} & \text { Tel: }:+84-4-3937-8075 \\ & \text { Fax : +84-4-3937-8076 } \end{aligned}$ |
| Malaysia | MITSUBISHI ELECTRIC SALES MALAYSIA SDN. BHD. <br> Lot 11, Jalan 219, 46100 Petaling Jaya, Selangor Darul Ehsan, Malaysia | $\begin{aligned} & \text { Tel: }:+60-3-7626-5000 \\ & \text { Fax : +60-3-7658-3544 } \end{aligned}$ |
| Indonesia | PT. MITSUBISHI ELECTRIC INDONESIA <br> Gedung Jaya 11th Floor, JL. MH. Thamrin No.12, Jakarta Pusat 10340, Indonesia | $\begin{aligned} & \text { Tel : +62-21-3192-6461 } \\ & \text { Fax : +62-21-3192-3942 } \end{aligned}$ |
| India | MITSUBISHI ELECTRIC INDIA PVT. LTD. Pune Branch <br> Emerald House, EL-3, J Block, M.I.D.C., Bhosari, Pune-411026, Maharashtra, India | $\begin{aligned} & \text { Tel : +91-20-2710-2000 } \\ & \text { Fax : +91-20-2710-2100 } \end{aligned}$ |
| Australia | MITSUBISHI ELECTRIC AUSTRALIA PTY. LTD. <br> 348 Victoria Road, P.O. Box 11, Rydalmere, N.S.W 2116, Australia | $\begin{aligned} & \text { Tel : +61-2-9684-7777 } \\ & \text { Fax : +61-2-9684-7245 } \end{aligned}$ |


[^0]:    This handbook confers no industrial property rights or any rights of any other kind, nor does it confer any patent licenses. Mitsubishi Electric Corporation cannot be held responsible for any problems involving industrial property rights which may occur as a result of using the contents noted in this handbook.

[^1]:    For MELSEC-A/QnA (large type) Series to Q Series transition related products manufactured by Mitsubishi Electric Engineering Co., Ltd. or Mitsubishi Electric System \& Service Co., Ltd., contact your local sales office or representative

[^2]:    *1 The Q00UJCPU does not have the standard RAM.
    *2 There are files that cannot be used in the Q00UJCPU.
    *3 Only one drive, drive 1 or drive 2, can be used.
    *4 Q00UJCPU, Q00UCPU, Q01UCPU does not have a memory card.
    *5 The Universal model CPU, whose program memory is a Flash ROM, does not have to perform the ROM operation.

[^3]:    *1 These modules can be replaced with the renewal tool for A0J2. For details, refer to Appendix 2.
    *2 Use the QX70 when using 12VDC.

[^4]:    *1 Note that the buffer memory address may differ between the $A$ series and $Q$ series.
    *2 The high-speed and retentive timers are automatically converted according to the parameter settings.

[^5]:    *1 Note that the buffer memory address may differ between the $A$ series and $Q$ series.

[^6]:    *1 As the instruction is used for different functions, being deleted or corrected is required.
    *2 Converted to "SM1255" as instructions that cannot be converted.

[^7]:    *1 The QD62-H02 is a dedicated module for replacing the A0J2-D61S1/AD61-S1 with Q series modules.
    The QD62-H02 and the A0J2-D61S1/AD61-S1 use the same input filter method.

